```
; int tog led interrupt toggle led
     3
 4
             PROCESSOR PIC16F628
 5
             #include <P16F628.INC>
              __CONFIG __CP_OFF & _MCLRE_OFF & _HS_OSC & _LVP_OFF & _WDT OFF
 7
 8
              cblock 0x20
 9
                 temp
10
                  temp1
11
                  count
12
                  count0
13
                  count1
14
                  count2
15
                  w temp
16
                  OPTION REG temp
17
                  STATUS_temp
18
             endc
19
                              ;reset vector
             ORG 0x00
20
              goto main
21
22
                             ;interrupt vector
             ORG 0x04
             goto EXT ISR
23
                                ;exit Interrupt Service Routine
24
25 ;---push pop---
                     MACRO
26
            PUSH
             movwf w temp

      movwf
      w_temp
      ;
      w_temp = w

      swapf
      w_temp, f
      ;
      swap them, ใช้ swap เพราะ movf มีโอกาสที่จะไปเปลี่ยน zero

27
             flag % status register
29
             banksel TRISA ; select bank1
            swapf    OPTION_REG,w;    w= OPTION_REG
movwf    OPTION_REG_temp; OPTION_REG_temp= w
30
31
            32
33
34
            MOVWF STATUS temp; STATUS temp= w
35
             ENDM
36
37
            POP MACRO
             SWAPF STATUS_temp, w
MOVWF STATUS
38
39
                               ; select bank1
40
             banksel TRISA
             swapf    OPTION_REG_temp, w
movwf    OPTION_REG
41
42
43
             banksel PORTA ; select bank0
44
              swapf w temp, w
45
             ENDM
46
   ;---end push pop---
47
48 main:
49
              ;---Port Config---
50
             movlw .7
             banksel CMCON ; select Bank0 (CMCON agBank0)
movwf CMCON ; Disable analog comparator
banksel TRISB ; select Bank1
bcf TRISA,0 ; Port RAO is an output pin
bsf TRISB,0 ; Port RBO is an input pin
51
52
53
54
55
             banksel PORTB
56
                                  ; select Bank0
             ;---End Port Config---
57
58
             ;---Interrupt Config---
59
             bsf INTCON,GIE ; enable global interrupt
60
             bsf INTCON,INTE ; enable external interrupt, INT = external
             interrupt, E=enable
             bcf INTCON,INTF ; clear external interrupt flag , F = flag
61
62
             ;; Or using movlw instead of bsf, bcf
63
             ;; BIT: 7654 3219
64
             ;movlw B'1001 0000'; GIE(bit7), INTE(bit4)
65
             ; movwf INTCON
66
             ;---End Interrupt Config---
```

```
67
            ;---Option Config---
68
            banksel OPTION REG
69
            bcf     OPTION REG, INTEDG; INTEDG=0 interrupt on falling edge of
            RB0/INT pin
70
            ;bsf OPTION REG,INTEDG; INTEDG=1 interrupt on rising edge of
            RB0/INT pin
71
            ;---End Option Config---
            banksel PORTA ; select Bank0 (can use CMCON instead of PORTA)
72
73
74 inf_loop:
75
             goto inf_loop
76
77
   ;---interrupt---
EXT_ISR:
78
79
80
             PUSH
                   INTCON, INTF
81
             bcf
            btfss PORTA, 0
goto SET_ON
82
83
84 SET_OFF:
85
            bcf
                   PORTA, 0
             goto    SET_DONE
bsf    PORTA, 0
86
87 SET ON: bsf
88 SET_DONE:
89
            POP
90
             RETFIE
91 ;---end interrupt---
92
93
94
95
            END
96
```