

Supreeta Venkatesan

<https://www.linkedin.com/in/supreetavenkatesan/>

supreetavenk@gmail.com

Phone: +1-213-448-2722

EDUCATION

University of Southern California

Master of Science (M.S.), Computer Engineering

Expected Dec 2020

SSN College of Engineering, Anna University

CGPA: 8.62/10.0

Bachelor of Engineering (B.E.), Electronics and Communication Engineering

May 2018

TECHNICAL SKILLS

Languages

C, C++, System Verilog, Verilog, Embedded C, Python

EDA Tools

Cadence Incisive, Questa Sim, Cadence Virtuoso, Xilinx ISE

Hardware Platform

Arduino, Intel 8051, Intel 8086, TI MSP 430, FPGA Trainer Kit, Raspberry Pi

Software Platform

Arduino IDE, Atmel Studio, MATLAB, P-Spice, Eagle

Operating Systems

Windows, Linux, Raspbian

IOT Platforms

Thingspeak, Adafruit

EXPERIENCE

Chip Design Verification Consultant

July 2018

Edveon Technologies Inc.

Chennai, India

- Understood the DUT specification, built test cases using SystemVerilog and UVM and documented the implementation.

Research Intern

July 2017

Central Electronics Engineering Research Institute

Chennai, India

- Worked on building an automated Security System capable of capturing and mailing the images of the intruder and sending text alerts using Twilio messaging service.

Trainee

December 2016

Data Patterns India Private Limited

Chennai, India

- Learnt the hardware and software life cycles, basics of FPGA programming and implementation of technologies such as SMT for fabrication of boards.

PROJECTS

Data Transmission Based on Valid and Stall

- Built a testbench environment using SystemVerilog and UVM to verify the given chip specification.
- Derived the checker logic and the testcases.
- Detected and resolved the bugs in DUT and testbench.
- Documented the implementation.
- Simulators used: Questa, Cadence.

Cloud Based Secure Parking

- Modelled and built a cloud based parking management system which provides the user the option of pre-booking the parking slot and security to the vehicle.

Automated Security System for Indoor Spaces

- Developed an automated security system that alerts the user in case of fire and presence of an intruder.

iStand (A prototype of the Segway)

- Built a self-balancing robot which works on the principle of an inverted pendulum system.

COURSEWORK

- **Graduate:** MOS VLSI Circuit Design, Computer Systems Organization
- **Undergraduate:** Electronic Devices, Circuit Theory, Electronic Circuits I & II, Microprocessor & Microcontroller, VLSI Design, Computer Architecture, Embedded & Real Time Systems

PUBLICATION

“Design and Implementation of an Automated Security System using Twilio Messaging Service”

- Presented at the International Conference on Smart Cities, Automation and Intelligent Computing Systems (ICON SONICS 2017), organized by IEEE Indonesia and Universitas Multimedia Nusantara, Indonesia.

CERTIFICATION

- Awarded course certificate for successfully completing the workshop cum competition, ‘Kaizen Robotics Program Advanced Level-2’ (Dec 2015) and ‘Kaizen Robotics Program Level-1’ by Lema Labs.
- Attended a workshop on ‘Printed Circuit Board Design’ at SSN College of Engineering.