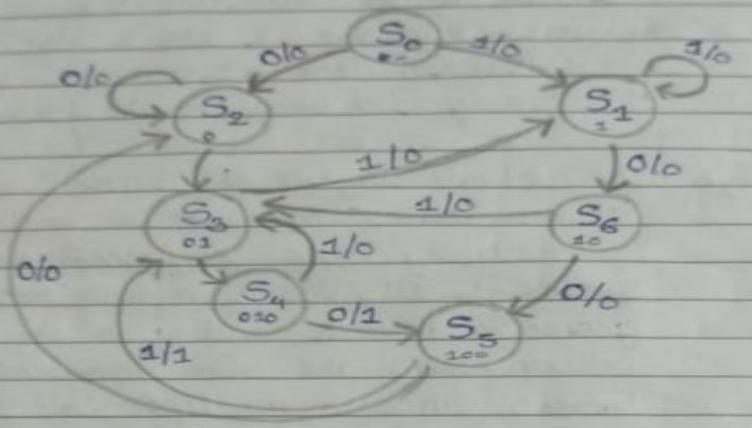
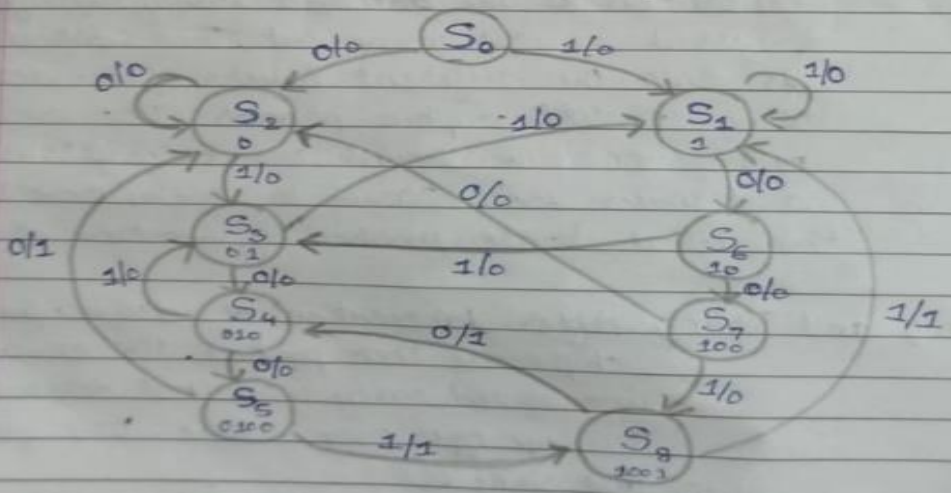
Q8. State Diagram for Mealy FSM



State diagram for Moore FSM



	Mealy FSM	Moore FSM
☆	Output depends on present state & the present input	Output depends only on the present state
☆	Lesser number of states are required	More number of states are required.
☆	React faster to inputs, ie react in same clock cycle	They generally react one clock cycle later
☆	Asynchronous generation of output, though its ~~generates~~ states alters to synchronous on the present clock	The state and output both change the ~~output~~ synchronously to its clock edge