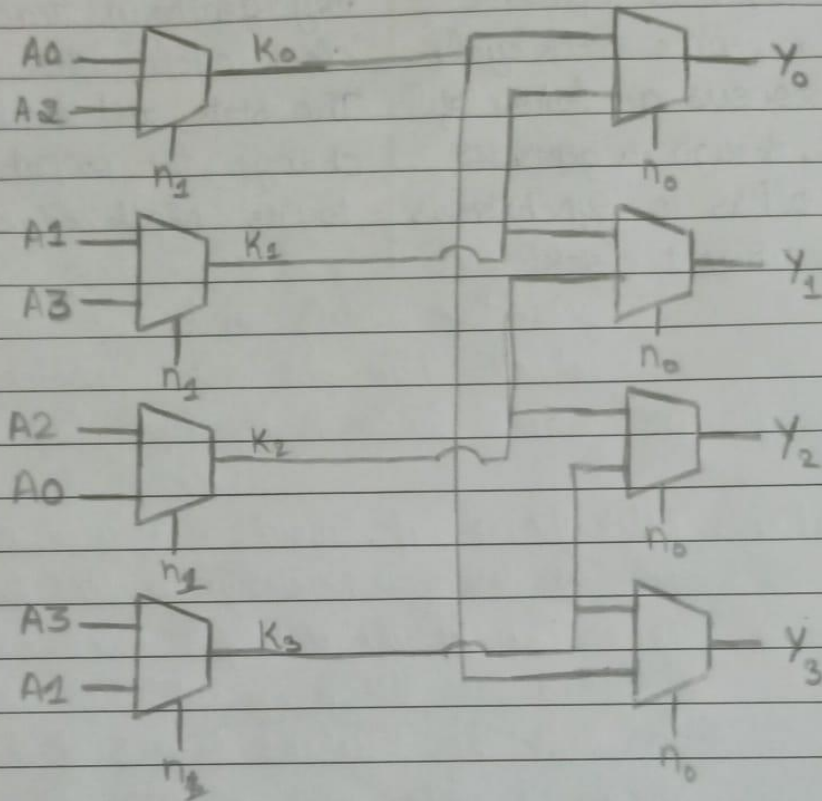


For an N bit number $\rightarrow N \cdot \log_2 N$ 2:1 mux are needed

Eg. For $N=8$, the circuit needs $8 \log_2 8$
ie 24 2:1 mux



Performs ROR $\{n_1, n_0\}$ times