



INDIAN INSTITUTE OF TECHNOLOGY BOMBAY

CS305/341

COMPUTER ARCHITECTURE

i7: Spectre and More

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November 6, 2018

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1 Abstract

1.1 What is it About?

This is a simple report template with the UCT logo. Feel free to use/modify it to suit your needs. Variables that need to be altered have been commented to make modifications easier. For example if you need to change the university logo, look for the comment `% University Logo` in this file and then make appropriate modifications in that line.

A Table of Contents and a bibliography have also been implemented. To add entries to your bibliography, simply edit `biblist.bib` in the root folder and then use the `\cite{...}` command in `main.tex` [1]. The Table of Contents will be updated automatically.

I hope that you find this template both visually appealing and useful.

1.2 Can someone Explain in Simple words?

— Linus

2 Baby Steps/Background

2.1 Out of order Execution

2.2 Speculation Execution

2.3 Branch Prediction

2.4 The Memory Hierarchy

2.5 Microarchitectural Side-Channel Attacks/Flush-Reload Techniques

2.6 Return Oriented Programming

3 What is Spectre? Why is it more famous than me?

3.1 General

3.2 Variet1

3.3 Variet2

3.4 Variet4

4 Our Result(Code+Result(screenshot))

5 Proof of Concept Study

6 The Experiment

6.1 Our Result

6.2 Our Technique

7 Mitigations

7.1 Preventing Speculative Execution

7.2 Preventing Access to Secret Data

7.3 Preventing Data from Entering Covert Channels

7.4 Limiting Data Extraction from Covert Channels

7.5 Preventing Branch Poisoning

8 Current Work Going On

9 Conclusion

10 Acknowledgement

References

- [1] Help on BibTeX entry types. <http://nwalsh.com/tex/texhelp/bibtex-7.html>. Accessed: 2015-03-12.