

National Institute of Technology, Delhi

Name of the Examination: B. Tech./September 2019

Branch : ECE

Semester : V

Title of the Course : Microprocessor and Microcontroller

Course Code : ECB 302

Time: 2 Hours

Maximum Marks: 25

Answer all the questions

Explain the function of the ALE and IO/M['] signals of the microprocessor with neat diagram. (2)

MVI A, 45H (2)

XRA A

Specify the content of the accumulator and the flag register.

List the difference between memory mapped I/O & peripheral mapped I/O. (2)

Write a program to read the content in the flag register and store into the memory address 9050. (2)

What is meant by pipeline concept in 8086 microprocessor and what is minimum and maximum mode of operation. (2)

List the operating modes of 8255 with its control word format and explain the modes of operation with neat diagram (3)

Explain interrupts in 8085 and 8086 microprocessor. (3)

Draw the timing diagram of LXI instruction in 8085 microprocessor. (3)

Write an assembly language program to perform BCD addition using 8085 instruction sets and calculate the time required to execute the program if the processor is 2MHZ. (4)

0. From the below Figure specify the memory address of EPROM. (2)

