SIMULATOR FOR SYNTHETIC MODULE

At

BHARAT ELECTRONICS LTD.

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Outline:

- Project Overview
- RADAR Concept
- Breaf about EW systems
- Synthetic Channel
- ✓ Familiarization of IC's
- Results & Analysis
- Conclusion & Future Scope

Project Overview

The aim of the project is to develop a PC based simulator jig for testing a subsystem called *Synthetic Module* of an Electronic Warfare systems.

The core activities are fabrication/assembling of a PCB consisting of micro-controller and field programmable array logic, Developing code for interfacing with PCB through PC and checking the output bit pattern with simulation of commands.

RADAR Concept

- 1940 by US Navy
- Radio Detection And Ranging
- RDF-Range and Direction Finding
- Electronic eye
- Funtions:

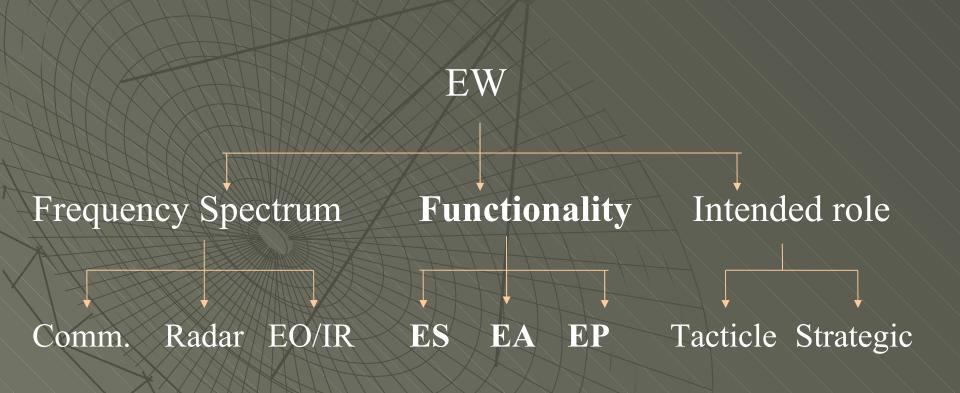
Range

Velocity

Angle

Electronic Warfare Systems

Involving the use of electromagnetic engergy to conrol the electromagnetic spectrum or to attack the enemy.





Electronic Support

- ESM-Electronic Support Measures
- Passive listening
- Search, intercept, identify, locate, record
- Paramerters like freq, DOA, AOA, PW
- Analyze radiated EM energy
- Wide open in freq, wide spatial angle
- Provides survillance, warning information required for decisions involving EA, EP, tactical employment of forces

Electronic Attack

- ECM-Electronic Counter Measures
- Active or Passive
- Interface with enemy's use of EM spectrum
- Use of Electromagnetic energy, Anti-radiated weapon
- Jamming enemy radar-one way transmission
 - · Noise: spot, barrage, sweep, sweep lock-on, cover pulse
 - Deception (False Targeting): Range, Velocity, Angle
- Confuse enemy intelligence, surveilance
- Directional beam antennas(Horn, Rotman lens Fed Multiple Beam Array)

Electronic Protection

- ECCM-Electronic Counter Counter Measure
- Degrade, neutralize, destroy
- Protect own platform against EA



RACK-1

Synthetic Channel(Port)

Technique Generator

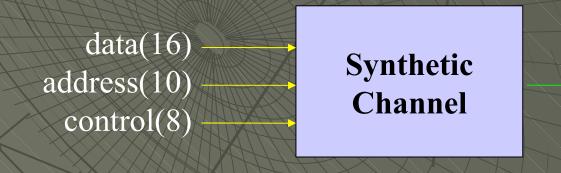
Synthetic Channel(STBD)

ECM Processor

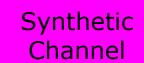
LVPS-4

Synthetic Channel

- LRU
- Generate modulated RF in the range 8-18GHz
- Module connected to the radar of NAVY VESSEL
- Interface card and RF generator(DT0's, PCB's, RF comp)



RF generator



NAVY VESSEL

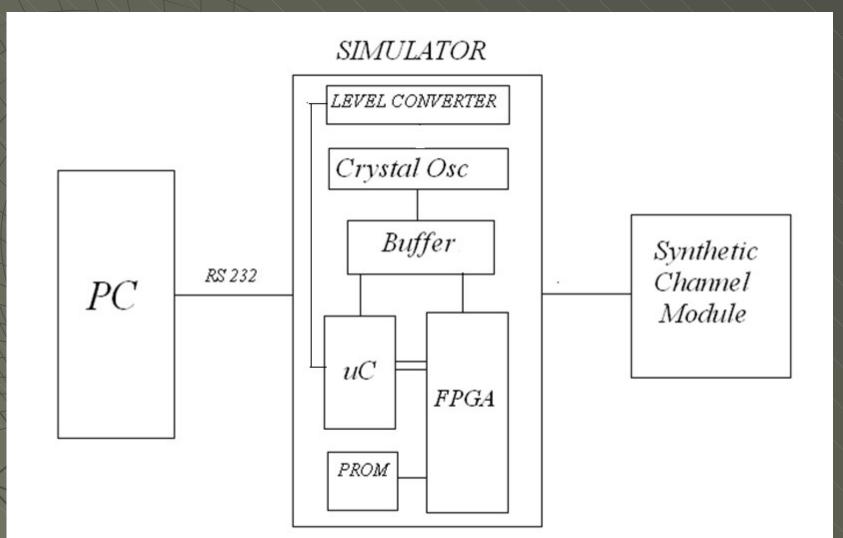
8-18GHz

CH 1
0-90°
CH 2
90-180°

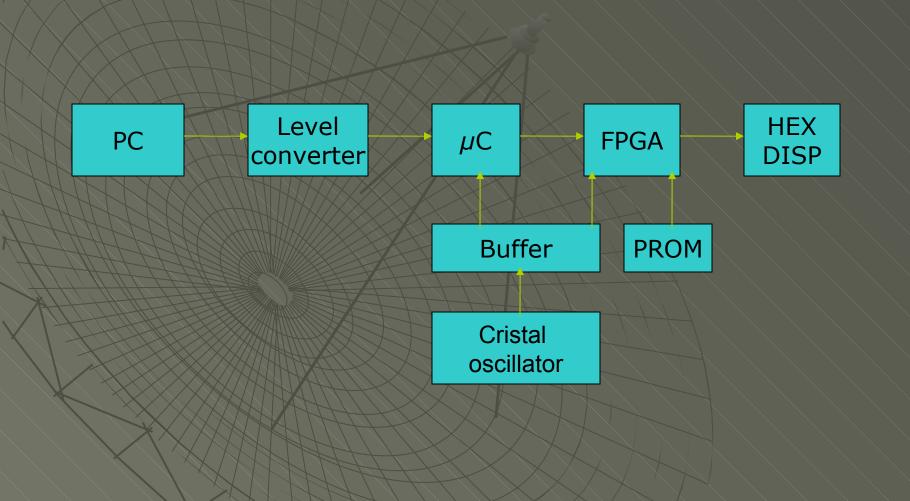
18-40GHz
CH 3
0-180°

configuration

Block Diagram



Functional Diagram



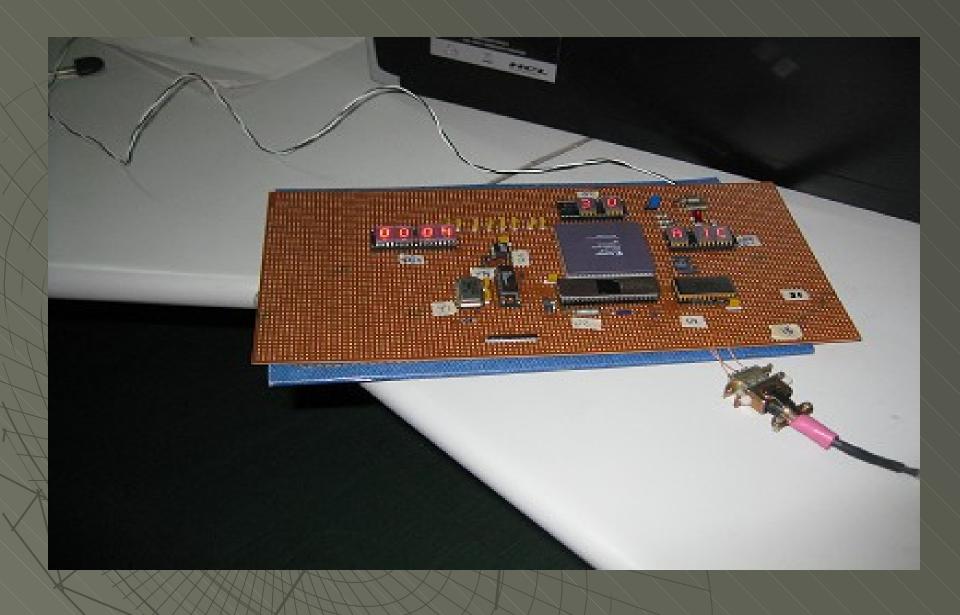


Fig.1 PCB designed TEST JIG



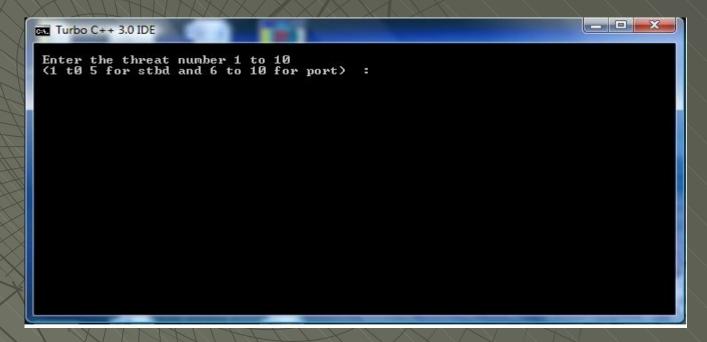
Fig.2 Experimental Test Setup

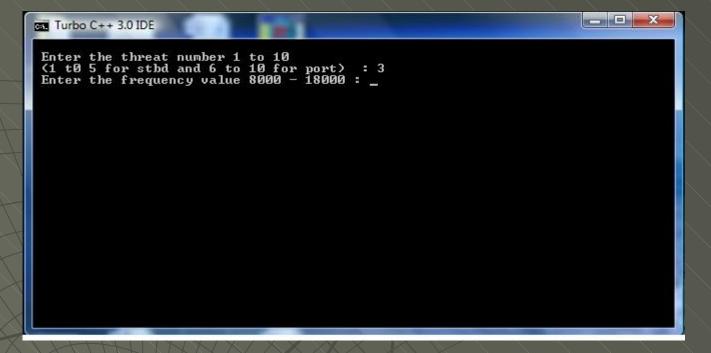
Familiarization of IC's

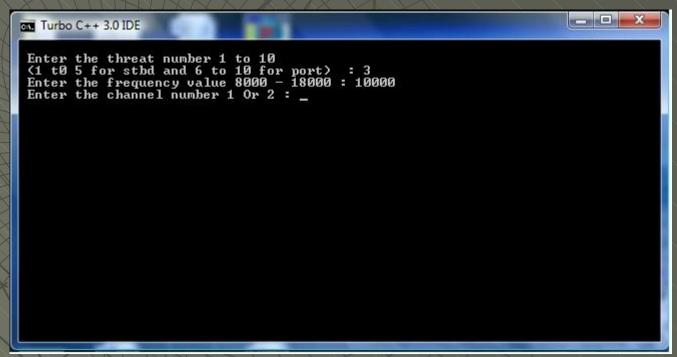
- > LEVEL CONVERTER (MAX 235)
- **► BUFFER (54F541)**
- MICROCONTROLLER (87C51)
- FPGA (XC4013E SERIES)
- PROM (AT17lv256)
- CRISTAL OSCILLATOR(10MHz)
- HEX DISPLAY (4N54)

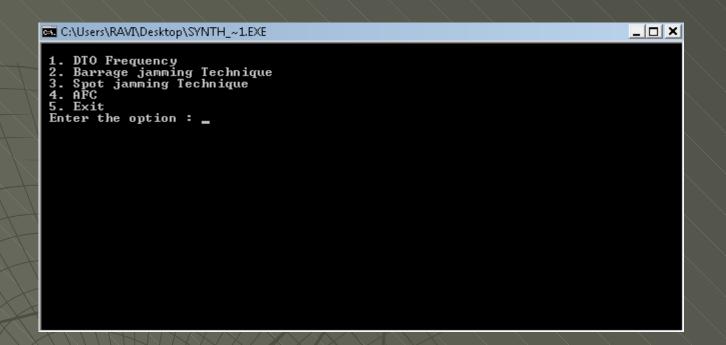
Results & Analysis:

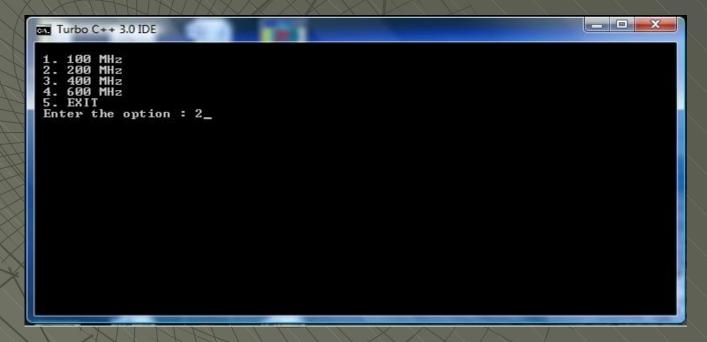
```
enter the comm port 1 or 2 :_
```











Few Examples

THREAT	FREQ	CHNL	TECH	B.W	ADDRES	DATA	CNTR
T1	8000	1	DTO		200	81AF	21
T2	9000	2	Barrage	100	204,219,207	A4F1,0004,0010	22
Т3	10000	1	Spot	20	208,21A,20B	8832,0004,0004	24
T4	11000	2	AFC		20C,215,216	AB73,0AAA,007B	68
T5	18000		DTO		210	A597	30
Т6	12000	1	DTO		A20	900F	21
T7	13000	2	Barrage	400	A24,A39,A27	B2A9,0004,0018	22
	T1 T2 T3 T4 T5 T6	T1 8000 T2 9000 T3 10000 T4 11000 T5 18000 T6 12000	T1 8000 1 T2 9000 2 T3 10000 1 T4 11000 2 T5 18000 T6 12000 1	T1 8000 1 DTO T2 9000 2 Barrage T3 10000 1 Spot T4 11000 2 AFC T5 18000 DTO T6 12000 1 DTO	T1 8000 1 DTO T2 9000 2 Barrage 100 T3 10000 1 Spot 20 T4 11000 2 AFC T5 18000 DTO T6 12000 1 DTO	T1 8000 1 DTO 200 T2 9000 2 Barrage 100 204,219,207 T3 10000 1 Spot 20 208,21A,20B T4 11000 2 AFC 20C,215,216 T5 18000 DTO 210 T6 12000 1 DTO A20	T1 8000 1 DTO 200 81AF T2 9000 2 Barrage 100 204,219,207 A4F1,0004,0010 T3 10000 1 Spot 20 208,21A,20B 8832,0004,0004 T4 11000 2 AFC 20C,215,216 AB73,0AAA,007B T5 18000 DTO 210 A597 T6 12000 1 DTO A20 900F

60

200

A28,A3A,A2B

A2C,A15,A16

A30,A3C,A3D

9544,0004,000C

B7DF,0492,007B

AC1A,0004,0014

24

68

30

Spot

AFC

Barrage

S

S

S

T8

Т9

T10

14000

15000

20000

1

2

Conclusion:

Input through the PC and see the output in hex display. The addresses of the inputs which are stored in the microcontroller. Thus the required bit pattern is displayed according to the user selected frequency and threat is further given to the synthetic channel for the generation of the selected jamming technique.

Future Scope:

- ➤ Indian Defence systems 40GHz
- ► U.S. Defence systems 60GHz
- Allocation of higher bandwidth for the synthetic channel.
- No need to change Hardware,
- Just modify the program only i.e. Software.

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