



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

Comm. No. **c 0386641** BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No. **21107000184**

Name of the College: **X7-SKSRITS, MOINABAD**

Name **BODAPOTHULA KISHORE KUMAR**

Month & Year of Final Exam: **June, 2014**

Hall Ticket No. **10X71A0410**

Year of Admission: **2010-2011**

Class Awarded: **FIRST CLASS**

S.No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	19	35	54	4	2	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	21	42	63	4
3	IT WORKSHOP & ENGINEERING WORKSHOP	20	50	70	4	4	MATHEMATICS - I	19	34	53	6
5	MATHEMATICAL METHODS	23	52	75	6	6	ENGINEERING PHYSICS	22	47	69	4
7	ENGINEERING CHEMISTRY	22	38	60	4	8	COMPUTER PROGRAMMING & DATA STRUCTURES	23	42	65	6
9	ENGINEERING DRAWING	21	32	53	4	10	COMPUTER PROGRAMMING LAB	20	47	67	4
11	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	42	64	4						
I SEMESTER						II YEAR					
1	MATHEMATICS-III	23	26	49	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	23	53	76	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	16	26	42*	3	2	ELECTRONIC CIRCUIT ANALYSIS	18	40	58	4
3	ENVIRONMENTAL STUDIES	20	30	50	3	3	PULSE & DIGITAL CIRCUITS	21	29	50	4
4	ELECTRIC CIRCUITS	23	32	55	4	4	SWITCHING THEORY & LOGIC DESIGN	23	47	70	4
5	ELECTRONIC DEVICES & CIRCUITS	23	28	51	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	18	44	62	4
6	SIGNALS & SYSTEMS	20	36	56	4	6	ELECTRICAL ENGINEERING LAB	20	42	62	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	22	46	68	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	24	44	68	2
8	BASIC SIMULATION LAB	23	46	69	2	8	PULSE & DIGITAL CIRCUITS LAB	18	49	67	2
I SEMESTER						III YEAR					
1	CONTROL SYSTEMS	22	28	50	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	20	27	47	4
2	COMPUTER ORGANIZATION	24	34	58	4	2	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	18	47	65	2
3	ANTENNAS & WAVE PROPAGATION	23	32	55	3	3	OPERATING SYSTEMS	22	30	52	4
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	19	26	45	4	4	DIGITAL COMMUNICATIONS	19	43	62	3
5	ANALOG COMMUNICATIONS	25	26	51	3	5	MICROPROCESSORS & MICROCONTROLLERS	23	44	67	4
6	IC APPLICATIONS	20	33	53	3	6	DIGITAL SIGNAL PROCESSING	22	26	48	4
7	ANALOG COMMUNICATIONS (LAB)	15	48	63	2	7	MICROPROCESSORS & MICROCONTROLLERS (LAB)	23	49	72	2
8	IC APPLICATIONS (LAB)	19	36	55	2	8	DIGITAL SIGNAL PROCESSING (LAB)	23	49	72	2
I SEMESTER						IV YEAR					
1	MANAGEMENT SCIENCE	24	72	96	3	1	DIGITAL DESIGN THROUGH VERILOG HDL	22	39	61	3
2	E-CAD AND VLSI LAB	22	48	70	2	2	INDUSTRY ORIENTED MINI PROJECT	-	48	48	2
3	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	23	48	71	2	3	SEMINAR	49	-	49	2
4	VLSI DESIGN	24	42	66	4	4	MAJOR PROJECT	48	144	192	10
5	MICROWAVE ENGINEERING	23	35	58	3	5	COMPREHENSIVE VIVA	-	95	95	2
6	COMPUTER NETWORKS	25	17	42*	0	6	SATELLITE COMMUNICATIONS	21	41	62	3
7	TELECOMMUNICATION SWITCHING SYSTEMS	24	39	63	3	7	RADAR SYSTEMS	24	39	63	3
8	OPTICAL COMMUNICATIONS	24	39	63	4						

(* Project Internal= 50, External=150)

Number of Credits registered for: 200 Aggregate Marks Secured for best: 193

Aggregate Marks Secured: 3576 OUT OF 5200 (68.77%)

Date of Issue: **September 14, 2014**
(see overleaf for Rules concerned to award of class)



(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS