

Surya S Dwivedi

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EDUCATION

UT AUSTIN

MS IN COMPUTER SCIENCE
AUG 2019 - FEB 2021 (EXPECTED)
GPA: 4.0/4.0

IIT KHARAGPUR

B.TECH IN COMPUTER SCIENCE
MAY 2019 | KHARAGPUR, INDIA
GPA: 9.09/10

COURSEWORK

GRADUATE

Reinforcement Learning
Numerical Optimization
Deep Learning Seminar
Programming Languages
Data Mining
Unconventional Computation

UNDERGRADUATE

Algorithms
Data Structures
Discrete Structures
Operating System
Distributed Systems
Database Management

SKILLS

LANGUAGES

PROFICIENT

C • C++ • Python

FAMILIAR

Java • MySQL • Clojure • Lisp

LIBRARIES

Pytorch • Tensorflow • Numpy
• OpenCV

FRAMEWORKS

Android • Django • Git

ACTIVITIES

- Member of National Service Scheme(NSS), IIT Kharagpur
- Software Team Member in Swarm Robotics, IIT Kharagpur
- Keyboardist and Badminton player for Azad hall

EXPERIENCE

TELESEAT INC. | SUMMER INTERNSHIP

MAY 2020 - PRESENT | SAN FRANCISCO, CA

- Founding member of Teleseat.com - a platform for hosting virtual events and conferences
- Implemented the complete video-communication backend from the ground up using WebRTC protocol
- Implemented a server side plugin for computer vision related tasks like virtual background, lighting adjustments etc.
- Technologies and Frameworks: WebRTC, OpenCV, GStreamer

GOOGLE SUMMER OF CODE 2019 | STUDENT DEVELOPER

MAY 2019 - SEP 2019 | ORG: CERN, GENEVA

- Developed LSTM and GRU layer functionalities in TMVA, a C++ library used at CERN for particle physics data analysis
- Tested the layers on a real particle physics classification task and achieved 8% higher accuracy than previously implemented RNN Layer

ADOBE RESEARCH | RESEARCH INTERN

MAY 2018 - JULY 2018 | BENGALURU, INDIA

- Proposed a novel method for extracting template out of banner images and inserting user content into it, optimizing the resulting layout
- Generated banners received an average user score of 3.7/5 compared to 4.0/5 for banners made by designers
- Work published at IUI 2019 [Link]. Filed a patent with USPTO

SELECTED PROJECTS

SEARCHING EFFICIENT NEURAL ARCHITECTURES USING RL

SEP 2019 - DEC 2019 | RL COURSE PROJECT - UT AUSTIN

- Implemented a policy gradient method for searching space and latency efficient neural architectures in pytorch
- Used an RNN as the policy network

DISTRIBUTED LOG DATABASE

JAN 2019 - MAY 2019 | DISTRIBUTED SYSTEMS COURSE PROJECT - IIT KGP

- Implemented a distributed system for storing and interacting with multiple logs in python using RPyC library
- Supported read, write and trim operations

HONORS AND AWARDS

- Secured 44th rank in ACM ICPC India Regionals 2017 at Chennai
- Awarded Gorelal Syngal Memorial Scholarship at IIT Kharagpur
- Secured 219th rank in IIT-JEE 2015 among 1.3 million candidates
- Secured 5th rank in KVPY 2014, an all India research fellowship exam
- Selected for Indian National Mathematics Olympiad 2013 and 2014