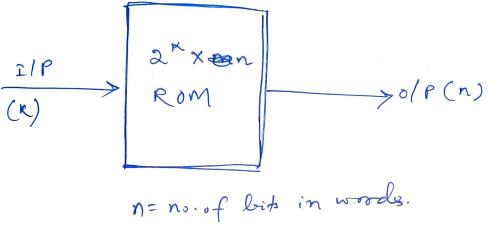
ROM:

(Read Only Memory). (fixed AND, fixed OR)

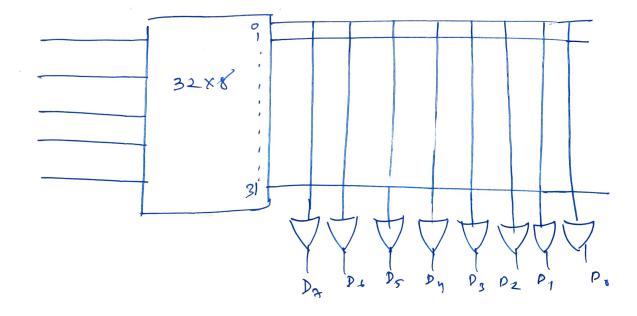
it is a memory device where fixed binary
information is stored.

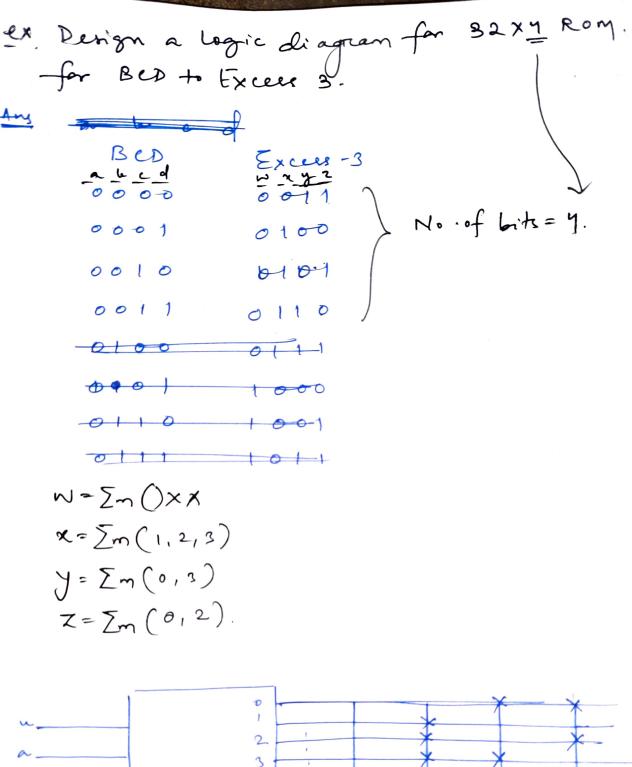
- It includes decoder and OR gate.

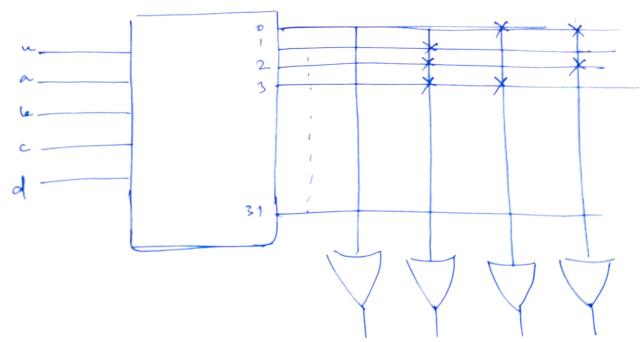
[Sp-2701]



ex Design 32×8 ROM.







ex Implement the following touth table vering Rom.

At Ao Fi F2

0 0 0 1

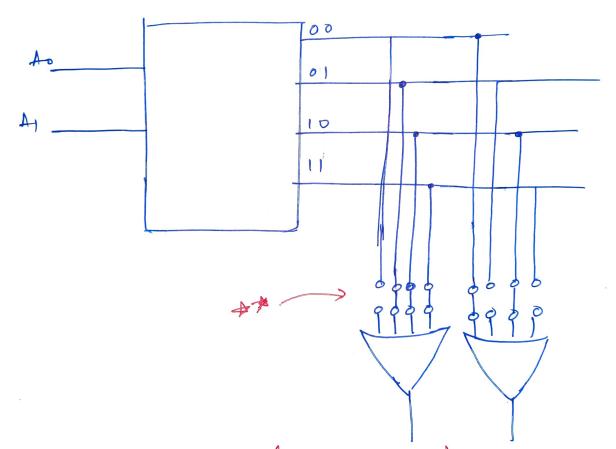
0 1 0

1 0 1

0

Ans. No. of boil words = 4.

$$f_1 = \sum_{m} (0, 1, 2, 3)$$
  
 $f_2 = \sum_{m} (0, 2)$ 



Note: 00 ) join (1 in touth table)
00 ) Not join (0 in touth table)

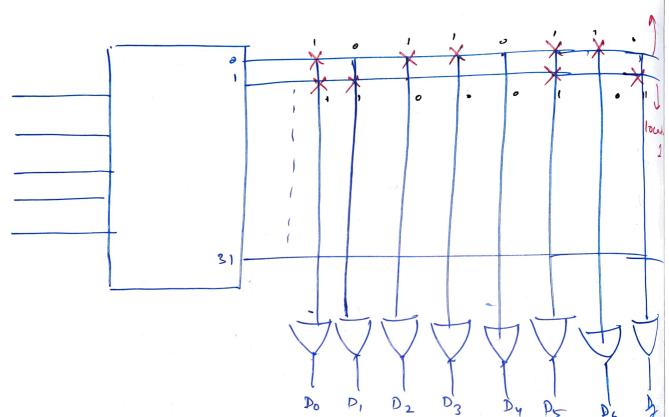
Q. Drow the diagram and find the content at location D and location 1. in 25 ×8 ROM.

location 0: 10110110

beatin 1:11000101

Ang

locatino



Full Adder vering ROM:  $\frac{1}{2} \frac{1}{2} \frac{1$ 

