

PLA:

(Programmable Logic Array)

- Programmable AND, followed by programmable OR.

* Design $F_1 = A\bar{B} + AC + \bar{A}BC$
 $F_2 = (AC + BC)'$ using PLA.

Ans.

Product term	Inputs			Outputs	
	A	B	C	F_1	F_2
$A\bar{B}$	1	0	—	1	—
AC	1	—	1	1	1
$\bar{A}BC$	0	1	1	1	—
BC	—	1	1	—	1

(P.T.O.)

Logic diagram:

