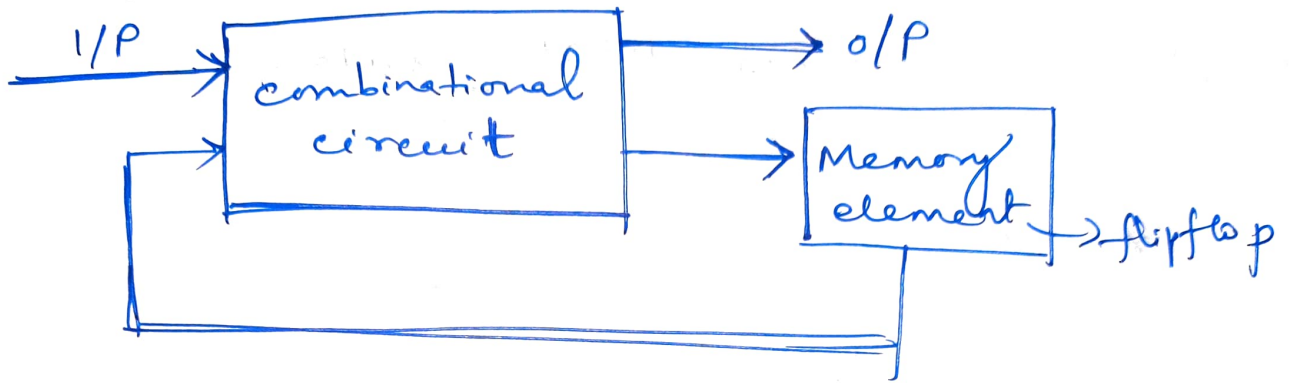


SEQUENTIAL circuit:

- present output depends on present input as well as previous output.



types of sequential circuit:

I) Asynchronous : o/p changes at any instance of time.

There is no clock.

- used in LATCHES.

II) Synchronous : o/p changes only on clock pulse.

- used in Flip flop.

LATCHES:

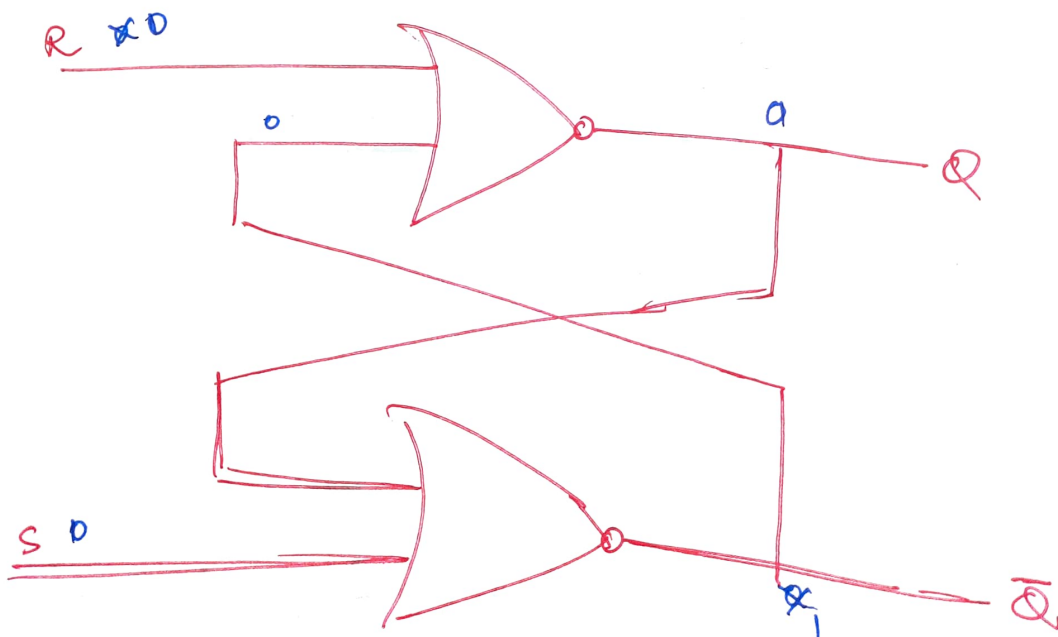
S - set \checkmark ($Q=1$)
R - reset \checkmark ($Q=0$)

* SR latch using NOR gate:

<u>x</u>	<u>y</u>	<u>F</u>
0	0	1
0	1	0
1	0	0
1	1	0

if either $1/P=1$ then
 $0/P=0$

else
 $0/P=1$



<u>S</u>	<u>R</u>	<u>Q</u>	<u>\bar{Q}</u>
1	0	1	0
0	0	1	0
0	1	0	1
0	0	0	1
1	1	X	

↓ No change || $Q = 1$ (prev. state)
 ↓ Reset
 ↓ No change
 Invalid / Forbidden.

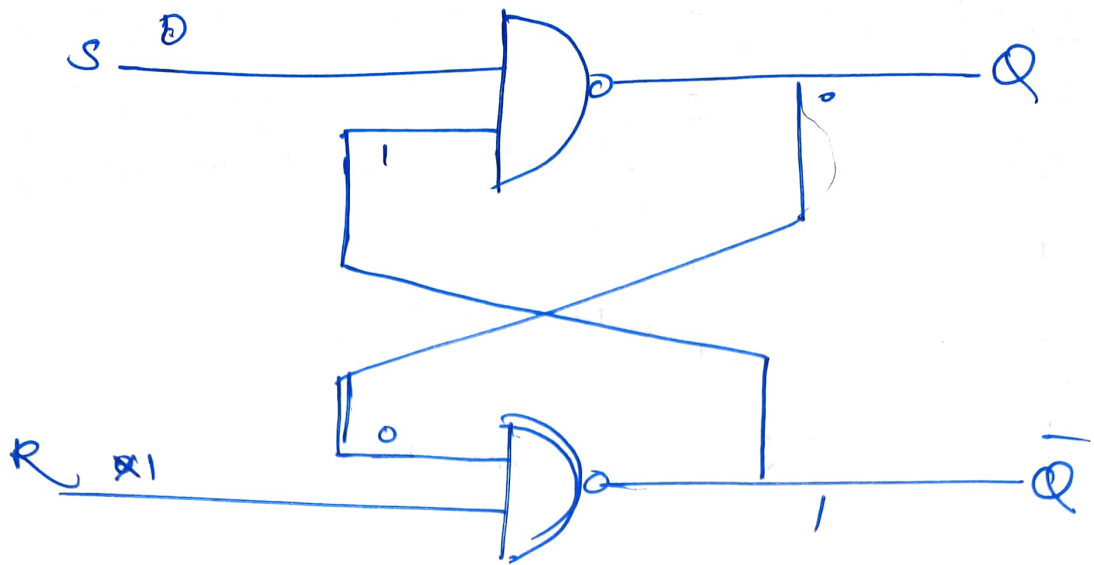
↓

<u>S</u>	<u>R</u>	<u>Q</u>	<u>\bar{Q}</u>
0	0	No change	
0	1	0	1
1	0	1	0
1	1	Invalid	

Reset ($Q = 0$)
 Set ($Q = 1$)

* SR - Latch using NAND gate:

<u>x</u>	<u>y</u>	<u>F</u>
0	0	1
0	1	1
1	0	1
1	1	0



S	R	Q	Q̄
1	0	0	1
0	0	0	1
0	1	1	0
0	0	1	0

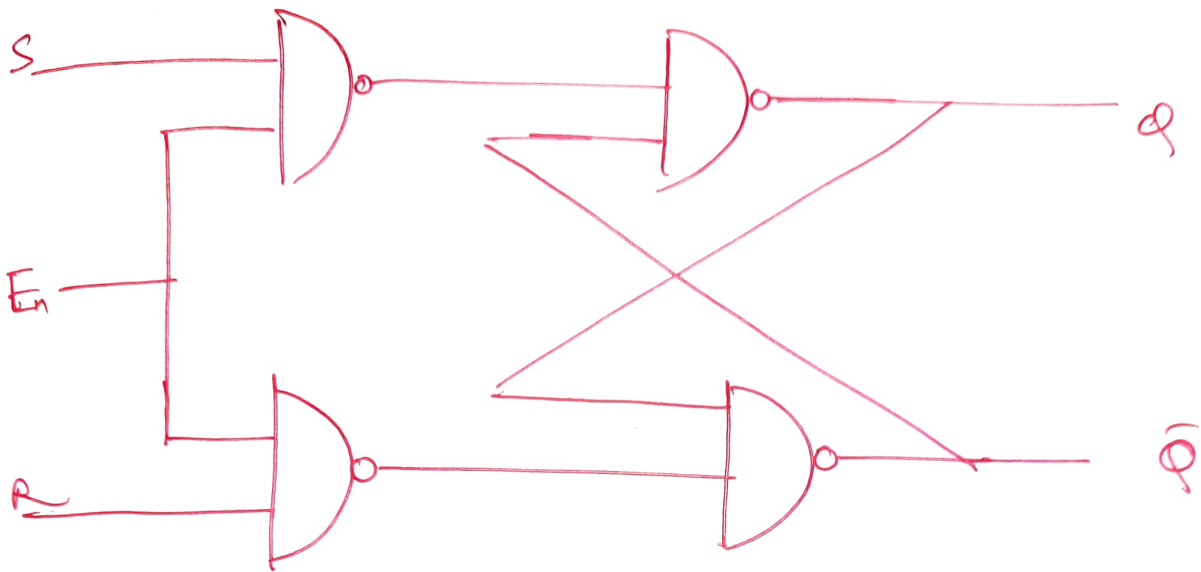
↘ No change
 || Q = prev. state = 0

S	R	Q	Q̄
0	0	Invalid	
0	1	set	
1	0	reset	
1	1	No change (Qs)	

} app. of NOR

~~Flip-Flop~~

SR-Latch with controlled inputs:



<u>E_n</u>	<u>S</u>	<u>R</u>	<u>Q</u>	<u>Q̄</u>
0	x	x	No change	
1	0	0	No change	
1	0	1	0	1
1	1	0	1	0
1	1	1	Invalid	