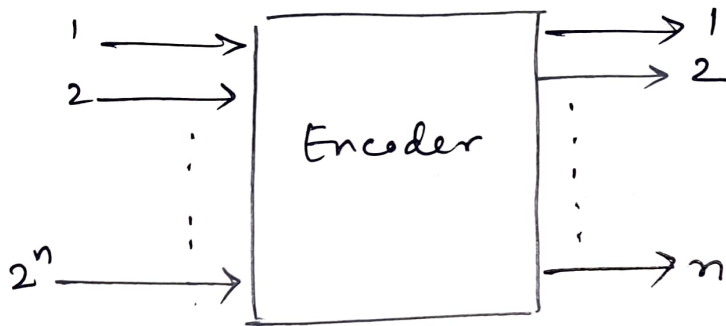


# ENCODER

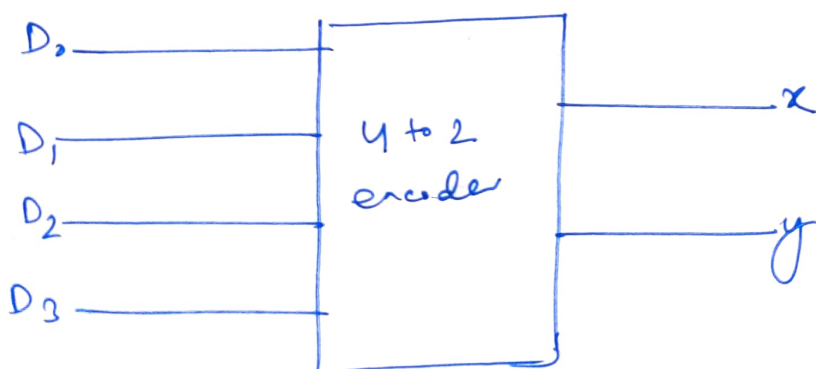
- It is a combinational circuit that has  $2^n$  no. of inputs and  $n$  no. of outputs.



- The O/P line generates the binary codes corresponds to each input value.

\* 4 to 2 line encoder:

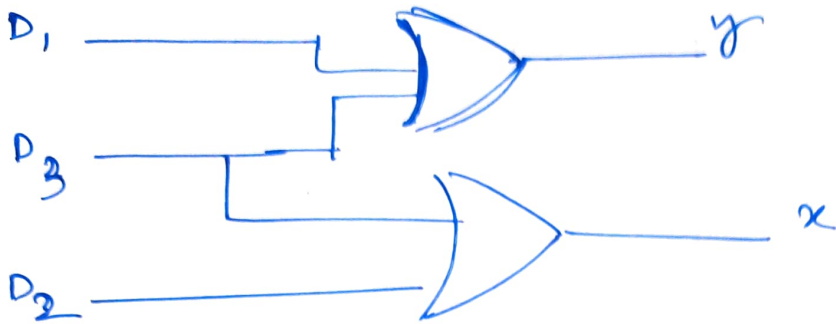
<u><math>D_0</math></u>	<u><math>D_1</math></u>	<u><math>D_2</math></u>	<u><math>D_3</math></u>		<u><math>x</math></u>	<u><math>y</math></u>
0	0	0	0		0	0
0	1	0	0		0	1
0	0	1	0		1	0
0	0	0	1		1	1



~~2~~

$$x = D_2 + D_3$$

$$y = D_1 + D_3$$



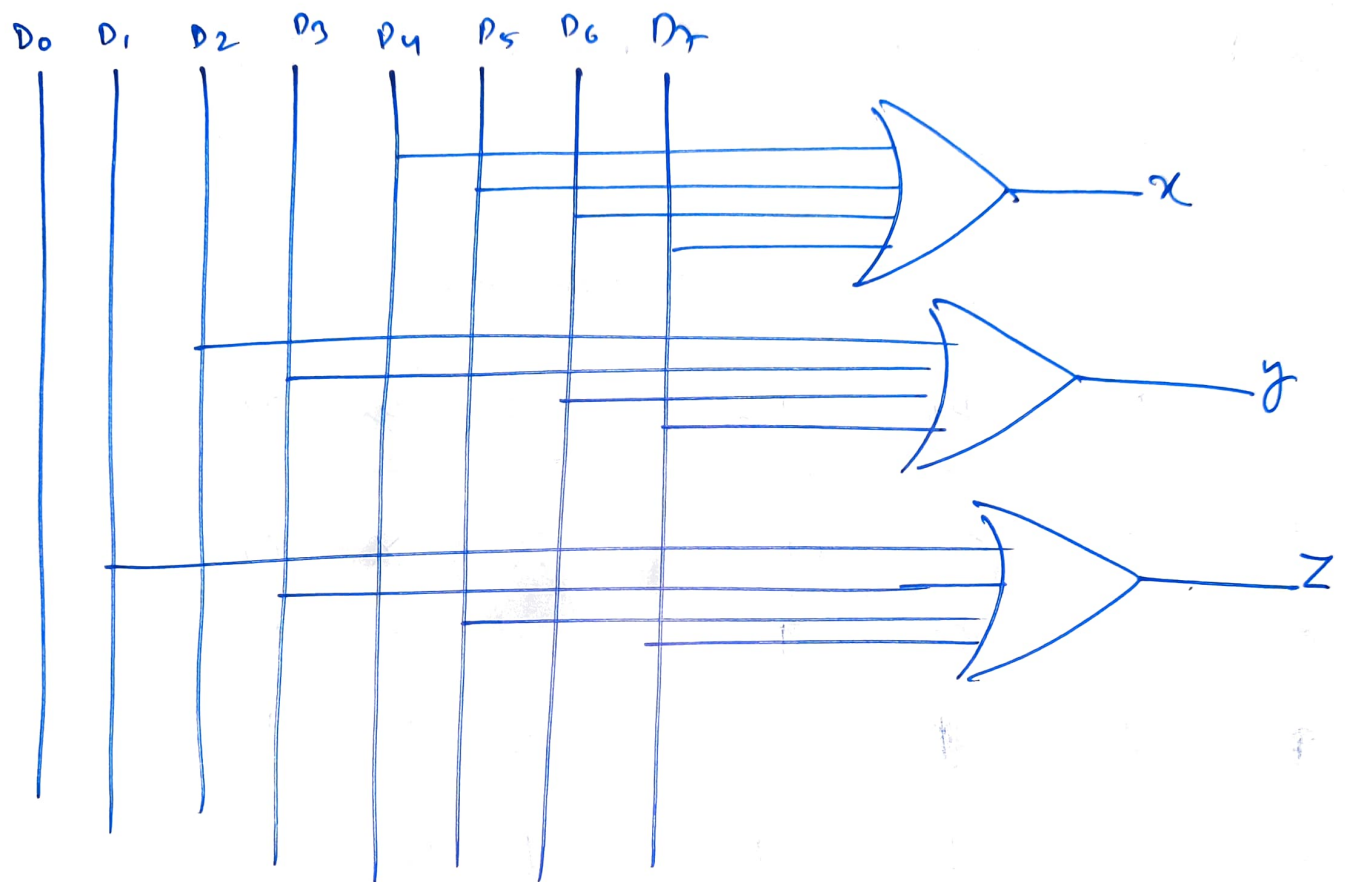
8\*3 Encoder :

$D_0$	$D_1$	$D_2$	$D_3$	$D_4$	$D_5$	$D_6$	$D_7$	$x$	$y$	$z$
1	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	1
0	0	1	0	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	0	1	1
0	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	1	0	0	1	0	1
0	0	0	0	0	0	1	0	1	1	0
0	0	0	0	0	0	0	1	1	1	1

$$x = D_4 + D_5 + D_6 + D_7$$

$$y = D_2 + D_3 + D_6 + D_7$$

$$z = D_1 + D_3 + D_5 + D_7$$



Disadvantages:

- i) Only one input can be active at a time.
- ii) If more than more inputs got active, we will get undefined combination.
- iii) To resolve this, we will use priority to ensure only one input is encoded.

# PRIORITY ENCODER:

It is a combinational that includes priority function, if two or more inputs are equal to 1 at the same time, then the input having highest priority will be considered.

ex. 4 to 2 priority encoder.

<u>D<sub>3</sub></u>	<u>D<sub>2</sub></u>	<u>D<sub>1</sub></u>	<u>D<sub>0</sub></u>	<u>x</u>	<u>y</u>	<u>z</u>
0	0	0	0	X	X	0
0	0	0	1	0	0	1
0	0	1	0	0	1	1
0	0	1	1	1	0	1
0	1	0	0	1	0	1
0	1	0	1	1	0	1
0	1	1	0	1	0	1
0	1	1	1	1	0	1
1	0	0	0	1	1	1
1	0	0	1	1	1	1
1	0	1	0	1	1	1
1	0	1	1	1	1	1
1	1	0	0	1	1	1

1	1	0	1	1	1	1
1	1	1	0	1	1	1
1	1	1	1	1	1	1

↓  
Summary type.

$D_0$	$D_1$	$D_2$	$D_3$	$x$	$y$	$v$
0	0	0	0	X	X	0
1	0	0	0	0	0	1
X	1	0	0	0	1	1
X	X	1	0	1	0	1
X	X	X	1	1	1	1

$$x = \sum m(4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15)$$

$D_3 D_2 \backslash D_1 D_0$		$\bar{D}_1 \bar{D}_0$	$\bar{D}_1 D_0$	$D_1 \bar{D}_0$	$D_1 D_0$
$\bar{D}_3 \bar{D}_2$		0	1	3	2
$\bar{D}_3 D_2$	4	1	1	1	1
$D_3 \bar{D}_2$	12	1	1	1	1
$D_3 D_2$	8	1	1	1	1

$$x = D_2 + D_3$$

$$Y = \sum m(2, 3, 8, 9, 10, 11, 12, 13, 14, 15)$$

		$D_1 D_0$			
		$\bar{D}_1 \bar{D}_0$	$\bar{D}_1 D_0$	$D_1 \bar{D}_0$	$D_1 D_0$
$D_3 D_2$	$\bar{D}_3 \bar{D}_2$	0	1	3	2
	$\bar{D}_3 D_2$	4	5	7	6
$D_3 D_2$	$D_3 \bar{D}_2$	12	13	15	14
	$D_3 D_2$	8	9	11	10

$$Y = D_3 + \bar{D}_2 D_1$$

$$V = \sum m(1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15)$$

		$D_1 D_0$			
		$\bar{D}_1 \bar{D}_0$	$\bar{D}_1 D_0$	$D_1 \bar{D}_0$	$D_1 D_0$
$D_3 D_2$	$\bar{D}_3 \bar{D}_2$	0	1	3	2
	$\bar{D}_3 D_2$	4	5	7	6
$D_3 D_2$	$D_3 \bar{D}_2$	12	13	15	14
	$D_3 D_2$	8	9	11	10

$$V = D_1 + D_2 + D_3 + D_0$$

Circuit diagram:

