Test Report

Created by:achennar 2024-01-22 19:51:55

Board: 10.123.100.99

 $Report\ printed\ in\ folder:\ C:/MentorGraphics/XStep.2020.01.0.3/XStepAPI/arm\,hf/work$

 $Report \, name: C:/Mentor Graphics/XS \, tep. 2020.01.0.3/XS \, tepAP \, I/arm \, hf/work/JESD \, 204C_4 \, Lane_1250 \, Mbps_Rx-achennar-2024-01-22_time_19-51-55.html \, and. pdf$

Test Case Name:JESD204C_4Lane_1250Mbps_Rx

Test Case Description:Example Test Case: JESD204C 1250Mbps 4 Lane, Rx Only, Trigger to EMB_START

Test Case started:2024/01/22 19:48:38

Created by:achennar 2024-01-22 19:51:55

Report printed in folder: C:/MentorGraphics/XStep.2020.01.0.3/XStepAPI/armhf/work

 $Report\ name: C:/Mentor Graphics/XS tep. 2020.01.0.3/XS tepAPI/arm hf/work/JESD204C_4Lane_1250 Mbps_Rx-achennar-2024-01-22_time_19-51-55. html\ and\ .pdf$

Hit Name	Status	Description	File	M odified	Cumulative Run Time (sec)	Number
JESD204C_Test_Case_Settings	Done	0 JESD204C_Test_Case_Settings	sarokal/reference_testhits/jesd204c/jesd204c_testcase_settings.hxx	Mon Mar 16 16:53:31 2020	6.539	0
JESD204C_Program_Device	Done	1 JESD204C_Program_Device	sarokal/reference_testhits/jesd204c/jesd204c_program.hxx	Mon Mar 16 16:53:31 2020	21.58	1
COMMON_Ext_Ref_Clock_Usage	Done	2 COMMON_Ext_Ref_Clock_Usage	sarokal/reference_testhits/common/common.hxx	Mon Mar 16 16:53:31 2020	21.59	2
JESD204C_RX_Copy_Input_Files	Done	3 JESD204C_RX_Copy_Input_Files	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	27.8	3
JESD204C_RX_Initialize_and_Trigger_Definition	Done	4 JESD204C_RX_Initialize_and_Trigger_Definition	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	27.88	4
JESD204C_RX_Reserve_Capture_Segment	Done	5 JESD204C_RX_Reserve_Capture_Segment	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	27.94	5
JESD204C_RX_Check_Receiver_Sync	Done	6 JESD204C_RX_Check_Receiver_Sync	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	27.94	6
JESD204C_RX_Start	Done	7 JESD204C_RX_Start	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	30.38	7
JESD204C_RX_Wait_For_Trigger_and_Capture	Done	8 JESD204C_RX_Wait_For_Trigger_and_Capture	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	30.39	8
JESD204C_RX_Analyze_Capture	Done	9 JESD204C_RX_Analyze_Capture	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	31.19	9
JESD204C_RX_Stop	Done	10 JESD204C_RX_Stop	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	31.2	10
JESD204C_RX_Print_Results_Vector	Done	11 JESD204C_RX_Print_Results_Vector	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	31.2	11
JESD204C_RX_Copy_Output_Files	Done	12 JESD204C_RX_Copy_Output_Files	sarokal/reference_testhits/jesd204c/jesd204c_rx_hits.hxx	Mon Mar 16 16:53:31 2020	108.7	12

Log:

Compile OK

Copy Ready

Copy DONE

```
Test JESD204C_4Lane_1250Mbps_Rx started: 2024/01/22 19:48:36
cd\ / root/XStep/work; chmod\ +x\ JESD204C\_4Lane\_1250Mbps\_Rx; LD\_LIBRARY\_PATH=/root/XStep/lib\ ./ JESD204C\_4Lane\_1250Mbps\_Rx; LD\_LIBRARY\_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_ANDARY_AN
#x.info 00:00:00:027 Using XSTEP DNRT4:1512011, CPU1512026, EFU1512006, rev:.
#x.info 00:00:00:108 MAC address 70:B3:D5:FA:20:71
#log.info 00:00:00:193 Looking for xstep-runtime license..
#log.info 00:00:00:462 License xstep-runtime checked out
Reset variables in VariableForm
Test JESD204C_4Lane_1250Mbps_Rx started: 2024/01/22 19:48:38
#testcase.run 00:00:00:797 Start test case JESD204C_4Lane_1250Mbps_Rx
#testcase.info 00:00:00:797 XStepApi version: 2020.01.0.3
#testcase.info 00:00:00:797 Import XStepApi.ini settings
#testcase.info 00:00:00:798 Import JESD204C_4Lane_1250Mbps_Rx.ini settings
#testcase.info 00:00:00:798 No arguments
#testcase.run 00:00:00:799 Start test hit 0 JESD204C_Test_Case_Settings
#log.info 00:00:00:802 JESD204C selected as the working directory for this test case.
#api:x.settings.workdir JESD204C
\#client.copyC2X\ 00:00:00:00:806\ JESD204C/jesd204c\_setup\_1G25\_1lane\_ABC\_usecase\_3A\_15\_12\_23.txt\ to: JESD204C/jesd204C\_setup\_1G25\_1Jane\_ABC\_usecase\_3A\_15\_12\_23.txt\ to: JESD204C/jesd204C\_setup\_1G25\_1Jane\_ABC\_usecase\_3A\_15\_12\_23.txt\ to: JESD204C/jesd204C\_setup\_1G25\_1Jane\_ABC
#client.copy.end ready
#log.info 00:00:06:494 Copy done
#testcase.run 00:00:06:539 Done test hit 0 JESD204C_Test_Case_Settings
#testcase.run 00:00:06:539 Start test hit 1 JESD204C_Program_Device
#apix.jesd204c.program 1G25 1
#jesd204c.info 00:00:06:540 Jesd204c program using 1G25 bitrate
#jesd204c.info 00:00:06:541 Jesd204c program 1 lane(s) configured
#program.info 00:00:06:543 Unload xilinx emaclite kernel module
#console.info 00:00:06:547
#console.info 00:00:06:551
#clock.info 00:00:06:552 Starting to set frequency 156250000
#program.info 00:00:06:607 FPGA is empty
#program.info 00:00:07:944 CPU md5Hash = 87f65c6ed5b22e2631779274ca34a92b
#program.info 00:00:07:945 CPU Ver=C04F1015
#program.info 00:00:08:362 Programming ../bin/CPU.bit
#program.info 00:00:08:372 CPU Board programmed
#program.info 00:00:09:586 IFU md5Hash = 95a7e83456f6cac59fd85c8aac2e9f0e
#program.info 00:00:09:586 IFU Ver=E0030015
\#program.info~00:00:09:587~Programming~../bin/EFU\_64B66B.bit
#program.info 00:00:09:600 Programming interface card...
#program.info 00:00:21:518 Number of active links set to 1
#program.info 00:00:21:520 Number of active lanes set to 4
#program.info 00:00:21:582 Memory if - OK
#program.info 00:00:21:583 Register if - OK
#testcase.run 00:00:21:585 Done test hit 1 JESD204C Program Device
#testcase.run 00:00:21:585 Start test hit 2 COMMON Ext Ref Clock Usage
#log.info 00:00:21:586 External reference clock usage not selected (useExtRefClock=0)
#testcase.run 00:00:21:587 Done test hit 2 COMMON_Ext_Ref_Clock_Usage
\# test case.run\ 00:00:21:588\ Start\ test\ hit\ 3\ JESD204C\_RX\_Copy\_Input\_Files
\#apix.jesd204c.rx.copyInputFiles\ jesd204c\_setup\_1G25\_1\\ lane\_ABC\_usecase\_3A\_15\_12\_23.txt
#log.info 00:00:21:899 License jesd204c-actor checked out
```

#client.copy.end ready #log.info 00:00:27:755 Copy done #testcase.run 00:00:27:798 Done test hit 3 JESD204C_RX_Copy_Input_Files #testcase.run 00:00:27:799 Start test hit 4 JESD204C_RX_Initialize_and_Trigger_Definition #log.info 00:00:27:800 Initializing variables and Rx trigger #apix.jesd204c.rx.reset #api:x.jesd204c.rx.init #log.info 00:00:27:879 Forcing the capture to start instantly after Rx start #api:x.jesd204c.rx.triggerMode 0 #testcase.run 00:00:27:880 Done test hit 4 JESD204C_RX_Initialize_and_Trigger_Definition #testcase.run 00:00:27:880 Start test hit 5 JESD204C_RX_Reserve_Capture_Segment #log.info 00:00:27:881 Defining capture segment & size and frame settings for the Analyzer #rx.info 00:00:27:911 Calculated Segment Size: 0x 4e200 #apix.jesd204c.rx.calculateReceiveSegmentSize jesd204c_setup_1G25_1lane_ABC_usecase_3A_15_12_23.txt 320000 #apix.jesd204c.rx.addSegment jesd204c_setup_1G25_1lane_ABC_usecase_3A_15_12_23.txt RxSegment1 320160 1 #rx.info 00:00:27:943 Capturing 0MB, free disk space 24236MB. #testcase.run 00:00:27:943 Done test hit 5 JESD204C_RX_Reserve_Capture_Segment #testcase.run 00:00:27:944 Start test hit 6 JESD204C_RX_Check_Receiver_Sync #testcase.run 00:00:27:945 Done test hit 6 JESD204C_RX_Check_Receiver_Sync #testcase.run 00:00:27:945 Start test hit 7 JESD204C_RX_Start #apix.jesd204c.rx.start #testcase.run 00:00:30:384 Done test hit 7 JESD204C RX Start #testcase.run 00:00:30:384 Start test hit 8 JESD204C_RX_Wait_For_Trigger_and_Capture #apix.jesd204c.rx.getState DONE #log.info 00:00:30:385 Rx FSM state = DONE in lane 0 #testcase.run 00:00:30:386 Done test hit 8 JESD204C RX Wait For Trigger and Capture #testcase.run 00:00:30:386 Start test hit 9 JESD204C_RX_Analyze_Capture #apix.jesd204c.rx.analyze jesd204c_setup_1G25_1lane_ABC_usecase_3A_15_12_23.txt RxSegment1 #rx.info 00:00:30:430 Running Analyzer #rx.info 00:00:30:432 Analyze segname: RxSegment1 link0 address 0x100000000 #protocol.info 00:00:30:432 JESD204C Analyzer started #protocol.info 00:00:30:455 Loading JESD204C setup #log.info 00:00:30:456 Load settings Jesd204cRxTriggerPulse.SavePayload= #log.info 00:00:30:457 Load settings Jesd204cRxTriggerPulse.SavePayloadFileFormat=HEX #log.info 00:00:30:458 Load settings Jesd204cRxTriggerPulse.FileHeader= #log.info 00:00:30:459 Load settings Jesd204cRxTriggerPulse.SavePayloadBytesInLine=8 #log.info 00:00:30:460 Load settings Jesd204cRxTriggerPulse.LsbFirst=0 #log.info 00:00:30:461 Load settings Jesd204cRxTriggerPulse.DeltaCalc= #log.info 00:00:30:462 Load settings Jesd204cRxTriggerPulse.DeltaCalcEdge= $\#log.info\ 00:\!00:\!30:\!462\ Load\ settings\ Jesd204cRxTriggerPulse.Report=0$ #log.info 00:00:30:465 Load settings Converter0.SavePayloadFileName=savepayload0.txt #log.info 00:00:30:466 Load settings Converter0.FileHeader= #log.info 00:00:30:467 Load settings Converter0.SavePayloadFileFormat=HEX 16 #log.info 00:00:30:468 Load settings Converter0.SavePayloadBytesInLine=8 #log.info 00:00:30:468 Load settings Converter0.AddIQFrontBits=0 $\#log.info\ 00:00:30:469\ Load\ settings\ Converter 1. Save Payload File Name = save payload 1.txt$ #log.info 00:00:30:470 Load settings Converter1.FileHeader= $\#log.info\ 00:00:30:471\ Load\ settings\ Converter1. Save Payload File Format = HEX_16$

#log.info 00:00:30:472 Load settings Converter1.SavePayloadBytesInLine=8

#log.info 00:00:30:473 Load settings Converter1.AddIQFrontBits=0	
#log.info 00:00:30:473 Load settings Converter2.SavePayloadFileName=savepayload2.txt	
#log.info 00:00:30:474 Load settings Converter2.FileHeader=	
#log.info 00:00:30:475 Load settings Converter2.SavePayloadFileFormat=HEX_16	
#log.info 00:00:30:476 Load settings Converter2.SavePayloadBytesInLine=8	
#log.info 00:00:30:477 Load settings Converter2.AddIQFrontBits=0	
#log.info 00:00:30:478 Load settings Converter3.SavePayloadFileName=savepayload3.txt	
#log.info 00:00:30:479 Load settings Converter3.FileHeader=	
#log.info 00:00:30:480 Load settings Converter3.SavePayloadFileFormat=HEX_16	
#log.info 00:00:30:480 Load settings Converter3.SavePayloadBytesInLine=8	
#log.info 00:00:30:481 Load settings Converter3.AddIQFrontBits=0	
#log.info 00:00:30:482 Load settings Converter4.SavePayloadFileName=savepayload4.txt	
#log.info 00:00:30:483 Load settings Converter4.FileHeader=	
#log.info 00:00:30:484 Load settings Converter4.SavePayloadFileFormat=HEX_16	
#log.info 00:00:30:485 Load settings Converter4.SavePayloadBytesInLine=8	
#log.info 00:00:30:486 Load settings Converter4.AddIQFrontBits=0	
#log.info 00:00:30:486 Load settings Converter5.SavePayloadFileName=savepayload5.txt	
#log.info 00:00:30:487 Load settings Converter5.FileHeader=	
#log.info 00:00:30:488 Load settings Converter5.SavePayloadFileFormat=HEX_16	
#log.info 00:00:30:489 Load settings Converter5.SavePayloadBytesInLine=8	
#log.info 00:00:30:490 Load settings Converter5.AddIQFrontBits=0	
#log.info 00:00:30:491 Load settings Converter6.SavePayloadFileName=savepayload6.txt	
#log.info 00:00:30:492 Load settings Converter6.FileHeader=	
#log.info 00:00:30:493 Load settings Converter6.SavePayloadFileFormat=HEX_16	
#log.info 00:00:30:494 Load settings Converter6.SavePayloadBytesInLine=8	
#log.info 00:00:30:495 Load settings Converter6.AddIQFrontBits=0	
#log.info 00:00:30:395 Load settings Converter7.SavePayloadFileName=savepayload7.txt	
#log.info 00:00:30:496 Load settings Converter7.FileHeader=	
#log.info 00:00:30:497 Load settings Converter7.SavePayloadFileFormat=HEX_16	
#log.info 00:00:30:498 Load settings Converter7.SavePayloadBytesInLine=8	
#log.info 00:00:30:499 Load settings Converter7.AddIQFrontBits=0	
#log.info 00:00:30:500 Load settings L = 1	
#log.info 00:00:30:501 Load settings F = 16	
#log.info 00:00:30:502 Load settings M = 8	
#log.info 00:00:30:503 Load settings CS = 0	
#log.info 00:00:30:503 Load settings N = 16	
#log.info 00:00:30:504 Load settings N_total = 16	
#log.info 00:00:30:504 Load settings S = 1	
#log.info 00:00:30:505 Load settings HD = 1	
#log.info 00:00:30:505 Load settings CF = 0	
#log.info 00:00:30:506 Load settings E = 1	
#log.info 00:00:30:506 Load settings NumberOfExtMultiblocks = 1000	
#log.info 00:00:30:507 Load settings CmdChannelSingleLaneMode = 1	
#log.info 00:00:30:507 Load settings ReverseReportBits = 0	
#log.info 00:00:30:508 Load settings Verbose = 0	
#log.info 00:00:30:508 Load settings OversamplePayloads = 1	
#log.info 00:00:30:509 Load settings IdleType = PayloadLoop	
#log.info 00:00:30:509 Load settings ControlChannelMode = DeviceControlChannel	
#log.info 00:00:30:510 Load settings Tailbits = high	

```
#log.info 00:00:30:510 Load settings LimitSavePayloadSize = 1
#log.info 00:00:30:511 Load settings CmdWordOutputFileName = output_command_words.txt
\#log.info\ 00:00:30:511\ Load\ settings\ RxReportFileName = RX\_frame\_report\_file.txt
#log.info 00:00:30:512 Load settings RxFrameCaptureStartTrigger = TRIGGER_IMMEDIATELY
\#log.info\ 00:00:30:512\ Load\ settings\ Lane 0. RxOutputFrameFileName = RX0\_frame\_file.txt
#log.info 00:00:30:513 Load settings BusWidthBytes = 4
#log.info 00:00:30:513 Load settings SyncWordMode = MODE_CMD_CHANNEL
#protocol.info 00:00:30:517 Multiblock synchronization found:
#protocol.info 00:00:30:518 Lane 0 offset: 80
#protocol.info 00:00:30:583 6403 bytes analyzed
#protocol.info 00:00:30:646 12806 bytes analyzed
#protocol.info 00:00:30:710 19209 bytes analyzed
#protocol.info 00:00:30:774 25612 bytes analyzed
#protocol.info 00:00:30:837 32015 bytes analyzed
#protocol.info 00:00:30:902 38418 bytes analyzed
#protocol.info 00:00:30:968 44821 bytes analyzed
#protocol.info 00:00:31:032 51224 bytes analyzed
#protocol.info 00:00:31:096 57627 bytes analyzed
#protocol.info 00:00:31:160 64030 bytes analyzed
#protocol.info 00:00:31:161 Jesd204C protocol warnings found:
#protocol.info 00:00:31:161
#protocol.info 00:00:31:162 Command channel faulty header 0
#protocol.info 00:00:31:162 Command channel faulty payload length 0
#protocol.info 00:00:31:163 Command channel reserved type received 0
#protocol.info 00:00:31:163 Sync word faulty pilot signal 0
#protocol.info 00:00:31:164 Sync word faulty EoEMB signal 0
#protocol.info 00:00:31:165 Sync word faulty parity bit 0
#protocol.info 00:00:31:165 Invalid sync header 0
#protocol.info 00:00:31:166
#protocol.info 00:00:31:171 Analysis completed successfully
#protocol.info 00:00:31:171 Savefiles generated:
#protocol.info 00:00:31:172 savepayload0.txt
#protocol.info 00:00:31:172 savepayload1.txt
#protocol.info 00:00:31:173 savepayload2.txt
#protocol.info 00:00:31:173 savepayload3.txt
#protocol.info 00:00:31:174 savepayload4.txt
#protocol.info 00:00:31:174 savepayload5.txt
#protocol.info 00:00:31:175 savepayload6.txt
#protocol.info 00:00:31:175 savepayload7.txt
#testcase.run 00:00:31:195 Done test hit 9 JESD204C RX Analyze Capture
#testcase.run 00:00:31:195 Start test hit 10 JESD204C RX Stop
#apix.jesd204c.rx.stop
#testcase.run 00:00:31:197 Done test hit 10 JESD204C_RX_Stop
#testcase.run 00:00:31:197 Start test hit 11 JESD204C RX Print Results Vector
#apix.jesd204c.rx.printResultsVector
#log.info 00:00:31:199 #jesd204c.results_vector:Command channel faulty header=0,Command channel faulty payload length=0,Command channel reserved type received=0,Sync word faulty pilot signal=0,Sync word faulty EoEMB
signal=0,Sync word faulty parity bit=0,Invalid sync header=0,
```

#testcase.run 00:00:31:200 Done test hit 11 JESD204C_RX_Print_Results_Vector #testcase.run 00:00:31:200 Start test hit 12 JESD204C_RX_Copy_Output_Files $\#apix.jesd204c.rx.copyOutputFiles\ jesd204c_setup_1G25_1lane_ABC_usecase_3A_15_12_23.txt$ $\#client.copyC2X\ 00:00:31:202\ JESD204C/jesd204c_setup_1G25_1lane_ABC_usecase_3A_15_12_23.txt\ to: JESD204C/jesd204C_setup_1G25_1Jane_ABC_usecase_3A_15_12_23.txt\ to: JESD204C/jesd204C_setup_1G25_1Jane_ABC_usecase_3A_15_12_23.txt\ to: JESD204C/jesd204C_setup_1G25_1Jane_ABC_usecase_3A_15_12_23.txt\ to: JESD204C/jesd204C_setup_1G25_1Jane_ABC_us$ #client.copy.end ready #log.info 00:00:37:094 Copy done $\#client.copyX2C\ 00:00:37:133\ JESD204C/savepayload0.txt\ to: JESD204C/savepayload0.txt$ #client.copy.end ready #log.info 00:00:43:852 Copy done #client.copyX2C 00:00:43:852 JESD204C/savepayload1.txt to: JESD204C/savepayload1.txt #client.copy.end ready #log.info 00:00:50:116 Copy done #client.copyX2C 00:00:50:117 JESD204C/savepayload2.txt to: JESD204C/savepayload2.txt #client.copy.end ready #log.info 00:00:56:364 Copy done #client.copyX2C 00:00:56:365 JESD204C/savepayload3.txt to: JESD204C/savepayload3.txt #client.copy.end ready #log.info 00:01:02:611 Copy done #client.copyX2C 00:01:02:612 JESD204C/savepayload4.txt to: JESD204C/savepayload4.txt #client.copy.end ready #log.info 00:01:08:974 Copy done #client.copyX2C 00:01:08:975 JESD204C/savepayload5.txt to: JESD204C/savepayload5.txt #client.copy.end ready #log.info 00:01:14:652 Copy done #client.copyX2C 00:01:14:653 JESD204C/savepayload6.txt to: JESD204C/savepayload6.txt #client.copv.end ready #log.info 00:01:20:459 Copy done #client.copyX2C 00:01:20:460 JESD204C/savepayload7.txt to: JESD204C/savepayload7.txt #client.copy.end ready #log.info 00:01:26:720 Copy done $\#client.copyX2C\ 00:01:26:721\ JESD204C/RX0_frame_file.txt\ to: JESD204C/RX0_frame_file.txt$ #client.copy.end ready #log.info 00:01:33:555 Copy done #client.copyX2C 00:01:33:555 JESD204C/RX_frame_report_file.txt to: JESD204C/RX_frame_report_file.txt #client.copy.end ready #log.info 00:01:42:858 Copy done #client.copyX2C 00:01:42:859 JESD204C/output_command_words.txt to: JESD204C/output_command_words.txt #client.copy.end ready #log.info 00:01:48:666 Copy done #testcase.run 00:01:48:667 Done test hit 12 JESD204C_RX_Copy_Output_Files #testcase.run 00:01:48:667 All hits run #testcase.run 00:01:48:668 Done test case JESD204C_4Lane_1250Mbps_Rx #log.info 00:01:48:668 Releasing license jesd204c-actor #log.info 00:01:48:713 License released #log.info 00:01:48:714 Releasing license xstep-runtime #log.info 00:01:48:758 License released

#log.info 00:01:48:759 Exit from XStep pid : 1418