

Let 2 numbers be X & Y .

Both 3-bit length \rightarrow $X = x_2 x_1 x_0$ + LSB
 $Y = y_2 y_1 y_0$ + LSB

Therefore, multiplication would look something like.

$$X * Y = \begin{matrix} & & y_2 & y_1 & y_0 \end{matrix}$$

$$\begin{array}{r} \text{Partial Products} \rightarrow \begin{matrix} & & x_2 & x_1 & x_0 \\ & & a_2 & a_1 & a_0 \\ & b_2 & b_1 & b_0 \end{matrix} \end{array}$$

$$\begin{array}{r} \begin{matrix} C_2 & C_1 & C_0 \end{matrix} \\ \hline R_5 & R_4 & R_3 & R_2 & R_1 & R_0 \end{array}$$

where

$$\begin{array}{lll} a_0 = x_0 y_0 & b_0 = x_1 y_0 & C_0 = x_2 y_0 \\ a_1 = x_0 y_1 & b_1 = x_1 y_1 & C_1 = x_2 y_1 \\ a_2 = x_0 y_2 & b_2 = x_1 y_2 & C_2 = x_2 y_2 \end{array}$$

Some nomenclature \rightarrow

* The final 6-bit result is $R_5 R_4 R_3 R_2 R_1 R_0$ + LSB

* There is no carry generated for R_0 and R_1 computation.

* The carry generated 'at R_1 ' (ie, for R_2)

~~has been named as~~ has not been named as it is $(a_1 \& b_0)$ only.

* the carry generated at R_2 (ie, for R_3) is represented by P_1 & P_2 .

$$\Rightarrow \left[\begin{array}{cc} \{P_2\} & \{a, \{b, c\}\} \\ \{P_1\} & a_2 \\ b_2 & b_1 \\ c_1 & C_0 \end{array} \right]$$

$$\begin{array}{cc} R_3 & R_2 \end{array}$$

As there are total 4-bits in R_2 position if all are 1, there would be two carry bits carried over to R_3 .

that's why P_1 & P_2

* Similarly at R_3 there are 4-bits

hence two carries can be generated for R_4 , \therefore they are represented by Z_1 & Z_2

$$\left[\begin{array}{cc} \{Z_2\} & \{P_1\} \{P_2\} \\ \{Z_1\} & b_2 \\ C_2 & C_1 \end{array} \right]$$

$$\begin{array}{cc} R_4 & R_3 \end{array}$$

* As there are only 3-bits at R_4 , only 1 carry bit can be generated ~~at~~ for R_5

\therefore it is represented by R_5 itself and computation for R_5 would be done likewise because there are no partial product bits for R_5 anyways.

$$(b_0, \text{if } a_1) \text{ if } a_2, \text{ if } b_1, \text{ if } c_0$$

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$$\rightarrow \text{if } a_1 \text{ then } (a_1 b_0) a_2 + a_1 b_0 b_1 + a_1 b_0 c_0 + a_2 b_1 + a_2 c_0 + b_1 c_0$$

$$\Rightarrow (a_1 b_0)(a_2 + b_1 + c_0) + a_1 b_0 c_0 + a_2 b_1 + a_2 c_0 + b_1 c_0$$

$$\Rightarrow a_1 b_0 (a_2 + b_1) + c_0 (a_2 + b_1) + a_1 b_0 c_0$$

$$\Rightarrow (a_2 + b_1)(a_1 b_0 + c_0) + a_1 b_0 c_0 \rightarrow$$

$$(a_1 b_0)(a_2 + b_1 + c_0)$$

$$x_0 y_1 x_1 y_0 x_2 y_2 + x_0 y_1 x_1 y_0 x_2 y_1 + x_0 y_1 x_1 y_0 x_2 y_0 + x_0 y_2 x_1 y_1 + x_0 y_2 x_2 y_0 + x_1 y_1 x_2 y_0$$

$$\Rightarrow x_0 x_1 y_0 y_1 y_2 + x_0 x_1 y_0 y_1 + x_0 x_1 x_2 y_0 y_1 y_2 + x_0 x_1 y_1 y_2 + x_0 x_2 y_0 y_2 + x_1 x_2 y_0 y_1$$

$$\Rightarrow x_0 x_1 y_1 y_2 + x_0 x_2 y_0 y_2 + x_1 x_2 y_0 y_1 + x_0 x_1 y_0 y_1 (y_2 + 1 + x_2 y_2)$$

$$\Rightarrow x_0 x_1 y_1 y_2 + x_0 x_2 y_0 y_2 + x_1 x_2 y_0 y_1 + x_0 x_1 y_0 y_1$$

$$\Rightarrow x_0 x_1 (y_1 y_2 + y_0 y_2) + y_0 y_1 (x_1 x_2 + x_0 x_1)$$

$$\Rightarrow x_0 y_1 (x_1 y_2 + x_1 y_0) + x_2 y_0 (x_0 y_2 + x_1 y_1)$$

$$P_2 \Rightarrow a_1 (b_2 + b_0) + c_0 (a_2 + b_1) \rightarrow \text{3tg}$$

$$P_1 = a_1 a_2 b_0 b_1 c_0$$

$$P_2 = a_1 (b_2 + b_0) + c_0 (a_2 + b_1)$$

~~$$Z_1 = a_1 a_2 b_0 b_1 c_0 b_2 c_1$$~~

$$Z_1 = P_1 P_2 b_2 c_1$$

$$= a_1 a_2 b_0 b_1 c_0 b_2 c_1 (a_1 b_2 + a_1 b_0 + c_0 a_2 + c_0 b_1)$$

$$= (x_0 y_1 x_1 y_2 + x_0 y_1 x_1 y_0 + x_2 y_0 x_0 y_2 + x_2 y_0 x_1 y_1) \text{ AND } (x_0 y_1 x_0 y_2 x_1 y_0 x_1 y_1 x_2 y_0 x_1 y_2 x_2 y_1)$$

$$= (\sim) \text{ AND } (x_0 x_1 x_2 y_0 y_1 y_2)$$

$$Z_1 \Rightarrow x_0 x_1 x_2 y_0 y_1 y_2 = x_0 y_0 x_1 y_1 x_2 y_2 = \underline{a_0 b_1 c_2}$$

$$Z_2 \Rightarrow P_1 P_2 + P_1 b_2 + P_1 c_1 + P_2 b_2 + P_2 c_1 + b_2 c_1$$

$$[x_0 y_1 x_0 y_2 x_1 y_0 x_1 y_1 x_2 y_0] \Rightarrow P_1 \Rightarrow P_1 = x_0 x_1 x_2 y_0 y_1 y_2$$

~~P₁~~ As P₁ has all 6 basic bits, ANDing P₁ with anything must only yield

$$Z_2 = x_0 x_1 x_2 y_0 y_1 y_2 + (x_0 x_1 y_0 y_1 + x_0 x_1 y_1 y_2 + x_2 y_0 x_0 y_2 + x_2 y_0 x_1 y_1) (x_1 y_2 + x_2 y_1 + x_2 y_0)$$

$$\Rightarrow x_0 x_1 x_2 y_0 y_1 y_2 + (x_0 x_1 y_0 y_1 y_2 + x_0 x_1 y_0 y_2 + x_0 x_1 x_2 y_0 y_2 + x_1 x_2 y_0 y_1 y_2 + x_0 x_1 x_2 y_0 y_1 + x_0 x_1 x_2 y_1 y_2 + x_0 x_2 y_0 y_1 y_2 + x_2 y_0 x_1 y_1)$$

$$\Rightarrow x_0 x_1 x_2 y_0 y_1 y_2 + x_1 y_2 + x_2 y_1 + x_0 x_1 x_2 y_0 (y_1 + y_2) + x_1 y_0 y_1 y_2 (x_2 + x_0) + x_0 x_2 y_1 y_2 (y_0 + x_1) + x_1 y_1 (x_2 y_0 + x_0 y_0)$$

$$Z_2 =$$

$$R_5 = c_2 \oplus z_1 \oplus z_2 = z_1(x_0x_1x_2y_0y_1y_2) = a_0b_1c_2$$

$$x_0x_1x_2y_0y_1y_2 + \left(x_0x_2 \left[x_1y_0(y_1+y_2) + y_1y_2(y_0+x_1) \right] + x_1y_1 \left[y_0y_2(x_2+x_0) + (x_2y_0+x_0y_2) \right] \right)$$

$$\Rightarrow 6tg$$

$$R_5 = \underbrace{z_1z_2 + z_1c_2 + z_2c_2}_{z_1} \text{ (as } z_1 \text{ has all 6 basic bits } x_0x_1x_2y_0y_1y_2)$$

$$e. \text{ for } z_2c_2 \quad (c_2 = x_1y_2)$$

$$x_1y_2 \left[x_0x_1x_2y_0y_1y_2 + \left(x_0x_1y_0y_1y_2 + x_0x_1y_1y_2 + x_0x_1x_2y_0y_2 + x_1x_2y_0y_1y_2 + x_0x_1x_2y_0y_1 + x_0x_1x_2y_1y_2 + x_0x_2y_0y_1y_2 + x_2y_0x_1y_1y_2 \right) \right]$$

$$\Rightarrow x_0x_1x_2y_0y_1y_2 + \left(x_0x_1x_2y_0y_1y_2 + x_0x_1x_2y_0y_1y_2 + x_0x_1x_2y_0y_1 + x_1x_2y_0y_1y_2 + x_0x_1x_2y_0y_2 + x_0x_1x_2y_1y_2 + x_0x_2y_0y_1y_2 + x_2y_0x_1y_1y_2 \right)$$

$$\Rightarrow x_0x_1x_2y_0y_1y_2 + \left(x_0x_1x_2y_0y_1y_2 + x_1x_2y_0y_1y_2 + x_0x_1x_2y_0y_2 + x_0x_2y_0y_1y_2 \right)$$

$$\Rightarrow x_0x_1x_2y_0y_1y_2 + \left(x_2y_0y_1 (x_0x_1y_1 + x_1y_1 + x_0x_1 + x_0y_1) \right)$$