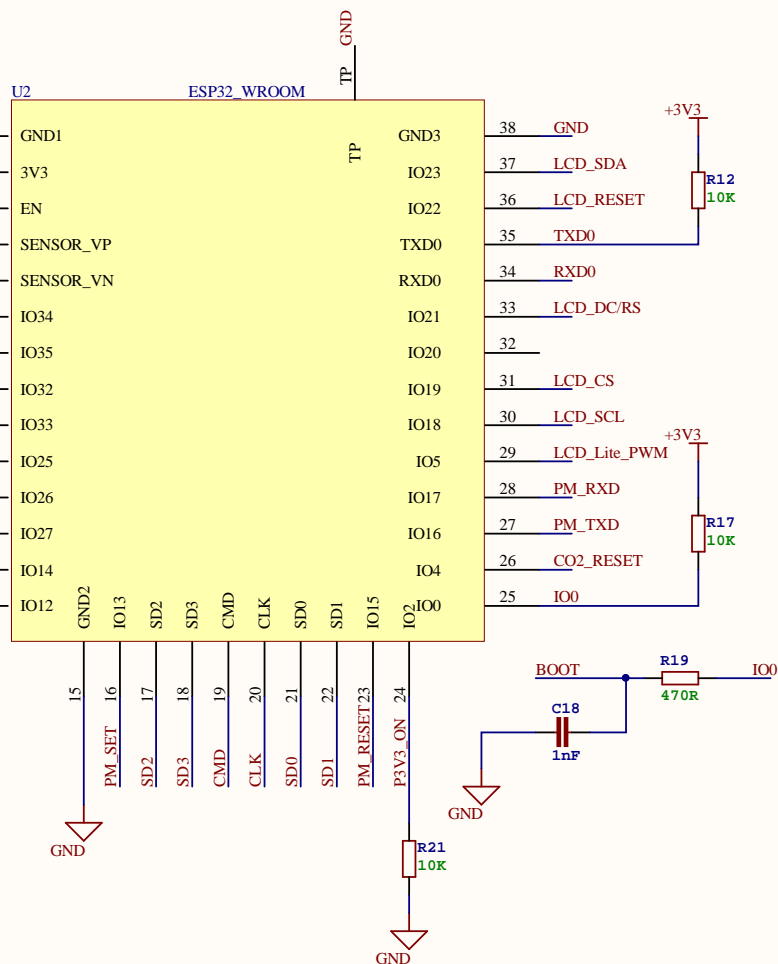
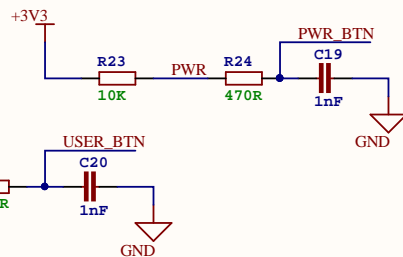
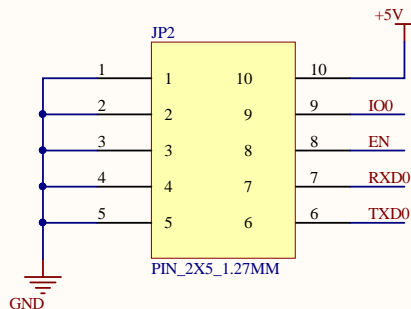
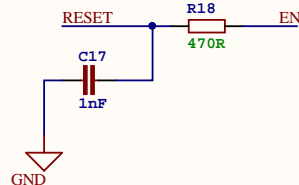
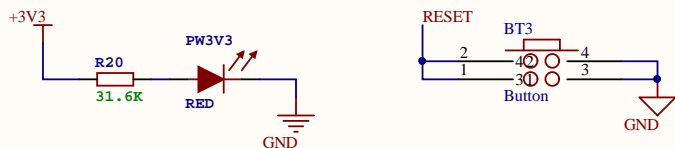
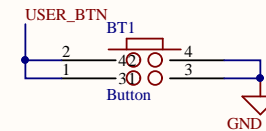
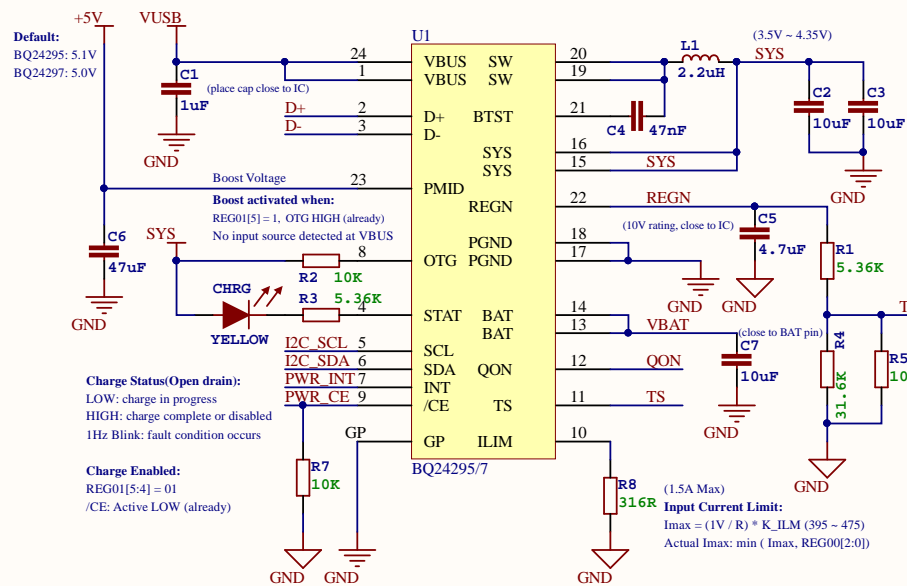


$V_{out} = (R_{fb1} / R_{fb2} + 1) * 0.8 \quad (R < 400K)$   
 270K, 86.6K  $\Rightarrow$  3.294V  
 301K, 95.3K  $\Rightarrow$  3.326V  
 357K, 113K  $\Rightarrow$  3.327V  
 390K, 124K  $\Rightarrow$  3.316V



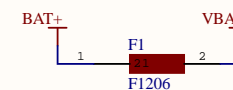
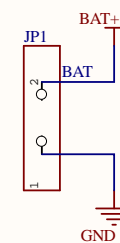
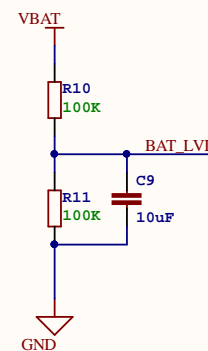
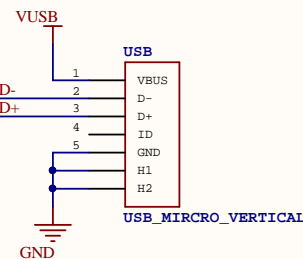
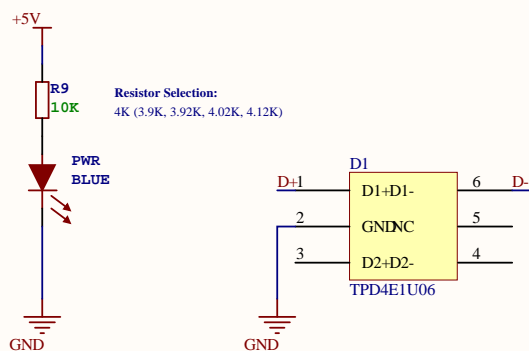
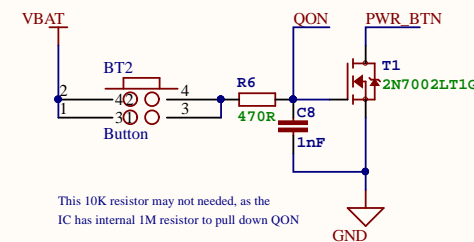
Title		
Size	Number	Revision
A4		
Date:	11/25/2017	Sheet of
File:	C:\Users\...\main.SchDoc	Drawn By:



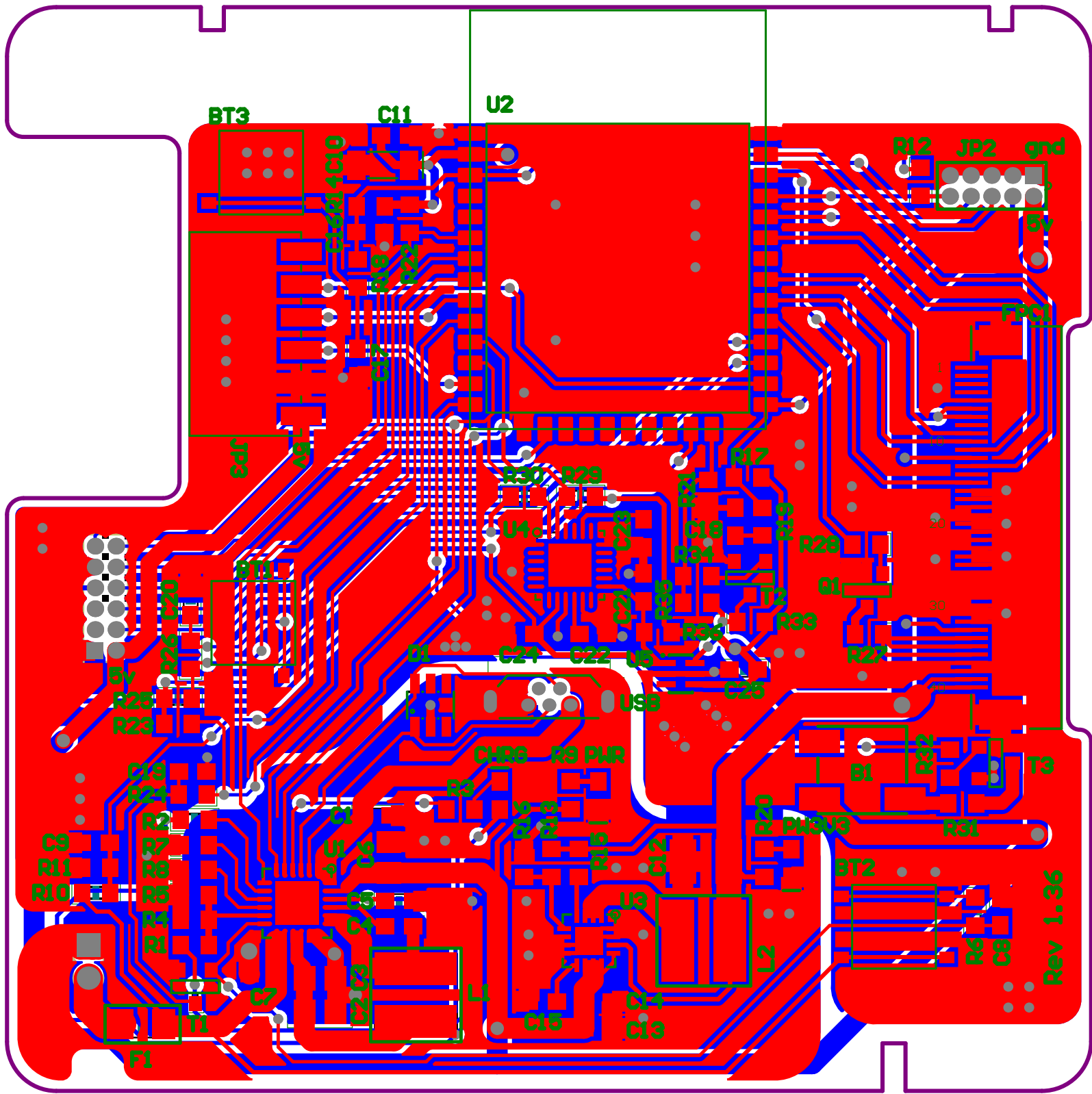


**Turn on BATFET: set REG07[5](BATFET\_DISABLE) bit**  
Events can Turn on BATFET and clear REG07[5](BATFET\_DISABLE) bit:

1. Plug in adapter
2. Write REG07[5] = 0
3. Watchdog timer expiration
4. Register reset (REG01[7] = 1)
5. A logic LOW to HIGH transition on QON pin



Title		
Size	Number	Revision
A4		
Date:	11/25/2017	Sheet of
File:	C:\Users\...power.SchDoc	Drawn By:



Rev 1.36