

Praveen Gore

Principal Consultant

Summary

11+ Experience in the industry, looking forward to hit the right balance between the execution, leading and managing the team. Experienced in Physical Design, Timing Analysis and Physical Verification activities.

Experience

Principal Consultant at Infosys

October 2015 - Present (1 year 6 months)

Staff Engineer at Qualcomm

September 2014 - September 2015 (1 year 1 month)

Working in Physical Design Team, Develop and deliver chips for latest technologies of multiple foundries.

Lead/Manage a team of engineers in PD. Understanding the concept of new technology nodes, stabilizing the flows, Methods and Tools. Experienced in Physical Design, Timing Analysis. Executing the Chip flow.

Lead Engineer, Senior at Qualcomm

June 2012 - September 2014 (2 years 4 months)

Working in Physical Design Team, Develop and deliver chips for latest technologies of multiple foundries.

Lead a team of engineers in PD. Understanding the concept of new technology nodes, stabilizing the flows, Methods and Tools. Experienced in Physical Design, Timing Analysis. Executing the Chip flow.

Technical Lead at Wipro Technologies

November 2010 - June 2012 (1 year 8 months)

I am working as a PD Engineer, working in full chip projects of multi million gate counts. My responsibilities involve

- * Leading the team
- * Communication with the customer
- * Scheduling the project
- * Digital Backend Flow Execution
- * Mentoring the juniors

Member of Technical Staff at Karnataka Microelectronic Design Centre

June 2004 - November 2010 (6 years 6 months)

Worked on Digital Backend flow of many modules with complete ownership from Netlist to GDSII, STA

Was also involved in training and mentoring of resources.

Cell library development, The job included the layout/characterization and CML generation of many standard cells/ IO cells, designed specifically for the customer Team.

Languages

Kannada

English

Hindi

Marathi

Tulu

Skills & Expertise

Automation

Shell Scripting

Perl

Unix Shell Scripting

TCL

Physical Design

Timing Closure

ASIC

Static Timing Analysis

Debugging

VLSI

Integrated Circuit Design

Verilog

Testing

Microprocessors

EDA

SoC

Logic Synthesis

SystemVerilog

DRC

Functional Verification

ARM

Semiconductors

RTL design

Cadence Virtuoso

Mixed Signal

LVS

RTL coding

IC

ModelSim

VHDL
NCSim
CMOS
Analog
Very-Large-Scale Integration (VLSI)
Application-Specific Integrated Circuits (ASIC)
System on a Chip (SoC)

Education

Visvesvaraya Technological University

BE, Electrical and Electronics, 2000 - 2004

SDM College, Ujire

PUC, Science, 1998 - 2000

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3 people have recommended Praveen

"Praveen is technically strong and very efficient in Physical Design flow. Under his able guidance our team has successfully implemented a full chip design. He is a motivation for entire team and a true team leader."

— **Rajdeep Bhuyar**, reported to Praveen at Wipro Technologies

"Praveen is very good in all the tasks of Physical Design, be it IO planning or layout or STA. Specially when it comes to timing closure, he is just great. Scripting is his USP. He is one of them from whom I learnt scripting. He is a unique Team Lead and team member. He doesn't want his teammates to just perform the assigned tasks but first he helps them with fundamentals and proper approach so that everyone have a clear picture of the task/issue. It is nice working with you Praveen. Wish you a very successful career. All the Best."

— **Manish Jain**, worked indirectly for Praveen at Wipro Technologies

"Being Digital Design Section Manager I did not get to hire Praveen myself, but I benefited from Praveen's expertise as part of our organization resources. We put his skills to the test multiple times with designs that pushed a few boundaries, and with very complicated clocking structures. Thanks to his skills, and his ability to adapt to late changes and re-spins, we were able to execute quite a few projects, sometimes concurrently. I can definitely recommend Praveen for the handling of Backend tasks when it comes to digital designs."

— **Juan Arango**, was Praveen's client

[Contact Praveen on LinkedIn](#)