

Registers

A	78
BC	00 06
DE	00 78
HL	00 00
PSW	00 00
PC	42 1B
SP	FF FF

Int-Reg 00

Flag

S 0
Z 1
AC 0
P 1
C 0

Decimal - Hex Conversion

Decimal

Hex

0

0

→ To Hex

← To Dec

I/O Ports

0

- +

00

Load me at

```

1  LDA 2001
2  MOV b,a
3  MVI c,#01
4  MVI e,#01
5  loop: MOV d,c
6  MVI a,00h
7  lp: ADD e
8  DCR d
9  JNZ lp
10 MOV e,a
11 INR c
12 DCR b
13 JNZ loop
14 MOV a,e
15 STA 2010
16 HLT

```

Data Stack Keypad Memory I/O Ports

Start 2001

OK

Address (Hex)	Address	Data
07D1	2001	5
07D2	2002	0
07D3	2003	0
07D4	2004	0
07D5	2005	0
07D6	2006	0
07D7	2007	0
07D8	2008	0
07D9	2009	0
07DA	2010	120

Line No Assembler Message

0 Program assembled successfully