

# /Projeto/TaxCalculation Backend

## PSP3 Project Plan Summary

### Project Settings

Project Owner

Susumu Asaga

Start Date

Nov 13, 2018 08:24:08 AM

Completion Date

Completed: ☐

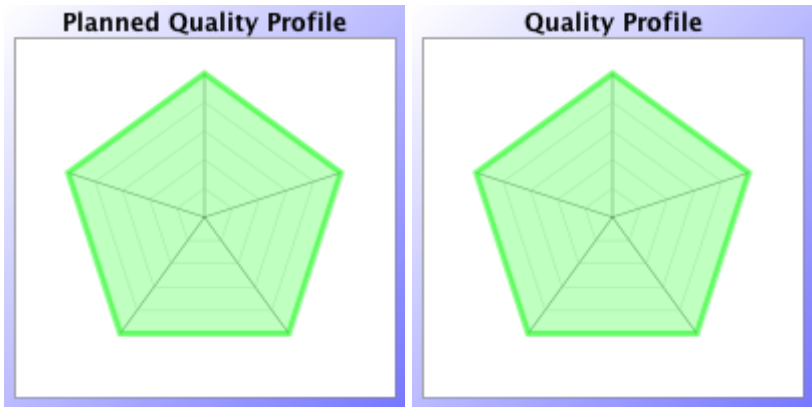
Keywords

Language

TypeScript

### Overall Metrics

Summary	Plan	Actual	To Date
Size/Hour	16.1	#VALUE!	8.7
Planned Time	18:40		10:00
Actual Time		2:53	44:21
CPI (Cost-Performance Index)			0.23
% Reuse	0%	#VALUE!	0%
% New Reusable	0%	#VALUE!	0%
Test Defects/KLOC or equivalent	5	#VALUE!	31.1
Total Defects/KLOC or equivalent	40	#VALUE!	31.1
Yield %	87.5%	#DIV/0!	0%
% Appraisal COQ	25%	0%	0.9%
% Failure COQ	15%	0%	44.9%
COQ A/F Ratio	1.67	#DIV/0!	0.02
PQI	1	1	0



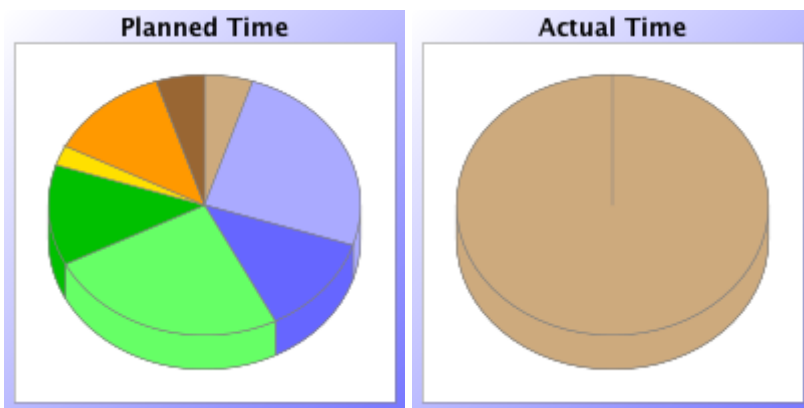
### Program Size

	Plan	Actual	To Date
Base (B)	0	0	14
Deleted (D)	0	0	0
Modified (M)	0	0	0

Added (A)	300	#VALUE!	386
Reused (R)	0	0	0
Added and Modified (A+M)	300	#VALUE!	386
Total Size (T)	300	?????	400
Total New Reusable	0	0	0
Estimated Proxy Size (E)	162	0	5
Upper Prediction Interval (70%)	N/A		
Lower Prediction Interval (70%)	N/A		

## Time In Phase

	Plan	Actual	To Date	To Date %
Planning	0:56	2:53	0:44	1.65%
HLD	0:00	0:00	0:00	0%
HLD Review	0:00	0:00	0:00	0%
Design	4:40	0:00	5:39	12.7%
Design Review	2:20	0:00	0:24	0.9%
Code	4:40	0:00	16:50	38.0%
Code Review	2:20	0:00	0:00	0%
Compile	0:28	0:00	1:06	2.48%
Test	2:20	0:00	18:48	42.4%
Reassessment	0:00	0:00	0:00	0%
Postmortem	0:56	0:00	0:50	1.88%
Total	18:40	2:53	44:21	
Total Time UPI (70%)	N/A			
Total Time LPI (70%)	N/A			



## Defects Injected

	Plan	Actual	To Date	To Date %
Before Development	0	0	0	0%
Planning	0	0	0	0%
HLD	0	0	0	0%
HLD Review	0	0	0	0%
Design	6	0	0	0%

Design Review	0	0	0	0%
Code	6	0	10	83.3%
Code Review	0	0	0	0%
Compile	0	0	0	0%
Test	0	0	2	16.7%
Total	12	0	12	



## Defects Removed

	Plan	Actual	To Date	To Date %
Planning	0	0	0	0%
HLD	0	0	0	0%
HLD Review	0	0	0	0%
Design	0	0	0	0%
Design Review	4.5	0	0	0%
Code	1.5	0	0	0%
Code Review	4.5	0	0	0%
Compile	0	0	0	0%
Test	1.5	0	12	100%
Total	12	0	12	
After Development	0	0	0	

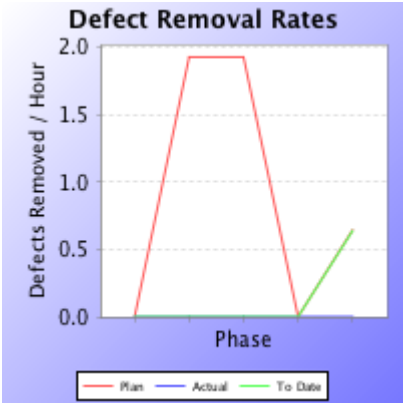


## Defect Removal Efficiency

	Plan	Actual	To Date
Defects/Hour - High-level Design Review	#DIV/0!	#DIV/0!	#DIV/0!

Defects/Hour - Design Review  
Defects/Hour - Code Review  
Defects/Hour - Compile  
Defects/Hour - Test  
DRL (HLDR/UT)  
DRL (DLDR/UT)  
DRL (Code Review/UT)  
DRL (Compile/UT)

1.93	#DIV/0!	0
1.93	#DIV/0!	#DIV/0!
0	#DIV/0!	0
0.64	#DIV/0!	0.64
#DIV/0!	#DIV/0!	#DIV/0!
3	#DIV/0!	0
3	#DIV/0!	#DIV/0!
0	#DIV/0!	0



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