

# /Projeto/TaxCalculation Backend

## PSP3 Project Plan Summary

### Project Settings

Project Owner

Susumu Asaga

Start Date

Nov 13, 2018 08:24:08 AM

Completion Date

Nov 24, 2018 01:09:20 PM

Completed: ☒

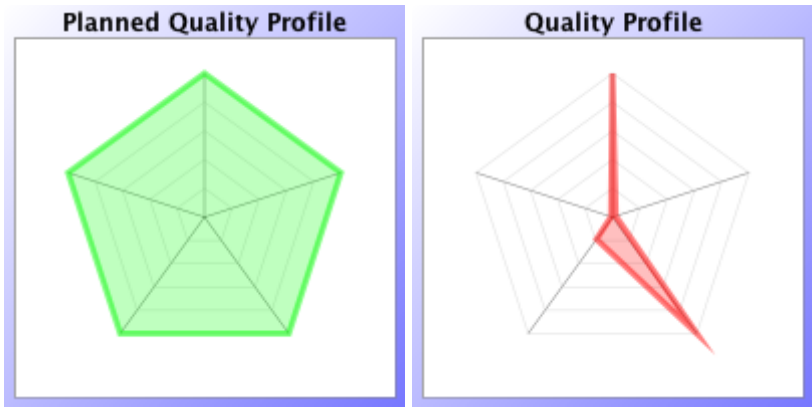
Keywords

Language

TypeScript

### Overall Metrics

Summary	Plan	Actual	To Date
Size/Hour	16.1	11.6	10.4
Planned Time	18:40		10:00
Actual Time		63:01	107:22
CPI (Cost-Performance Index)			0.27
% Reuse	0%	0%	0%
% New Reusable	0%	0%	0%
Test Defects/KLOC or equivalent	5	47.7	42.0
Total Defects/KLOC or equivalent	40	47.7	42.0
Yield %	87.5%	0%	0%
% Appraisal COQ	25%	0.58%	0.71%
% Failure COQ	15%	18.5%	29.4%
COQ A/F Ratio	1.67	0.03	0.02
PQI	1	0.0	0.0



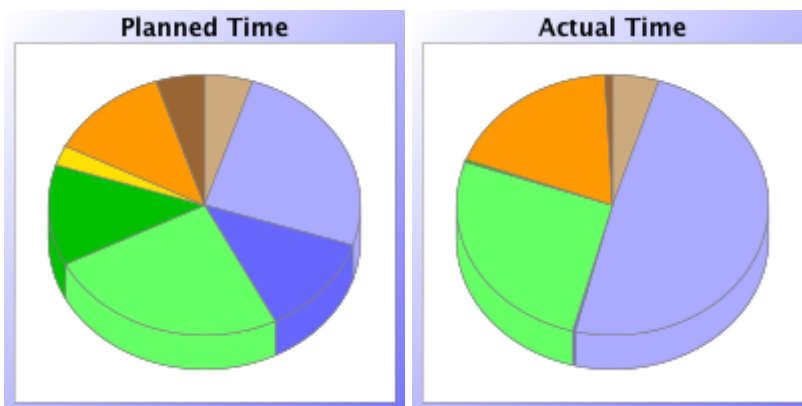
### Program Size

	Plan	Actual	To Date
Base (B)	0	0	14
Deleted (D)	0	0	0
Modified (M)	0	0	0

Added (A)	300	733	1119
Reused (R)	0	0	0
Added and Modified (A+M)	300	733	1119
Total Size (T)	300	733	1133
Total New Reusable	0	0	0
Estimated Proxy Size (E)	162	0	5
Upper Prediction Interval (70%)	N/A		
Lower Prediction Interval (70%)	N/A		

## Time In Phase

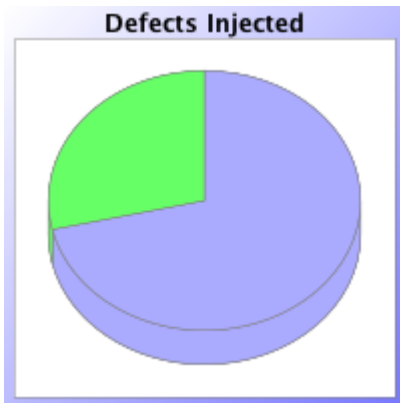
	Plan	Actual	To Date	To Date %
Planning	0:56	3:02	3:46	3.51%
HLD	0:00	0:00	0:00	0%
HLD Review	0:00	0:00	0:00	0%
Design	4:40	30:54	36:33	34.0%
Design Review	2:20	0:10	0:34	0.53%
Code	4:40	16:33	33:23	31.1%
Code Review	2:20	0:12	0:12	0.19%
Compile	0:28	0:00	1:06	1.02%
Test	2:20	11:41	30:29	28.4%
Reassessment	0:00	0:00	0:00	0%
Postmortem	0:56	0:29	1:19	1.23%
Total	18:40	63:01	107:22	
Total Time UPI (70%)	N/A			
Total Time LPI (70%)	N/A			



## Defects Injected

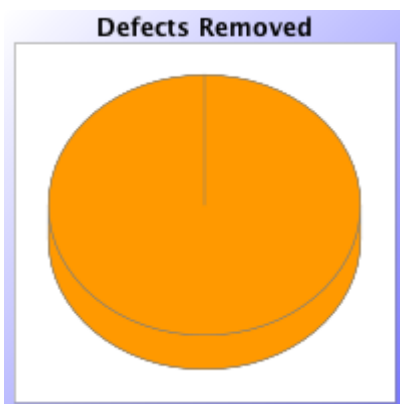
	Plan	Actual	To Date	To Date %
Before Development	0	0	0	0%
Planning	0	0	0	0%
HLD	0	0	0	0%
HLD Review	0	0	0	0%
Design	6	25	25	53.2%

Design Review	0	0	0	0%
Code	6	10	20	42.6%
Code Review	0	0	0	0%
Compile	0	0	0	0%
Test	0	0	2	4.26%
Total	12	35	47	



## Defects Removed

	Plan	Actual	To Date	To Date %
Planning	0	0	0	0%
HLD	0	0	0	0%
HLD Review	0	0	0	0%
Design	0	0	0	0%
Design Review	4.5	0	0	0%
Code	1.5	0	0	0%
Code Review	4.5	0	0	0%
Compile	0	0	0	0%
Test	1.5	35	47	100%
Total	12	35	47	
After Development	0	0	0	



## Defect Removal Efficiency

	Plan	Actual	To Date
Defects/Hour - High-level Design Review	#DIV/0!	#DIV/0!	#DIV/0!

Defects/Hour - Design Review  
Defects/Hour - Code Review  
Defects/Hour - Compile  
Defects/Hour - Test  
DRL (HLDR/UT)  
DRL (DLDR/UT)  
DRL (Code Review/UT)  
DRL (Compile/UT)

1.93	0	0
1.93	0	0
0	#DIV/0!	0
0.64	3.0	1.54
#DIV/0!	#DIV/0!	#DIV/0!
3	0	0
3	0	0
0	#DIV/0!	0



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