SUWEI YANG (Software/Firmware engineer)

Personal website Mobile: +1-385-490-4107

SKILLS

- **Programming languages**: C/C++(3-4 years), Python(1-2 years)
- Tools: GDB, UART, Logic analyzer, ICE, JTAG, Pytorch, Numpy, VSCode, Git, Script, QEMU, Ubuntu
- Knowledge: Operating system, Real-time operating system, Memory architecture, Object-oriented programming, Scheduling algorithms, Signal processing algorithms, Debugging

Work experience

Silicon Motion Technology Corporation

Hsinchu, Taiwan

May. 2020 – Nov. 2022

Email:u1429034@umail.utah.edu

- SSD firmware Engineer
 - $\circ~$ Solve issues from internal tests and customer tests.
 - $\circ\,$ Program and erase fail handling verification. (Kingston NV2 (PCIE))
 - Read, Raid, and Debug flow presentation. (Kingston NV2 (PCIE))
 - RAID engine validation and MPISP (boot code) verification on FPGA. (IC SM2259XT3 validation)
 - Turbo RAID implementation and verification (rescue 6 planes retention data). (Crucial BX500 (SATA))
 - RDT(Reliability Demonstration Test) maintenance and development. (Crucial BX500 (SATA))
 - o Tools: C, UART, ICE, JTAG, Logic analyzer, RTOS

ACADEMIC EXPERIENCE

University of Utah

Salt lake city, Utah, USA

Jan. 2024 – Apr. 2024

Teaching Assistant - Probabilistic machine learning
• Release, grade assignments and office hours

University of Utah

Salt lake city, Utah, USA

 $Research\ Assistant-Independent\ study$

Aug. 2023 - May. 2024

- o Topic: Fractional Fourier neural operator on partial differential equations
- o Author: Su-Wei Yang
- $\circ \ \operatorname{Result:} \ 92\% (1D \ dataset), \ 55\% (2D \ dataset), \ and \ 27\% (3D \ dataset) \ \operatorname{less \ error \ than \ FNO \ vanilla;} \\ 36\% (1D \ dataset), \ 21\% (2D \ dataset), \ and \ 16\% (3D \ dataset) \ \operatorname{less \ error \ than \ Full \ mode \ FNO.}$
- o Tools: Signal processing, Numerical methods, Non-stationary random fields, Partial differential equations, PyTorch

National Taiwan University of Science and Technology

Taipei, Taiwan

Mar. 2018 - Jun. 2018

Teaching Assistant - Embedded System software design

o Design, assess, and grade students' exams and projects

• Papers Sep. 2017 – Aug. 2019

- Topic: GPU Swap-aware Scheduler virtual memory management for GPU applications
- o Authors: Su-Wei Yang, Zhao-Wei Qiu, Ya-Shu Chen
- Result: Improve 16% performance in real cases.
- Conference 2020 ACM/SIGAPP Symposium On-Applied Computing
- o Topic: Energy-Efficient Task Offloading for Time-sensitive Application in Fog Computing
- o Authors: Yu-Lin Jiang, Ya-Shu Chen, Su-Wei Yang, Chia-Hsueh Wun
- o Journal IEEE System Journal

EDUCATION

University of Utah
Master of Science in Computer Science
National Taiwan University of Science and Technology

Salt lake city, Utah, USA
Jan. 2023 – Dec. 2024
Taipei, Taiwan

Master of Science in Electrical Engineering

RWTH Aachen University

Exchange student, Electrical Engineering

National Taiwan University of Science and Technology

Bachelor of Science in Electrical Engineering

Aachen, Germany Oct. 2019 – Mar. 2020 Taipei, Taiwan Sep. 2013 – Jun. 2017

Sep. 2017 – Mar. 2020

Courses

• Courses: Artificial Intelligence, Advanced Operating System, Computer Architecture, Embedded system software design, Nature Language Processing, Operating System, Probabilistic Machine Learning, RTOS, Software Verification