Suyash Mahar

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Education

Indian Institute of Technology, Roorkee

Roorkee, India

suyashmahar.me

B. Tech., Electronics and Communication Engineering (GPA: 9.149/10)

July 2016 - Present

 Coursework includes: Computer Architecture, Digital Logic Design, Data Structures & Algorithms, Semiconductor Devices.

Experience

FPGA based FIB design for NDN Routers

IIT Roorkee, India

Undergraduate Researcher

July 2017 - present

- Working under Prof. Vaskar Raychoudhury and Diviya Saxena on ways to accelerate FIB look ups in NDN routers.
- Work includes design of efficient FPGA based FIB for NDN routers.

FPGA implmentation of CORDIC Equation

IIT Roorkee, India

Short term project

May 2017 - June 2017

- Studied and implemented CORDIC algorithms on FPGA.
- Used Python (Jupyter notebook) for modeling and Verilog for design and implementation.

Awards and Achievements

• Dedicated member award, National Service Scheme (IIT Roorkee)

April 2017

• Joint Entrance Examination, Advanced (Indian Institute of Technology)

All India Rank 1387, 99.3 percentile

May 2016

• International Cyber Olympiad (SilverZone) Olympiad Rank 11, State Rank 1

Dec. 2014

Skills

Languages: C++, (System) Verilog, SPICE, LATEX, Python, C#/VB.NET, Java, Shell

Software: MatLab, Vivado, Visual Studio 2017, Android Studio, Jupyter notebook

Languages: English, Hindi

Interests

Academic: Computer Architecture, Digital logic design, FPGA applications, Operating systems.

Computers: Currently maintains two applications on GitHub (Android and Windows).

Campus Groups: Active member of IEEE Special Interest Group and Mobile Development

Group.