

Varshanjali Sayyaparaju

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Objective:

To gain experience in the field of electronic and communication engineering

Education:

| Campus | Location | Graduation Date | Field | GPA |
|---|------------------|-----------------|--|---------|
| University High School | California, USA | June 2010 | ----- | 3.8/4 |
| International Institute of Information Technology | Hyderabad, India | May 2014 | Electrical and Electronics Engineering | 6.63/10 |

Relevant Courses:

- **Programming:** Computer Programming & Data Structures,
- **VLSI & Embedded System:** Embedded Hardware Design, Digital Logic and Processors, Basic Electronic Circuits, Linear Electronic Circuits
- **Signal Processing:** Digital Signal Processing, Digital Image processing, Speech Signal processing, Speech Technology
- **Practical/Lab:** Electronics Workshop I & II, ECE Lab
- **Robotics:** Statistical Methods in AI, Artificial Neural Networks

Experience:

- VisionTek December 2011
Intern
Work: Understanding working of business, and workplace environment.
Mini-project: Create a memory storage space.
- IIIT-H On going
Teaching Assistance
Work: Teach Science I to 2nd year engineering students; Correct papers & conduct tutorials
- IIIT-H Speech Lab August 2013
Research Assistance
Work: Prosody modification of speech using frequency warping on android platform

Course Projects: Hand-gesture recognition (Matlab), Nearest Neighbor Classifier (Matlab), Batch-Perceptron, Single-Perceptron, Ho-Kashyap, Single Sample Relaxation, Batch Relaxtion, Widrow-Hoff Classifiers (C++),

Projects:

- **B-tech Project:** November 2013
Student
Work: Prosody modification of speech on android platform
Mentor: Kishore Prahallad
Description: Creating an android application, which changes input voice characteristics, e.g. TomCat
- **Hand-gesture recognition:**
Description: Identify mudras of Indian classical dance forms in still images and classifying them.

| <u>Level</u> | <u>Operating Systems</u> | <u>Hardware Description Language</u> | <u>Languages</u> | <u>Assembly Languages</u> | <u>Simulation software</u> | <u>Micro-Controller and FPGA</u> | <u>VLSI tools</u> | <u>Hardware</u> | <u>Miscellaneous</u> |
|-----------------|--------------------------|--------------------------------------|----------------------|---------------------------|--|--|-------------------|--|---|
| <u>Basic</u> | | | Android Platform | MIPS, ARM, Intel 8085 | | | | | |
| <u>Moderate</u> | | AVR Coding | Python, JAVA | | Xilinx ISE, Multisim, Simulink, Altera Modelsim, HiD Bootflash, Digilent Adept Tool, | Atmel ATmega16, Xilinx Nexus 2 board with Spartan 3 family | Tanner EDA, | | |
| <u>Advanced</u> | Windows, Linux, Mac | VHDL | C, C++, Matlab, HTML | | Audacity Wavesurfer | FPGA | Cadence | All general IC's, soldering, breadboard, PCB | Octave, PSpice, Adobe Photoshop, Gimp, Microsoft Office |

Skills:

Activities & Honors:

IIIT-H, Hyd, AP, India

August 2012 – August 2013

Sports-Coordinator

Duties: Organizing Events, and managing people

Ashakiran

August 2013 - present

Teacher

Duties: Teaching underprivileged children

Apex Body

August 2013 – December 2013

In-Charge

Duties: Welcoming freshmen and helping them deal with problems.

University High School

Principle's Honor Award for GPA above 3.5/4

June 2010

References available at request

"I hereby declare that the above information is correct to the best of my knowledge"

Last updated on Jan 4th 2013