

**A Project Report
on**

**POWER SYSTEM FAULT DIAGNOSIS USING
MACHINE LEARNING**

*submitted in partial fulfilment of requirements for the
award of the degree of*

BACHELOR OF TECHNOLOGY

in

ELECTRICAL AND ELECTRONICS ENGINEERING

by

EDAGOTTI PAVANKUMAR	16121A0253
AVILALA DEDEEPIYA	16121A0208
GAYAKAWADA CHANDRASEKHAR	16121A0265
AVULA PRAVALIKA	16121A0209

under the guidance of

Dr. S. HEMACHANDRA *M.Tech., Ph.D*
Professor



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

A. RANGAMPET, TIRUPATI - 517 102
INDIA
2019 - 2020