Project Documentation

Commodore Port Breakout: IEEE-488 Output

Project number: 203

Revision: 0

Date: 12.11.2022

Commodore Port Breakout: IEEE-488 Output Rev. 0

Module Description

Introduction

This assembly provides the edge connector for a Commodore PET IEEE-488 Port. The purpose is connecting it to the 2x12 Breakout board via a ribbon cable.

Every pin of the edge connector is connected to a pin of the (2x13p) box connector/pin header. The "GND" pins of the IEEE-488 port are all connected on this board.

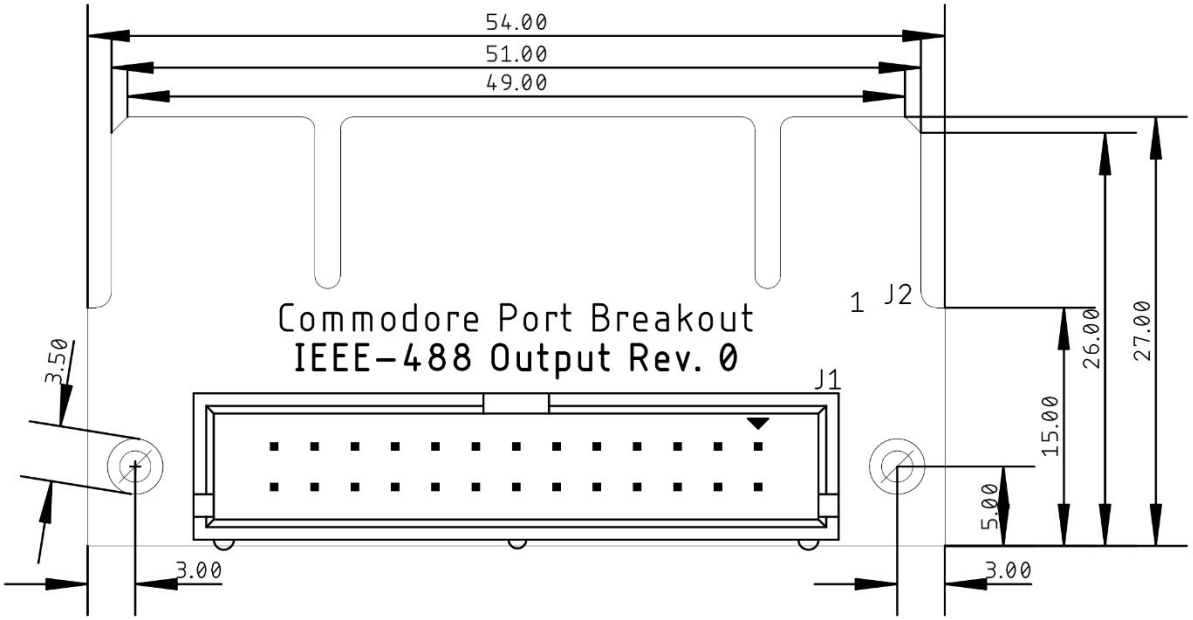


Figure 1: Dimensions of the PCB

Connector Pinout

J1: 2x13 box connector/pin header 2.54mm pitch.

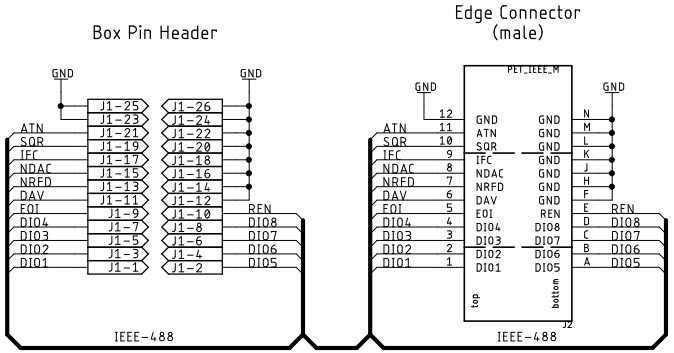
J2: board edge connector (a structure on the PCB).

J1	IEEE-488	J2
1	DIO1	1
3	DIO2	2
5	DIO3	3
7	DIO3	4
9	EOI	5
11	DAV	6
13	NRFD	7
15	NDAC	8
17	IFC	9
19	SQR	10
21	ATN	11
23, 25	GND	12
2	DIO5	A
4	DIO6	B
6	DIO7	C
8	DIO8	D
10	REN	E
12	GND	F
14	GND	H
16	GND	J
18	GND	K
20	GND	L
22	GND	M
24, 26	GND	N

Revision History

Rev. 0

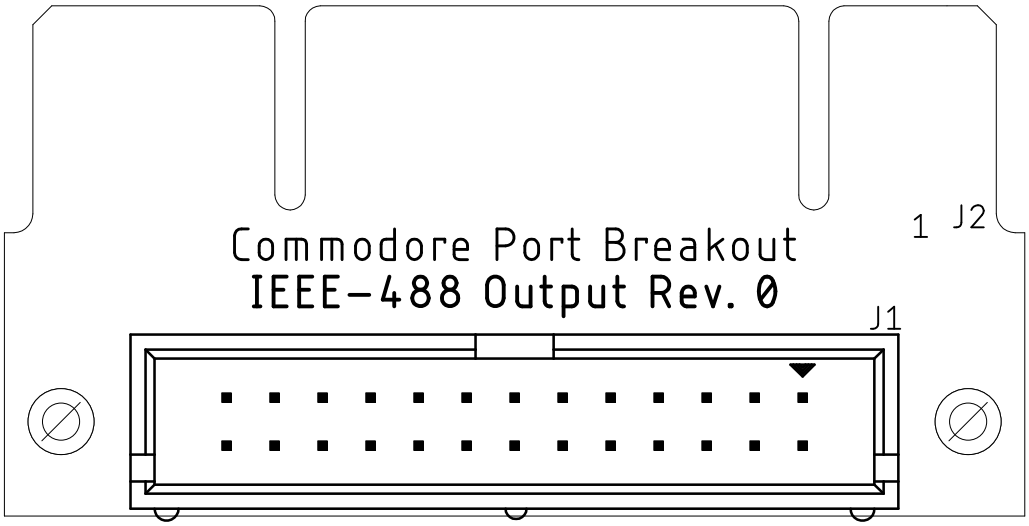
- Prototype



open source hardware

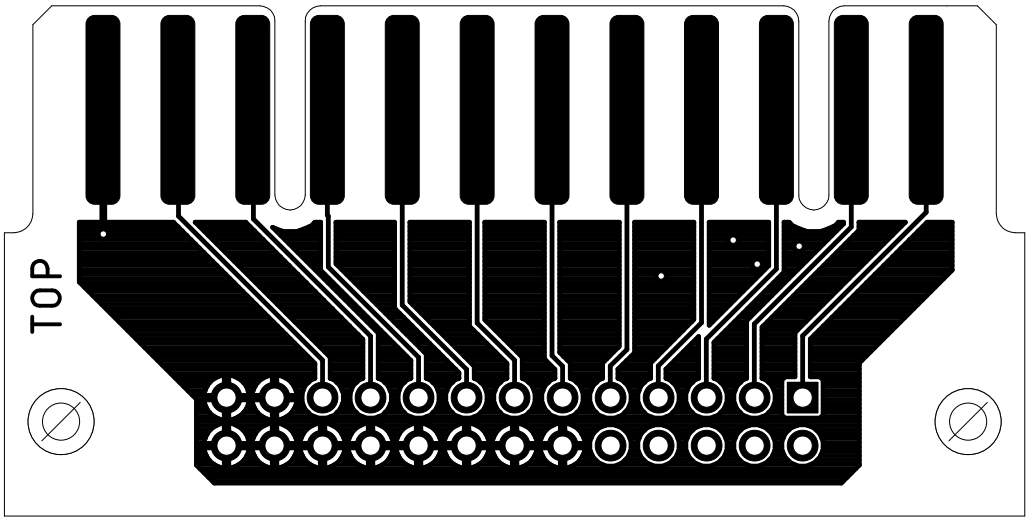
Title:	Commodore Port Breakout: IEEE-488 Output	Doc.-No.:	203-1-01-00
		Draft.:	Sven Petersen
Date:	12.11.2022 15:48	Rev.: 0	Page 1/1
File:	CPB_IEEE_out		
	http://github.com/svenpetersen1965		A3

Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers: 2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
placement component side		

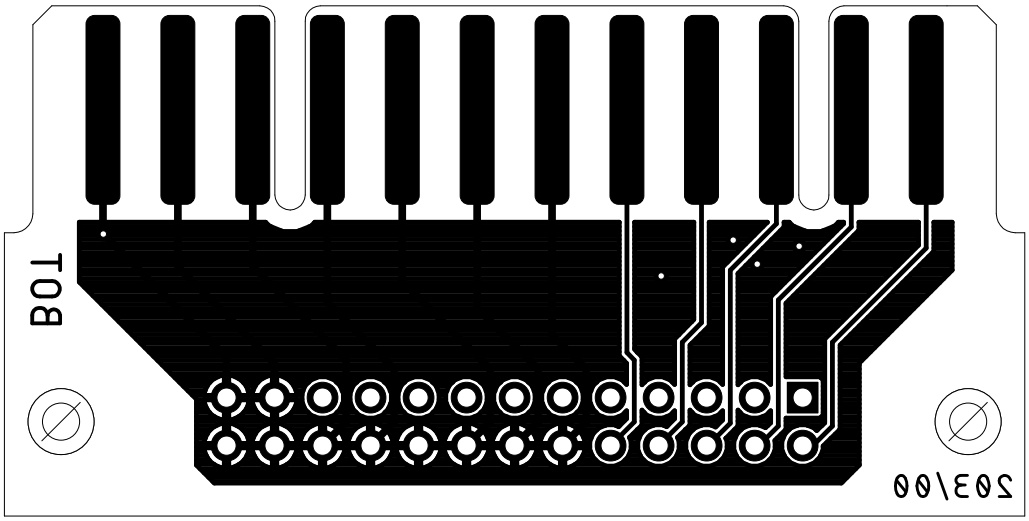


Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers:2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
placement solder side		

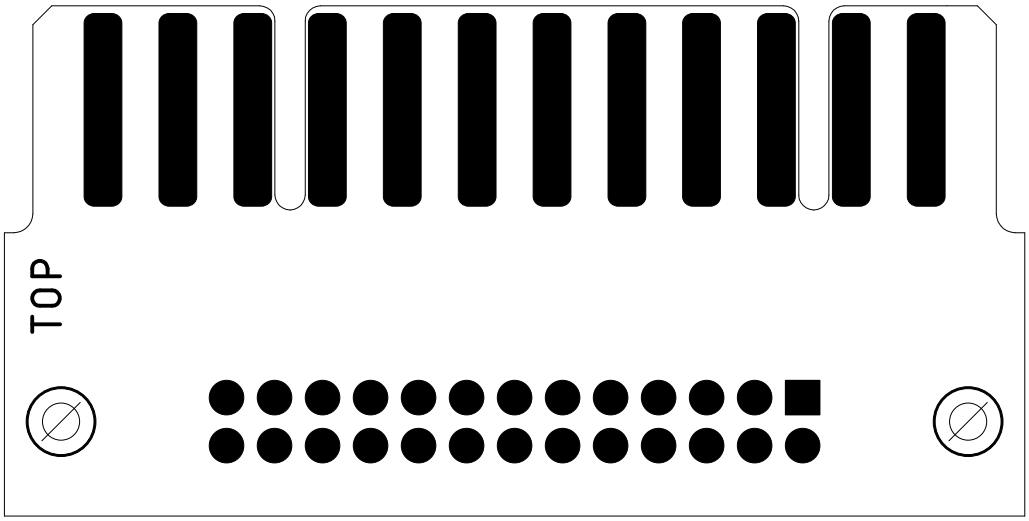
Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers: 2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
top		



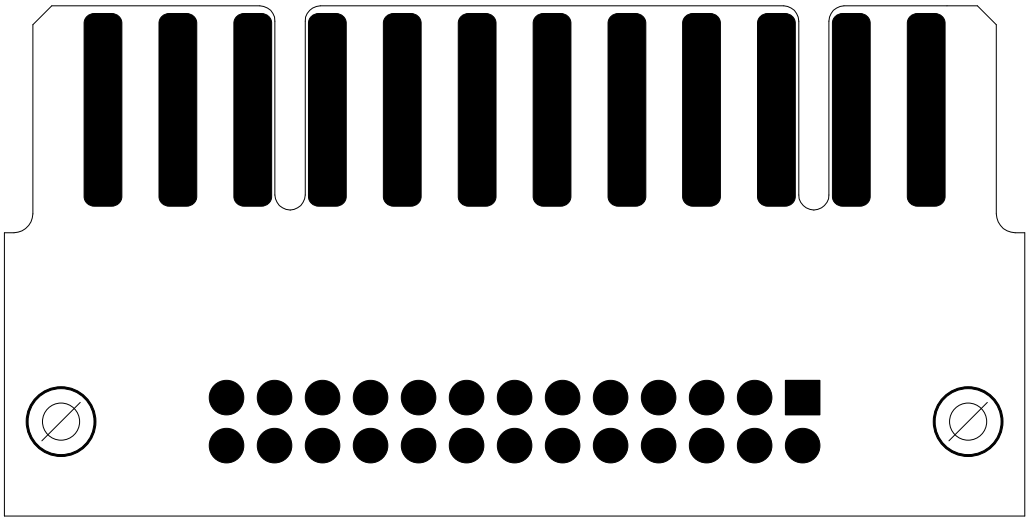
Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers: 2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
bottom		



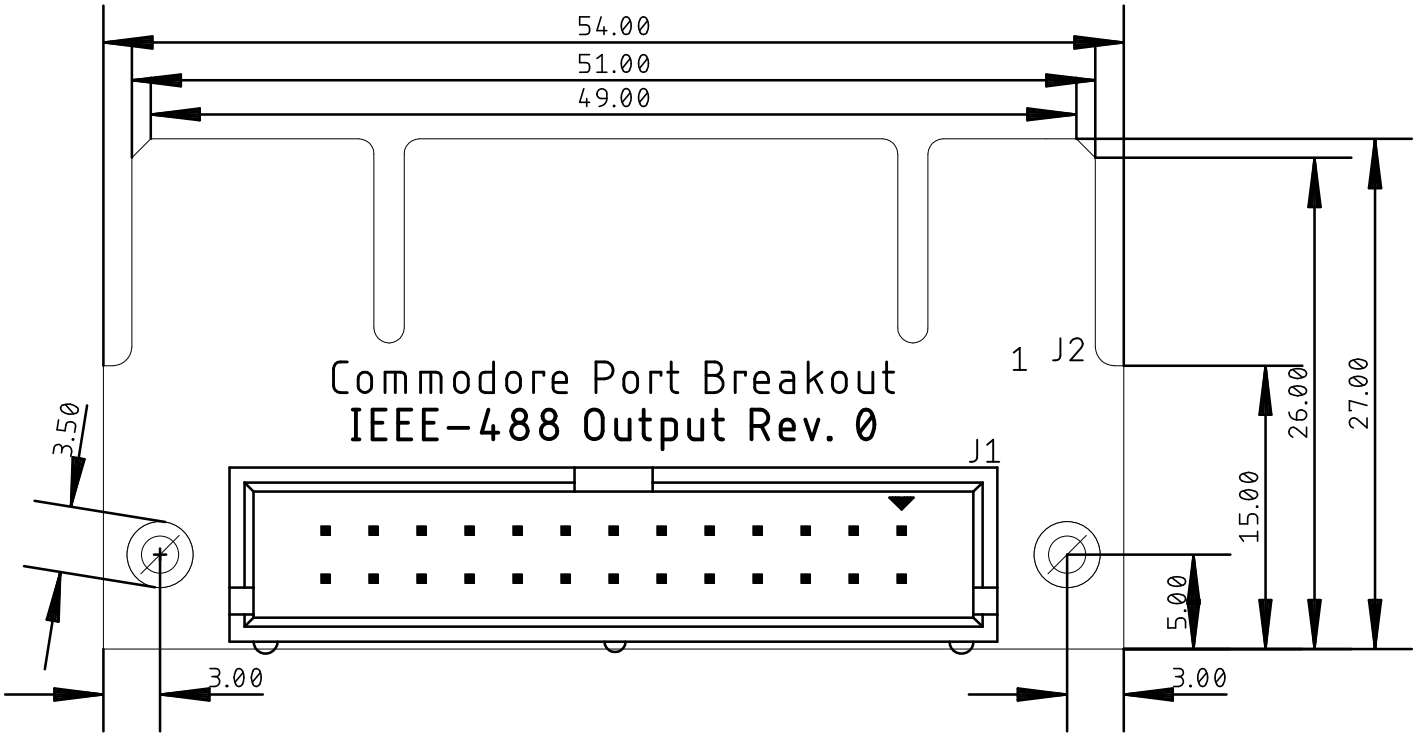
Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers: 2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
stopmask component side		



Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers: 2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
stopmask solder side		



Sven Petersen 2022	Doc.-No.: 203-2-01-00	
	Cu: 35µm	Cu-Layers: 2
CPB_IEEE_out		
12.11.2022 19:25		Rev.: 0
placement component side measures		



Commodore Port Breakout: IEEE-488 Output Rev. 0
Bill of Material Rev. 0.0

Pos.	Qty	Value	Footprint	Ref.-No.	Comment
1	1	203-2-01-00	2 Layer	PCB Rev. 0	2 layer, Cu 35μ, HASL, 54.0mm x 27.0mm, 1.6mm FR4
2	1	2x13 box connector	2X01WV	J1	e.g. Reichelt WSL 26G