

Management Day Session 150

Tradeoffs and Choices for Emerging SoCs in High-End Applications

Chair:

Nic Mokhoff
EE Times, Manhasset, NY

Organizer:

Yervant Zorian
Virage Logic Corp., Fremont, CA

Abstract

Design and manufacturing flows and methodologies are directly impacted by the demand for emerging SoCs with increasing performance and parallelism. Moving to new semiconductor technology nodes can significantly affect the choices of suppliers. This session will provide an overview of changing needs and corresponding management decision criteria to make the right choices from a pool of alternate options for flows, methodologies and suppliers.

150.1 Assessing Process Nodes and IP for SoC Development

Speaker: Ken Wagner, PMC-Sierra, Inc.,
British Columbia, Canada

150.2 Open-IP: How Your Selection of IP Drives Your ASIC Success

Speaker: Rajesh Shah - Open-Silicon, North
Milpitas, CA

150.3 Yield, Manufacturability and Test: The Criteria to Judge the Right Design Investment

Speakers: Rajesh Galivanche - Intel Corp.,
Santa Clara, CA
Kee Sup Kim – Intel Corp., Folsom, CA