

ADCDData(9dto)
 ADCOverflow
 ADCClock
 ADCOE-B
 ADCStandby

Col Reg codes:

010 B
 110 A

APS Row CLK
 Row In
 Col CLK
 Col In
 Col Mode (1dto)
 TX Gate - B

active low

resetSettle 5 6bits
 rowSettle 1
 colSettle 1

exposure 5 μ s (18bit)
 frameDelay

RS: reset column A+CMI

wait ResetSettle time

read column A+CMI

→ Read SM, row bit

read column B+CMI

→ Read SM, row bit

(if distance proper = exposure)

wait frameDelay time

Read SM:

wait for ColSettle time

~~feed row~~

wait for RowSettle time

latch value

done

Col Modes:

00 NULL
 01 ReadA
 10 ReadB
 11 ResetA

RS: always open

GS: opening exposure (170+1)
 (open during reset?)

GS: reset whole array

wait ResetSettle time

read column B+CMI

→ Read SM, row bit

wait Exposure time (TXGate open)

read column B+CMI

→ read SM, row bit

wait frameDelay time

sensor size 180 rows
 register size 180
 240 columns
 x 242
 1+240+1
 for 3bit col register
 code, 1 extra on
 both sides needed