CS311: Computer Architecture Lab

Assignment 5 - Report

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The aim was to develop a pipelined processor Simulator for ToyRISC.

Statistics -

Sl No.	Program	No. of Instructions executed	No. of cycles taken	No. of OF Stalls	No. of Wrong Instructions
1.	descending.asm	277	734	162	176
2.	even-odd.asm	6	18	5	0
3.	fibonacci.asm	78	153	36	32
4.	palindrome.asm	49	117	40	14
5.	prime.asm	29	87	19	10

Table: *Statistics of Processor performance on Test Programs*

(provided in the assignment)