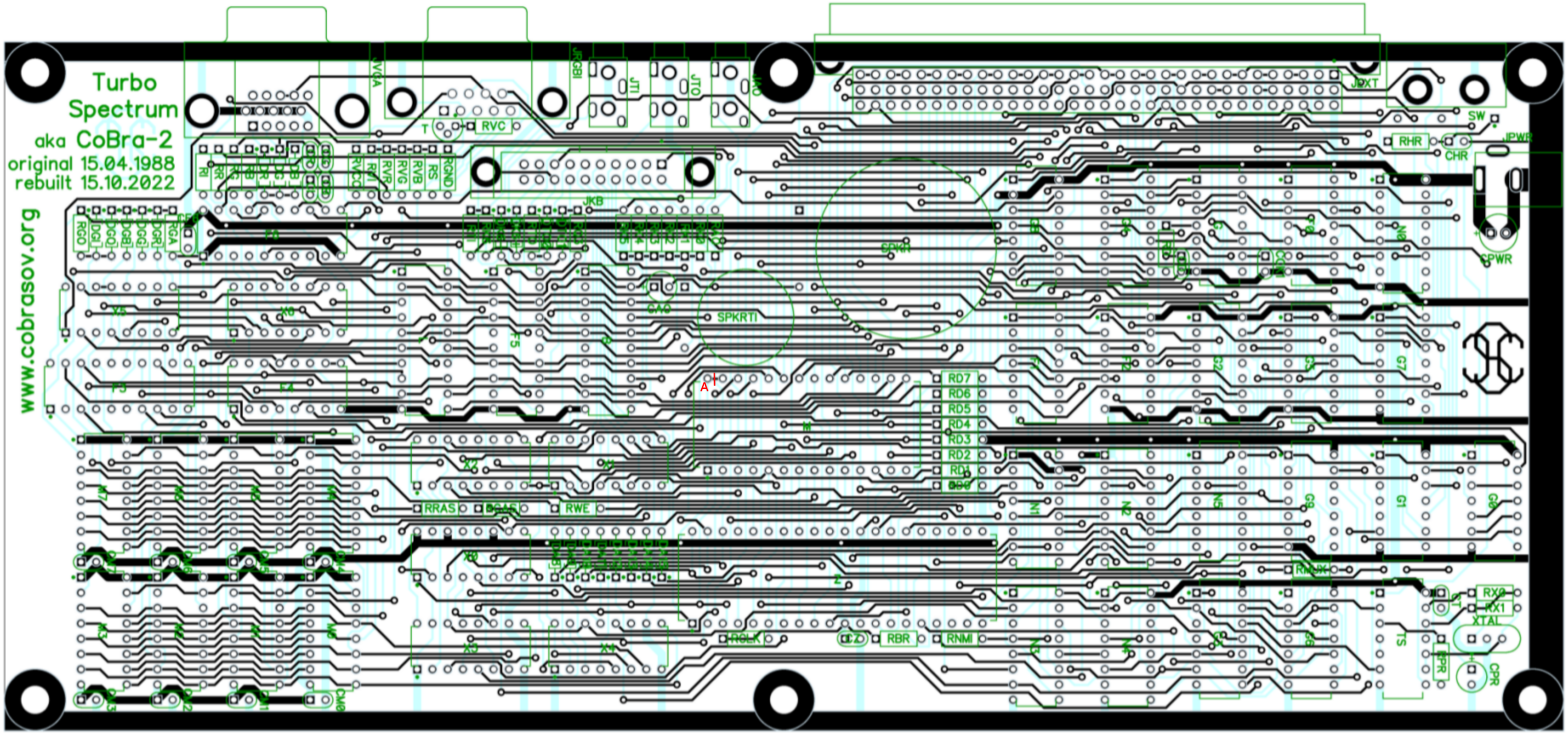


Figure 32: PCB Top Layer