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## Credential Evaluation Report

Name: Venkata Sai Karthik SURVEPALLI  
Country: India  
Type: Academic  
Institution:  
1. Anna University, India  
2. Sathyabama University, India

Date: March 30, 2013  
Ref: RM/033013/RS  
Purpose: Immigration

### Academic equivalent in the United States: Master of Science in VLSI Design

#### Evaluation outline & Analysis:

Mr. Survepalli completed Bachelor of Engineering in Electronics & Communication Engineering from Anna University and Master of Technology in VLSI Design from Sathyabama University, India. Based on the foregoing academic credentials Mr. Survepalli has an educational equivalent of Master of Science in VLSI Design from a regionally accredited college or university in the United States.

#### Academic:

Mr. Survepalli commenced his post-secondary studies in Bachelor of Engineering program at Anna University following his completion of secondary level-studies. Anna University is an accredited institution of higher learning in India. Graduation from high school is the requirement for admission and enrollment in Anna University. Mr. Survepalli entered university in 2001 and completed four-year program of required academic studies, including general studies and concentrated studies in 2005.

Mr. Survepalli completed all the required course work, which includes both general studies and specialized courses of his area of concentration. Mr. Survepalli has satisfied requirements substantially similar to those required towards the completion of a Bachelor of Science in Electrical Engineering with a specialization in Electronics & Communication from an accredited college or university in the United States.

Thereafter, Mr. Survepalli enrolled in graduate-level Master of Technology at Sathyabama University. Sathyabama University is an accredited institution of higher education in India. Graduation from Bachelor's degree and competitive entrance examination scores are the requirements for admission and enrollment to graduate-level programs at Sathyabama University. Mr. Survepalli entered university in 2008 and completed two-year required academic studies in 2010. Mr. Survepalli has satisfied requirements substantially similar to those required towards the completion of academic studies leading to graduate-level Master of Science in VLSI Design from an accredited college or university in the United States.