



SATHYABAMA

(Established under section 3 of the UGC Act, 1956)

CONSOLIDATED

NAME OF THE CANDIDATE		VENKATA SAI KARTHIK S			
DATE OF PUBLICATION		17/04/2010			
COURSE & BRANCH		M.Tech - VLSI DESIGN			
SEM	SUBJECT CODE	SUBJECT TITLE	CREDIT	GRADE	MONTH & YEAR OF PASSING
1	782101	Transforms and Probability for Electronics Engineering	3	C	11/08
1	782102	Advanced Digital System Design	3	B	11/08
1	782103	VLSI Design	3	B	04/09
1	782104	MOS Device Modeling	3	C	11/08
1	782105	VLSI Technology	3	B	11/08
1	782150	EDA Tools Lab	3	A	11/08
2	782201	Advanced Digital Signal and Image Processing	3	C	04/09
2	782202	Programming in HDL	3	B	04/09
2	782204	Analog and Mixed Signal Integrated Circuits	3	B	04/09
2	782205	CAD for VLSI Circuits	3	B	04/09
*** END OF STATEMENT ***					
CLASS OBTAINED : FIRST CLASS Overall CGPA : 7.68					