



Certificate of Participation

This is to certify that **Santhoshkumar S** of

National Institute of Technology Puducherry

has successfully completed the Internship Program on

FPGA Design and Verification

held from 4th January to 3rd February 2021, from

Sandeepaní School of Embedded System Design, Bangalore.



Ms. Sadiya Arshad

Director Education and Skill Development