Timer0

```
#include <xc.h>

// CONFIG

#pragma config FOSC = HS    // Oscillator Selection bits (RC oscillator)
#pragma config WDTE = OFF   // Watchdog Timer Enable bit (WDT disabled)
#pragma config PWRTE = OFF  // Power-up Timer Enable bit (PWRT disabled)
#pragma config BOREN = OFF  // Brown-out Reset Enable bit (BOR disabled)
#pragma config LVP = OFF    // Low-Voltage (Single-Supply) In-Circuit
Serial Programming Enable bit (RB3 is digital I/O, HV on MCLR must be
used for programming)
#pragma config CPD = OFF    // Data EEPROM Memory Code Protection bit
(Data EEPROM code protection off)
#pragma config WRT = OFF    // Flash Program Memory Write Enable bits
(Write protection off; all program memory may be written to by EECON
control)
#pragma config CP = OFF     // Flash Program Memory Code Protection bit
(Code protection off)

// #pragma config statements should precede project file includes.
// Use project enums instead of #define for ON and OFF.

#define _XTAL_FREQ 20000000

main()
{
      TRISD0=0;
      RD0=0;
      GIE=1;                //enabling global interrupt
      PEIE=1;               //enabling periferal interrupt
      T0IE=1;               //timer0 enable
      TMR0=0x03;            //initialising timer value
      T0CS=0;               // timer mode
      PSA=0;                //prescale alignment to timer mode
      PS2=PS1=PS0=0;        //prescale asignment
      while(1);
}

interrupt isr()
{
      RD0=1;
      T0IF=0;               //clearing flag
}
```