Counter0

```c
#include <xc.h>

// CONFIG
#pragma config FOSC = HS      // Oscillator Selection bits (RC oscillator)
#pragma config WDTE = OFF     // Watchdog Timer Enable bit (WDT disabled)
#pragma config PWRTE = OFF    // Power-up Timer Enable bit (PWRT disabled)
#pragma config BOREN = OFF    // Brown-out Reset Enable bit (BOR disabled)
#pragma config LVP = OFF      // Low-Voltage (Single-Supply) In-Circuit
Serial Programming Enable bit (RB3 is digital I/O, HV on MCLR must be
used for programming)
#pragma config CPD = OFF      // Data EEPROM Memory Code Protection bit
(Data EEPROM code protection off)
#pragma config WRT = OFF      // Flash Program Memory Write Enable bits
(Write protection off; all program memory may be written to by EECON
control)
#pragma config CP = OFF       // Flash Program Memory Code Protection bit
(Code protection off)

// #pragma config statements should precede project file includes.
// Use project enums instead of #define for ON and OFF.

#define _XTAL_FREQ 20000000

main()
{
    char i;
    TRISD0=0;
    TRISC0=TRISC1=0;
    RD0=0;
    GIE=1;                 //enabling global interrupt
    PEIE=1;                //enabling periferal interrupt
    T0IE=1;                //timer0 enable
    TMR0=0xfe;             //initialising timer value
    T0CS=1;                // counter mode
    T0SE=1;                // HIGH TO LOW EDGE TRIGGER
    PSA=0;                 //prescale alignment to timer mode
    PS2=PS1=PS0=0;         //prescale asignment
    while(1)               //running two lights
    {
        RC0=~RC0;
        RC1=~RC1;
        for(i=0;i<=100;i++)
            __delay_ms(10);
    }
}

interrupt isr()
{
    RD0=1;
    T0IF=0;                //clearing flag
}
```