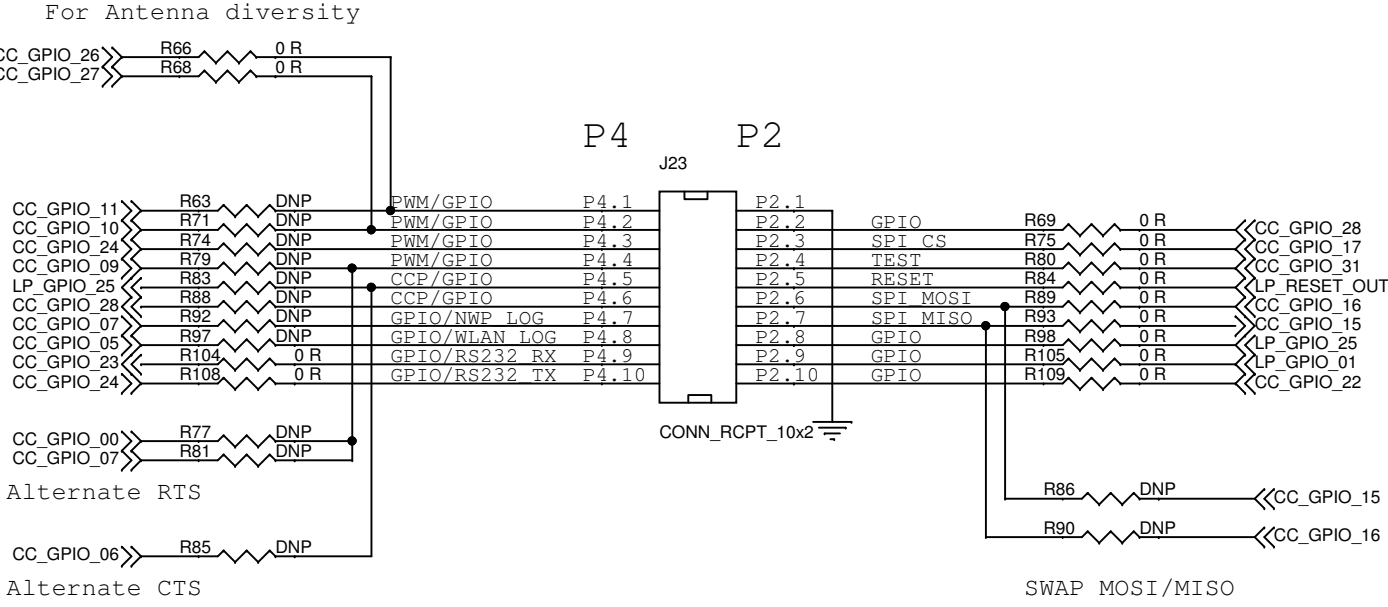
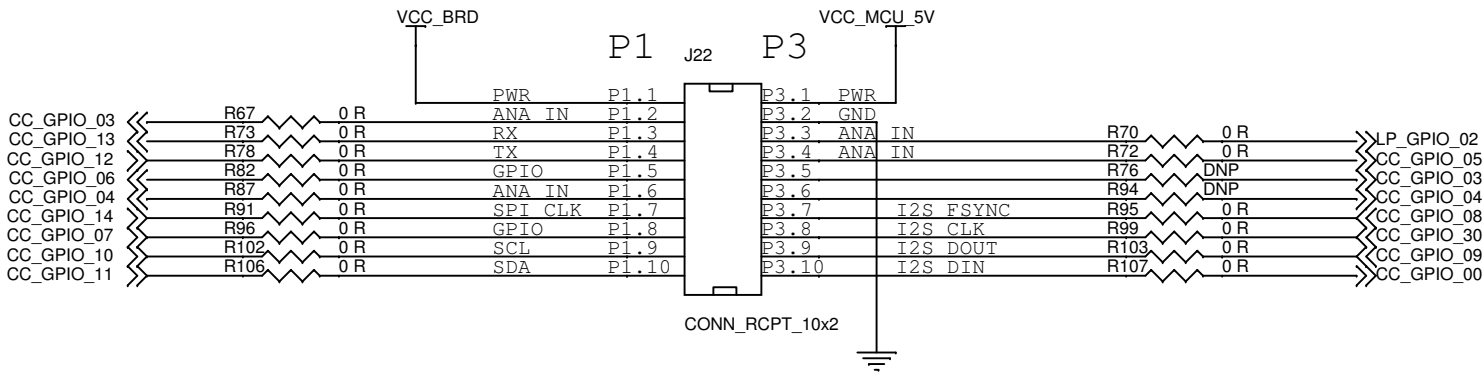
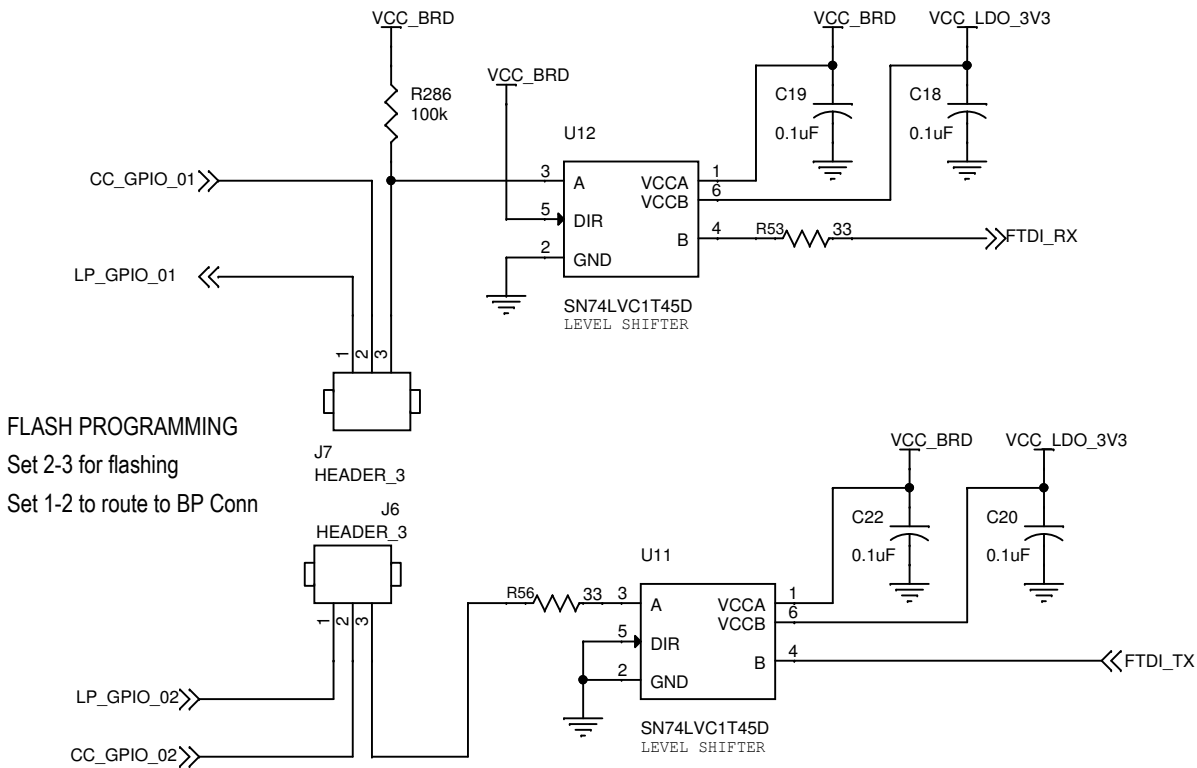


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Drawn By	T1	Date	Size A2	Project No <Project Number>	Rev 4.0A
Layout	T1	Date	Scale	Date Thursday, June 05, 2014	Sheet 1 of 5

BOOSTER PACK INTERFACE

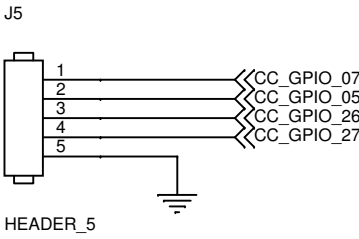


UART MUXING

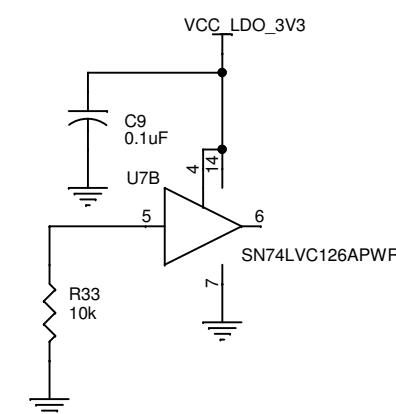
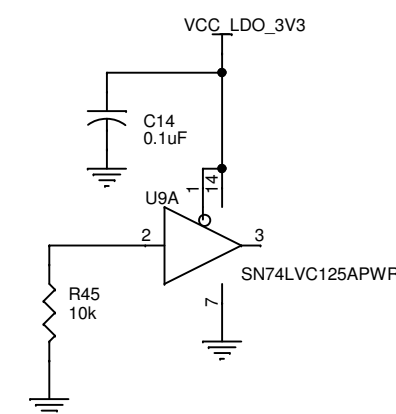



TEST POINTS

OBSERVE NWP & MAC LOG




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				Date	Thursday, May 15, 2014
				Sheet	2 of 5



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Drawn By	TI	Date	Size A2	Project No <Project Number>	Rev 4.0
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Board Revision control naming convention
X.Y-Z (X= schemtic ver, Y = layout version (include silk changes), Z = BOM variant)

Revision history
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Changes in Rev 4.0-A
1. Add pull-ups and pull-down resistors for the serial flash
2. Move the nRESET pull from VCC_BRD to VBAT_CC
3. Add pulll-up on UART_TX going to the FTDI to prevent false start bits.
4. Add pull-up resistor for Acccelerometer address to avoid conflict with Audio Booster pack
5. Add 100K pull-up on RESET_OUT net for any BP without RESET pulls.
6. Change R61 to 2.7K, R57-> 270 Ohms

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Layout	Tessolve	Date	Scale	Date Thursday, June 05, 2014	Sheet 5 of 5