

CC32xx uDMA Application

Overview

The μ DMA controller is a flexible and highly configurable DMA controller designed to work efficiently with the microcontroller's Cortex-M4 processor core. It supports multiple data sizes and address increment schemes, multiple levels of priority among DMA channels, and several transfer modes to allow for sophisticated programmed data transfers.

Different transfer modes supported are:

1. **Basic mode:** In Basic mode, the μ DMA controller performs transfers as long as there are more items to transfer, and a transfer request is present. This mode is used with peripherals that assert a μ DMA request signal whenever the peripheral is ready for a data transfer.
2. **Auto Mode:** Auto mode is similar to Basic mode, except that once a transfer request is received, the transfer runs to completion, even if the μ DMA request is removed. This mode is suitable for software-triggered transfers. Generally, Auto mode is not used with a peripheral.
3. **Ping-pong Mode:** Ping-Pong mode is used to support a continuous data flow to or from a peripheral. To use Ping-Pong mode, both the primary and alternate data structures must be implemented. Both structures are set up by the processor for data transfer between memory and a peripheral. The transfer is started using the primary control structure. When the transfer using the primary control structure is complete, the μ DMA controller reads the alternate control structure for that channel to continue the transfer. Each time this happens, an interrupt is generated, and the processor can reload the control structure for the just-completed transfer.
4. **Scatter-gather Mode:** Memory Scatter-Gather mode is a complex mode used when data must be transferred to or from varied locations in memory instead of a set of contiguous locations in a memory buffer.

Application details

The objective of this application is to showcase different DMA modes of transfer. Demonstrates the use of the uDMA controller to transfer data between memory buffers using Auto Mode and Scatter Gather Mode, and to transfer data to and from UART using Ping Pong Mode. In Auto and Scatter Gather mode correctness of Data transfer is verified once the transfer is complete. In Ping Pong mode transfer rate is calculated and displayed.

Source Files briefly explained

1. **pinmux** - Pinmux configurations as required by the application.
 2. **main** - Main file that showcases different DMA modes of Transfer
 3. **uart_if** - To display status information over the UART
 4. **udma_if** - Initialize and Setup the uDMA controller
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Usage

1. Setup a serial communication application (HyperTerminal/TeraTerm) on the host PC. The settings are
 - Port:** Enumerated COM port
 - Baud rate:** 115200
 - Data:** 8 bit
 - Parity:** None
 - Stop:** 1 bit
 - Flow control:** None
2. Run the reference application (Flashing the bin/IAR/CCS).
3. Observe the status messages on the host over serial port to understand the sequence of operations performed by the application.

Limitations/Known Issues

None.

Article Sources and Contributors

CC32xx uDMA Application *Source:* <http://processors.wiki.ti.com/index.php?oldid=178074> *Contributors:* Codycooke, Malokyle