CC32xx Interrupt Demo Application

Application details

The objective of this application is to showcase interrupt preemption and tail-chaining capabilities. Nested interrupts are synthesized when the interrupts have the same priority, increasing priorities, and decreasing priorities. With increasing priorities, preemption will occur; in the other two cases tail-chaining will occur.

Source Files briefly explained

- **pinmux** - Pinmux configurations as required by the application.
- **main** - Main file that showcases Interrupt Preemption capabilities
- **uart_if** - To display status information over the UART
- **systick_if** - Setup the systick values and registers SysTick handler

Usage

- Setup a serial communication application (HyperTerminal/TeraTerm). For detail info visit Terminal setup

On the host PC. The settings are:
  - **Port**: Enumerated COM port
  - **Baud rate**: 115200
  - **Data**: 8 bit
  - **Parity**: None
  - **Stop**: 1 bit
  - **Flow control**: None

- Run the reference application (Flashing the bin/IAR/CCS).
- Observe the status messages on the host over serial port to understand the sequence of operations performed by the application.

Limitations/Known Issues

None.

Article Sources and Contributors

CC32xx Interrupt Demo Application *Source*: http://processors.wiki.ti.com/index.php?oldid=178060 *Contributors*: Codycooke, Jitgupta, Malokyle