Michael Kattwinkel

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Objective:

- Obtain a software engineering internship to grow my development skills and experience.

Education:

Virginia Polytechnic Institute and State University (Virginia Tech). Blacksburg, VA

- B.S. in Computer Engineering, Major: Software Systems, Minor: Computer Science.
- Expected Graduation: Dec 2023, GPA: 3.94.

Relevant Coursework:

- Data Structures and Algorithms, Applied Software Design, Cloud Software Development, Principles Comp Architecture, Fundamentals Digital Systems, Software Design & Data Structures, Embedded Systems

Work Experience:

Software Engineer Co-Op. Peraton. Blacksburg, VA (May 2022- Dec 2022)

- Design, develop, and test leading edge technology solutions in areas such as satellite ground systems, etc.
- Work in a collaborative, team-oriented Agile software development environment.
- Develop full stack application with PostgreSQL, Spring Boot, OpenApi, and ExtJS.

Intern. Albemarle County Public Schools Department of Technology (summer 2018, 2019)

Target Cashier, Soccer Referee

Relevant Skills:

- Programming Languages: Java, JavaScript, C/C++, Python, HTML/CSS, MATLAB
- Software tools: Git/GitHub, Linux, Docker, Angular, Databases
- Hardware: MIPS Assembly Language, Verilog
- Other: AC and DC Circuit Analysis, Arduino, TI MSP432, Autodesk Fusion, ArcGIS, Excel

Leadership:

Student Leader. Chi Alpha (Christian Campus Ministry) (Aug 2021 – Present)

- Lead and mentor a freshman small group; volunteer in campus outreach totaling around 15 hours a week.
- Coordinate and organize meetings, events, etc. Serve by cleaning up Lane Stadium after football games.

Eagle Scout. Troop 75. Boy Scouts of America

Projects/Activities:

To-do Webapp

- Built an Angular UI with CRUD functionality and 3rd party calendar importing.
- Built a back-end Web API with Nodejs, Express, and MongoDB.

Lisp Interpreter

- Developed a simplified Lisp interpreter with a GUI using C++.

GIS Data Searcher

- Programmed a Java application that allows for multiple types of efficient searching for GIS records.
- Implemented the database with a PR Quadtree, a hash table, and a buffer pool.

Pulse Oximeter

- Designed, built, and tested a Pulse Oximeter to detect heart rate and Sp02% as a semester long project.

Simple Computer

- Wrote Verilog HDL for a single-cycle CPU, which supports a simplified MIPS instruction set.

Math, Engineering, and Science Academy (High School)