



NOTES:

HIGHER MODULATION VOLTAGE MEANS ON ENV A:
 ATTACK CV - LONGER TIME
 DCY I CV - LONGER TIME
 DCY II CV - LONGER TIME
 REL CV - LONGER TIME
 ON ENV B THROUGH S&H B:
 DECAY ENV B MOD - SHORTER TIME

S&H TRIGGERS WHEN GATE GOES HIGH SO WHEN GATE LED GOES ON

WHEN GATE IN CONNECTOR IS NOT USED AND WHEN IN REPEAT MODE THE GATE FOR ENV A IS AS LONG AS THE ENV B ATTACK TIME

ENV A SWITCH SET TO TRIG MODE: ATTACK PHASE OF THE ENVELOPE MUST HAVE REACHED ITS TOP LEVEL BEFORE THE ENVELOPE CAN BE RETRIGGERED BY A NEW GATE PULSE.

IF A GATE COMES SOONER THE ATTACK CONTINUES TO RISE, WHICH MEANS THAT AN ATTACK PHASE WILL ALWAYS BE COMPLETELY FINISHED TO ITS TOP LEVEL EVEN IF THE GATE IS SHORTER THAN THE ATTACK PHASE, AND VERY SHORT GATE PULSES CAN PRODUCE A FULL ADBDR OR AD ENVELOPE.

ENVELOPE B IS ALWAYS IN TRIG MODE AND ATTACK HAS TO REACH ITS TOP VALUE BEFORE RETRIGGERING IS POSSIBLE