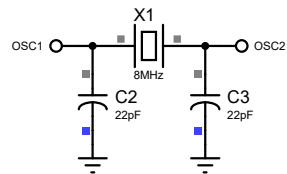
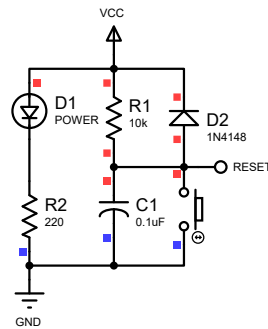


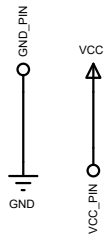
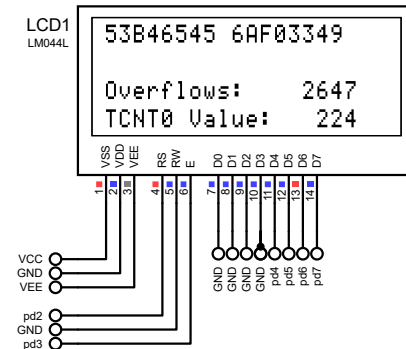
Oscillator Circuit (8MHz)



RESET Circuit

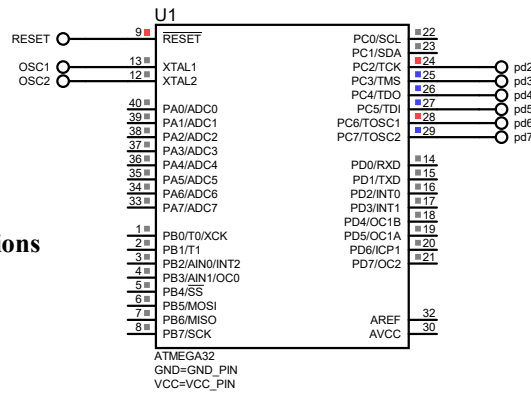


LCD (Text Vector & Code Execution Timer Status)

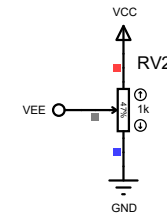


POWER & GND Connections

Atmega_32_Controller_IC



LCD Brightness Control Connections



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ANU Block Cipher Implementation

Test Vector & Execution Time Computation

Sheet 1 Of 1

REVISION swapnil

29 July 2021

