

ESM Lab Grading Report

Course Number: 3

Module: 2: Lab 4 on Strain Gauges

Lab Report Date <u>11/08/2021</u>

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Each Section of this Lab Report Counts for 20 points. Points will be allocated as follows:

20 points: section fully meets requirements of this rubric 15 points: section mostly meets requirements of this rubric

10 points: section meets roughly half the requirements of this rubric

5 points: section does not meet requirements, but shows a weak attempt

0 points: section blank

<u>Goal:</u> The purpose of this lab is to learn how to interface a strain gauge to a microprocessor such as the Cypress PSoC Chip. A secondary goal is to learn how to mitigate the negative effects of the input offset voltage of an amplifier.

Background: Strain gauges can be used to directly measure the strains in a machine or structure. They can also be used a component in a weight measuring instrument called a load cell. They are difficult to mechanically attach to structures, as you must glue them in a prescribed way for them to get accurate readings. Rather than make you glue your own strain gauges, we have specified a strain gauge kit from the web site www.sparkfun.com for you in your lab. This way, you can focus on the circuit and software associated with accurately measuring the strain gauge signal, rather than the production methods of gluing the gauge itself.

If you were to blindly measure the amplified signal of the strain gauge through the PGA, you would introduce the error of the input offset voltage of the PGA. Therefore, we have created an extra circuit using a DAC, Opamp, three resistors in order to null out this voltage. The compensated signal is then fed to the ADC. When you create your PSoC software to execute the nulling process, make sure to avoid touching putting any force on the strain gauges. Also, make sure that the ambient temperature at which you null the input offset voltage is within a couple of degrees C of the ambient temperature that you take your strain gauge readings. The video supplied with this lab gives you the appropriate instructions.

In this lab we want you to read the amplified strain gauge data on your PC. The UART (Universal Asynchronous Receiver Transmitter) in the schematic is required to communicate with the terminal emulation program that you run on

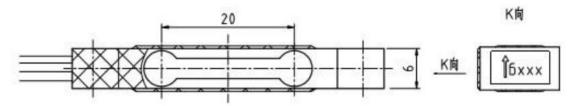
your PC. By using it, you avoid the need to use an LCD in your circuit. The PSoC chip communicates with the PC over it's USB interface. In Windows, a USB connection can be configured as a "virtual COM port". This means that it translates the USB traffic into a format that legacy UART software can interpret

Windows will create a virtual COM port employing the same USB connection that you use to power the board. It will have all the usual UART set up parameters like data rate, parity, number of stop bits, etc. However, this is all emulated in software. This allows programs like TeraTerm to think they're talking to a COM port. This COM port is the printed circuit connection, not the J6 micro-USB connector.

You need to go into the Windows Control Panel to see which COM port Windows uses. You must also install and run a terminal emulation program on your PC. We recommend the use of a free program called Tera Term. (Go https://download.cnet.com/Tera-Term/3000-2094_4-75766675.html for one site that offers a free download.) You must make sure that the communication parameters in Tera Term match those in the UART.

For this lab, you will display the amplified strain gauge data on Tera Term. You create a strain in your Sparkfun kit by anchoring one end of the metal beam to your lab bench or work table, and *lightly* pulling down with one or two fingers on the other end. Then you should see a voltage output reading on your PC screen that corresponds to the amplified bridge offset signal.

The Sparkfun kit has one end (right side in this picture) for lightly anchoring it to your table, and the other end where the wires come out. You can even hold down the anchored end with one hand, and lightly push down on the other end.



Note the specifications of the kit. We ordered the 100g model. Note the excitation voltage to the beam (5V nominal, but get exact data), and the output reading on your PC in volts. Then, calculate how many grams of force the strain gauges are reading. (You should be reading a value between 0 and 100 grams). You should be able to do all of the calculations in software, so that your PC directly shows the grams of force, as opposed to a raw voltage.

Specifications:				
capacity	g	100, 150, 200	300,500,750	1000,1500
rated output	mV/V	0.6 ± 0.15	0.7 ± 0.15	1.0 ± 0.15
safe overload	%FS	150		
ultimate overload	%FS	200		
excitation voltage	Vdc	≤6		

- (A) Functional demonstration of your circuit to our TA. In this exercise, you schedule an appointment with your TA to show that your hardware functions as designed. For the Strain Gauge lab, this will involve the following steps:
 - 1. Show that all hardware is in place, and that pushing down *lightly* on the beam gets you a voltage output to read on your PC.
 - 2. Show that the grams of force you read correlates to how hard you are pushing down on the beam. You should get close to zero grams when you have no force on the beam. How close depends on the accuracy of your nulling circuit that nulls the effect of input offset voltage. When you push down harder, you should get closer to 100 g's. *Do not push as hard as you can*. The ultimate overload is only 200%, which means that pushing with 200 g's force (~ 1 pound) can damage the strain gauges.

If you are an on-campus student, then show your circuit to one of our TA's during office hours.

If you are a distance learning student, make an online appointment with your TA to demonstrate your work via Zoom meeting or other Web-based meeting tool. You can use the camera on your laptop PC or suitable plugin webcam (Logitech etc.) to demonstrate a working circuit.

(B) Place photos here of your hardware setup, including PSoC board, connections to PC and/or nScope, wiring, and components. Label all components.

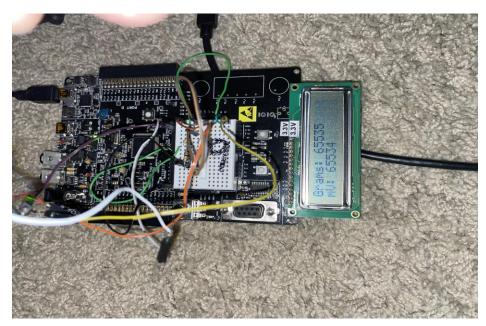


Figure 1: Negative Values of Grams and mV

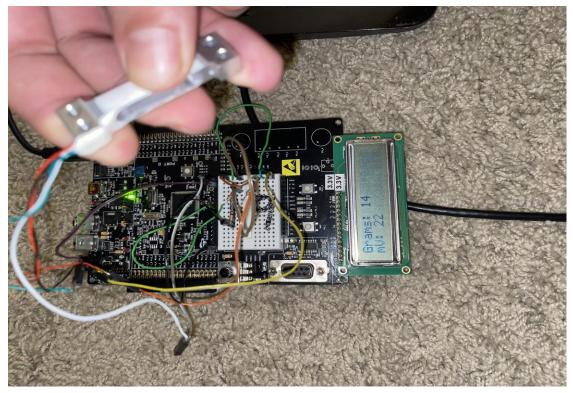


Figure 2: Positive Values of Grams and mV

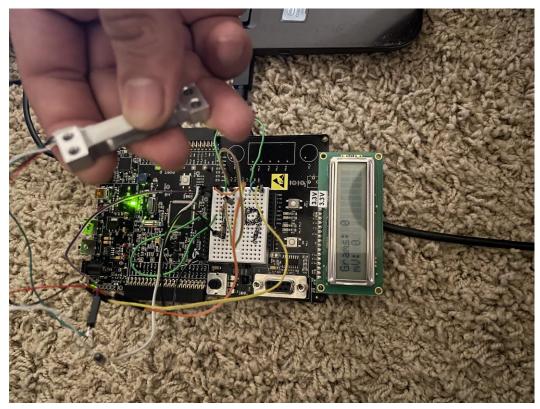


Figure 3: Zero (initial values) of Grams and mV

(C) Place complete PSoC schematic here. This schematic must include internal components from the PSoC board (amplifier, MUX, etc.), as well as external components (thermistor, resistor, display).

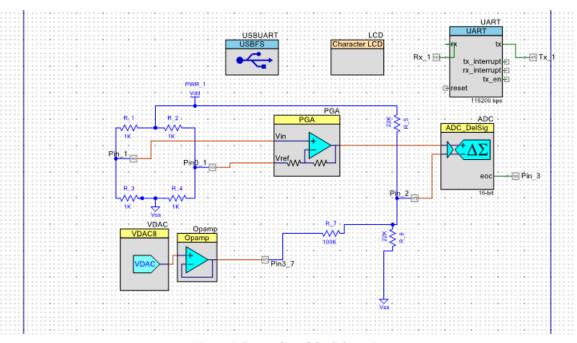
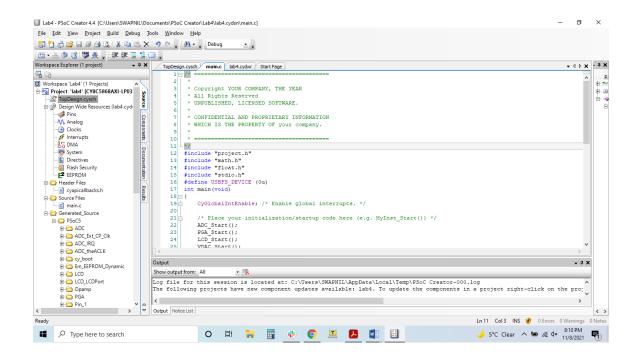
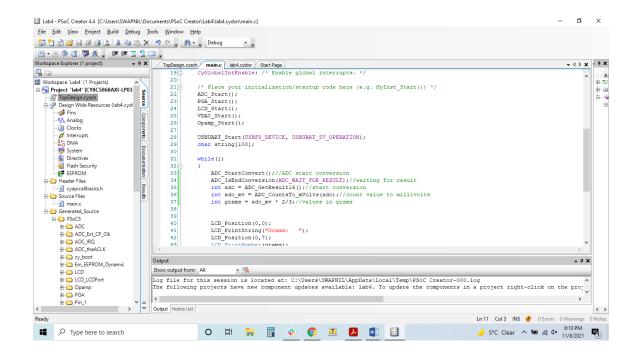


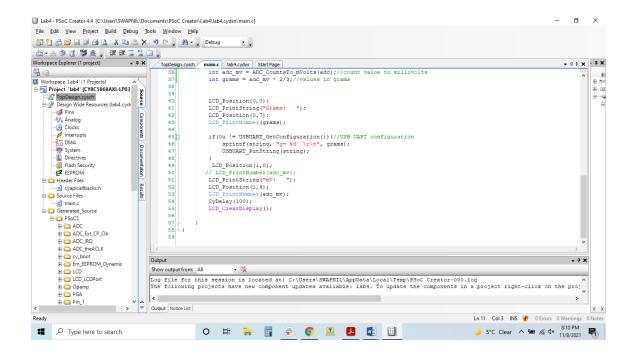
Figure 4: Screenshot of the Schematic

(D) Complete PSoC software. This software must include calls to all internal functions, appropriate comments, and functional code that you included. We will not grade you on the exact syntax and structure, as there are numerous ways to structure the code and still provide a force reading. Instead, we will grade you on the completeness of the code relative to using the appropriate PSoC functions to gather the necessary data.

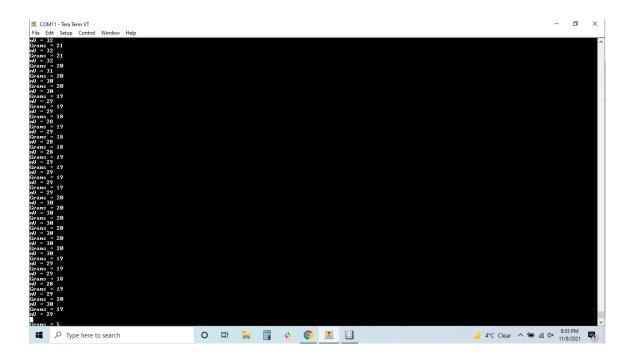
Along with this completed file, upload to Canvas a .zip file with the PSoC file main.c. This screen shot from PSoC shows where the file main.c is located in your main screen.

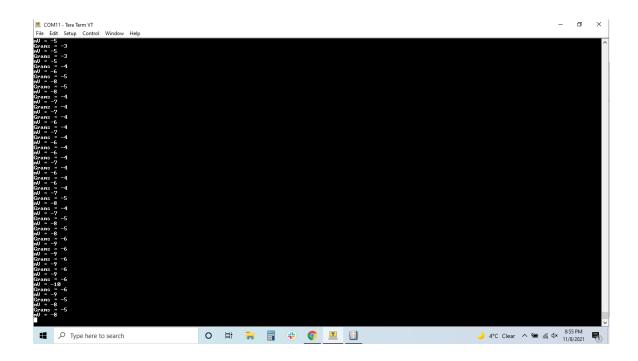


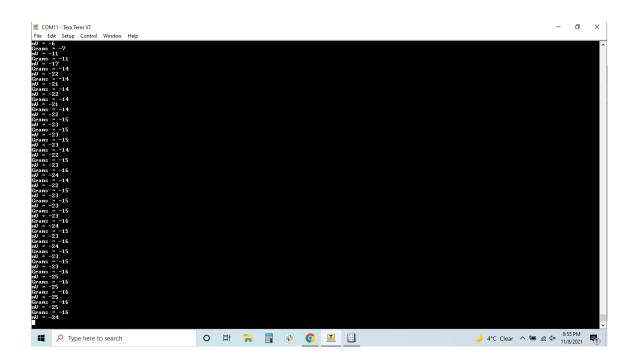


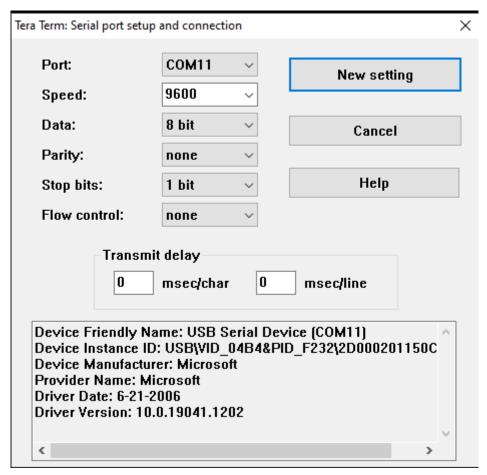


E) Place screenshots from your PC showing the Tera Term settings, as well as the display on the Tera Term screen of your force output of the bridge









Tera Term Serial Port Settings