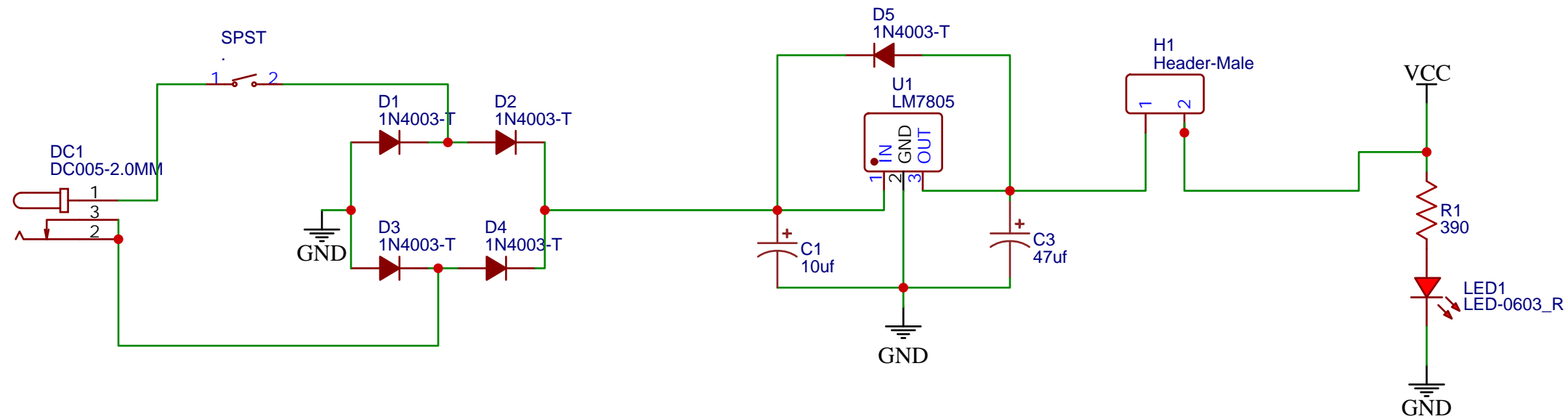
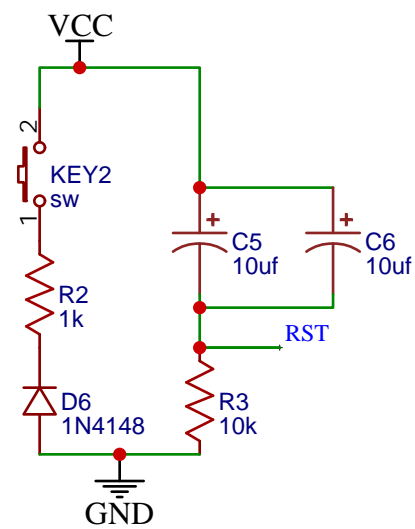


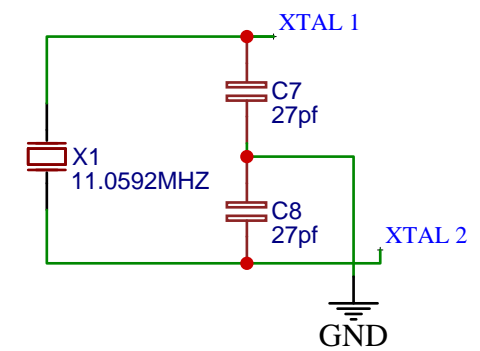
Power Supply Circuit



Reset Circuit

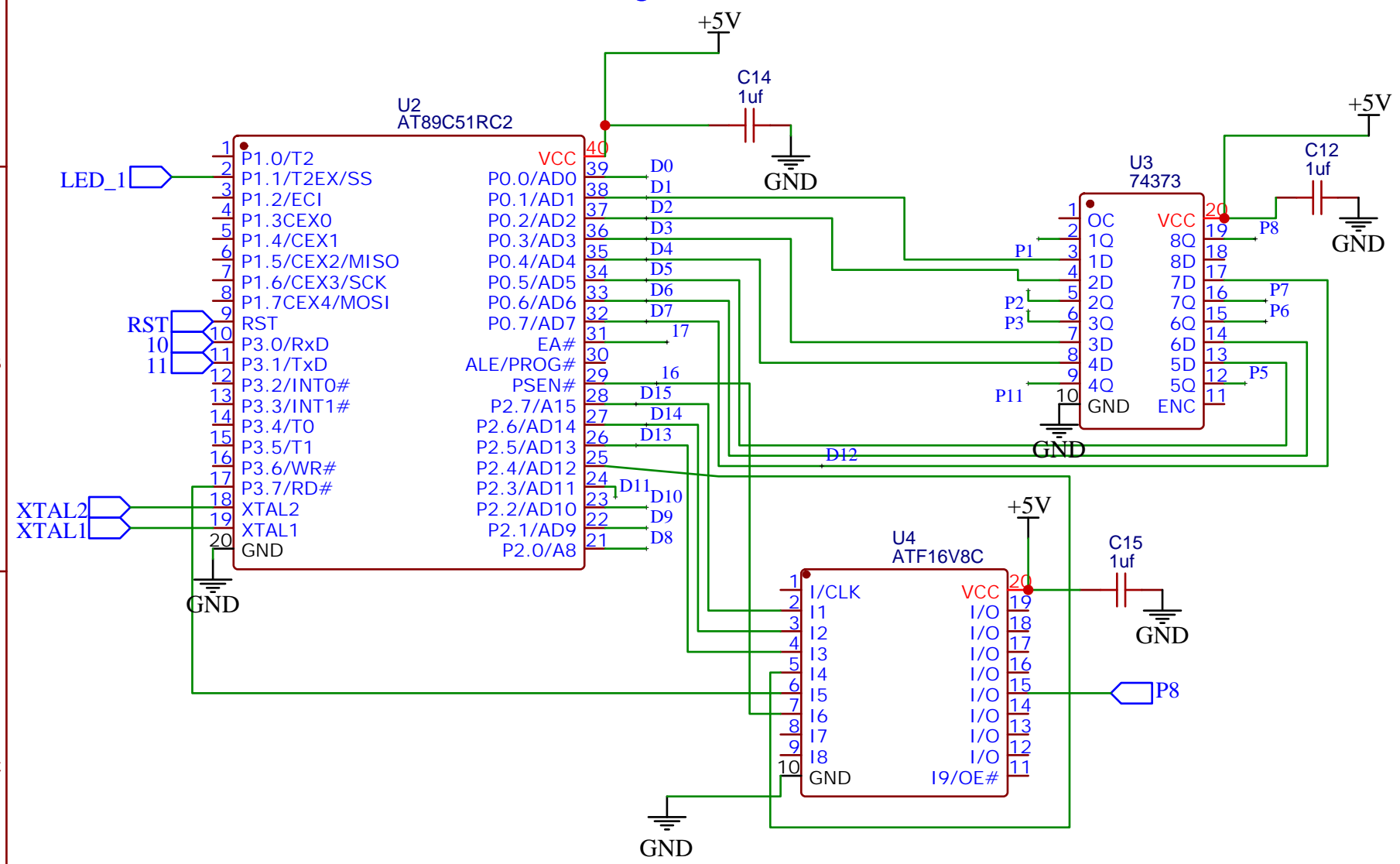


Oscillator Circuit

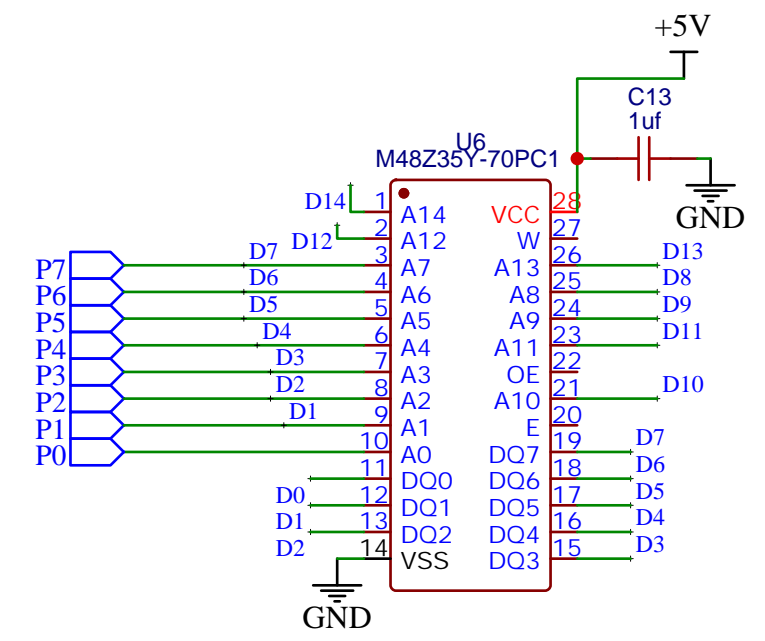


TITLE: ESD LAB 1		REV: 1.0
	Company: University of Colorado Boulder	Sheet: 1/1
	Date: 2022-01-27	Drawn By: swapnilghonge

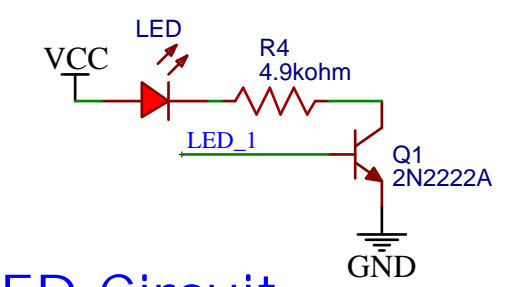
8051 Circuit with Memory Latch and SPLD IC



NVRAM



LED Circuit



TITLE: ESD LAB 2		REV: 1.0
Company: University of Colorado Boulder		Sheet: 1/2
Date: 2022-02-02		Drawn By: swapnilghonge

