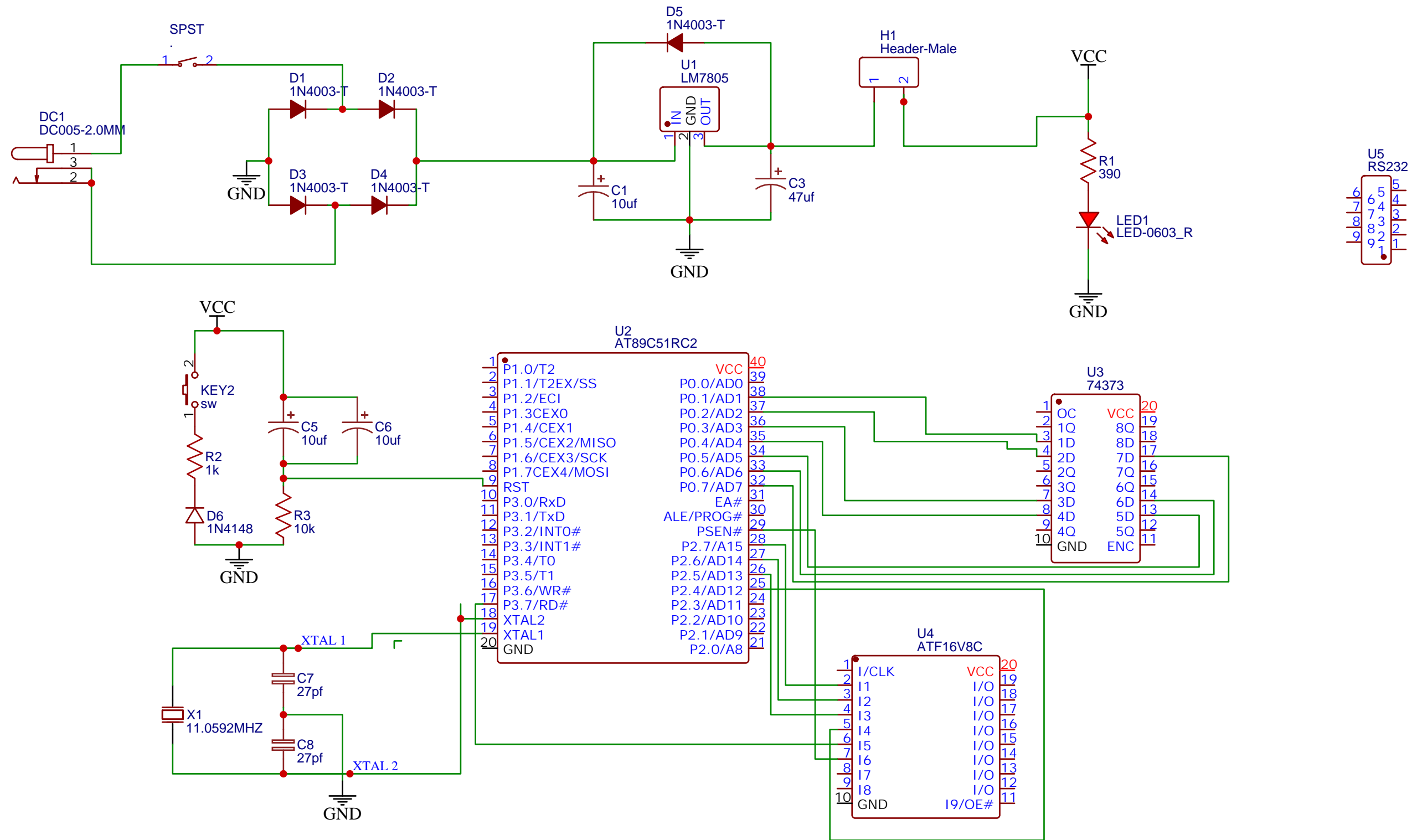


8051 Circuit with Memory Latch and SPLD IC



TITLE: ESD LAB 1		REV: 1.0
	Company: University of Colorado Boulder	Sheet: 1/1
	Date: 2022-01-27	Drawn By: swapnilghonge