

You will need to obtain the signature of your TA on the following items in order to receive credit.

The Part 1 & Part 2 Elements of Lab #1 should be completed and signed off by **Friday, Jan. 28, 2022** in order to give you time to complete the Part 3 Elements upon receipt of your parts kit. All signoffs are due by **Friday, Feb. 4, 2022**. You need to submit both of your signoff sheets and other required elements by **11:59pm Sunday, Feb. 6, 2022**. Labs completed after the signature due date or submitted after the submission due date will usually receive grade reductions, but there is leniency on Lab #1.

Print your name below and then demonstrate your working hardware/firmware in order to obtain the necessary signatures. All items must be completed to get a signature, but partial credit is given for incomplete labs. Receiving a signature on this signoff sheet does not mean that your work is eligible for any particular grade; it merely indicates that you have completed the work at an acceptable level.

**Student Name:** Swapnil Gholge

**Checklist**

- Student demonstrates detailed knowledge of a simulator (including changing register values, editing data memory, using breakpoints, single stepping, uses /overlay option, etc.)
- Student assembly program works correctly
- Student demonstrates detailed knowledge of WinCUPL and WinSim, logic equations correct

**Student Answers to Lab Questions**

1. **How many bytes of code space does your program require?**

(Show how you arrived at your answer.)

Code Size? 57

2. **How long did your program take to execute for X=0x12 and Y=0x0A? Assume an 11.0592 MHz clock and include the instructions executed from the beginning until you reach the ENDLOOP label. Show the TA your detailed calculations on the code listing during your signoff.**

Execution Time? 132 MS

**Instructor/TA Comments:**

**TA signature and date**

*Ghoshal* 01/28/22

<b>FOR INSTRUCTOR USE ONLY</b>	<input type="checkbox"/> Not Applicable	<input type="checkbox"/> Poor/Not Complete	<input type="checkbox"/> Meets Requirements	<input checked="" type="checkbox"/> Exceeds Requirements	<input type="checkbox"/> Outstanding
SPLD code	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Assembly Language Code Style	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Required Elements functionality	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Sign-off done without excessive retries	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Student understanding and skills	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Overall Demo Quality	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>

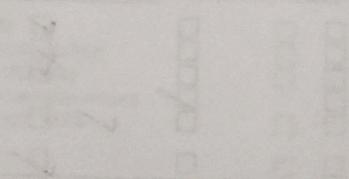
**Comments:**

NOTE: This submission sheet should be the top/first sheet of your submission.

### Lab 1 Part 1&2 Signoff

- Assembly code functionality pending
- + SPLD code correct and simulation works
- + Lab questions correctly answered.
- + Neat schematic with required components

20/09/10



Print your name below, answer the questions, and then demonstrate your working hardware in order to obtain the necessary signatures. All items must be completed to get a signature.

Student Name: Surphil George

Checklist

- Schematic of acceptable quality. Student name on board in permanent ink
- Pins and signals labeled, decoupling capacitors, and two 28-pin wire wrap sockets present on board
- Mounting hardware present (e.g. standoffs or an enclosure)
- Power switch and LED, voltage regulator functional, power jack present
- Power-on Reset (RC) and Run-time Reset (pushbutton), 8051 bypass cap is present
- RS-232 connector mounted, 74LS373 transparent latch wired
- Logic outputs correct (e.g. SPLD generation of /READ and /CSPERIPH; view SPLD code)
- Student displays good knowledge of oscilloscope
- Peak to peak noise measured across processor VCC and GND is < 800mV
- Oscillator functional (check for correct ALE/XTAL2 signals after power on-off cycles)
- ARM development board functional, student can demonstrate the basic software.

Student Answers to Lab Questions

1. What voltage is present at the regulator input? Use a digital multimeter. 7.79V
2. What voltage is present at the regulator output? Use a digital multimeter. 4.998V
3. What peak to peak noise is present across the processor VCC and GND? Use an oscilloscope.

Measured value at processor package pins on top side of board: 140mV

Measured value at wire wrap socket pins on bottom side of board: 160mV

4. How long is the processor held in reset after the run-time reset pushbutton is released? Use an oscilloscope and try to measure the time between the release of the pushbutton and the time when noise from ALE is observed on the RST signal.

Measured value: 422ms

5. What frequency is present at the ALE pin? Use an oscilloscope. 1.8432MHz

Instructor/TA Comments:

TA signature and date

P.Jahnavi

2/4/2022

<u>FOR INSTRUCTOR USE ONLY</u>	<input type="checkbox"/> Not Applicable	<input type="checkbox"/> Poor/Not Complete	<input checked="" type="checkbox"/> Meets Requirements	<input type="checkbox"/> Exceeds Requirements	<input type="checkbox"/> Outstanding
Schematics, SPLD code	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Hardware physical implementation	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Required Elements functionality	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Sign-off done without excessive retries	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Student understanding and skills	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Overall Demo Quality	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Comments:

(+) Neat schematic.

(+) Wire wrapping is done well.

NOTE: This submission sheet should be the second sheet of your submission.

(+) SPLD Code is fully functional

- (+) Demonstrated good understanding of CCS and ARM board
- (+) Can explore more features of the oscilloscope in order to get familiar with it

## Submission Sheet

Instructions: Print your name below and sign the honor code pledge. Separate the signoff and submission sheets from the rest of the lab and turn in a scan (or clear picture) of these signed forms, the items in the checklist below, and the answers to any applicable lab questions in order to receive credit for your work. No cover sheet please. **Submit all items electronically via Canvas to reduce paper usage. Canvas is <https://canvas.colorado.edu>.**

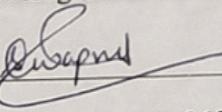
In addition to the items listed on the signoff checklist, be sure to review the lab for additional requirements for submission, including:

- Scan of signed and dated Part 1 signoff sheet as the top sheet (No cover sheet please)
- Scan of signed and dated Part 3 signoff sheet as the second sheet
- Scan of submission sheet with signed honor code pledge as the third sheet
- PDF of complete and accurate final schematic of acceptable quality (all components shown).
- Fully, neatly, and clearly commented assembly code.
- Clear high-resolution pictures of the top and bottom sides of your 8051 board.

Make copies of your code, SPLD code, and schematic files and save them as an archive.

**Student Name:** Swapnil Ghonge

**Honor Code Pledge:** "On my honor, as a University of Colorado student, I have neither given nor received unauthorized assistance on this work. I have clearly acknowledged work that is not my own."

**Student Signature:** 

1. **How much power is dissipated in the regulator, assuming a load current of 135mA? Assume that the regulator is drawing the max quiescent current shown in the data sheet (use the correct data sheet for the regulator you have on your board). Neatly show all your work.**

$$I_L = 135\text{mA}$$

$$V_{in} = 7.74\text{V}$$

$$V_{out} = 4.98\text{V}$$

$$I_Q = 5\text{mA} \text{ (acc. to datasheet)}$$

$\therefore$  Power dissipation

$$= (V_{in} - V_{out})(I_L + I_Q)$$

$$(7.74 - 4.98)(135 + 5)$$

$$= 0.38\text{W}$$

Calculated value: 0.38W

**Comments:**

NOTE: This submission sheet should be the third sheet of your submission.