

## Experiment no. 04

Title:
CUOA program for Addition of two large
Vectors and Matrix multiplication:

Objectives:

The objectives is to lunderstand how to write CUDA program for vector addition and Malrix multiplication. We will learn the basic Concept of parallel programming and how to use CUDA C to write program that can sun on GPUs.

Problem Statement:

We will be performing two operation

Using CUDA C. The first operation is vector is
addition, where will be adding two large

Vector. The Second operation is matrix

Multiplication, where we will be multiply a

two large matrices using CUDAC.

Slw Requirement:

1) CUDA TOOLkit 2) Clott Compiler 3) Any text editor or IDE



HIW Requirement: 1) A computer with a NVIDIA GOU that 2) 64 bit open source Linux or window or its derivatives Minimum 4/8 gb RAM. Theory! CUDA Toxogramming Model:

The CUDA programming

Model is designed to exploit the parallelism

in GIPU architectures to accelerate the execution of computation. The CUDA platforms
provides a programming model that allow
divelopers to write C++ program with
additional beywood and construct to express parallelism CUDA Kernal are the parallel functions that execute on the GPV. Each kernel is executed by many threads in parallel Where each thread per perform the Same computation but on different data elements.



2)	Vector Addition:
	Vector addition is the process
	of adding two or more vectors to obtains
	a new vector. In CUDA program we
	can use parallel computing to perform
	vector addition on large vectors. Each
	thread in a cyph kernel is responsible
_	too adding a surgle element of the vector.
	The general algorithm for vector addition is as follow.
	is as follow.
\	Λ: 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1-
1)	Initalize two Vector, A and B, With random
-1	Value.
	Allocate memory on the GPU for rector A&B
4)	Copy vector A &B from the GAD CDU to GAD. Launch a Kernal Function on the GAD to
	perform the vector addition
• 5)	Copy the result vector from the Crpv to
	the CPO
6)	Free the memory allocated on the GPU.
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3)	Matrix Multiplication!
	Matrix Multiplication in the
	process of multiplying two matrices to
	process of multiplying two matrices to obtain a new matrix. In CUPA, we can
	Use parallel Computing to perform matix multiplication on large matrices.
	multiplication on large matrices.
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The element of the resulting matrix is coloulated by multiplying the
calculated by multiplying the
Marices Pach thread in a CODA Kernel
matrices. Rach thread in a CUPA Kernel
is responsible for computing a lengte.
dement of the output matrix. The
general algorithm for malrix multiplicators is as follow.
in as follow.
Initialize two matrices, A and B, with
Halidam Value.
Allocate memony on the CAPO for matrices
A and B.
Copy matrices A and B from the CDO
11 1. 014 4. 0. 0.
Launch a Kernel function on the GPU to perform the matrix multiplication Copy the result matrix from the GPU
to perform the makix multiplication
Copy the result makix from the GPU
tocpo
Free the memory allocated on the GPU.
Conclusion:
we learned how to implement
Vecho addition and malors muly
multiplication using COBJA CUBAC.
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