

Swarup Majumder

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EDUCATION

University of Illinois at Urbana-Champaign <i>Bachelors of Science in Computer Engineering</i> Relevant Coursework: Data Structures and Algorithms (C++), Operating Systems (C , RISC-V , UART), FPGA Lab (SystemVerilog , AXI , SPI), GPU Parallel Programming (CUDA), Digital and Analog Signal Processing	GPA: 3.74/4.00 <i>Graduation: May 2027</i>
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EXPERIENCE

Undergraduate Teaching Assistant <i>University of Illinois at Urbana-Champaign</i> <ul style="list-style-type: none">ECE 220: Computer Systems Programming<ul style="list-style-type: none">Mentored 30+ students weekly, providing technical feedback for C, C++, and Assembly machine projectsECE 385: Digital Systems (FPGA) Laboratory<ul style="list-style-type: none">Assisted students with FPGA-based SystemVerilog designs with FSMs, VGA control, and SOC integrationDebugged timing, synthesis, and simulation issues using Vivado and on-board instrumentation	Aug 2025 – Present <i>Champaign, IL</i>
Undergraduate Research Assistant <i>Parasol Lab, University of Illinois at Urbana-Champaign</i> <ul style="list-style-type: none">Benchmarked C++ STAPL and STL algorithm runtimes across 64 samples (99% Confidence Interval)Enabled multi-GPU STAPL execution using C++ and CUDA, achieving 100x speedup over CPU baselineStreamlined compilation, linking and execution for 25+ MPI configurations via Bash and Linux toolchainsAutomated data visualization using Python to graph runtime metrics, accelerating algorithm analysis	May 2025 – Present <i>Champaign, IL</i>
Robotics Software Mentor <i>FIRST Robotics Competition Team 4096</i> <ul style="list-style-type: none">Utilized Python APIs to interface with the CAN bus and reliably control 30+ motors across 8 subsystemsAuthored structured Markdown technical documentation for software onboarding within a 40+ student teamDeployed computer vision AI object detection to provide operator feedback, reducing cycle times by 30%Guided students in implementing a RUBIK-Pi based localization system, integrating camera-based pose estimation	Jun 2024 – Present <i>Champaign, IL</i>

PROJECTS

Camera Vision Pipeline on FPGA <i>SystemVerilog, RTL Design, I2C, Vivado</i> <ul style="list-style-type: none">Designed a pure RTL real-time camera pipeline with microsecond-scale frame latency on Spartan-7 FPGAInitialized 40 camera registers via I2C protocol with a custom SystemVerilog controller for stable color outputImplemented filters and UV thresholding in SystemVerilog with line buffers for seamless edge detectionIntegrated 7-bit Vivado XADC IP-based brightness dial with 128 discrete levels for precise luminance scaling	Nov 2025 – Dec 2025
GPT-2 Transformer Model <i>C++, CUDA, Bash, Linux, Slurm</i> <ul style="list-style-type: none">Accelerated matmul and layernorm CUDA kernels with tensor cores and reduction trees, achieving 30x throughputBuilt Bash sweep script to evaluate 80+ kernel configurations, optimizing inference throughput on Slurm clustersLeveraged CUDA constant memory for weights/biases, reducing global memory traffic and forward pass latency	Oct 2025 – Dec 2025
Interactive Thermometer <i>Oscilloscope, Multimeter, Circuit Design</i> <ul style="list-style-type: none">Built a temperature measurement circuit with op-amps and comparators, supporting a range of 0–99 CDesigned an analog-to-digital stage to drive 7-segment displays showing temperature and user thresholdsVerified signal integrity, temperature, and threshold accuracy using an Oscilloscope and Multimeter	Feb 2025 – May 2025
24-Hour Clock <i>Arduino, C++, Circuit Design</i> <ul style="list-style-type: none">Built an Arduino-based 24-hour clock using C++ to display time with precise minute-level accuracyImplemented multiplexed control for 4x7-segment displays, reducing I/O to 11 pins with 4 ms cyclesIntegrated a potentiometer interface converting analog input to time values for real-time calibration	Feb 2025

SKILLS

Programming: C, C++, SystemVerilog, Verilog, Python, CUDA, Bash, Java
Protocols/Interfaces: I2C, UART, SPI, CAN, AXI-4 Lite
Tools: Linux, git, gdb, Vivado, MPI, NVIDIA NSight, Slurm
Hardware/Equipment: Oscilloscope, Logic Analyzer, Function Generator, Multimeter, Soldering Iron