College Station, Texas
Jan 2, 2018

To whomsoever it may concern,

I am a passionate Computer Engineering grad at Texas A&M University. My interests are as follows:

1. VLSI
2. Computer architecture

I developed my interest in VLSI while I was working on my undergrad projects. Eventually, I joined Texas Instruments, Bangalore, India as a Design Engineer. Over 3 years, I had the opportunity to work on different phases of design cycle, ranging from architecture analysis to gate level simulations. During this period, I developed good simulation debug skills.

I am proficient in Verilog and good at C++ programming. Here is my personal web page outlining my projects:
https://swathic5.github.io/
https://github.com/swathic5

Given an opportunity, I believe I can contribute my best by putting my Master's knowledge and past work experience into effect.