

# Quiz 1

**Due** Sep 15 at 1:05pm **Points** 20 **Questions** 4  
**Available** until Sep 15 at 1:10pm **Time Limit** 20 Minutes

This quiz was locked Sep 15 at 1:10pm.

## Attempt History

	Attempt	Time	Score
LATEST	<a href="#">Attempt 1</a>	15 minutes	19 out of 20

❗ Correct answers are hidden.

Score for this quiz: **19** out of 20

Submitted Sep 15 at 12:57pm

This attempt took 15 minutes.

Incorrect

### Question 1

5 / 5 pts

Provide 2 of the 4 reasons for designing embedded systems from an Architectural POV (point of view):

allows non technical

enables reuse of co

**Answer 1:**

allows non technical people to estimate the risks of the project

**Answer 2:**

enables reuse of components

Incorrect

**Question 2****5 / 5 pts**

Name 2 standards organizations that are concerned with **safety** of electronics products.

1. Underwriter's labora

2. Factory mutual

**Answer 1:**

Underwriter's laboratory/Canadian Standards Association

**Answer 2:**

Factory mutual

Partial

**Question 3****4 / 5 pts**

Circle, True or false about the ARM Cortex M0+

☒ It has a register bank of at least 16 32-bit registers T F

☒ It has a Link Register used to store the return addresss of a subroutine or function call. T F

☒ On reset the processor loads the program counter (PC) with the value of the rest vector at 0x0000. T F

☐ It is a Harvard Architecture CPU. T F

☒ It is a RISC Architecture CPU. T F

#### Question 4

5 / 5 pts

Indicate which are valid blocks in the Cortex-M0+ Block Diagram

☒ Memory Protection Unit

☐ VME Bus

☐ MMU

☒ Micro Trace Buffer

☒ Debug Access Port

Quiz Score: **19** out of 20