

FGPIO Peripheral Access
Layer

FGPIO Register Masks



```
graph LR; A[FGPIO Register Masks] --> B[FGPIO Peripheral Access Layer]
```

The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'FGPIO Peripheral Access Layer' on two lines. The box on the right is gray with a black border and contains the text 'FGPIO Register Masks' on one line. A black arrow points from the right side of the gray box to the left side of the white box, indicating a dependency or relationship between the two components.