Kuan-Hsun "Sway" Wang

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Experience

Synopsys Inc. / R&D Engineer, Sr I
July 2015 - Present

Hsinchu, Taiwan

- Develop and maintain Synopsys Verdi Power-Aware Debug tool
- Develop a unified UPF parser for all Synopsys tools
- Modifying existing Synopsys low power products for customer needs
- Improve existing low power verification flow/methodology
- Write software design specifications and develop testcases
- Collaborate across R&D teams and participate in code reviews/discussions

Pixart Imaging Inc. / Sr. Software Engineer
September 2011 - July 2015

Hsinchu, Taiwan

- Developed and maintained firmware for HID products
- FPGA/ASIC/SOC verification
- Improved the touch algorithm and estimated the performance (US patent)
- Designed and developed desktop GUI applications using Python

Education

National Tsing Hua University
Master Degree / Computer Science
September 2009 - August 2011

Hsinchu, Taiwan

- Thesis: A Lightweight Debugging Tool for Resource Constrained Wireless Sensor Platforms

National Chung Cheng University
Bachelor Degree / Computer Science
September 2005 - July 2009

Chiayi, Taiwan

Proficiencies

- C/C++, Python, Embedded Systems, OOP, Design Pattern, Verilog/VHDL, Low power verification, Synopsys Verdi/VCS

Selected Award

Telecom Innovation & Application Contest 2010
First Prize
Work: Home appliance controlling system integrated with
iPhone and sensor network

Chunghwa Telecom

Embedded System Contest 2011
Honorable Mention

Ministry of Education, Taiwan

Selected Patents

- Devices and methods for appliance usage scheduling and control using on-line calendar services with open API (United States US20120166010)
- Optical touch system (United States US20140176964 A1)