Integration Manual ADC

Table of Contents

[1 Dependencies 2](#_Toc355090013)

[1.1 SWCs 2](#_Toc355090014)

[1.2 Configuration Files to be provided by Integration Project 2](#_Toc355090015)

[1.3 Functions to be provided to Integration Project 2](#_Toc355090016)

[2 Configuration 3](#_Toc355090017)

[2.1 Build Time Config 3](#_Toc355090018)

[2.2 Generator Config 3](#_Toc355090019)

[3 Integration 4](#_Toc355090020)

[3.1 Global Data 4](#_Toc355090021)

[3.2 Component Conflicts 4](#_Toc355090022)

[3.3 Include Path 4](#_Toc355090023)

[3.4 Configurator Changes 4](#_Toc355090024)

[4 Runnable Scheduling 5](#_Toc355090025)

[5 Memory Mapping 6](#_Toc355090026)

[5.1 Mapping 6](#_Toc355090027)

[5.2 Usage 6](#_Toc355090028)

[6 Revision Control Log 7](#_Toc355090029)

# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
|  |  |

## Configuration Files to be provided by Integration Project

NOTE:

For Projects using 33E, make sure “D\_ADC1CURRENTMODE\_ULS\_LGC” is defined Adc\_Cfg.h file.

For Projects using 33C, make sure “D\_ADC1CURRENTMODE\_ULS\_LGC” is **NOT** defined Adc\_Cfg.h file.

Template for Configuration file:

Adc\_Cfg.h



Adc2\_Cfg.h



## Functions to be provided to Integration Project

Adc2\_ReadConversion

Adc2\_Init1

Adc2\_StartGroupConversion

Adc2\_EnableGroupNotification

Adc\_Init\_FixedCfg

Adc\_StartGroupConversion

Adc\_ReadGroup

Adc\_GetGroupStatus

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Generator Config

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Global Data

None

## Component Conflicts

IoHwAb

## Include Path

$PROJECTPATH$\Adc\include

Adc.h, Adc2.h, Adc\_Common.h

## Configurator Changes

None

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| Adc\_Init() | Before **Adc2\_Init1** initialization | ECU Startup |
| Adc2\_Init1() | Before **PWMCdd** initialization | ECU Startup |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| Adc\_StartGroupConversion | None | ISR |

\*Note:

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| ADC2\_START\_SEC\_CODE |  |  |
| ADC2\_START\_SEC\_CONST\_32 |  |  |
| ADC\_START\_SEC\_CONST\_32 |  |  |
| ADC\_START\_SEC\_CODE |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| <Memmap usuage info> |  |  |

Table 1: ARM Cortex R4 Memory Usage

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 09-Apr-13 | Selva |