Integration Manual – BatteryVoltage

Table of Contents

[1 Dependencies 2](#_Toc353976525)

[1.1 SWCs 2](#_Toc353976526)

[1.2 Configuration Files to be provided by Integration Project 2](#_Toc353976527)

[1.3 Functions to be provided to Integration Project 2](#_Toc353976528)

[2 Configuration 3](#_Toc353976529)

[2.1 Build Time Config 3](#_Toc353976530)

[2.2 Generator Config 3](#_Toc353976531)

[3 Integration 4](#_Toc353976532)

[3.1 Global Data 4](#_Toc353976533)

[3.2 Component Conflicts 4](#_Toc353976534)

[3.3 Include Path 4](#_Toc353976535)

[3.4 Configurator Changes 4](#_Toc353976536)

[4 Runnable Scheduling 5](#_Toc353976537)

[5 Memory Mapping 6](#_Toc353976538)

[5.1 Mapping 6](#_Toc353976539)

[5.2 Usage 6](#_Toc353976540)

[5.3 NvM Blocks 6](#_Toc353976541)

[6 Revision Control Log 7](#_Toc353976542)

# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

## Configuration Files to be provided by Integration Project

None

## Functions to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Generator Config

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Global Data

None

## Component Conflicts

None

## Include Path

None

## Configurator Changes

None

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| None | None | None |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| BatteryVoltage\_Per1 | None | RTE (2 ms) |
| BatteryVoltage\_Per2 | None | RTE (4 ms) |

\*Note: ECU Voltage Determination sub-function (BatteryVoltage\_Per1) must execute prior to the Battery Overvoltage Monitoring Sub-function (ADC ISR).

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| RTE Memory Mapping |  |  |
|  |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| Full driver |  |  |

Table 1: ARM Cortex R4 Memory Usage

## NvM Blocks

|  |
| --- |
| Block Name |
| OvervoltageData |

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 16-Apr-13 | Jared |