Integration Manual –Stability Control Torque Overlay(StbCTO)

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# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be referred. Developer should track the references.

## Global Functions(Non RTE) to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Configuration Files to be provided by Integration Project

Ap\_StbCTO\_Cfg.h generated by Ap\_StbCTO\_Cfg.h.tt

### Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| Parameter | Notes | SWC |
| None |  |  |

### DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| ISR Name | VIM # | Priority Dependency | Notes |
| None |  |  |  |

### Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Required Global Data Inputs

DSTEnableRqst\_Cnt\_lgc

DSTErrCntrRecvLevel\_Cnt\_u08

DSTExtSystemFltActive\_Cnt\_lgc

DSTFuncPresent\_Cnt\_lgc

DSTSlewComplete\_Cnt\_lgc

DSTTOCState\_Uls\_enum

DSTTrqOvCmdRqst\_HwNm\_f32

DSTTrqOvRqstValid\_Cnt\_lgc

DSTRevGearValid\_Cnt\_lgc

DiagRedAssistLowVoltageActive\_Cnt\_lgc

DiagStsNonRecRmpToZeroFltPres\_Cnt\_lgc

DiagStsRecRmpToZeroFltPres\_Cnt\_lgc

LimitPercentFiltered\_Uls\_f32

TOEOLDisable\_Cnt\_lgc

TrqOvReverseGearEngage\_Cnt\_lgc

VehicleSpeedValid\_Cnt\_lgc

VehicleSpeed\_Kph\_f32

## Required Global Data Outputs

DSTActive\_Cnt\_lgc

DSTSlewStart\_Cnt\_lgc

DSTState\_Cnt\_u08

## Specific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| StbCTO\_Init1 | Called from RTE before any call to the periodic functions | RTE init |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| StbCTO\_Per1 | None | RTE 2ms |

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| STBCTO\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |  |  |
| STBCTO\_START\_SEC\_VAR\_NOINIT\_32 |  |  |
| STBCTO\_START\_SEC\_VAR\_CLEARED\_32 |  |  |
| STBCTO\_START\_SEC\_VAR\_CLEARED\_8 |  |  |
| STBCTO\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| <Memmap usuage info> |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| Block Name |
| <NVM block used Non RTE functions > |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| Block Name |
| <NVM block used in RTE functions > |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

<Define all the preprocessor Macros needed and conditions when needed>.

## Optimization Settings

<Define Optimization levels that are needed and conditions when needed>.

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 3-Oct-13 | MR |
| 2 | Updated to CF-08B v005 | 30-Jan-14 | VT |