Integration Manual – Thermal Duty cycle

Table of Contents

[1 Dependencies 2](#_Toc352923196)

[1.1 SWCs 2](#_Toc352923197)

[1.2 Configuration Files to be provided by Integration Project 2](#_Toc352923198)

[1.3 Functions to be provided to Integration Project 2](#_Toc352923199)

[2 Configuration 3](#_Toc352923200)

[2.1 Build Time Config 3](#_Toc352923201)

[2.2 Generator Config 3](#_Toc352923202)

[3 Integration 4](#_Toc352923203)

[3.1 Global Data 4](#_Toc352923204)

[3.2 Component Conflicts 4](#_Toc352923205)

[3.3 Include Path 4](#_Toc352923206)

[3.4 Configurator Changes 4](#_Toc352923207)

[4 Runnable Scheduling 5](#_Toc352923208)

[5 Memory Mapping 6](#_Toc352923209)

[5.1 Mapping 6](#_Toc352923210)

[5.2 Usage 6](#_Toc352923211)

[6 Revision Control Log 7](#_Toc352923212)

# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

## Configuration Files to be provided by Integration Project

None

## Functions to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Generator Config

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Global Data

None

## Component Conflicts

None

## Include Path

None.

## Configurator Changes

None

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| ThrmlDutyCycle\_ \_Per1 | None | RTE (100ms) |

\*Note:

Proper Initialization of input signals should occur before running each function for the first time. **(ThrmlDutyCycle\_Init1 ).**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| RTE Memory mapping |  |  |
|  |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| Full driver |  |  |

Table 1: ARM Cortex R4 Memory Usage

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 25-Mar-13 | Selva |