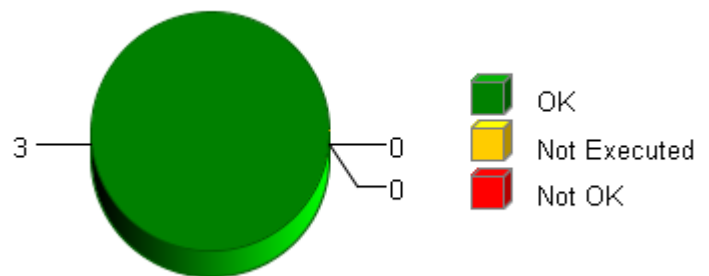


Summary

Total Test Objects: 3
Successful: 3
Failed: 0
Not Executed: 0
Date: 2015-01-08
Time: 10:35:56+0530

Overall Test Object Results (including Coverage)



Selected Project Items

Test Object "CBD_UnitTest/MtrVel2/MtrVel2_Init"
Test Object "CBD_UnitTest/MtrVel2/MtrVel2_Per1"
Test Object "CBD_UnitTest/MtrVel2/MtrVel2_Per2"

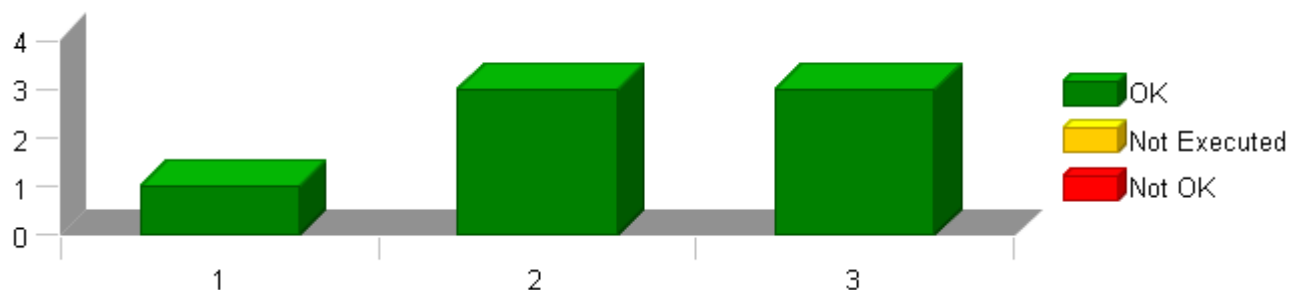
Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

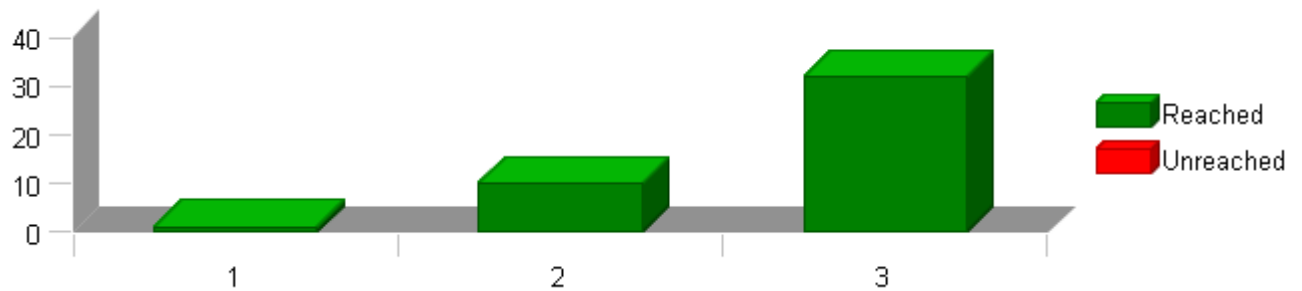
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No
Instrumentation: Test Object Only
Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)



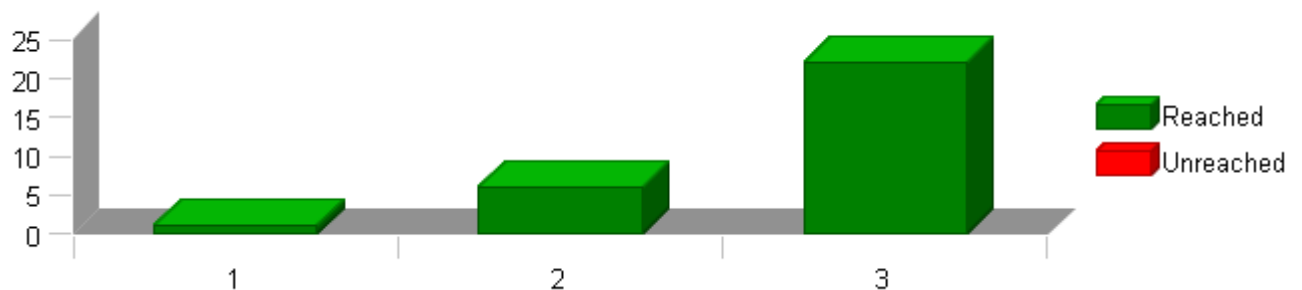
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



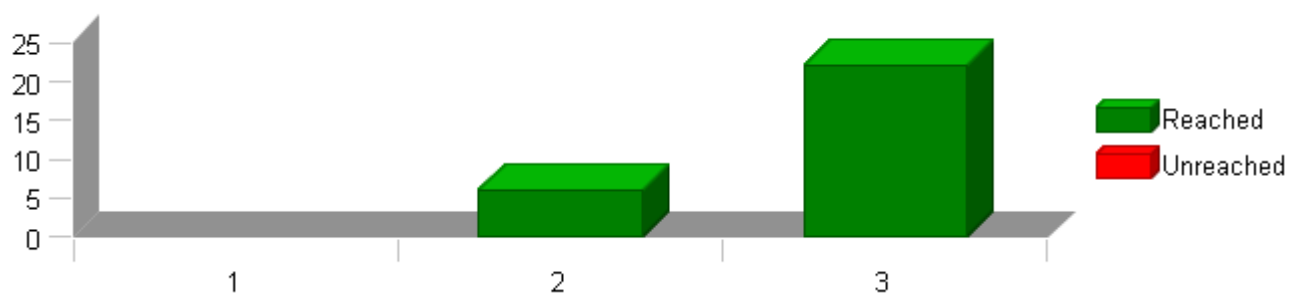
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

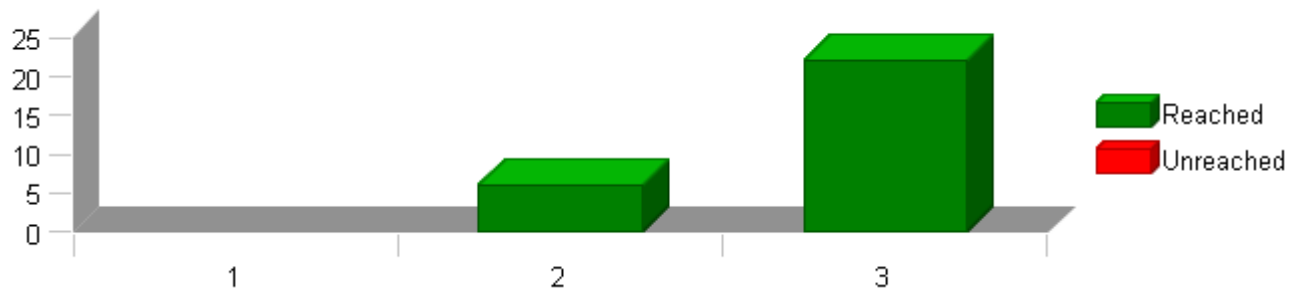
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

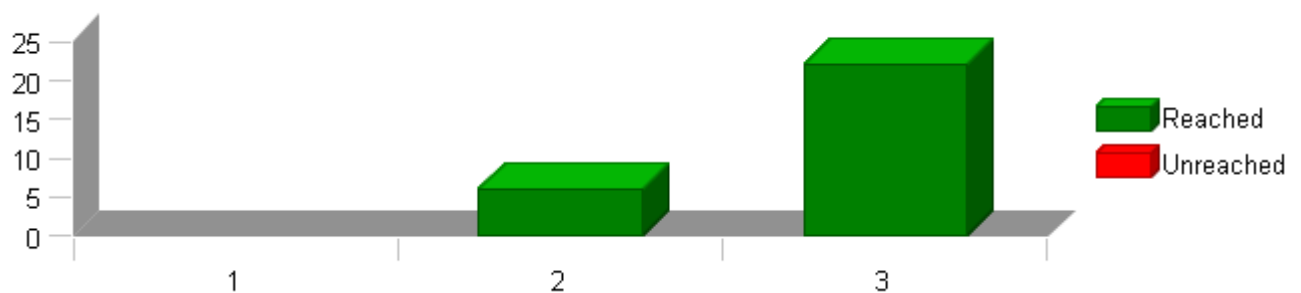
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	MtrVel_Digi	100 %	100 %	100 %	100 %	100 %	7 of 7 passed	✓
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	7 of 7 passed	✓
	MtrVel2	100 %	100 %	100 %	100 %	100 %	7 of 7 passed	✓
1	MtrVel2_Init	100 %	100 %	-	-	-	1 of 1 passed	✓
2	MtrVel2_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
3	MtrVel2_Per2	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓

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MtrVel2_Per1



Project	MtrVel_Digi
Module	MtrVel2
Test Object	MtrVel2_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
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TEST DETAILS REPORT

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MtrVel2_Per1



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy__Work__Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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MtrVel2_Per1

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:
	TS1.1 1220.00 Cycles TS1.2 1140.00 Cycles
Description	Vector Description: TS1.1Longest path :(SysC_diagDeltaTime_Sec_f32 > FLT_EPSILON)=False TS1.2Shortest(SysC_diagDeltaTime_Sec_f32 > FLT_EPSILON)=True

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.56015372		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-350		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	0		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0	0 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-6.10865211	-6.10865259 ± 0.000017	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-4834.40283	-4834.40283 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	0	0 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-4834.40283	-4834.40283 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	65454564		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.35468006		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	4.36589003		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-150.326004		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.200000003		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	20.3649998		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0.355436295	0.355436295	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1855.05811	1855.05811 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1855.05811	1855.05811 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

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MtrVel2_Per1



Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS2.1 1264.00 Cycles
TS2.2 1174.00 Cycles
TS2.3 1140.00 Cycles
TS2.4 1140.00 Cycles
TS2.5 1140.00 Cycles
TS2.6 1140.00 Cycles
TS2.7 1140.00 Cycles
TS2.8 1157.00 Cycles
TS2.9 1163.00 Cycles
TS2.10 1163.00 Cycles
TS2.11 1163.00 Cycles
TS2.12 1163.00 Cycles
TS2.13 1163.00 Cycles
TS2.14 1163.00 Cycles
TS2.15 1163.00 Cycles
TS2.16 1162.00 Cycles
TS2.17 1162.00 Cycles
TS2.18 1163.00 Cycles
TS2.19 1162.00 Cycles
TS2.20 1163.00 Cycles
TS2.21 1163.00 Cycles
TS2.22 1163.00 Cycles
TS2.23 1163.00 Cycles
TS2.24 1163.00 Cycles
TS2.25 1163.00 Cycles
TS2.26 1163.00 Cycles
TS2.27 1163.00 Cycles
TS2.28 1134.00 Cycles
TS2.29 1163.00 Cycles
TS2.30 1163.00 Cycles
TS2.31 1163.00 Cycles
TS2.32 1162.00 Cycles
TS2.33 1163.00 Cycles

Description Vector Description

TS2.1All Min
TS2.2All Max
TS2.3AsstAssemblyPolarity_Cnt_s08 = Min
TS2.4AsstAssemblyPolarity_Cnt_s08 = Max
TS2.5CumMechMtrPosMRF_Deg_f32 = Min
TS2.6CumMechMtrPosMRF_Deg_f32 = Max
TS2.7CumMechMtrPosMRF_Deg_f32 = Zero
TS2.8CumMechMtrPosMRF_Deg_f32 = Pos
TS2.9CumMechMtrPosMRF_Deg_f32 = Neg
TS2.10MechMtrPos1Timestamp_USec_u32 = Min
TS2.11MechMtrPos1Timestamp_USec_u32 = Max
TS2.12MechMtrPos1Timestamp_USec_u32 = Pos
TS2.13MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32 = Min
TS2.14MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32 = Max
TS2.15MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32 = Zero
TS2.16MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32 = Pos
TS2.17MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32 = Neg
TS2.18MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Min
TS2.19MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Max
TS2.20MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Zero
TS2.21MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Pos
TS2.22MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Neg
TS2.23MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Min
TS2.24MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Max
TS2.25MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Zero
TS2.26MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Pos
TS2.27MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Neg
TS2.28k_GearRatio_Uls_f32 = Min
TS2.29k_GearRatio_Uls_f32 = Max
TS2.30k_GearRatio_Uls_f32 = Pos
TS2.31MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32 = Min
TS2.32MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32 = Max
TS2.33MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32 = Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-6.28318548
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-360
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	0
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32

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MtrVel2_Per1

Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0	0 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-6.28318548	-6.28318548	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-0	0 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	0	-4.4408921e-013 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-0	0 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0	-4.4408921e-013 ± 0.0625	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	4.2949673e+009			
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	6.28318548			
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_GearRatio_Uls_f32	1			
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	360			
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	4294967295			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	4294.96729	4294.96729 ± 0.0625	✓	
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	6.28318548	6.28318548	✓	
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	0	4.4408921e-013 ± 0.0625	✓	
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	0	4.4408921e-013 ± 0.0625	✓	
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	0	4.4408921e-013 ± 0.009	✓	
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0	4.4408921e-013 ± 0.0625	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	119304648			
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.14104199			
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	2.33333993			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	100.236			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_GearRatio_Uls_f32	0.100000001			
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1			
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	10.1350002			
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✓	
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0.176889122	0.176889122	✓	
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✓	
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1658.96545	1658.96558 ± 0.0625	✓	
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓	
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1658.96545	1658.96558 ± 0.0625	✓	

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MtrVel2_Per1

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	65454564		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.35468006		
MtrVel2_SysChWVelCRF_HwRadpS_M_f32	4.36589003		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-150.326004		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.20000003		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	20.3649998		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0.355436295	0.355436295	✔
MtrVel2_SysChWVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1855.05811	1855.05811 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1855.05811	1855.05811 ± 0.0625	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	334853760		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.35059786		
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	12.4960899		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1150.98572		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.300000012		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-360		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-6.28318548	-6.28318548	✔
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-4816.8916	-4816.8916 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-4816.8916	-4816.8916 ± 0.0625	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

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MtrVel2_Per1

Test Step 2.6 (Repeat Count = 1) ✓

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	442603488
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.45537591
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	14.5286398
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	25.6350002
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.400000006
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	360
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	6.28318548	6.28318548	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1413.90466	1413.90479 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1413.90466	1413.90479 ± 0.0625	✓

T ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1) ✓

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	550353280
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.56015372
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	16.5611897
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-25.3619995
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.5
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	0
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0	0	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1780.07678	-1780.0769 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1780.07678	-1780.0769 ± 0.0625	✓

T ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	658103040
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-4.85014534
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-2.33658004
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	75.4250031
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.600000024

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MtrVel2_Per1

Name	Input Value
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-91.9749985
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-1.60526657	-1.60526657	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1622.43933	1622.43933 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1622.43933	1622.43933 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.9 (Repeat Count = 1) ✓

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	765852736
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.06378317
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-4.5875001
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	175.987
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.699999988
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	102.205002
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	1.78381383	1.78381371	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	3423.79834	3423.79858 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	3423.79834	3423.79858 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

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MtrVel2_Per1

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	873602496
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.277421
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-6.83841991
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	275.98349
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.800000012
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-59.3484993
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	0
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0	0 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-1.03582668	-1.03582668	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	2120.79712	2120.79712 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	2120.79712	2120.79712 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	981352256
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.49105883
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-9.08934021
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	375.365387
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.899999976
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-69.0208969
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	4294967295
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	4294.96729	4294.96729 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-1.20464194	-1.20464194	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	2143.2085	2143.2085 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	2143.2085	2143.2085 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1.23456794e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.70469666
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-11.3402596
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	475.644653
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.0221120007

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MtrVel2_Per1

Name	Input Value		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-78.6932983		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2142433142		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2142.43311	2142.43311 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-1.37345719	-1.37345719	✔
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	2165.61963	2165.61987 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	2165.61963	2165.61987 ± 0.0625	✔

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.13 (Repeat Count = 1)				✓
Name		Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		3.21456794e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		-6.28318548		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		-13.5911798		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		575.626404		
Rte_Inst_Sa_MtrVel2		tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32		0.0221200008		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value		-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value		-88.3656998		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value		1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name		Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		1234.56799	1234.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		-1.54227245	-1.54227245	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		-42	-42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		2370.4563	2370.45654 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value		-42	-42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value		2370.4563	2370.45654 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

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MtrVel2_Per1

Test Step 2.14 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3.21456794e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	6.28318548
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-15.8421001
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	675.608215
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.400000006
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-98.0381012
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-1.7110877	-1.71108758	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-3997.13647	-3997.13647 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-3997.13647	-3997.13647 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2.65478963e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	12.4960899
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	775.589966
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.5
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-107.710503
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-1.87990284	-1.87990284	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-939.951355	-939.951416 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-939.951355	-939.951416 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1.23456794e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.14104199
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	14.5286398
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	875.571716
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.785253346

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MtrVel2_Per1

Name	Input Value		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-117.382896		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamP_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-2.04871798	-2.04871798	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-2594.87988	-2594.88013 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-2594.87988	-2594.88013 ± 0.0625	✔

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.17 (Repeat Count = 1)				✓
Name		Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		3.21456794e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		-3.14104199		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		16.5611897		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		975.553528		
Rte_Inst_Sa_MtrVel2		tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32		0.0261199996		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value		-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value		-127.055298		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value		2654789515		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name		Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		2654.78955	2654.78955 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		-2.21753335	-2.21753335	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		-12.0610218	-12.0610247 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		461.754303	461.754395 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value		-12.0610218	-12.0610247 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value		461.754303	461.754395 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

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MtrVel2_Per1

Test Step 2.18 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2.65478963e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.14104199		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	18.5937405		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.0271199998		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	194.274994		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.39073825	3.39073849	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	3.38588095	3.3858819 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	124.848122	124.848152 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	3.38588095	3.3858819 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	124.848122	124.848152 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2.70534835e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.24582005		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	20.6262894		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.0281199999		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	204.505005		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.56928563	3.56928563	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-4.54792595	-4.54792595 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	161.732788	161.732788 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-4.54792595	-4.54792595 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	161.732788	161.732788 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2.81309798e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.35059786
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	22.6588402
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	0
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.0291200001

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MtrVel2_Per1

Name	Input Value		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	214.735001		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.74783278	3.74783278	✔
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	5.78374004	5.78373957 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	198.617447	198.617432 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	5.78374004	5.78373957 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	198.617447	198.617432 ± 0.0625	✔

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.21 (Repeat Count = 1)				✓
Name		Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		2.92084787e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		3.45537591		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		24.6913891		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		1100.33203		
Rte_Inst_Sa_MtrVel2		tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32		0.0301200002		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value		-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value		224.964996		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value		1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name		Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		1234.56799	1234.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		3.92637992	3.92637992	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		-7.09332037	-7.09332275 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		235.501999	235.502075 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value		-7.09332037	-7.09332275 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value		235.501999	235.502075 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

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MtrVel2_Per1

Test Step 2.22 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3.0285975e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.56015372
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	26.7239399
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1300.36499
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.0110999998
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	235.195007
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	4.10492706	4.10492706	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	3.02349186	3.02349257 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	272.386658	272.386719 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	3.02349186	3.02349257 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	272.386658	272.386719 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3.13634739e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.66493154
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	100
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.0111400001
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-194.7621
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.39923978	-3.39924002	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	39.3474312	39.347435 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-3532.08545	-3532.08569 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	39.3474312	39.347435 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-3532.08545	-3532.08569 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3.24409702e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	3.76970959
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-150.365402
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.0111800004

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MtrVel2_Per1

Name	Input Value		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-204.434494		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamP_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.56805491	-3.56805515	✔
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	-41.0181046	-41.0181046 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-3668.88208	-3668.88232 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-41.0181046	-41.0181046 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-3668.88208	-3668.88232 ± 0.0625	✔

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.25 (Repeat Count = 1)				✓
Name		Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		3.35184691e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		3.8744874		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		0		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		-400.354095		
Rte_Inst_Sa_MtrVel2		tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32		0.0112199998		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value		-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value		-214.106903		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value		1234567892		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name		Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		1234.56799	1234.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		-3.73687029	-3.73687029	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		42	42 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32		-3805.67871	-3805.67896 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value		42	42 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value		-3805.67871	-3805.67896 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

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MtrVel2_Per1

Test Step 2.26 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3.45959654e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.06378317		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	32.6580009		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-650.987488		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.0112600001		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-223.779297		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.90568542	-3.90568566	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	6.52008963	6.52008963 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	579.048828	579.048767 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	6.52008963	6.52008963 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	579.048828	579.048767 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1.23456794e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.277421		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-15.6574001		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-901.191101		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.0113000004		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-233.451706		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-4.07450104	-4.07450104	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-6.79649782	-6.79649925 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	601.459961	601.460144 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-6.79649782	-6.79649925 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	601.459961	601.460144 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.28 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3.21456794e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-5.49105883
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-24.8457794
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1151.5022
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0

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MtrVel2_Per1

Name	Input Value		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-243.1241		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-4.24331617	-4.24331617	✔
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	623.871277	623.87146 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	0	0 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	623.871277	623.87146 ± 0.0625	✔

T					✓
Actual Function		Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached		1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached		1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.29 (Repeat Count = 1)		✓
Name		Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32		3.21456794e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32		4.29359913
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32		-27.0967007

tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32

tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32

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MtrVel2_Per1

Test Step 2.30 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2.65478963e+009		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	4.39837694		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-29.34762		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-25.9874001		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.5		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-262.468903		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-4.58094645	-4.58094645	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-4489.66162	-4489.66162 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-4489.66162	-4489.66162 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	4.50315475		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-31.5985394		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	75.9824982		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.0101100001		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	327.265015		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	5.71185207	5.7118516	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-4.60297497e-006	-4.60297406e-006 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	0.000455289322	0.000455289206 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-4.60297497e-006	-4.60297406e-006 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0.000455289322	0.000455289206 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	4.2949673e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	4.60793257
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-33.8494606
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	175.524307
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.0104999999

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MtrVel2_Per1

Name	Input Value
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	337.494995
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	5.89039898	5.89039898	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	6.73294783	6.73294783 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	641.233154	641.233154 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	6.73294783	6.73294783 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	641.233154	641.233154 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.33 (Repeat Count = 1) ✓

Name	Input Value
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2.14243315e+009
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	4.71271038
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-36.1003799
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	275.247986
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_GearRatio_Uls_f32	0.010889996
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	347.725006
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	3214567894
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	3214.56787	3214.56787 ± 0.0625	✓
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	6.06894636	6.06894636	✓
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-7.38470459	-7.38470268 ± 0.0625	✓
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	678.117981	678.117798 ± 0.0625	✓
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-7.38470459	-7.38470268 ± 0.009	✓
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	678.117981	678.117798 ± 0.0625	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓	

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MtrVel2_Per1

Test Case 3: Path Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS3.1 1220.00 Cycles TS3.2 1196.00 Cycles TS3.3 1140.00 Cycles
Description	Vector Description TS3.1(SysC_diagDeltaTime_Sec_f32 > FLT_EPSILON)=False TS3.2(SysC_diagDeltaTime_Sec_f32 > FLT_EPSILON)=True && MtrVel2_SysCHwVelCRF_HwRadpS_M_f32=Limit_m(MtrVel2_SysCHwVelCRF_HwRadpS_M_f32,D_MINHANDWHEELVEL_HWRADPS_F32,D_MAXHANDWHEELVEL_HWRADPS_F32)= TS3.3MtrVel2_SysCHwVelCRF_HwRadpS_M_f32=Limit_m(MtrVel2_SysCHwVelCRF_HwRadpS_M_f32,D_MINHANDWHEELVEL_HWRADPS_F32,D_MAXHANDWHEELVEL_HWRADPS_F32)=

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	119304648		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.14104199		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	2.33333993		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	100.236		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.100000001		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	10.1350002		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0.176889122	0.176889122	✔
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1658.96545	1658.96558 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1658.96545	1658.96558 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	0		
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-4.85014534		
MtrVel2_SysChWVelCRF_HwRadpS_M_f32	26		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	75		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_GearRatio_Uls_f32	0.0199999996		
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	-150		
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	2654789515		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	2654.78955	2654.78955 ± 0.0625	✔
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-2.61799383	-2.61799383 ± 0.000017	✔
MtrVel2_SysChWVelCRF_HwRadpS_M_f32	1.68160332e-005	1.68160332e-005 ± 0.0625	✔
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	0.000840801687	0.000840801629 ± 0.0625	✔
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	1.68160332e-005	1.68160332e-005 ± 0.009	✔
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0.000840801687	0.000840801629 ± 0.0625	✔

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MtrVel2_Per1

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	65454564			
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-3.35468006			
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	4.36589003			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-150.326004			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_GearRatio_Uls_f32	0.20000003			
tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32.value	20.3649998			
tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32.value	1234567892			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel2_Per1_AsstAssemblyPolarity_Cnt_s08			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Per1_CumMechMtrPosMRF_Deg_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32	tgt_MtrVel2_Per1_MechMtrPos1Timestamp_USec_u32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_PrevMechMtrPosTimeStamp_Sec_M_f32	1234.56799	1234.56787 ± 0.0625	✓	
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0.355436295	0.355436295	✓	
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42	42 ± 0.0625	✓	
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1855.05811	1855.05811 ± 0.0625	✓	
tgt_MtrVel2_Per1_SysCDiagHwVel_HwRadpS_f32.value	42	42 ± 0.009	✓	
tgt_MtrVel2_Per1_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1855.05811	1855.05811 ± 0.0625	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per1_CP1_CheckpointReached	1	✓

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MtrVel2_Per2



Project	MtrVel_Digi
Module	MtrVel2
Test Object	MtrVel2_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy__Work__Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy__Work__Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel2.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel2 -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(PROJECTROOT)\NxrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel2 -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(PROJECTROOT)\NxrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrVel2'	***** Name of Tester: Ankita Bhardwaj Code File(s) Under Test: 5 Code File(s) Version: Sa_MtrVel2.c Module Design Document: MotorVelocity2_MDD Module Design Document Version: 6 Data Dictionary Version: 4 Unit Test Plan Version: 2 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 678 Total RAM Used (Bytes): 36 Total CALS Used (Bytes): 54 Special Test Requirements: Test Date: 01-08-15 Comments: "Note1: Inline functions defined in "GlobalMacro.h" are not unit tested. Note2: The range of the variable "MechMtrPos1Timestamp_USec_u32" is taken as 0 to Full (0 to 4294967295) and the same is updated in the DD. Note3: In function "MtrVel2_Per1" variables "SysCDiagMtrVelMRF_MtrRadpS_f32" and "MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32" are going out of range, accepted by developer. Note4: "CBD_Sandbox_dbg.map" map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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MtrVel2_Per2



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy__Work__Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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MtrVel2_Per2

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 1695.00 Cycles TC1.2 1621.00 Cycles
Description	Vector Description: TS1.1"Longest Path :(SysCMtrVelCorrLDif_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=True (DiagFailed_m(MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=True (SysCHwVelCorrLDif_HwRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=True (DiagFailed_m(MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=True (NTCParam_Cnt_T_u8 != D_ZERO_CNT_U8)=True" TS1.2"Shortest Path :(SysCMtrVelCorrLDif_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=False (DiagFailed_m(MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=False (SysCHwVelCorrLDif_HwRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=False (DiagFailed_m(MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=False (NTCParam_Cnt_T_u8 != D_ZERO_CNT_U8)=False"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0	0	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

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MtrVel2_Per2

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	255		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	255		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	235	235	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	235	235	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Case 2: Boundary Test

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MtrVel2_Per2

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:
	TC2.1 1682.00 Cycles
	TC2.2 1650.00 Cycles
	TC2.3 1673.00 Cycles
	TC2.4 1653.00 Cycles
	TC2.5 1629.00 Cycles
	TC2.6 1616.00 Cycles
	TC2.7 1616.00 Cycles
	TC2.8 1591.00 Cycles
	TC2.9 1577.00 Cycles
	TC2.10 1577.00 Cycles
	TC2.11 1616.00 Cycles
	TC2.12 1591.00 Cycles
	TC2.13 1577.00 Cycles
	TC2.14 1735.00 Cycles
	TC2.15 1708.00 Cycles
	TC2.16 1591.00 Cycles
	TC2.17 1577.00 Cycles
	TC2.18 1577.00 Cycles
	TC2.19 1618.00 Cycles
	TC2.20 1577.00 Cycles
	TC2.21 1580.00 Cycles
	TC2.22 1577.00 Cycles
	TC2.23 1577.00 Cycles
	TC2.24 1577.00 Cycles
	TC2.25 1577.00 Cycles
	TC2.26 1577.00 Cycles
	TC2.27 1561.00 Cycles
	TC2.28 1561.00 Cycles
	TC2.29 1619.00 Cycles
	TC2.30 1577.00 Cycles
	TC2.31 1577.00 Cycles
	TC2.32 1577.00 Cycles
	TC2.33 1619.00 Cycles
	TC2.34 1577.00 Cycles
	TC2.35 1577.00 Cycles
	TC2.36 1577.00 Cycles
	TC2.37 1617.00 Cycles
	TC2.38 1577.00 Cycles
	TC2.39 1617.00 Cycles
	TC2.40 1577.00 Cycles
	TC2.41 1617.00 Cycles
	TC2.42 1577.00 Cycles
	TC2.43 1577.00 Cycles
	TC2.44 1640.00 Cycles
	TC2.45 1620.00 Cycles
	TC2.46 1595.00 Cycles
	TC2.47 1591.00 Cycles
	TC2.48 1625.00 Cycles
	TC2.49 1591.00 Cycles
	TC2.50 1591.00 Cycles
	TC2.51 1591.00 Cycles
	TC2.52 1591.00 Cycles
	TC2.53 1591.00 Cycles
	TC2.54 1591.00 Cycles
	TC2.55 1577.00 Cycles
	TC2.56 1577.00 Cycles

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MtrVel2_Per2

Description	Vector Description
	TS2.1All Min
	TS2.2All Max
	TS2.3HandwheelVel_HwRadpS_f32 = Min
	TS2.4HandwheelVel_HwRadpS_f32 = Max
	TS2.5HandwheelVel_HwRadpS_f32 = Zero
	TS2.6HandwheelVel_HwRadpS_f32 = Pos
	TS2.7HandwheelVel_HwRadpS_f32 = Neg
	TS2.8MotorVelMRF_MtrRadpS_f32 = Min
	TS2.9MotorVelMRF_MtrRadpS_f32 = Max
	TS2.10MotorVelMRF_MtrRadpS_f32 = Zero
	TS2.11MotorVelMRF_MtrRadpS_f32 = Pos
	TS2.12MotorVelMRF_MtrRadpS_f32 = Neg
	TS2.13MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Min
	TS2.14MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Max
	TS2.15MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Zero
	TS2.16MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Pos
	TS2.17MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32 = Neg
	TS2.18k_MtrVelCorrLim_MtrRadpS_f32 = Min
	TS2.19k_MtrVelCorrLim_MtrRadpS_f32 = Max
	TS2.20k_MtrVelCorrLim_MtrRadpS_f32 = Zero
	TS2.21k_MtrVelCorrLim_MtrRadpS_f32 = Pos
	TS2.22k_MtrVelCorrLim_MtrRadpS_f32 = Neg
	TS2.23MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16 = Min
	TS2.24MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16 = Max
	TS2.25MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16 = Pos
	TS2.26k_MtrVelCorrLim_Cnt_Str.Threshold = Min
	TS2.27k_MtrVelCorrLim_Cnt_Str.Threshold = Max
	TS2.28k_MtrVelCorrLim_Cnt_Str.Threshold = Pos
	TS2.29k_MtrVelCorrLim_Cnt_Str.Pstep = Min
	TS2.30k_MtrVelCorrLim_Cnt_Str.Pstep = Max
	TS2.31k_MtrVelCorrLim_Cnt_Str.Pstep = Pos
	TS2.32k_MtrVelCorrLim_Cnt_Str.Nstep = Min
	TS2.33k_MtrVelCorrLim_Cnt_Str.Nstep = Max
	TS2.34k_MtrVelCorrLim_Cnt_Str.Nstep = Pos
	TS2.35k_HwVelCorrLim_Cnt_Str.Threshold = Min
	TS2.36k_HwVelCorrLim_Cnt_Str.Threshold = Max
	TS2.37k_HwVelCorrLim_Cnt_Str.Threshold = Pos
	TS2.38k_HwVelCorrLim_Cnt_Str.Pstep = Min
	TS2.39k_HwVelCorrLim_Cnt_Str.Pstep = Max
	TS2.40k_HwVelCorrLim_Cnt_Str.Pstep = Pos
	TS2.41k_HwVelCorrLim_Cnt_Str.Nstep = Min
	TS2.42k_HwVelCorrLim_Cnt_Str.Nstep = Max
	TS2.43k_HwVelCorrLim_Cnt_Str.Nstep = Pos
	TS2.44k_HwVelCorrLim_HwRadpS_f32 = Min
	TS2.45k_HwVelCorrLim_HwRadpS_f32 = Max
	TS2.46k_HwVelCorrLim_HwRadpS_f32 = Zero
	TS2.47k_HwVelCorrLim_HwRadpS_f32 = Pos
	TS2.48k_HwVelCorrLim_HwRadpS_f32 = Neg
	TS2.49MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Min
	TS2.50MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Max
	TS2.51MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Zero
	TS2.52MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Pos
	TS2.53MtrVel2_SysCHwVelCRF_HwRadpS_M_f32 = Neg
	TS2.54MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16 = Min
	TS2.55MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16 = Max
	TS2.56MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16 = Pos

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0	0	✓
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	42			
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	255			
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	255			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_HwVelCorrLim_Cnt_Str.Threshold	255			
k_HwVelCorrLim_Cnt_Str.PStep	255			
k_HwVelCorrLim_Cnt_Str.NStep	20			
k_HwVelCorrLim_HwRadpS_f32	32			
k_MtrVelCorrLim_Cnt_Str.Threshold	255			
k_MtrVelCorrLim_Cnt_Str.PStep	255			
k_MtrVelCorrLim_Cnt_Str.NStep	20			
k_MtrVelCorrLim_MtrRadpS_f32	1118			
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	42			
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	1350			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	235	235	✓	
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	235	235	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	2.33333993			
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	220			
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	15			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	500.235596			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_HwVelCorrLim_Cnt_Str.Threshold	77			
k_HwVelCorrLim_Cnt_Str.PStep	60			
k_HwVelCorrLim_Cnt_Str.NStep	2			
k_HwVelCorrLim_HwRadpS_f32	-7.69850016			
k_MtrVelCorrLim_Cnt_Str.Threshold	100			
k_MtrVelCorrLim_Cnt_Str.PStep	35			
k_MtrVelCorrLim_Cnt_Str.NStep	1			
k_MtrVelCorrLim_MtrRadpS_f32	-1008.57849			
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-42			
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-135.326004			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	77	77	✓	
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	50	50	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	4.36589003		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	205		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	20		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	535.857422		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	35		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-4.98570013		
k_MtrVelCorrLim_Cnt_Str.Threshold	123		
k_MtrVelCorrLim_Cnt_Str.PStep	10		
k_MtrVelCorrLim_Cnt_Str.NStep	3		
k_MtrVelCorrLim_MtrRadpS_f32	-900.986511		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-150.658401		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	100	100	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	30	30	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	6.39843988		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	2		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	25		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	570.96582		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	123		
k_HwVelCorrLim_Cnt_Str.PStep	10		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	13.9856997		
k_MtrVelCorrLim_Cnt_Str.Threshold	110		
k_MtrVelCorrLim_Cnt_Str.PStep	85		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.47852		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	0		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-165.986496		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0	0	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	110	110	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	8.43099022		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	175		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	30		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	605.214478		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	110		
k_HwVelCorrLim_Cnt_Str.PStep	85		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	0.958739996		
k_MtrVelCorrLim_Cnt_Str.Threshold	150		
k_MtrVelCorrLim_Cnt_Str.PStep	96		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-105.986504		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	32.3264999		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-180.125397		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	110	110	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	126	126	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	10.4635401		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	160		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	640.154175		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	150		
k_HwVelCorrLim_Cnt_Str.PStep	96		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	1.68540001		
k_MtrVelCorrLim_Cnt_Str.Threshold	200		
k_MtrVelCorrLim_Cnt_Str.PStep	35		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	99.7894974		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-15.3259001		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-195.951202		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	150	150	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	70	70	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	12.4960899			
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	145			
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	40			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	675.758606			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_HwVelCorrLim_Cnt_Str.Threshold	200			
k_HwVelCorrLim_Cnt_Str.PStep	35			
k_HwVelCorrLim_Cnt_Str.NStep	9			
k_HwVelCorrLim_HwRadpS_f32	2.41206002			
k_MtrVelCorrLim_Cnt_Str.Threshold	220			
k_MtrVelCorrLim_Cnt_Str.PStep	200			
k_MtrVelCorrLim_Cnt_Str.NStep	11			
k_MtrVelCorrLim_MtrRadpS_f32	-1002.24579			
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-24.8457794			
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1350			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	180	180	✓	
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	220	220	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	14.5286398			
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	130			
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	45			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	710.142517			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_HwVelCorrLim_Cnt_Str.Threshold	220			
k_HwVelCorrLim_Cnt_Str.PStep	200			
k_HwVelCorrLim_Cnt_Str.NStep	11			
k_HwVelCorrLim_HwRadpS_f32	3.13872004			
k_MtrVelCorrLim_Cnt_Str.Threshold	230			
k_MtrVelCorrLim_Cnt_Str.PStep	220			
k_MtrVelCorrLim_Cnt_Str.NStep	13			
k_MtrVelCorrLim_MtrRadpS_f32	200.325867			
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-27.0967007			
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	1350			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	220	220	✓	
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	230	230	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	16.5611897		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	115		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	50		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	745.758606		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	230		
k_HwVelCorrLim_Cnt_Str.PStep	220		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	3.86538005		
k_MtrVelCorrLim_Cnt_Str.Threshold	50		
k_MtrVelCorrLim_Cnt_Str.PStep	230		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	-500.785614		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-29.34762		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	0		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	230	230	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	50	50	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	18.5937405		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	100		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	55		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	780.258728		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	230		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	4.59204006		
k_MtrVelCorrLim_Cnt_Str.Threshold	250		
k_MtrVelCorrLim_Cnt_Str.PStep	50		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	-600.986511		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-31.5985394		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	1200.5		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	50	50	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	105	105	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	20.6262894		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	85		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	60		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	815.1698		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	250		
k_HwVelCorrLim_Cnt_Str.PStep	50		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	5.31869984		
k_MtrVelCorrLim_Cnt_Str.Threshold	180		
k_MtrVelCorrLim_Cnt_Str.PStep	250		
k_MtrVelCorrLim_Cnt_Str.NStep	19		
k_MtrVelCorrLim_MtrRadpS_f32	300.785889		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-33.8494606		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1000.59998		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	135	135	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	180	180	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	22.6588402		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	70		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	65		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	180		
k_HwVelCorrLim_Cnt_Str.PStep	250		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	6.04536009		
k_MtrVelCorrLim_Cnt_Str.Threshold	185		
k_MtrVelCorrLim_Cnt_Str.PStep	125		
k_MtrVelCorrLim_Cnt_Str.NStep	2		
k_MtrVelCorrLim_MtrRadpS_f32	700.587402		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-36.1003799		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	100.236		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	180	180	✓
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	185	185	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	24.6913891		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	55		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	70		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	185		
k_HwVelCorrLim_Cnt_Str.PStep	125		
k_HwVelCorrLim_Cnt_Str.NStep	2		
k_HwVelCorrLim_HwRadpS_f32	6.77201986		
k_MtrVelCorrLim_Cnt_Str.Threshold	190		
k_MtrVelCorrLim_Cnt_Str.PStep	32		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	-900.635193		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	32.365799		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-150.326004		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	180	180	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	102	102	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	26.7239399		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	40		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	0		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	190		
k_HwVelCorrLim_Cnt_Str.PStep	32		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	7.49868011		
k_MtrVelCorrLim_Cnt_Str.Threshold	195		
k_MtrVelCorrLim_Cnt_Str.PStep	100		
k_MtrVelCorrLim_Cnt_Str.NStep	6		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.78589		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	38.5874481		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-400.96579		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	72	72	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	175	175	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	28.7564907		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	25		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	80		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1200.19995		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	195		
k_HwVelCorrLim_Cnt_Str.PStep	100		
k_HwVelCorrLim_Cnt_Str.NStep	6		
k_HwVelCorrLim_HwRadpS_f32	8.22533989		
k_MtrVelCorrLim_Cnt_Str.Threshold	80		
k_MtrVelCorrLim_Cnt_Str.PStep	123		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	456.985687		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-2.98749995		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-650.362427		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	125	125	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	80	80	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	30.7890396		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	10		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1000.29999		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	80		
k_HwVelCorrLim_Cnt_Str.PStep	123		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	-0.698570013		
k_MtrVelCorrLim_Cnt_Str.Threshold	85		
k_MtrVelCorrLim_Cnt_Str.PStep	110		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	-1025.25867		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-4.3657999		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-900.987488		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	80	80	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85	85	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	32.8215904		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	20		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	90		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	100.236		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	85		
k_HwVelCorrLim_Cnt_Str.PStep	110		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-2.58470011		
k_MtrVelCorrLim_Cnt_Str.Threshold	90		
k_MtrVelCorrLim_Cnt_Str.PStep	150		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-6.15980005		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1150.98572		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	85	85	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	90	90	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	34.8541412		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	30		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	95		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-150.326004		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	90		
k_HwVelCorrLim_Cnt_Str.PStep	150		
k_HwVelCorrLim_Cnt_Str.NStep	12		
k_HwVelCorrLim_HwRadpS_f32	-4.47082996		
k_MtrVelCorrLim_Cnt_Str.Threshold	95		
k_MtrVelCorrLim_Cnt_Str.PStep	200		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-8.12450027		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	25.6350002		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	90	90	✓
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	81	81	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	36.8866882		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	40		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	100		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-400.96579		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	95		
k_HwVelCorrLim_Cnt_Str.PStep	200		
k_HwVelCorrLim_Cnt_Str.NStep	14		
k_HwVelCorrLim_HwRadpS_f32	-6.35695982		
k_MtrVelCorrLim_Cnt_Str.Threshold	55		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	0		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-10.9875002		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-25.3619995		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	95	95	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	55	55	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	38.919239		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	50		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	105		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-650.362427		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	55		
k_HwVelCorrLim_Cnt_Str.PStep	220		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	-8.24308968		
k_MtrVelCorrLim_Cnt_Str.Threshold	50		
k_MtrVelCorrLim_Cnt_Str.PStep	230		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	1008.98749		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-12.8573999		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	75.4250031		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	55	55	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	89	89	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-2.33658004		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	60		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	110		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-900.987488		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	230		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	-10.12922		
k_MtrVelCorrLim_Cnt_Str.Threshold	45		
k_MtrVelCorrLim_Cnt_Str.PStep	240		
k_MtrVelCorrLim_Cnt_Str.NStep	3		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.85699		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-14.6597996		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	175.987		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	50	50	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	45	45	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-4.5875001		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	70		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1150.98572		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	45		
k_HwVelCorrLim_Cnt_Str.PStep	240		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	-12.0153503		
k_MtrVelCorrLim_Cnt_Str.Threshold	40		
k_MtrVelCorrLim_Cnt_Str.PStep	250		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	654.235596		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-16.1245594		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	275.98349		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	45	45	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	40	40	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-6.83841991		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	80		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	255		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	25.6350002		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	40		
k_HwVelCorrLim_Cnt_Str.PStep	250		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-13.9014797		
k_MtrVelCorrLim_Cnt_Str.Threshold	35		
k_MtrVelCorrLim_Cnt_Str.PStep	10		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-632.986511		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-18.3265972		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	375.365387		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	40	40	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35	35	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-9.08934021		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	90		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	128		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-25.3619995		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	35		
k_HwVelCorrLim_Cnt_Str.PStep	10		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	-15.7876101		
k_MtrVelCorrLim_Cnt_Str.Threshold	30		
k_MtrVelCorrLim_Cnt_Str.PStep	85		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	-900.875427		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-20.3245602		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	475.644653		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	35	35	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	30	30	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.26 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-11.3402596		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	100		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	75.4250031		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	30		
k_HwVelCorrLim_Cnt_Str.PStep	85		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	-17.6737404		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	160		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.98566		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-22.9874992		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	575.626404		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	30	30	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-13.5911798		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	123		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	175.987		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	25		
k_HwVelCorrLim_Cnt_Str.PStep	160		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	-19.5598698		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	235		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	456.326599		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-24.2154007		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	675.608215		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	25	25	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	255	255	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.28 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	-15.8421001			
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	110			
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	275.98349			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_HwVelCorrLim_Cnt_Str.Threshold	20			
k_HwVelCorrLim_Cnt_Str.PStep	235			
k_HwVelCorrLim_Cnt_Str.NStep	13			
k_HwVelCorrLim_HwRadpS_f32	8.95199966			
k_MtrVelCorrLim_Cnt_Str.Threshold	128			
k_MtrVelCorrLim_Cnt_Str.PStep	15			
k_MtrVelCorrLim_Cnt_Str.NStep	15			
k_MtrVelCorrLim_MtrRadpS_f32	352.652008			
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-26.5240993			
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	775.589966			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	20	20	✓	
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	111	111	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-20.3439407		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	200		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	475.644653		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	205		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	10.4053202		
k_MtrVelCorrLim_Cnt_Str.Threshold	85		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	19		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.91241		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	2.33333993		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	975.553528		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	10	10	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85	85	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-22.5948601		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	220		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	575.626404		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	5		
k_HwVelCorrLim_Cnt_Str.PStep	190		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	11.1319799		
k_MtrVelCorrLim_Cnt_Str.Threshold	96		
k_MtrVelCorrLim_Cnt_Str.PStep	128		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	-105.923698		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	4.36589003		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	100		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	5	5	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96	96	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	-24.8457794		
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	230		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	675.608215		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	15		
k_HwVelCorrLim_Cnt_Str.PStep	175		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	11.8586397		
k_MtrVelCorrLim_Cnt_Str.Threshold	35		
k_MtrVelCorrLim_Cnt_Str.PStep	15		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	451.185699		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	6.39843988		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-150.365402		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	15	15	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35	35	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-27.0967007		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	50		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	775.589966		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	25		
k_HwVelCorrLim_Cnt_Str.PStep	160		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	12.5853004		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	20		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	526.653198		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	8.43099022		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-400.354095		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	25	25	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	55	55	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-29.34762		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	250		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	200		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	875.571716		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	35		
k_HwVelCorrLim_Cnt_Str.PStep	145		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	13.3119602		
k_MtrVelCorrLim_Cnt_Str.Threshold	85		
k_MtrVelCorrLim_Cnt_Str.PStep	25		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	74.1244965		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	10.4635401		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-650.987488		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	35	35	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85	85	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.35 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-31.5985394		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	180		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	220		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	975.553528		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	130		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	14.03862		
k_MtrVelCorrLim_Cnt_Str.Threshold	96		
k_MtrVelCorrLim_Cnt_Str.PStep	30		
k_MtrVelCorrLim_Cnt_Str.NStep	2		
k_MtrVelCorrLim_MtrRadpS_f32	-632.9823		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	12.4960899		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-901.191101		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0	0	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96	96	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.36 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-33.8494606		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	185		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	230		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	100.4758		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	115		
k_HwVelCorrLim_Cnt_Str.NStep	14		
k_HwVelCorrLim_HwRadpS_f32	-25.2182598		
k_MtrVelCorrLim_Cnt_Str.Threshold	35		
k_MtrVelCorrLim_Cnt_Str.PStep	35		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	-900.784973		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	14.5286398		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1151.5022		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	255	255	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35	35	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.37 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-36.1003799		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	190		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	50		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-150.365402		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	128		
k_HwVelCorrLim_Cnt_Str.PStep	100		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-27.1043892		
k_MtrVelCorrLim_Cnt_Str.Threshold	200		
k_MtrVelCorrLim_Cnt_Str.PStep	40		
k_MtrVelCorrLim_Cnt_Str.NStep	6		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.32648		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	16.5611897		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	25.3547001		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	128	128	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	90	90	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-38.3512993		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	195		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	250		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-400.354095		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	95		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	12		
k_HwVelCorrLim_HwRadpS_f32	-28.9905205		
k_MtrVelCorrLim_Cnt_Str.Threshold	220		
k_MtrVelCorrLim_Cnt_Str.PStep	45		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	-105.985703		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-2.33658004		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-25.9874001		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	95	95	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	220	220	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.39 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-17.9853992		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	80		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	125		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-650.987488		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	-30.8766499		
k_MtrVelCorrLim_Cnt_Str.Threshold	230		
k_MtrVelCorrLim_Cnt_Str.PStep	50		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	99.8753967		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-4.5875001		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	75.9824982		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	100	100	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	175	175	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-21.9864006		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	85		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	32		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-901.191101		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	105		
k_HwVelCorrLim_Cnt_Str.PStep	128		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	2.76277995		
k_MtrVelCorrLim_Cnt_Str.Threshold	50		
k_MtrVelCorrLim_Cnt_Str.PStep	55		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	-1002.25861		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-6.83841991		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	175.524307		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	105	105	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	50	50	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	10.1350002		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	90		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1151.5022		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	110		
k_HwVelCorrLim_Cnt_Str.PStep	115		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	4.64891005		
k_MtrVelCorrLim_Cnt_Str.Threshold	250		
k_MtrVelCorrLim_Cnt_Str.PStep	60		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	200.142563		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-9.08934021		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	275.247986		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	110	110	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	95	95	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	20.3649998		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	95		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	25.3547001		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	115		
k_HwVelCorrLim_Cnt_Str.PStep	100		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	16.5350399		
k_MtrVelCorrLim_Cnt_Str.Threshold	125		
k_MtrVelCorrLim_Cnt_Str.PStep	65		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	-500.362		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-11.3402596		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-135.326004		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	115	115	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	125	125	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	30.5949993		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	55		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	80		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-25.9874001		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	120		
k_HwVelCorrLim_Cnt_Str.PStep	85		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	29.6580009		
k_MtrVelCorrLim_Cnt_Str.Threshold	32		
k_MtrVelCorrLim_Cnt_Str.PStep	70		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	-600.879517		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-13.5911798		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-150.658401		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	120	120	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	32	32	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.44 (Repeat Count = 1)				
Name	Input Value			
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	40.8250008			
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	50			
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75			
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	75.9824982			
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2			
k_HwVelCorrLim_Cnt_Str.Threshold	125			
k_HwVelCorrLim_Cnt_Str.PStep	70			
k_HwVelCorrLim_Cnt_Str.NStep	13			
k_HwVelCorrLim_HwRadpS_f32	-32			
k_MtrVelCorrLim_Cnt_Str.Threshold	35			
k_MtrVelCorrLim_Cnt_Str.PStep	75			
k_MtrVelCorrLim_Cnt_Str.NStep	3			
k_MtrVelCorrLim_MtrRadpS_f32	300.654694			
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-15.8421001			
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-165.986496			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	120	120	✓	
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	72	72	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-20.6588993		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	40		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	275.247986		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	135		
k_HwVelCorrLim_Cnt_Str.PStep	40		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	80		
k_MtrVelCorrLim_Cnt_Str.PStep	85		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	-900.104492		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	14.5286398		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-195.951202		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	80	80	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	80	80	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.47 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-30.3313007		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	360.987488		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	140		
k_HwVelCorrLim_Cnt_Str.PStep	25		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	16.6520004		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	90		
k_MtrVelCorrLim_Cnt_Str.NStep	6		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.32562		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	16.5611897		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-150.365402		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	60	60	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75	75	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.48 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-40.0037003		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	30		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	395.587402		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	145		
k_HwVelCorrLim_Cnt_Str.PStep	10		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-20.9853992		
k_MtrVelCorrLim_Cnt_Str.Threshold	85		
k_MtrVelCorrLim_Cnt_Str.PStep	95		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	456.985687		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	18.5937405		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-400.354095		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	40	40	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85	85	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	25		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	430.986511		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	150		
k_HwVelCorrLim_Cnt_Str.PStep	25		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	16.2185993		
k_MtrVelCorrLim_Cnt_Str.Threshold	96		
k_MtrVelCorrLim_Cnt_Str.PStep	100		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	-1025.35779		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	20.6262894		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-650.987488		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	50	50	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96	96	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	20		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	465.124512		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	155		
k_HwVelCorrLim_Cnt_Str.PStep	40		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	16.9452591		
k_MtrVelCorrLim_Cnt_Str.Threshold	35		
k_MtrVelCorrLim_Cnt_Str.PStep	105		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	-1008.98572		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	22.6588402		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-901.191101		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	60	60	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35	35	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.51 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	0		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	15		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	500.759796		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	160		
k_HwVelCorrLim_Cnt_Str.PStep	55		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	17.6719208		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	110		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	-105.745796		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	24.6913891		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1151.5022		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	70	70	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75	75	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.52 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	32.6580009		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	10		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	200		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	535.365784		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	165		
k_HwVelCorrLim_Cnt_Str.PStep	70		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	18.3985806		
k_MtrVelCorrLim_Cnt_Str.Threshold	85		
k_MtrVelCorrLim_Cnt_Str.PStep	115		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	99.325798		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	26.7239399		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	25.3547001		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	3	3	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	85	85	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.53 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	-15.9849997		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	5		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	220		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	570.757996		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	170		
k_HwVelCorrLim_Cnt_Str.PStep	85		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-10.3072996		
k_MtrVelCorrLim_Cnt_Str.Threshold	96		
k_MtrVelCorrLim_Cnt_Str.PStep	120		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	452.985687		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-5		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-25.9874001		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	90	90	✓
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	96	96	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

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T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-17.6520004		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	605.857422		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	175		
k_HwVelCorrLim_Cnt_Str.PStep	100		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-12.1934299		
k_MtrVelCorrLim_Cnt_Str.Threshold	35		
k_MtrVelCorrLim_Cnt_Str.PStep	125		
k_MtrVelCorrLim_Cnt_Str.NStep	3		
k_MtrVelCorrLim_MtrRadpS_f32	-500.758392		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-9		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	75.9824982		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	100	100	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	35	35	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.55 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	5.9854002		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	255		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	75		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	640.897583		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	180		
k_HwVelCorrLim_Cnt_Str.PStep	115		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	14.0795603		
k_MtrVelCorrLim_Cnt_Str.Threshold	200		
k_MtrVelCorrLim_Cnt_Str.PStep	130		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	-600.365173		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	22		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	175.524307		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	180	180	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	200	200	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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MtrVel2_Per2

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 2.56 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	9.75839996		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	128		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	115		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	675.154175		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	185		
k_HwVelCorrLim_Cnt_Str.PStep	130		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	15.9656897		
k_MtrVelCorrLim_Cnt_Str.Threshold	220		
k_MtrVelCorrLim_Cnt_Str.PStep	135		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-300.984985		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	23		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	850.234985		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	109	109	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	220	220	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Case 3: Path Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 1695.00 Cycles TS1.2 1621.00 Cycles TS1.3 1568.00 Cycles TS1.4 1706.00 Cycles TS1.5 1629.00 Cycles
Description	Vector Description TS3.1"(SysCMtrVelCorrLdif_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=True (DiagFailed_m(MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=True (SysChwVelCorrLdif_HwRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=True (DiagFailed_m(MtrVel2_SysChwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=True (NTCParam_Cnt_T_u8 != D_ZERO_CNT_U8)=True" TS3.2"(SysCMtrVelCorrLdif_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=False (DiagFailed_m(MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=False (SysChwVelCorrLdif_HwRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=False (DiagFailed_m(MtrVel2_SysChwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=False (NTCParam_Cnt_T_u8 != D_ZERO_CNT_U8)=False" TS3.3(MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16> k_MtrVelCorrLim_Cnt_Str.Nstep)=False TS3.4"DiagPStep_m(MtrVel2_SysChwVelDiffAcc_Cnt_M_u16, k_HwVelCorrLim_Cnt_Str) = True DiagPStep_m(MtrVel2_SysChwVelDiffAcc_Cnt_M_u16, k_HwVelCorrLim_Cnt_Str) = True" TS3.5MtrVel2_SysChwVelDiffAcc_Cnt_M_u16 = DiagNStep_m(MtrVel2_SysChwVelDiffAcc_Cnt_M_u16, k_HwVelCorrLim_Cnt_Str)==>False

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
MtrVel2_SysChwVelICRF_HwRadpS_M_f32	-42
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	0
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350

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MtrVel2_Per2

Name	Input Value		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0	0	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1) ✓

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	255		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	255		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	235	235	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	235	235	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1) ✓

Name	Input Value
MtrVel2_SysCHwVelICRF_HwRadpS_M_f32	42
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	255
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	10
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	1350
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_HwVelCorrLim_Cnt_Str.Threshold	255

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MtrVel2_Per2

Name	Input Value		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	235	235	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value		
MtrVel2_SysCHwVelCRF_HwRadpS_M_f32	-42		
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	128		
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	128		
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
k_HwVelCorrLim_Cnt_Str.Threshold	250		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	250		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	-42		
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	128	128	✔
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	128	128	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)

Name	Input Value
MtrVel2_SysChwVelCRF_HwRadpS_M_f32	6.39843988
MtrVel2_SysChwVelDiffAcc_Cnt_M_u16	2
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	25
MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32	570.96582
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2
k_HwVelCorrLim_Cnt_Str.Threshold	123
k_HwVelCorrLim_Cnt_Str.PStep	10
k_HwVelCorrLim_Cnt_Str.NStep	3

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MtrVel2_Per2

Name	Input Value
k_HwVelCorrLim_HwRadpS_f32	13.9856997
k_MtrVelCorrLim_Cnt_Str.Threshold	110
k_MtrVelCorrLim_Cnt_Str.PStep	85
k_MtrVelCorrLim_Cnt_Str.NStep	5
k_MtrVelCorrLim_MtrRadpS_f32	-1008.47852
tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32.value	0
tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32.value	-165.986496
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_HandwheelVel_HwRadpS_f32	tgt_MtrVel2_Per2_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel2_Per2_MotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel2_SysCHwVelDiffAcc_Cnt_M_u16	0	0	✓
MtrVel2_SysCMtrVelDiffAcc_Cnt_M_u16	110	110	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	205	205	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP0_CheckpointReached	1	✓	
Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel2_NxtrDiagMgr_SetNTCStatus	1	✓	
Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel2_Per2_CP1_CheckpointReached	1	✓	

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MtrVel2_Init



Project	MtrVel_Digi
Module	MtrVel2
Test Object	MtrVel2_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy__Work__Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy__Work__Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel2.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel2 -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel2 -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrVel2'	***** Name of Tester:Ankita Bhardwaj Code File(s) Under Test:5 Code File(s) Version:Sa_MtrVel2.c Module Design Document:MotorVelocity2_MDD Module Design Document Version:6 Data Dictionary Version:4 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):678 Total RAM Used (Bytes):36 Total CALS Used (Bytes):54 Special Test Requirements: Test Date:01-08-15 Comments:"Note1: Inline functions defined in ""GlobalMacro.h"" are not unit tested. Note2 : The range of the variable ""MechMtrPos1Timestamp_USec_u32"" is taken as 0 to Full (0 to 4294967295) and the same is updated in the DD. Note3:In function ""MtrVel2_Per1"" variables ""SysCDiagMtrVelMRF_MtrRadpS_f32"" and ""MtrVel2_SysCMtrVelMRF_MtrRadpS_M_f32"" are going out of range,accepted by devloper. Note4: ""CBD_Sandbox_dbg.map"" map file is embedded for refrence." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl

TEST DETAILS REPORT

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MtrVel2_Init



Attributes	
Name	Value
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy__Work__Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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MtrVel2_Init



Test Case 1: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS1.1 21.00 Cycles
TS1.2 21.00 Cycles
TS1.3 21.00 Cycles
TS1.4 21.00 Cycles
TS1.5 21.00 Cycles

Description Vector Description

TS1.1 CumMechMtrPosMRF_Deg_f32 = Min
TS1.2 CumMechMtrPosMRF_Deg_f32 = Max
TS1.3 CumMechMtrPosMRF_Deg_f32 = Zero
TS1.4 CumMechMtrPosMRF_Deg_f32 = Pos
TS1.5 CumMechMtrPosMRF_Deg_f32 = Neg

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32.value	-360		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Init_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-6.28318548	-6.283185307 ± 0.0000175	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32.value	360		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Init_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	6.28318548	6.283185307 ± 0.0000175	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32.value	0		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Init_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	0	0 ± 0.0000175	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32.value	125.98		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Init_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	2.19876575	2.198765792 ± 0.0000175	✓

Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
Rte_Inst_Sa_MtrVel2	tgt_Rte_Inst_Sa_MtrVel2		
tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32.value	-150.876		
tgt_Rte_Inst_Sa_MtrVel2.MtrVel2_Init_CumMechMtrPosMRF_Deg_f32	tgt_MtrVel2_Init_CumMechMtrPosMRF_Deg_f32		
Name	Actual Value	Expected Value	Result
MtrVel2_PrevSysCDiagCumMtrPos_Rad_M_f32	-2.63328314	-2.633282962 ± 0.0000175	✓