

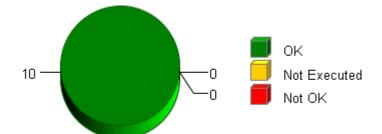
#### **Summary**

#### **Overall Test Object Results (including Coverage)**

Total Test Objects: 10

Successful: 10
Failed: 0
Not Executed: 0

**Date:** 2014-09-04 **Time:** 16:46:29+0530



#### **Selected Project Items**

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos Init1"

Test Object "CBD UnitTest/AbsHwPos Tcl2lVd/AbsHwPos Per1"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_Per2"

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos Per3"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_Per4"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_CustClrTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_CustSetTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_NxtClearTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_NxtSetTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/TrimNotPerfDiag"

#### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

#### **Batch Operation Settings**

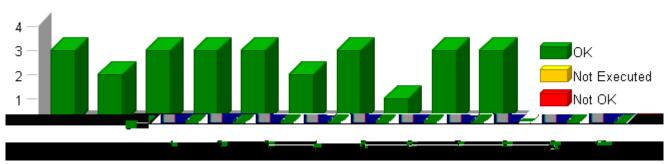
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

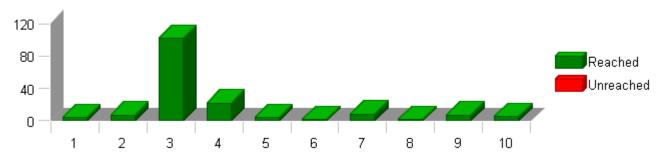
#### Test Case Results for Each Test Object (without Coverage)





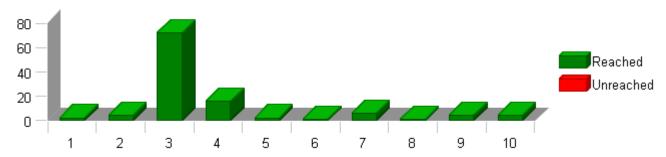
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

#### Statement (C0) Coverage: Total Statements for Each Test Object



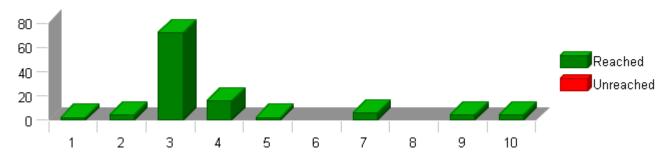
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

#### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

## **Decision Coverage: Total Decision Outcomes for Each Test Object**

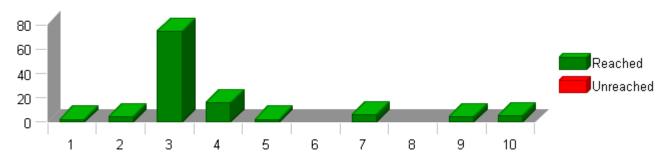


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



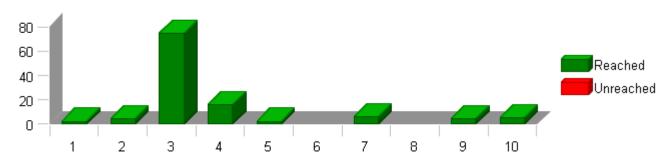
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

#### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



# **Test Object List**

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	<b>C</b> 1	DC	MC/DC	MCC	Test Cases Result
	AbsHwPos_Tcl2cVd	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
	AbsHwPos_Tcl2IVd	100 %	100 %	100 %	100 %	100 %	26 of 26 passed 💌
1	AbsHwPos_Init1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
2	AbsHwPos_Per1	100 %	100 %	100 %	100 %	100 %	2 of 2 passed
3	AbsHwPos Per2	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
4	AbsHwPos Per3	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
5	AbsHwPos Per4	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
6	AbsHwPos SCom CustClrTrim	100 %	100 %	-	-	-	2 of 2 passed
7	AbsHwPos SCom CustSetTrim	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
8	AbsHwPos SCom NxtClearTrim	100 %	100 %	-	-	-	1 of 1 passed
9	AbsHwPos SCom NxtSetTrim	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
10	<u>TrimNotPerfDiag</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per4

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification					
Name	Text				
Module 'AbsHwPos_Tcl2lVd'	Name of Tester:				



Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 1354.00 Cycles TS1.2 1341.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $If (Abs\_m (AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32) > k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32) => True" \\$ 

TS1.2 Longest Execution Path:

 $If (Abs\_m(AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32) > k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32) => False "$ 

Test Step 1.1 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	

Τ						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>✓</b>		

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

Τ						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•		
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	-		



#### Test Case 2: Boundary Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1354.00 Cycles TS2.2 1346.00 Cycles TS2.3 1344.00 Cycles TS2.4 1344.00 Cycles TS2.5 1339.00 Cycles TS2.6 1344.00 Cycles TS2.7 1338.00 Cycles TS2.8 1344.00 Cycles TS2.8 1344.00 Cycles TS2.9 1344.00 Cycles TS2.10 1336.00 Cycles

#### Vector Description: Description

TS2.1 All Min TS2.2 All Max

TS2.3 AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32=>Min IS2.3 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.4 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.5 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.6 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.7 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.8 k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32=>Min
TS2.9 k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32=>Min
TS2.10 k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32=>Min

Test Step 2.1 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte Call An AbeHwPoe NytrDiagMar SetNTCStatus(Status Cnt T anum)	1	1		

T .						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~		
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>✓</b>		

Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	1478		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	99.0100021		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	365.02			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	358.01001			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.7 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-254.12		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	2769		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	6585		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-7895.00977		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~	

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	100		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	487.24		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~



#### Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 1354.00 Cycles TS3.2 1341.00 Cycles

**Description** Vector Description:

 $\label{eq:total_$ 

Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~	

Test Step 3.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>	
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>✓</b>	

AbsHwPos\_SCom\_CustSetTrim

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_CustSetTrim

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Time Enabled	false

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Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### **Test Case 1: Metrics Test**

AbsHwPos\_SCom\_CustSetTrim

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 2163.00 Cycles TS1.2 2974.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

 $\label{eq:cont_u08} \begin{tabular}{ll} if (TurnsCntrValidiy_Cnt_T_u08 == D_TCVVALID_CNT_U08) => False \\ if (AbsHwPos_VehCntrValid_Cnt_M_lgc == FALSE) => True" \\ \end{tabular}$ 

TS1.2 Longest Execution Path:

 $if (Abs\_f32\_m(AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => True \\ if (AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc == FALSE) => False"$ 

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMod	e_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr\	/alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	e_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrV	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.0112959 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>

AbsHwPos\_SCom\_CustSetTrim

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Name	Actual Value	Expected Value	Result
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.0112959 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	<b>✓</b>

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

# **Test Case 2: Boundary Test** Specification "Performance Metrics (With "None" Instrumentation and WithPS Environment" CPU Cycles: TS2.1 2163.00 Cycles TS2.2 2387.00 Cycles TS2.3 2753.00 Cycles TS2.4 2387.00 Cycles TS2.4 2387.00 Cycles TS2.5 2166.00 Cycles TS2.6 2385.00 Cycles TS2.7 2166.00 Cycles TS2.8 2972.00 Cycles TS2.9 2166.00 Cycles TS2.11 2758.00 Cycles TS2.11 2758.00 Cycles TS2.12 2387.00 Cycles TS2.12 2387.00 Cycles TS2.14 2385.00 Cycles TS2.15 2758.00 Cycles TS2.16 2380.00 Cycles TS2.17 2165.00 Cycles TS2.18 2965.00 Cycles TS2.19 2167.00 Cycles TS2.19 2167.00 Cycles TS2.19 2167.00 Cycles TS2.21 2385.00 Cycles TS2.21 2385.00 Cycles TS2.22 2385.00 Cycles TS2.23 2385.00 Cycles TS2.24 2385.00 Cycles TS2.25 2385.00 Cycles TS2.26 2385.00 Cycles TS2.21 2758.00 Cycles TS2.22 2380.00 Cycles TS2.23 2165.00 Cycles Description Vector Description: TS2.1 All Min TS2.2 All Max TS2.3 ManufMode\_Cnt\_enum==>Min TS2.4 ManufMode\_Cnt\_enum==>Max TS2.5 ManufMode\_Cnt\_enum==>Pos TS2.5 ManufMode\_Cnt\_enum==>Max TS2.5 ManufMode\_Cnt\_enum==>Pos TS2.6 TurnsCntrValidity\_Cnt\_enum==>Min TS2.7 TurnsCntrValidity\_Cnt\_enum==>Max TS2.8 TurnsCntrValidity\_Cnt\_enum==>Pos TS2.9 AlignedCumMtrPos\_HwDeg\_M\_f32==>Max TS2.10 AlignedCumMtrPos\_HwDeg\_M\_f32==>Pos TS2.11 AlignedCumMtrPos\_HwDeg\_M\_f32==>Pos TS2.12 AlignedCumMtrPos\_HwDeg\_M\_f32==>Pos TS2.13 AlignedCumMtrPos\_HwDeg\_M\_f32==>Neg TS2.14 k\_VehCntrOffValidLimit\_HwDeg\_f32==>Max TS2.15 k\_VehCntrOffValidLimit\_HwDeg\_f32==>Pos TS2.16 k\_VehCntrOffValidLimit\_HwDeg\_f32==>Pos TS2.17 VehCntrOffset\_HwDeg\_M\_f32==>Max TS2.18 VehCntrOffset\_HwDeg\_M\_f32==>Pos TS2.19 VehCntrOffset\_HwDeg\_M\_f32=>Pos TS2.20 VehCntrOffset\_HwDeg\_M\_f32==>Pos TS2.21 VehCntrOffset\_HwDeg\_M\_f32==>Neg TS2.22 VehCntrValid\_Cnt\_M\_lgc==>Min TS2.23 VehCntrValid\_Cnt\_M\_lgc==>Max

TS2.23 VehCntrValid\_Cnt\_M\_lgc==>Max

Test Step 2.1 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_M	lanufMode_Cnt_enum_data		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_T	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data		
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_CustSetTrim()	34	34	~	

AbsHwPos\_SCom\_CustSetTrim

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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	-
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Manu	ufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turns	sCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>~</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	<b>~</b>

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2783.013736		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.475961		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	987.4038704		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1298.706022		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1377.424898		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1209.602194		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	55		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_M	anufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tu	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	16		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

AbsHwPos\_SCom\_CustSetTrim

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Name	Actual Value	Expected Value	Result
tat Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2075.327965		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-64.2319839		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMo	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCnt	rValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1386.308063		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	358		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2223.71791		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1012.772977		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1031.592314		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	673		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



T .				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2778.262678		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	876.8872956		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1053.975584		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	988		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1324.017901		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	nufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tu	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	409.8052046		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1303		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	123		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓



T .				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-524.0916791		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	771.3155495		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1618		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.11 (Repeat Count = 1)			
Name	Input Value		_
	· ·		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.0112959 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.0112959 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~



T and the second se				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1140.874864		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	988.8657172		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2248		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	64		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.13 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-619.01		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-452.81907		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrV	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	205.2756743		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2563		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	33		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.14 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	166.198763		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	404.8247678		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2878		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	185		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.15 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-612.0464776		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	870.1470994		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3193		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-612.046448	-612.0464776 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-612.046448	-612.0464776 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	e_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrV	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3508		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	78		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	907.8806047		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMod	e_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr	/alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	575.5253226		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3823		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	50		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1563.765938		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	178.0013868		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4138		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	_	

Test Step 2.19 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2688.781728		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1172.12		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrV	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1164.878309		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4453		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	-



T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>	

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-941.7753812		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1122.769698		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4768		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	933.2282791		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-578.13		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Man	ufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turn	sCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1599.216564		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5083		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	933.228271	933.2282791 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	933.228271	933.2282791 ± 0.0009	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	<b>✓</b>



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5398		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	14		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	-

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.23 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	804.1952507		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1562.118949		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	904.2132521		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5713		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	225		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



T							
Actual Function	Count	Expected Function	Count	Result			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~			
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>V</b>			

#### Test Case 3: Path Test Specification "Performance Metrics (With "None" Instrumentation and WithPS Environment" CPU Cycles:

TS3.1 2163.00 Cycles TS3.2 2974.00 Cycles TS3.3 2973.00 Cycles TS3.4 2980.00 Cycles

Description Vector Description:

 $TS3.1 \quad \text{if (TurnsCntrValidity\_Cnt\_T\_u08} == D\_TCVVALID\_CNT\_U08) => False \\ TS3.2 \quad \text{"if (TurnsCntrValidity\_Cnt\_T\_u08} == D\_TCVVALID\_CNT\_U08) => True \\ \text{if (Abs\_f32\_m(AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False \\ \text{if (AbsHwPos\_VehCntrValid\_Cnt\_M\_igc} == FALSE] => True'' \\ TS3.3 \quad \text{"if (Abs_f32\_m(AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => True \\ \text{if (AbsHwPos\_VehCntrValid\_Cnt\_M\_igc} == FALSE) => False'' \\ TS3.4 \quad \text{"((ManufMode\_Cnt\_T\_enum != ManufacturingMode)} = True&& \\ \text{(Rte\_Pim\_EOLVehCntrOffset()} >> EOLHwPosTrimPerformed\_Cnt\_u16 != D\_TRIMPERFORMED\_CNT\_U16)} ) = False'' \\ \end{aligned}$ 

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_I	ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_	TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	✓

T ·					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•	

Test Step 3.2 (Repeat Count = 1)	van de la companya d
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2783.013736
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.475961
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	987.4038704

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

tgt\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32

tgt\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16

AbsHwPos\_SCom\_CustSetTrim

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1

0

65535



65535 ± 0.09

0

Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~

Т			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Mar	ufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turn	nsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.0112959 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>~</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.0112959 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	<b>~</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	•

Test Step 3.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	153.14	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.475961	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	425.12	
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	

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Name	Input Value		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	153.139999	153.14 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	153.139999	153.14 ± 0.0009	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	✓

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

AbsHwPos\_SCom\_NxtSetTrim

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_NxtSetTrim

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDeftinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:29 Unit Test Plan Version:20 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Time Enabled	false

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AbsHwPos\_SCom\_NxtSetTrim

Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



AbsHwPos\_SCom\_NxtSetTrim

#### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 937.00 Cycles TS1.2 1724.00 Cycles

#### Description Vector Description:

TS1.1 Shortest Execution Path:

 $if \ (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => False"$ 

TS1.2 Longest Execution Path:

 $\label{eq:cont_unit_def} \begin{tabular}{ll} if (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => True \\ if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => True \\ \end{tabular}$ 

Test Step 1.1 (Repeat Count = 1)			✓	
Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Offset_HwDeg_T_f32	-4294967295			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data			
k_VehCntrOffValidLimit_HwDeg_f32	0			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_NxtSetTrim()	34	34	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	✓	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	500	500		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1268.56			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Offset_HwDeg_T_f32	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data		
k_VehCntrOffValidLimit_HwDeg_f32	1589.108169			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100			
Name	Actual Value	Expected Value	Resul	
AbsHwPos_SCom_NxtSetTrim()	0	0	•	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	500	500 ± 0.0009	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	500	500 ± 0.0009		

T ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•	



#### **Test Case 2: Boundary Test**

#### Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

CPU Cycles:

TS'2.1 937.00 Cycles
TS2.2 936.00 Cycles
TS2.3 1511.00 Cycles
TS2.4 1512.00 Cycles
TS2.4 1512.00 Cycles
TS2.5 1725.00 Cycles
TS2.6 1511.00 Cycles
TS2.7 1511.00 Cycles
TS2.8 936.00 Cycles
TS2.19 936.00 Cycles
TS2.10 934.00 Cycles
TS2.11 1511.00 Cycles
TS2.12 1512.00 Cycles
TS2.13 1512.00 Cycles
TS2.14 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.16 1512.00 Cycles
TS2.17 1725.00 Cycles
TS2.18 1511.00 Cycles

#### Description

#### Vector Description:

TS2.1 All Min TS2.2 All Max

TS2.3 Offset\_HwDeg\_T\_f32=>Min

TS2.4 Offset\_HwDeg\_T\_f32=>Max
TS2.5 Offset\_HwDeg\_T\_f32=>Zero
TS2.6 Offset\_HwDeg\_T\_f32=>Pos
TS2.6 Offset\_HwDeg\_T\_f32=>Neg
TS2.7 Offset\_HwDeg\_T\_f32=>Neg
TS2.8 TurnsCounterValidity\_Cnt\_u08=>Max
TS2.10 TurnsCounterValidity\_Cnt\_u08=>Max

TS2.10 TurnsCounterValidity\_Cnt\_u08=>Zero
TS2.11 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Min
TS2.12 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Max

TS2.13 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Zero

TS2.14 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Pos TS2.15 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Neg TS2.16 k\_VehCntrOffValidLimit\_HwDeg\_f32=>Min

TS2.17 k\_VehCntrOffValidLimit\_HwDeg\_f32=>Max
TS2.18 k\_VehCntrOffValidLimit\_HwDeg\_f32=>Pos

Test Step 2.1 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Offset_HwDeg_T_f32	-4294967295			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data			
k_VehCntrOffValidLimit_HwDeg_f32	0	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_NxtSetTrim()	34	34	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-1600	-1600 ± 0.009	<b>✓</b>	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	_

Test Step 2.2 (Repeat Count = 1)			<u> </u>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
			~

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AbsHwPos\_SCom\_NxtSetTrim

Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•	

Test Step 2.3 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2171.085737	-2171.085737		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.49			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Offset_HwDeg_T_f32	-4294967295			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data			
k_VehCntrOffValidLimit_HwDeg_f32	1541.247884	1541.247884		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.23			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_NxtSetTrim()	49	49	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.48999	1039.49 ± 0.009	✓	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.22998	1039.23 ± 0.009	✓	

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.4 (Repeat Count = 1)			✓	
Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2617.103771			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	700.85			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Offset_HwDeg_T_f32	4294967295			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	os_TurnsCntrValidity_Cnt_u08_data		
k_VehCntrOffValidLimit_HwDeg_f32	1493.816735	1493.816735		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_NxtSetTrim()	49	49	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	700.849976	700.85 ± 0.0009	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0	0 ± 0.000009	✓	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	_

Test Step 2.5 (Repeat Count = 1)		V
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	500	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1268.56	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	
Offset_HwDeg_T_f32	0	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1589.108169	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700	

 $tgt\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc

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500



500 ± 0.0009

AbsHwPos_SCom_NxtSetTrim			at
Input Value			
tgt_Pim_EOLVehCntrOffset			
100			
Actual Value	Expected Value		Result
0	0		~
500	500 ± 0.0009		~
	tgt_Pim_EOLVehCntrOffset 100 Actual Value 0	tgt_Pim_EOLVehCntrOffset  100  Actual Value  0  Expected Value  0	tgt_Pim_EOLVehCntrOffset           100           Actual Value         Expected Value           0         0

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>

Test Step 2.6 (Repeat Count = 1)			✓	
Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	600			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-887			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Offset_HwDeg_T_f32	277082.5472			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	S		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data		
k_VehCntrOffValidLimit_HwDeg_f32	1344.177408			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	100			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_NxtSetTrim()	49	49	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-887	-887 ± 0.0009	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	100	100 ± 0.0009	~	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	_

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1639.564105		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-460.23		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-123.209999		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	655.3265634		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	200		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-460.230011	-460.23 ± 0.0009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	200	200 ± 0.0009	<b>✓</b>

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.8 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-648.31
AbsHwPos_VehCntrOffset_HwDeg_M_f32	171.1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Offset_HwDeg_T_f32	1296856.832

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AbsHwPos\_SCom\_NxtSetTrim

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_	_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1289.24598		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	300.01001		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	171.100006	171.1 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	300.01001	300.01001 ± 0.0009	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	988.5		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-461.79		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	332748.3392		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1584.074584	1584.074584	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	400		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-461.790009	-461.79 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	400	400 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Name	Input Value		
	•		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1024		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-946	-946	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	
Offset_HwDeg_T_f32	60205312	60205312	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	413.7657129	413.7657129	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	500		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	56		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-946	-946 ± 0.0009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	500	500 ± 0.0009	•

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	~

Test Step 2.11 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125

AbsHwPos\_SCom\_NxtSetTrim

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Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.49		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	3267.28		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	s_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1541.247884		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.48999	1039.49 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039	1039 ± 0.009	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	416.5		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	547		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1112.066602		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	700.02002		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	416.5	416.5 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	700.02002	700.02002 ± 0.0009	~

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1153.65		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	305.294		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	s_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	27.19890016		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	800		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1153.65002	-1153.65 ± 0.009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	800	800 ± 0.0009	•

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

AbsHwPos\_SCom\_NxtSetTrim



Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1245.01		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1404.81		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	194.788		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	698		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-100		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1404.81006	-1404.81 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-100	-100 ± 0.0009	<b>✓</b>

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-3264.02		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	430.045		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	177.82		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1347.2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-200		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	430.045013	430.045 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-200	-200 ± 0.0009	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2590.633843		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1084.22		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	542.1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPe	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-300.019989		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1084.21997	1084.22 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-300.019989	-300.019989 ± 0.0009	<b>✓</b>

Т						
Actual Function	Count	Expected Function	Count	Result		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~		



AbsHwPos\_SCom\_NxtSetTrim

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	104.2976486		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-564		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	964.2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	0	0	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-859.902344	-859.9023514 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-859.902344	-859.9023514 ± 0.0009	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>✓</b>

Test Step 2.18 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1481.360235		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	874		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	356		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1000.12		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	736		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	874	874 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	736	736 ± 0.0009	<b>✓</b>

Τ				•
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

#### Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 937.00 Cycles TS3.2 1724.00 Cycles TS3.3 1513.00 Cycles

#### Description Vector Description:

 $\label{eq:total_$ 

Test Step 3.1 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125	
AbsHwPos VehCntrOffset HwDeg M f32	-1600	

AbsHwPos\_SCom\_NxtSetTrim

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Name	Input Value		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos	s_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	0	0	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	✓

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	500		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1268.56		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos	_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1589.108169		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	500	500 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	500	500 ± 0.0009	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>✓</b>

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2171.085737		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.49		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	:	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1541.247884	1541.247884	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.23		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.48999	1039.49 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.22998	1039.23 ± 0.009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	<b>V</b>



 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	2	
Successful	2	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Time Enabled	false



Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles

TS1.1 1158.00 Cycles TS1.2 1714.00 Cycles

**Description** Vector Description:

TS1.1 Shortest Execution Path:

 $(AbsHwPos\_RelHwPos\_HwDeg\_M\_f32>=D\_RELHWPOSMAX\_HWDEG\_F32)=>True"$ 

TS1.2 Longest Execution Path:

(AbsHwPos\_RelHwPos\_HwDeg\_M\_f32<=D\_RELHWPOSMAX\_HWDEG\_F32)=>True"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9875	9875 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	9875	9875 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	3200	3200 ± 0.1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	-

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.01		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	25	25 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	25	25 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	25	25 ± 0.1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•



#### **Test Case 2: Boundary Test**

#### Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1718.00 Cycles
TS2.2 1162.00 Cycles
TS2.2 11715.00 Cycles
TS2.3 1715.00 Cycles
TS2.4 1715.00 Cycles
TS2.5 1715.00 Cycles
TS2.6 1715.00 Cycles
TS2.7 1721.00 Cycles
TS2.9 1715.00 Cycles
TS2.9 1715.00 Cycles
TS2.10 1715.00 Cycles
TS2.11 1715.00 Cycles
TS2.12 1715.00 Cycles
TS2.12 1715.00 Cycles
TS2.13 1715.00 Cycles
TS2.14 1715.00 Cycles
TS2.15 1715.00 Cycles
TS2.16 1715.00 Cycles
TS2.17 1715.00 Cycles
TS2.18 1715.00 Cycles
TS2.19 1715.00 Cycles
TS2.19 1715.00 Cycles
TS2.19 1715.00 Cycles
TS2.20 1715.00 Cycles

#### Description

#### Vector Description:

TS2.1 All Min

TS2.1 All Min
TS2.2 All Max
TS2.3 AlignedCumMechMtrPosCRF\_Deg\_f32==>Min
TS2.4 AlignedCumMechMtrPosCRF\_Deg\_f32==>Max
TS2.5 AlignedCumMechMtrPosCRF\_Deg\_f32==>Pos
TS2.6 AlignedCumMechMtrPosCRF\_Deg\_f32==>Zero
TS2.7 AlignedCumMechMtrPosCRF\_Deg\_f32==>Neg
TS2.8 ComplError\_HwDeg\_f32==>Min
TS2.9 ComplError\_HwDeg\_f32==>Min
TS2.10 ComplError\_HwDeg\_f32==>Pos
TS2.11 ComplError\_HwDeg\_f32==>Xero
TS2.12 ComplError\_HwDeg\_f32==>Neg
TS2.13 CumMechMtrPosCRF\_Deg\_f32==>Max
TS2.14 CumMechMtrPosCRF\_Deg\_f32==>Max
TS2.15 CumMechMtrPosCRF\_Deg\_f32==>Pos
TS2.16 CumMechMtrPosCRF\_Deg\_f32==>Neg
TS2.17 CumMechMtrPosCRF\_Deg\_f32==>Neg
TS2.18 k\_GearRatio\_Uls\_f32==>Min
TS2.19 k\_GearRatio\_Uls\_f32==>Max
TS2.20 k\_GearRatio\_Uls\_f32==>Pos

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.01		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPos	CRF_Deg_1 tgt_AbsHwPos_Per1_AlignedCum	MechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_D	eg_f32 tgt_AbsHwPos_Per1_CumMechMt	rPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_h	lwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	25	25 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	25	25 ± 0.00009	✓
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	25	25 ± 0.1	<b>✓</b>

T				<b>~</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_transformed and the property of the prope$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9875	9875 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	9875	9875 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	3200	3200 ± 0.1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	0.02		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	88.6		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	647.2		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-288.600006	-288.6 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-75.6559982	-75.656 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-75.6559982	-75.656 ± 0.1	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•	

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.03		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	101.2		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-361		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Degrade and the property of t$	t tgt_AbsHwPos_Per1_AlignedCumMechMtrl	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	198.800003	198.8 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-112.029999	-112.03 ± 0.0009	~
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	-112.029999	-112.03 ± 0.1	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•



Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.04		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	543.87		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-54.8		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	912		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_1$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	76.5547943	76.5548 ± 00.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	91.2799988	91.28 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	91.2799988	91.28 ± 0.1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.05		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	0		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	31.7		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	6123.6		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-31.7000008	-31.7 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	274.480011	274.48 ± 0.0009	<b>V</b>
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	274.480011	274.48 ± 0.1	~

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.7 (Repeat Count = 1)			· ·
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-879.6		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	89		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-4456		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Inst\_Ap\_AbsHwPosAbsHw$	Deg_1 tgt_AbsHwPos_Per1_AlignedCu	mMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplErro	r_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f3:	2 tgt_AbsHwPos_Per1_CumMech	MtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos	_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-968.599976	-968.6 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-4545	-4545 ± 00.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-3200	-3200 ± 0.1	·

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•



Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.07		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	7645.12		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	1023.54		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_1	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	660.158386	660.1584 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	196.647797	196.6478 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	196.647797	196.6478 ± 0.1	•

T						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~		

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	0.08		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	8123.4		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4789.6		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_Inst\_Ap\_AbsHwPosA$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	524.871948	524.872 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	258.167999	258.168 ± 0.0009	•
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	258.167999	258.168 ± 0.1	~

Τ						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•		

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.09		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-8601.68		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	66.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	8555.66		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_De	g_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrl	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-840.651184	-840.6512 ± 0.0009	-
AbsHwPos_RelHwPos_HwDeg_M_f32	703.50946	703.5094 ± 0.0009	-
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	703.50946	703.5094 ± 0.1	•

Т					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~	



Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	9079.96		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	0		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	648.6		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	907.996033	907.996 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	64.8600006	64.86 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	64.8600006	64.86 ± 0.1	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.12 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.11		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	647.2		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-75.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	987.1		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_t tgt_AbsHwPos_Per1_AlignedCumMechMtrl	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	146.692001	146.692 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	184.080994	184.081 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	184.080994	184.081 ± 0.1	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~	

Test Step 2.13 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.12		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-361		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	41.9		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-85.2200012	-85.22 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-1241.90002	-1241.9 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-1241.90002	-1241.9 ± 0.1	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•



Test Step 2.14 (Repeat Count = 1)			· ·
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	0.13		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	912		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	69.4		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Derivative and the property of the property o$	Deg_1 tgt_AbsHwPos_Per1_AlignedC	umMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplErr	or_HwDeg_f32	
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_CumMechMtrPosCRF\_Deg\_f32$	tgt_AbsHwPos_Per1_CumMec	hMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPo	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	49.159996	49.16 ± 0.00009	<b>✓</b>
AbsHwPos_RelHwPos_HwDeg_M_f32	1230.59998	1230.6 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	1230.59998	1230.6 ± 0.1	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.15 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.14		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	545.3		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-50.8		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	743.87		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_AbsHwPosA$	_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_t	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	127.141998	127.142 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	154.941803	154.9418 ± 0.0009	<b>✓</b>
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	154.941803	154.9418 ± 0.1	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.15		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-3479		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	99		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_De	t tgt_AbsHwPos_Per1_AlignedCumMechMtrf	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-620.850037	-620.85 ± 0.0009	-
AbsHwPos_RelHwPos_HwDeg_M_f32	-99	-99 ± 0.00009	-
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	-99	-99 ± 0.1	-

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.17 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.16		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	7258		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-105.6		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-849.6		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1266.88	1266.88 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-30.3359909	-30.336 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-30.3359909	-30.336 ± 0.1	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.18 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.01		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-6123.6		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	120.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4426		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-181.735992	-181.736 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-76.2400055	-76.24 ± 0.00009	-
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-76.2400055	-76.24 ± 0.1	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.19 (Repeat Count = 1)			✓.
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-2456		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	96.6		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	589		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2552.6001	-2552.6 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	492.399994	492.4 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	492.399994	492.4 ± 0.1	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•



Test Step 2.20 (Repeat Count = 1)			<b>~</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.85		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	1023.54		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-44.8		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-456.5		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_transformed and the property of the prope$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	914.809021	914.809 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-343.225037	-343.225 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-343.225037	-343.225 ± 0.1	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

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AbsHwPos\_SCom\_NxtClearTrim

 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_NxtClearTrim

### Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

#### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\\\alpha_AbsHwPos -I\\$(PROJECTROOT)\NxtrLib\include -I\\$(PROJECTROOT)\\StdDef\include -I\\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

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AbsHwPos\_SCom\_NxtClearTrim

Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



### **Test Case 1: Boundary Test**

AbsHwPos\_SCom\_NxtClearTrim

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 381.00 Cycles TS1.2 381.00 Cycles TS1.3 381.00 Cycles TS1.4 381.00 Cycles TS1.5 381.00 Cycles

Description Vector Description:

TS1.1 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Min TS1.2 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Max TS1.3 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Mid TS1.4 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Pos TS1.5 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Neg

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.4 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_VehCntrOffset_HwDeg_M_f32	12564.01
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos

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AbsHwPos_SCom_NxtClearTrim	
Name	Input Value
tot Dto José As Abollo Des Dies FOLVab Ostroffe et	1-1 Di- FOLV-10

Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1523.02		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 TrimNotPerfDiag

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include

Name	Text
/lodule 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Library	libc.a	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Time Enabled	false	

TrimNotPerfDiag

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Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy Work Area\CBD AbsHwPos TcI2cVd\UnitTestEnv\config\UDE TMS570 DEBUG.WSP



#### **Test Case 1: Metrics Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 740.00 Cycles TS1.2 1728.00 Cycles

Description Vector Description:

TS 1.1 Shortest Execution Path:

(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=False, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And\_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>True

TS 1.2 Longest Execution Path:

(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And \_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>False

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

T					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~



#### **Test Case 2: Boundary Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 740.00 Cycles
TS2.2 1728.00 Cycles
TS2.3 739.00 Cycles
TS2.4 1731.00 Cycles
TS2.5 739.00 Cycles
TS2.6 1521.00 Cycles
TS2.7 739.00 Cycles
TS2.8 1731.00 Cycles
TS2.8 1731.00 Cycles
TS2.9 739.00 Cycles
TS2.10 1520.00 Cycles

#### Vector Description: Description

TS 2.1 All Min TS 2.2 All Max

TS 2.2 All Max
TS 2.3 ManufMode\_Cnt\_T\_enum=>Min
TS 2.4 ManufMode\_Cnt\_T\_enum=>Max
TS 2.5 ManufMode\_Cnt\_T\_enum=>Pos
TS 2.6 Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Min
TS 2.7 Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Max
TS 2.8 Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Pos
TS 2.9 k\_UseTurnsCntr\_Cnt\_lgc=>Min
TS 2.10 k\_UseTurnsCntr\_Cnt\_lgc=>Max

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	*none*	<b>✓</b>

T					
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	~	

Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

<b>Τ</b>						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~		

Test Step 2.3 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~





T						
Actual Function	Count	Expected Function	Count	Result		
*none*	0	*** No Call Expected ***	0	~		

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				V
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~





Test Step 2.7 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

T					<b>✓</b>
Actual Function	Count	Expected Function	Count	Resu	ılt
*none*	0	*** No Call Expected ***	0		•

Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2449		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1284		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	<b>V</b>

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6258		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~





T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

#### Test Case 3: Path Test

Specification "Performance M

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 741.00 Cycles TS3.2 1727.00 Cycles TS3.3 1731.00 Cycles

**Description** Vector Description:

TS 3.1 "(k\_UseTurnsCntr\_Cnt\_Igc == TRUE)=False, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And \_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>True"
TS 3.2 "(k\_UseTurnsCntr\_Cnt\_Igc == TRUE)=True, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And \_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>False"
TS 3.3 "(k\_UseTurnsCntr\_Cnt\_Igc == TRUE)=True,( (ManufMode\_Cnt\_T\_enum != ManufacturingMode) && (Rte\_Pim\_EOLVehCntrOffset()->EOLHwPosTrimPerformed\_Cnt\_u16 != D\_TRIMPERFORMED\_CNT\_U16))=>False"

Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 0 *none*		~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Cnt_T_enum) 0 *none*		✓

Τ			V	
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0		~

T .				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	

Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1		-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

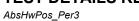
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T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per3

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Library	libc.a	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>	
Time Enabled	false	

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AbsHwPos\_Per3



Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 1029.00 Cycles TS1.2 1574.00 Cycles

Description Vector Description:

TS1.1"Shortest Execution Path:
if (I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=>False"
TS1.2"Longest Execution Path:
if (I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=>True
if (I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=>True
(FALSE == AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc)=True
if (Abs\_f32\_m(I2CHwAbsPos\_HwDeg\_T\_f32 - NewRelHwPos\_HwDeg\_T\_f32) >= k\_HWAtoMtrADiffLimit\_HwDeg\_f32)=>False
(AbsHwPos\_HwAtoMtrAFItAcc\_Cnt\_M\_u16 == 0U)=True

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAb	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	125		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	420		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	182		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	<b>✓</b>
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•





T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	-

### **Test Case 2: Boundary Test**

#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1029.00 Cycles
TS2.2 1585.00 Cycles
TS2.3 1575.00 Cycles
TS2.4 969.00 Cycles
TS2.5 1565.00 Cycles
TS2.6 1119.00 Cycles
TS2.7 1104.00 Cycles
TS2.7 1104.00 Cycles
TS2.8 1060.00 Cycles
TS2.10 1056.00 Cycles
TS2.11 1549.00 Cycles
TS2.11 1549.00 Cycles
TS2.12 1056.00 Cycles
TS2.13 1086.00 Cycles
TS2.14 1056.00 Cycles
TS2.15 1069.00 Cycles
TS2.15 1069.00 Cycles
TS2.15 1069.00 Cycles
TS2.16 1056.00 Cycles
TS2.17 1549.00 Cycles
TS2.18 1549.00 Cycles
TS2.19 1060.00 Cycles
TS2.19 1060.00 Cycles
TS2.19 1060.00 Cycles
TS2.20 1529.00 Cycles
TS2.21 1060.00 Cycles
TS2.22 1069.00 Cycles
TS2.23 1060.00 Cycles
TS2.24 1559.00 Cycles
TS2.25 1549.00 Cycles
TS2.26 1056.00 Cycles
TS2.27 1056.00 Cycles
TS2.28 1034.00 Cycles
TS2.28 1034.00 Cycles
TS2.29 1056.00 Cycles
TS2.31 949.00 Cycles
TS2.33 949.00 Cycles
TS2.33 1956.00 Cycles
TS2.33 1056.00 Cycles
TS2.33 1549.00 Cycles
TS2.34 1069.00 Cycles
TS2.35 1549.00 Cycles
TS2.35 1549.00 Cycles

#### Description

Vector Description:

TS2.1All Min TS2.2All Max TS2.3I2CHwAbsPosValid Cnt lgc=>True TS2.4I2CHwAbsPosValid\_Cnt\_lgc=>False TS2.5I2CHwAbsPos\_HwDeg\_f32==>Min TS2.6I2CHwAbsPos\_HwDeg\_f32==>Max TS2.7I2CHwAbsPos\_HwDeg\_f32==>Zero TS2.8l2CHwAbsPos\_HwDeg\_f32==>Pos TS2.9l2CHwAbsPos\_HwDeg\_f32==>Neg TS2.10AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Min TS2.11AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Max TS2.12AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Zero TS2.13AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Pos TS2.14AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Neg TS2.15AbsHwPos\_HwAtoMtrAFltAcc\_Cnt\_M\_u16==>Min TS2.16AbsHwPos\_HwAtoMtrAFltAcc\_Cnt\_M\_u16==>Max TS2.17AbsHwPos\_HwAtoMtrAFltAcc\_Cnt\_M\_u16==>Pos TS2.18k\_HWAtoMtrADiffLimit\_HwDeg\_f32==>Min TS2.19k\_HWAtoMtrADiffLimit\_HwDeg\_f32==>Max TS2.20k\_HWAtoMtrADiffLimit\_HwDeg\_f32==>Pos TS2.21k\_HwAtoMtrAError\_str.Threshold==>Min TS2.22k\_HwAtoMtrAError\_str.Threshold==>Max TS2.23k\_HwAtoMtrAError\_str.Threshold==>Pos TS2.24k\_HwAtoMtrAError\_str.PStep==>Min TS2.25k\_HwAtoMtrAError\_str.PStep==>Max TS2.26k\_HwAtoMtrAError\_str.PStep==>Pos TS2.20K\_HWAtoMtrAError\_str.PStep==>Pos
TS2.27K\_HWAtoMtrAError\_str.NStep==>Min
TS2.28K\_HWAtoMtrAError\_str.NStep==>Max
TS2.29K\_HWAtoMtrAError\_str.NStep==>Pos
TS2.30AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc==>Min
TS2.31AbsHwPos\_MtrToHwAOfst\_HwDeg\_M\_f32==>Min
TS2.32AbsHwPos\_MtrbHwAOfst\_HwDeg\_M\_f32==>Min TS2.33AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Max TS2.34AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Pos TS2.35AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Zero TS2.36AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Neg

# Test Step 2.1 (Repeat Count = 1) Name Input Value AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16 Input Value 0

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

AbsHwPos\_Per3

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Name	Input Value		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosVa	id_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_H	wDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535	65535	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	11026 ± 0.09	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996	
AbsHwPos_RelHwPos_HwDeg_M_f32	420	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5	
k_HwAtoMtrAError_str.Threshold	470	
k_HwAtoMtrAError_str.PStep	124	
k_HwAtoMtrAError_str.NStep	182	
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1	

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) \\ Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) \\$ 

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	(Siper)	210
Input Value		
666.200012		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg	_f32	
Actual Value	Expected Value	Result
0	0	~
1	1	•
-246.200012	-246.199997 ± 0.0009	~
117	117	~
	666.200012 tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cr tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg  Actual Value 0 1 -246.200012	666.200012 tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32  Actual Value  0

0

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

0

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	148		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6214.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	-329.299988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	355.600006		
k_HwAtoMtrAError_str.Threshold	493		
k_HwAtoMtrAError_str.PStep	48		
k_HwAtoMtrAError_str.NStep	173		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-457		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	.bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	.bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6214.20996	-6214.20996 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	*none*	<b>✓</b>

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Test Step 2.5 (Repeat Count = 1)	1 434.1		
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	1718		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2553.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	238.600006		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	378.700012		
k_HwAtoMtrAError_str.Threshold	516		
k_HwAtoMtrAError_str.PStep	66		
k_HwAtoMtrAError_str.NStep	164		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	.bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516	516	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2553.20996	-2553.20996 ± 0.009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	•

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	194		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1107.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	147.899994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	10		
k_HwAtoMtrAError_str.NStep	155		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	39	39	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-752.099976	-752.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>	

Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	217		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	57.2000008		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994		
k_HwAtoMtrAError_str.Threshold	562		
k_HwAtoMtrAError_str.PStep	77		
k_HwAtoMtrAError_str.NStep	146		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbs	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	294	294	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	4768.79004 ± 0.009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	<b>₩</b>

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•





Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	2840		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	8429.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	-33.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	448		
k_HwAtoMtrAError_str.Threshold	654		
k_HwAtoMtrAError_str.PStep	91		
k_HwAtoMtrAError_str.NStep	137		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	145.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	2703	2703	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-179.100006	-179.100006 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	263		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1245.22998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1000		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	471.100006		
k_HwAtoMtrAError_str.Threshold	677		
k_HwAtoMtrAError_str.PStep	105		
k_HwAtoMtrAError_str.NStep	128		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-558.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	135	135	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1245.22998	1245.22998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.10 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	286	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-800.210022	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	494.200012	
k_HwAtoMtrAError_str.Threshold	7000	
k_HwAtoMtrAError_str.PStep	119	

AbsHwPos\_Per3

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Name Input Value

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	•

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	355		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-4829.18018		
AbsHwPos_RelHwPos_HwDeg_M_f32	465.299988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	563.5		
k_HwAtoMtrAError_str.Threshold	1718		
k_HwAtoMtrAError_str.PStep	161		
k_HwAtoMtrAError_str.NStep	92		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	312.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_Hwl	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516	516	✓
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-4829.18018	-4829.18018 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>~</b>

Name	Input Value		
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	378		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6172.16992		
AbsHwPos_RelHwPos_HwDeg_M_f32	-88.9899979		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	586.599976		
k_HwAtoMtrAError_str.Threshold	194		
k_HwAtoMtrAError_str.PStep	175		
k_HwAtoMtrAError_str.NStep	83		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-469.399994		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	295	295	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	380.410004	380.410004 ± 0.0009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•



Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7515.16016		
AbsHwPos_RelHwPos_HwDeg_M_f32	101.400002		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	212.399994		
k_HwAtoMtrAError_str.Threshold	217		
k_HwAtoMtrAError_str.PStep	189		
k_HwAtoMtrAError_str.NStep	74		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	626.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	189	189	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7515.16016	-7515.16016 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.16 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-8858.15039		
AbsHwPos_RelHwPos_HwDeg_M_f32	-457		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	62.7999992		
k_HwAtoMtrAError_str.Threshold	2840		
k_HwAtoMtrAError_str.PStep	203		
k_HwAtoMtrAError_str.NStep	65		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-782.799988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	I_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_Hw	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	65470	65470	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	325.799988	325.799988 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.17 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	4475	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10201.1396	
AbsHwPos_RelHwPos_HwDeg_M_f32	123.800003	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	655.900024	
k_HwAtoMtrAError_str.Threshold	263	



Name	Input Value		
k_HwAtoMtrAError_str.PStep	217		
k_HwAtoMtrAError_str.NStep	105		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	692.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	263	263	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10201.1396	-10201.1396 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)			<u> </u>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	470		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	700.22998		
AbsHwPos_RelHwPos_HwDeg_M_f32	756		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	286		
k_HwAtoMtrAError_str.PStep	231		
k_HwAtoMtrAError_str.NStep	119		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-601.400024		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAl	osPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAl	osPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	286	286	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	700.22998	700.22998 ± 0.0009	·
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	·
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	493		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1897.31995		
AbsHwPos_RelHwPos_HwDeg_M_f32	-465.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	3709		
k_HwAtoMtrAError_str.PStep	245		
k_HwAtoMtrAError_str.NStep	133		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	510.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	osValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	os_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	360	360	•

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Name	Actual Value	Expected Value	Result
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-976.200012	-976.200012 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	<b>✓</b>

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	516		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	3094.40991		
AbsHwPos_RelHwPos_HwDeg_M_f32	312.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	894.01001		
k_HwAtoMtrAError_str.Threshold	332		
k_HwAtoMtrAError_str.PStep	236		
k_HwAtoMtrAError_str.NStep	147		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	420		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	332	332	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	3094.40991	3094.40991 ± 0.009	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	

Test Step 2.21 (Repeat Count = 1)			✓.
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	5398		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4291.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	564		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	227		
k_HwAtoMtrAError_str.NStep	161		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-329.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	5237	5237	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	798.699951	798.700012 ± 0.0009	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~





T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	562		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	5488.58984		
AbsHwPos_RelHwPos_HwDeg_M_f32	1016.09998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1021.70001		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	218		
k_HwAtoMtrAError_str.NStep	175		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-238.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwI	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	780	780	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	5488.58984	5488.58984 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	585		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	6685.68018		
AbsHwPos_RelHwPos_HwDeg_M_f32	-782.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	179.399994		
k_HwAtoMtrAError_str.Threshold	378		
k_HwAtoMtrAError_str.PStep	182		
k_HwAtoMtrAError_str.NStep	189		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	147.899994		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	396	396	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-930.699951	-930.700012 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>		



Test Step 2.24 (Repeat Count = 1)			V	
Name	Input Value			
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	108	108		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0			
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	7882.77002			
AbsHwPos_RelHwPos_HwDeg_M_f32	692.099976			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_HWAtoMtrADiffLimit_HwDeg_f32	137.100006			
k_HwAtoMtrAError_str.Threshold	489			
k_HwAtoMtrAError_str.PStep	0			
k_HwAtoMtrAError_str.NStep	203			
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1			
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	600			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32		
Name	Actual Value	Expected Value	Result	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	92.0999756	92.0999985 ± 0.00009	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.25 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6317		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	9079.86035		
AbsHwPos_RelHwPos_HwDeg_M_f32	756		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	694.799988		
k_HwAtoMtrAError_str.Threshold	646		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	217		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-33.5		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	646	646	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	9079.86035	9079.86035 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	654
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	10276.9502
AbsHwPos_RelHwPos_HwDeg_M_f32	819.900024
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HWAtoMtrADiffLimit_HwDeg_f32	123

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Name	Input Value			
k_HwAtoMtrAError_str.Threshold	8974	974		
k_HwAtoMtrAError_str.PStep	191			
k_HwAtoMtrAError_str.NStep	199			
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1			
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-897.099976			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg	g_f32		
Name	Actual Value	Expected Value	Result	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	455	455	~	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1717	1717 ± 0.009	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~	

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

Name	Input Value		
	677		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16			
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-5478.31982		
AbsHwPos_RelHwPos_HwDeg_M_f32	-883.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1310.19995		
k_HwAtoMtrAError_str.Threshold	424		
k_HwAtoMtrAError_str.PStep	173		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	465.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	677	677	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1349.09998	-1349.09998 ± 0.009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	-

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	7000		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1		
AbsHwPos_RelHwPos_HwDeg_M_f32	947.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1367.90002		
k_HwAtoMtrAError_str.Threshold	4477		
k_HwAtoMtrAError_str.PStep	164		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-88.9899979		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	d_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_Hw	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6745	6745	•
AbsHwPos MtrToHwAOfstLearnt Cnt M Igc	1	1	

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Name	Actual Value	Expected Value	Result
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1	1 ± 0.000009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	723		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1457.31995		
AbsHwPos_RelHwPos_HwDeg_M_f32	-1011.59998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1425.59998		
k_HwAtoMtrAError_str.Threshold	1470		
k_HwAtoMtrAError_str.PStep	155		
k_HwAtoMtrAError_str.NStep	66		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	75.75		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAb	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	657	657	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1087.34998	-1087.34998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.30 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1245.59998		
AbsHwPos_RelHwPos_HwDeg_M_f32	-465.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	378.700012		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	203		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	692.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1245.59998	-1245.59998 ± 0.009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.31 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	5398		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1247		
AbsHwPos_RelHwPos_HwDeg_M_f32	312.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	493		
k_HwAtoMtrAError_str.PStep	32		
k_HwAtoMtrAError_str.NStep	217		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-601.400024		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1247	1247 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

Test Step 2.32 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	562		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994		
k_HwAtoMtrAError_str.Threshold	516		
k_HwAtoMtrAError_str.PStep	145		
k_HwAtoMtrAError_str.NStep	231		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	510.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAb	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516	516	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	· ·
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.33 (Repeat Count = 1)		
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	585	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	
AbsHwPos_RelHwPos_HwDeg_M_f32	1016.09998	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	



Name	Input Value		
k_HWAtoMtrADiffLimit_HwDeg_f32	448		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	99		
k_HwAtoMtrAError_str.NStep	245		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	420		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	340	340	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	596.099976	596.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.34 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	108		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	2145.66992		
AbsHwPos_RelHwPos_HwDeg_M_f32	-782.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	471.100006		
k_HwAtoMtrAError_str.Threshold	562		
k_HwAtoMtrAError_str.PStep	87		
k_HwAtoMtrAError_str.NStep	236		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-329.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	195	195	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	2145.66992	2145.66992 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6317		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	0		
AbsHwPos_RelHwPos_HwDeg_M_f32	692.099976		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	494.200012		
k_HwAtoMtrAError_str.Threshold	654		
k_HwAtoMtrAError_str.PStep	128		
k_HwAtoMtrAError_str.NStep	227		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-238.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsI	PosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	654	654	•

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AbsHwPos\_Per3

Name	Actual Value	Expected Value	Result
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	0	0 ± 0.000009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	-

Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	654		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7894.12012		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	517.299988		
k_HwAtoMtrAError_str.Threshold	677		
k_HwAtoMtrAError_str.PStep	214		
k_HwAtoMtrAError_str.NStep	91		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	100		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	563	563	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	369.399994	369.399994 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

#### Test Case 3: Path Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)  $\,$ 

CPU Cycles:

TS3.1 1029.00 Cycles TS3.2 1585.00 Cycles TS3.3 1575.00 Cycles TS3.4 1119.00 Cycles TS3.5 1104.00 Cycles

#### Description Vector Description:

TS3.1(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=False
TS3.2"(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=True
(FALSE == AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc)=False
(Abs\_f32\_m(I2CHwAbsPos\_HwDeg\_T\_f32 - NewRelHwPos\_HwDeg\_T\_f32) >= k\_HWAtoMtrADiffLimit\_HwDeg\_f32)=True
(DiagFailed\_m(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16, k\_HwAtoMtrAError\_str) == TRUE)=True"
TS3.3"(FALSE == AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc)=True
(Abs\_f32\_m(I2CHwAbsPos\_HwDeg\_T\_f32 - NewRelHwPos\_HwDeg\_T\_f32) >= k\_HWAtoMtrADiffLimit\_HwDeg\_f32)=False
(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16 == 0U)=True"
TS3.4(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16 == 0U)=False
TS3.5(DiagFailed\_m(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16, k\_HwAtoMtrAError\_str) == TRUE)=False

Test Step 3.1 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	

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Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535	65535	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	11026 ± 0.09	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

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Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996
AbsHwPos_RelHwPos_HwDeg_M_f32	420
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5
k_HwAtoMtrAError_str.Threshold	470
k_HwAtoMtrAError_str.PStep	124
k_HwAtoMtrAError_str.NStep	182
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc
tqt Rte Inst Ap AbsHwPos.AbsHwPos Per3 I2CHwAbsPos HwDeg f32	tat AbsHwPos Per3 I2CHwAbsPos HwDea f32

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Name	Input Value			
k_HwAtoMtrAError_str.PStep	77			
k_HwAtoMtrAError_str.NStep	146			
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1			
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	0			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbs	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	294	294	<b>✓</b>	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	4768.79004 ± 0.009	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

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AbsHwPos\_SCom\_CustClrTrim



 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_CustClrTrim

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	2	
Successful	2	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

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AbsHwPos\_SCom\_CustClrTrim

Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



AbsHwPos\_SCom\_CustClrTrim

#### **Test Case 1: Metrics Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 792.00 Cycles TS1.2 1012.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

If ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And D\_TRIMPERFORMED\_CNT\_U16))=>False"

(Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <>

TS1.2 Longest Execution Path:

If ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And D\_TRIMPERFORMED\_CNT\_U16))=>True"

(Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <>

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	47		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Man	ufMode_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

T ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>	



# AbsHwPos\_SCom\_CustClrTrim

#### Test Case 2: Boundary Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 792.00 Cycles TS2.2 1012.00 Cycles TS2.3 792.00 Cycles TS2.4 1012.00 Cycles TS2.5 792.00 Cycles

Description Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ManufMode\_Cnt\_enum==>Min
TS2.4 ManufMode\_Cnt\_enum==>Max
TS2.5 ManufMode\_Cnt\_enum==>Pos

Test Step 2.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_	ManufMode_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>
tat Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data

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AbsHwPos\_SCom\_CustClrTrim

Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1542		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.4 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos	_ManufMode_Cnt_enum_data		
k_UseTurnsCntr_Cnt_lgc	1			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	364	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2			
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Dto Call An Abally Dag Nyth DigaMan CathITCStatus/			0	

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(

AbsHwPos\_SCom\_CustClrTrim

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Init1

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\-I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\-Ip\_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Time Enabled	false

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Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 1113.00 Cycles TS1.2 1347.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

 $If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False \\ If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffs$ 

TS1.2 Longest Execution Path:

 $If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) >> True \\ If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLvehCntrOffset.EOLvehCntrOffs$ 

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enur	n	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.992837846	0.992837853 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>~</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

T				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSourc	e_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPo	psStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.961661458	0.961661459 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039	1039 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

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AbsHwPos\_Init1

Name	Actual Value	Expected Value	Result
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

# 

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_C	nt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPos	Status_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0	0 ± 0.015625	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tot AbsHwPos Init1 SrlComHwPosStatus Cnt u16.value	65534	65534	<b>✓</b>

 $\ensuremath{\text{@}}$  Report created by TESSY V3.1.7, report template V2.1

Test Step 2.1 (Repeat Count = 1)

4



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	500		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.998132586	0.998132557 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>~</b>
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

T				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	e_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.992837846	0.992837853 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt AbsHwPos Init1 HwPosSource Cnt u16.value	65534	65534	<b>✓</b>
tgt_Absriwi os_initi_riwi osoodice_ont_u ro.valde			

Count Expected Function

TrimNotPerfDiag

Actual Function

TrimNotPerfDiag

Count Result



Test Step 2.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	25.18166721		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	436.9878821		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Init1_ManufMode_Cnt_enum tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		ı	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.271262884	0.271262842 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•

Τ				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.01001		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enur	m	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.961661458	0.961661459 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.01001	1039.01001 ± 0.009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.6 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	379.2717962



Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1201.851037		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1519		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enui	m	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.991486251	0.991486226 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0	0 ± 0.000009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	401.1355943		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	436.9807251		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700.02		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1984		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum tgt_AbsHwPos_Init1_		t_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.993531525	0.993531549 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt AbsHwPos Init1 SrlComHwPosStatus Cnt u16.value	65534	65534	•

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.8 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	162.9279281
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	0
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	879.9430598
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2449

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Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	n	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.870931625	0.870931593 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	487.3372427		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1555.129529		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2914		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cr	nt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosSta	atus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.997810423	0.997810453 ± 0.015625	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1555.12952	-1555.129529 ± 0.009	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrValid_Cnt_M_lgc	-1555.12952 1	1111	· ·
		-1555.129529 ± 0.009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	-1555.129529 ± 0.009	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 504	-1555.129529 ± 0.009 1 *none*	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 504 1	-1555.129529 ± 0.009 1 *none* *none*	\(\frac{1}{2}\)

T				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~





Test Step 2.10 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	402.9378512		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1434.02		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	835.0762143		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3379		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16	3	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_C	cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.993676424	0.993676398 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	835.076233	835.0762143 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	49.55192			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1007.182555			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3844			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_	u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_en	um		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0	0 ± 0.015625	✓	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.12 (Repeat Count = 1)	<b>→</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	500
k_UseTurnsCntr_Cnt_lgc	1



Name	Input Value		
k_VehCntrOffValidLimit_HwDeg_f32	516.6709843		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	669.6303178		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4309		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16	3	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum	ı	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.998132586	0.998132557 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.13 (Repeat Count = 1)			~	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	111.03			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	1264.996348			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1454.825825			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4774			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cr	ıt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.752227485	0.752134034 ± 0.015625	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	-	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	
tgt AbsHwPos Init1 HwPosSource Cnt u16.value	65534	65534	<b>✓</b>	
tgt_/tbs/fivir ob_init1_fivir obcodioc_ont_u1o.valae				

T T				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.14 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	433.7
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	678
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	564.5
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5239
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16

tgt\_AbsHwPos\_Init1\_SrlComHwPosStatus\_Cnt\_u16.value

AbsHwPos\_Init1

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Input Value tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Init1\_ManufMode\_Cnt\_enum tgt\_AbsHwPos\_Init1\_ManufMode\_Cnt\_enum tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Init1\_SrlComHwPosStatus\_Cnt\_u16 tgt\_AbsHwPos\_Init1\_SrlComHwPosStatus\_Cnt\_u16  $tgt\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset$ tgt\_Pim\_EOLVehCntrOffset **Actual Value Expected Value** AbsHwPos\_HandwheelPositionLPF\_M\_str.K\_Uls\_f32 0.995703876 0.995703845 ± 0.015625 AbsHwPos\_HwPosSource\_Cnt\_M\_u16 65534 65534 HWPOS\_STATE\_INIT AbsHwPos\_HwPosState\_Cnt\_M\_enum HWPOS\_STATE\_INIT AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16 65534 65534 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32 564.5 564.5 ± 0.0009 AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 504 504 Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) 1 1  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ tgt\_AbsHwPos\_Init1\_HwPosSource\_Cnt\_u16.value 65534 65534

T				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

65534

65534

Test Step 2.15 (Repeat Count = 1)			✓	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_HwPosOutputLPFCoeffFc_Hz_f32	78			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	984			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-255			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5704			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	n		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.624755323	0.624755291 ± 0.015625	<b>~</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>~</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-255	-255 ± 0.0009	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>~</b>	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>V</b>

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	64		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	127.6		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	987		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6169		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_	u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_en	um	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus	s_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.552575648	0.552575611 ± 0.015625	

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AbsHwPos\_Init1

Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

#### Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 1113.00 Cycles TS3.2 1347.00 Cycles

Description Vector Description:

 $TS3.1 \quad (Abs\_m(Rte\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) = False \\ TS3.2 \quad (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) = True \\ TS3.2 \quad (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EO$ 

Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	710.623859			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSourc	e_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.992837846	0.992837853 ± 0.015625	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
tgt AbsHwPos Init1 SrlComHwPosStatus Cnt u16.value	65534	65534		

T					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	

Test Step 3.2 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039	

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Name	Input Value			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cr	nt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosS	Status_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.961661458	0.961661459 ± 0.015625	<b>~</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039	1039 ± 0.009	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•



 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per2

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd	
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml	
Target Environment	TI TMS 570 PLS UDE (Default)	
Kind of Test	Unit Test	
Linker Options		
Source File(s)		
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c	
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include	

Name	Text
Module 'AbsHwPos_Tcl2lVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Library	libc.a	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Time Enabled	false	



Attributes		
Name	Value	
Time Unit	Cycles	
Timer Prescale	0	
Timer Resolution		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



# Specification Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 1463.00 Cycles TS1.2 1942.00 Cycles TS1.2 1942.00 Cycles TS1.2 1942.00 Cycles TS1.2 1942.00 Cycles TS1.1 "Shortest Execution Path: ((DiagStatusHwPosReducedPerf\_Cnt\_T\_lgc == TRUE) || (AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_INPUTINVALID)) >> True (DiagStatusHwPosReducedPerf\_Cnt\_T\_lgc == TRUE) >> False (AbsHwPos\_VelCntr/OstLeam\_Cnt\_M\_lgc == TRUE) >> False (AbsHwPos\_NelWoSState\_Cnt\_M\_enum == HWPOS\_STATE\_INPUTINVALID) >> False (AbsHwPos\_NelWoSstate\_Cnt\_M\_enum == HWPOS\_STATE\_INPUTINVALID) >> False (AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_INFID=False (AbsHwPos\_VelCntrValid\_Cnt\_M\_lgc == TRUE) >> False (TurnsCntrValdity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => True (AbsHwPos\_VelCntrValid\_Cnt\_M\_lgc == TRUE) >> False (SnstlessAuthority\_Us\_T\_f32 >> k\_MasSensorlessAuthority\_Us\_f32) => True (AbsHwPos\_VelCntrValid\_Cnt\_M\_lgc == TRUE) => True (AbsHwPos\_VelCntr

Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	9556.42969
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	204.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75999999
AbsHwPos_HwPosSource_Cnt_M_u16	1348
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	442.940002
AbsHwPos RelHwPos HwDeg M f32	442.940002
AbsHwPos SrlComHwPosStatus Cnt M u16	26817
AbsHwPos SrlComHwPos HwDeg M f32	1700.22998
AbsHwPos TargetHwAuthority Uls M f32	1
AbsHwPos TempHwPos HwDeg M f32	5054.37012
AbsHwPos VehCntrOffset HwDeg M f32	8178.93018
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
KNO_INIC_ \$\phi_1 \text{ borring of } \text{ HwPosAuthorityStep Uls f32}	0
K HwPosOutputLPFError HwDeg f32	674.299988
C I2CHwAuthority Uls f32	0.75
<pre>&lt;</pre>	0.839999974
<pre></pre>	822.799988
MinSensorlessAuthority Uls f32	0.75
< TurnsCntrAuthority Uls f32	0.75
<ul> <li>UseTurnsCntr Cnt lgc</li> </ul>	0
VehCntrOffValidLimit HwDeq f32	674.299988
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
rarget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
arget AbsHwPos Per2 ManufMode Cnt enum.value	2
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1587.5
rarget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2325.1001
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9299
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatu	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPo	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1700.22998	1700.22998 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001	2325.1001 ± 0.009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 1.2 (Repeat Count = 1)	▼
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	3787.70996
AbsHwPos HandwheelAuthority Uls M f32	0.280000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001
AbsHwPos_HwPosSource_Cnt_M_u16	296
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1232.09998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.270000011
k_HwPosOutputLPFError_HwDeg_f32	1179.19995
k_I2CHwAuthority_UIs_f32	0.270000011
k_MaxSensorlessAuthority_Uls_f32	0.340000004
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.270000011
k_TurnsCntrAuthority_UIs_f32	0.270000011
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	822.799988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012



Name	Input Value			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStat	us 3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedF	Perf_( target_AbsHwPos_Per2_DiagStatusHv	vPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3.	2 target_AbsHwPos_Per2_HandwheelAr	uthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg	_f32 target_AbsHwPos_Per2_HandwheelPo	osition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSourc	e_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igo	target_AbsHwPos_Per2_I2CHwAbsPo	sValid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPo	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_	Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f3:	2 target_AbsHwPos_Per2_SensorlessAu	uthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_	f32 target_AbsHwPos_Per2_SensorlessH	wPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u	116 target_AbsHwPos_Per2_SrlComHwPo	sStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPo	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrVal	idity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	-	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	-	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1232.09998	1232.09998 ± 0.1	<b>✓</b>	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>	
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	-	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	-	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	-	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1232.09998	1232.09998 ± 0.1	-	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•



Test Case 2: Boundary Test



#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1467.00 Cycles TS2.2 2917.00 Cycles TS2.3 1877.00 Cycles TS2.4 1933.00 Cycles TS2.5 1838.00 Cycles TS2.6 1921.00 Cycles TS2.7 1341.00 Cycles TS2.8 1885.00 Cycles TS2.9 1360.00 Cycles TS2.9 1360.00 Cycles TS2.10 1862.00 Cycles TS2.11 1360.00 Cycles TS2.12 1417.00 Cycles TS2.13 1352.00 Cycles TS2.14 1868.00 Cycles TS2.15 1347.00 Cycles TS2.16 1895.00 Cycles TS2.17 1347.00 Cycles TS2.18 1902.00 Cycles TS2.19 1450.00 Cycles TS2.20 1904.00 Cycles TS2.21 1388.00 Cycles TS2.22 1428.00 Cycles TS2.23 1362.00 Cycles TS2.24 1428.00 Cycles TS2.24 1428.00 Cycles TS2.25 1341.00 Cycles TS2.26 1428.00 Cycles TS2.27 1908.00 Cycles TS2.28 1889.00 Cycles TS2.29 1844.00 Cycles TS2.30 1889.00 Cycles TS2.31 1361.00 Cycles TS2.32 1902.00 Cycles TS2.33 1373.00 Cycles TS2.33 1373.00 Cycles TS2.34 1902.00 Cycles TS2.35 1428.00 Cycles TS2.36 1861.00 Cycles TS2.37 1844.00 Cycles TS2.38 1904.00 Cycles TS2.39 1824.00 Cycles TS2.40 1904.00 Cycles TS2.40 1904.00 Cycles TS2.41 1844.00 Cycles TS2.42 1861.00 Cycles TS2.42 1861.00 Cycles TS2.43 2347.00 Cycles TS2.44 2812.00 Cycles TS2.45 1373.00 Cycles TS2.46 2763.00 Cycles TS2.47 1373.00 Cycles TS2.49 1373.00 Cycles TS2.50 1907.00 Cycles TS2.51 1352.00 Cycles TS2.51 1352.00 Cycles TS2.52 1417.00 Cycles TS2.52 1417.00 Cycles TS2.52 1417.00 Cycles TS2.53 1352.00 Cycles TS2.54 1868.00 Cycles TS2.55 1815.00 Cycles TS2.56 1840.00 Cycles TS2.57 1352.00 Cycles TS2.57 1352.00 Cycles TS2.59 1352.00 Cycles TS2.60 1840.00 Cycles TS2.61 1426.00 Cycles TS2.62 2799.00 Cycles TS2.63 2358.00 Cycles TS2.63 2358.00 Cycles TS2.64 1922.00 Cycles TS2.65 1943.00 Cycles TS2.66 1384.00 Cycles TS2.66 1387.00 Cycles TS2.69 1904.00 Cycles TS2.70 1373.00 Cycles TS2.71 1904.00 Cycles TS2.72 1361.00 Cycles TS2.73 1904.00 Cycles TS2.73 1904.00 Cycles TS2.74 1904.00 Cycles TS2.75 1373.00 Cycles TS2.76 1904.00 Cycles TS2.77 1361.00 Cycles TS2.78 1889.00 Cycles TS2.79 1361.00 Cycles TS2.80 1904.00 Cycles TS2.81 1361.00 Cycles TS2.82 1889.00 Cycles TS2.83 1361.00 Cycles TS2.84 1904.00 Cycles TS2.85 1373.00 Cycles TS2.86 1904.00 Cycles TS2.87 1361.00 Cycles TS2.88 1904.00 Cycles TS2.89 1373.00 Cycles TS2.90 1904.00 Cycles TS2.91 1368.00 Cycles TS2.92 1889.00 Cycles TS2.93 1338.00 Cycles TS2.94 1889.00 Cycles TS2.95 1303.00 Cycles TS2.96 1840.00 Cycles TS2.97 1359.00 Cycles TS2.98 1885.00 Cycles TS2.99 1352.00 Cycles TS2.99 1352.00 Cycles TS2.100 1840.00 Cycles TS2.101 2310.00 Cycles TS2.102 2740.00 Cycles TS2.103 2287.00 Cycles TS2.104 2753.00 Cycles TS2.105 2288.00 Cycles TS2.106 1838.00 Cycles TS2.107 1465.00 Cycles TS2.108 1945.00 Cycles TS2.109 1815.00 Cycles



TS2.110 1898.00 Cycles
TS2.111 1386.00 Cycles
TS2.112 1874.00 Cycles
TS2.113 1862.00 Cycles
TS2.114 1926.00 Cycles
TS2.115 1379.00 Cycles
TS2.116 1839.00 Cycles
TS2.117 1451.00 Cycles
TS2.118 2812.00 Cycles
TS2.119 2358.00 Cycles
TS2.121 2306.00 Cycles
TS2.121 2306.00 Cycles
TS2.122 2358.00 Cycles
TS2.122 2358.00 Cycles
TS2.123 2792.00 Cycles



#### **Description** Vector Description:

```
TS2.1All Min
 TS2.2All Max
 TS2.3TurnsCntrValidity_Cnt_u08=>Min
TS2.4TurnsCntrValidity_Cnt_u08=>Max
TS2.5TurnsCntrValidity_Cnt_u08=>Pos
 TS2.6ManufMode_Cnt_enum=>Min
TS2.7ManufMode_Cnt_enum=>Max
TS2.8ManufMode_Cnt_enum=>Pos
 TS2.9DiagStatusHwPosReducedPerf_Cnt_lgc=>Min
TS2.10DiagStatusHwPosReducedPerf_Cnt_lgc=>Max
TS2.11I2CHwAbsPosValid_Cnt_lgc=>Min
IS2.11/I2CHwAbsPosValid_Cnt_lgc=>Mln
TS2.12/CHwAbsPosValid_Cnt_lgc=>Max
TS2.13SensorlessAuthority_Uls_f32=>Min
TS2.14SensorlessAuthority_Uls_f32=>Max
TS2.15SensorlessAuthority_Uls_f32=>Max
TS2.15SensorlessHwPos_HwDeg_f32=>Min
TS2.16SensorlessHwPos_HwDeg_f32=>Min
TS2.18SensorlessHwPos_HwDeg_f32=>Zero
 TS2.19SensorlessHwPos_HwDeg_f32=>Pos
TS2.20SensorlessHwPos_HwDeg_f32=>Neg
TS2.21I2CHwAbsPos_HwDeg_f32=>Min
 TS2.22I2CHwAbsPos_HwDeg_f32=>Max
TS2.23I2CHwAbsPos_HwDeg_f32=>Zero
TS2.24I2CHwAbsPos_HwDeg_f32=>Pos
 TS2.25I2CHwAbsPos_HwDeg_f32=>Neg
TS2.26k_UseTurnsCntr_Cnt_lgc=>Min
TS2.27k_UseTurnsCntr_Cnt_lgc=>Max
  TS2.28k_MinSensorlessAuthority_Uls_f32=>Min
TS2.29k_MinSensorlessAuthority_Uls_132=>Msx TS2.30k_MinSensorlessAuthority_Uls_132=>Pos TS2.31k_MaxSensorlessAuthority_Uls_132=>Min TS2.32k_MaxSensorlessAuthority_Uls_132=>Max TS2.32k_MaxSensorlessAuthority_Uls_132=>Pos TS2.33k_MaxSensorlessAuthority_Uls_132=>Pos TS2.34k_VehCntrOffValidLimit_HwDeg_132=>Min TS2.35k_VehCntrOffValidLimit_HwDeg_132=>Min TS2.35k_VehCntrOffVali
 TS2.35k_VehCntrOffValidLimit_HwDeg_f32=>Max
TS2.36k_VehCntrOffValidLimit_HwDeg_f32=>Pos
TS2.37Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Min
 TS2.38Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hwbeg_f32=>Max
TS2.39Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hwbeg_f32=>Zero
TS2.40Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hwbeg_f32=>Pos
 TS2.41Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Neg
TS2.42k_MaxVehCntrOffDiff_HwDeg_f32=>Min
TS2.43k_MaxVehCntrOffDiff_HwDeg_f32=>Max
  TS2.44k_MaxVehCntrOffDiff_HwDeg_f32=>Pos
TS2.45k_HwPosOutputLPFError_HwDeg_f32=>Min
TS2.45k_HwPosOutputLPFError_HwDeg_f32=>Max
TS2.47k_HwPosOutputLPFError_HwDeg_f32=>Max
TS2.47k_HwPosOutputLPFError_HwDeg_f32=>Pos
TS2.49k_HwPosAuthorityStep_UIs_f32=>Min
TS2.49k_HwPosAuthorityStep_UIs_f32=>Max
  TS2.50k_HwPosAuthorityStep_Uls_f32=>Pos
 TS2.51k_I2CHwAuthority_UIs_f32=>Min
TS2.52k_I2CHwAuthority_UIs_f32=>Max
TS2.53k_I2CHwAuthority_UIs_f32=>Pos
  TS2.54k_TurnsCntrAuthority_Uls_f32=>Min
 TS2.55k_TurnsCntrAuthority_Uls_f32=>Max
TS2.56k_TurnsCntrAuthority_Uls_f32=>Pos
 TS2.57Rte Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Min
TS2.58Rte Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Max
TS2.59Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Pos
 TS2.60AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Min
 TS2.61AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Max
TS2.62AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Zero
TS2.63AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Pos
 TS2.64AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Neg
TS2.65AbsHwPos_HandwheelAuthority_Uls_M_f32=>Min
TS2.66AbsHwPos_HandwheelAuthority_Uls_M_f32=>Max
 TS2.67AbsHwPos_HandwheelAuthority_Uls_M_f32=>Pos
TS2.68AbsHwPos_HwPosSource_Cnt_M_u16=>Min
TS2.69AbsHwPos_HwPosSource_Cnt_M_u16=>Max
 TS2.70AbsHwPos_HwPosSource_Cnt_M_u16=>Pos
TS2.71AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_SENSORVALID
TS2.72AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_INPUTINVALID
 TS2.73AbsHwPos HwPosState Cnt M enum=>HWPOS STATE INIT TS2.74AbsHwPos HwPosState Cnt M enum=>HWPOS STATE SENSORINVALID TS2.75AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Min
 TS2.76AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Max
TS2.77AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Zero
TS2.78AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Pos
  TS2.79AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Neg
IS2.79AbSHWPOS_PreVHandWheelPosition_HwDeg_M_f32=S1S2.80AbSHWPOS_RelHwPOS_HwDeg_M_f32=>Min TS2.81AbSHWPOS_RelHwPOS_HwDeg_M_f32=>Max TS2.82AbSHWPOS_RelHwPOS_HwDeg_M_f32=>Zero TS2.83AbSHWPOS_RelHwPOS_HwDeg_M_f32=>PoS TS2.84AbSHWPOS_RelHwPOS_HwDeg_M_f32=>Neg TS2.85AbSHWPOS_SrlComHwPOS_HwDeg_M_f32=>Min TS2.86AbSHWPOS_SrlComHwPOS_HwDeg_M_f32=>Max TS2.87AbSHWPOS_SrlComHwPOS_HwDeg_M_f32=>Zero TS2.88AbSHWPOS_SrlComHwPOS_HWDeg_M_f32=>PoS TS2.88AbSHWPOS_SrlComHwPOS_HWDeg_M_f32=>PoS TS2.89AbSHWPOS_SrlComHwPOS_HWDeg_M_f32=>PoS TS2.89AbSHWPOS_SrlComHwPOS_HWDeg_M_f32=>Nea
 TS2.89AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Neg
TS2.90AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Min
TS2.91AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Max
  TS2.92AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Pos
TS2.93AbsHwPos_TargetHwAuthority_Uls_M_f32=>Min
TS2.94AbsHwPos_TargetHwAuthority_Uls_M_f32=>Max
```



```
TS2.95AbsHwPos_TargetHwAuthority_Uls_M_f32=>Pos
TS2.96AbsHwPos_TempHwPos_HwDeg_M_f32=>Min
TS2.97AbsHwPos_TempHwPos_HwDeg_M_f32=>Max
TS2.98AbsHwPos_TempHwPos_HwDeg_M_f32=>Zero
TS2.99AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Neg
TS2.101AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Nin
TS2.102AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Max
TS2.103AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.104AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.106AbsHwPos_VehCntrOfstLeam_Cnt_M_lgc=>Min
TS2.107AbsHwPos_VehCntrOfstLeam_Cnt_M_lgc=>Min
TS2.109AbsHwPos_VehCntrValid_Cnt_M_lgc=>Max
TS2.108AbsHwPos_VehCntrValid_Cnt_M_lgc=>Min
TS2.110AbsHwPos_HandwheelPositionLPF_M_str.SV=>Min
TS2.111AbsHwPos_HandwheelPositionLPF_M_str.SV=>Max
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.114AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.114AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.115AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.114AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.115AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.114AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.115AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Max
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Nin
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Nin
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Nos
TS2.118Rt_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NOT_OK
TS2.120Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NOT_OK
TS2.121Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NOT_OK
TS2.123Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NOT_NVALIDATED
```

Test Step 2.1 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0
AbsHwPos_HwPosSource_Cnt_M_u16	1
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k HwPosAuthorityStep Uls f32	0
k HwPosOutputLPFError HwDeg f32	0
	0
k MaxSensorlessAuthority Uls f32	0
k MaxVehCntrOffDiff HwDeg f32	0
k MinSensorlessAuthority Uls f32	0
k_TurnsCntrAuthority_Uls_f32	0
k_UseTurnsCntr_Cnt_Igc	0
	0
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900
	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValidity Cnt u08



Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.09	•	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725	-11725 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725	-11725 ± 0.1	•	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	•	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•	
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	•	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	•	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	•	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	•	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	•	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.2 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	10125
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	11725
AbsHwPos HandwheelPositionLPF M str.K Uls f32	1
AbsHwPos HwPosSource Cnt M u16	65534
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	11725
AbsHwPos_RelHwPos_HwDeg_M_f32	10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	1
AbsHwPos_TempHwPos_HwDeg_M_f32	11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	1
k_HwPosOutputLPFError_HwDeg_f32	1620
k_I2CHwAuthority_Uls_f32	1
k_MaxSensorlessAuthority_Uls_f32	1
k_MaxVehCntrOffDiff_HwDeg_f32	1600
k_MinSensorlessAuthority_Uls_f32	1
k_TurnsCntrAuthority_UIs_f32	1
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1600
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosAbsHwPo$	( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc



Name	Input Value
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOL\

Input Value
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_AbsHwPos_Per2_ManufMode_Cnt_enum
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08
arget_Pim_EOLVehCntrOffset

target_Rte_inst_Ap_AbsHwFos.Filit_EOLVerichttonset	target_Filli_EOLVERCHUONSet		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)	<b>→</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	128.470001
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	584.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	88
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1487
AbsHwPos_RelHwPos_HwDeg_M_f32	1487
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21849
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.100000001
k_HwPosOutputLPFError_HwDeg_f32	407
k_I2CHwAuthority_UIs_f32	0.100000001
k_MaxSensorlessAuthority_Uls_f32	0.100000001
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.100000001
k_TurnsCntrAuthority_UIs_f32	0.100000001
k_UseTurnsCntr_Cnt_lgc	1



Name	Input Value		
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	100.099998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	90.3000031		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	123		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	1		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerror and all the properties of the$	f_( target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3$	2 target_AbsHwPos_Per2_HandwheelPosition	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1487	1487 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	806.663025	806.663025 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	806.663025	806.663025 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012	9026.62012 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5	1328.5 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	806.663025	806.663025 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	90.3000031	90.3000031 ± 0.00009	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	269.209991	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	605.179993	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003	
AbsHwPos_HwPosSource_Cnt_M_u16	96	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1507.09998	
AbsHwPos_RelHwPos_HwDeg_M_f32	1507.09998	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21921	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	



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Name	Input Value		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	436.700012		
k_I2CHwAuthority_Uls_f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.109999999		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.200000003		
k_TurnsCntrAuthority_Uls_f32	0.200000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.109999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	120.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	247		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1507.09998	1507.09998 ± 0.009	✓ ×
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	524.650024	524.650024 ± 0.1	_
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS STATE SENSORINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	524.650024	524.650024 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	8920.66016 ± 0.1	
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	1358.69995 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	·
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Citt_T_endth)	0	0	-
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	524.650024	524.650024 ± 0.1	
target_AbeHwPos_Per2_HwPosSource_Cnt_u16_value	65534	65534	

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

65534

65534

1600

120.5

65534

65534

1600 ± 0.1 120.5 ± 0.0009

target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value



Test Step 2.5 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-409.950012		
AbsHwPos HandwheelAuthority Uls M f32	0.30000012		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	625.460022		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.300000012		
AbsHwPos HwPosSource Cnt M u16	104		
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	1527.19995		
AbsHwPos_RelHwPos_HwDeg_M_f32	1527.19995		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21993		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.300000012		
k_HwPosOutputLPFError_HwDeg_f32	466.399994		
k_I2CHwAuthority_Uls_f32	0.300000012		
k_MaxSensorlessAuthority_Uls_f32	0.119999997		
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994		
k_MinSensorlessAuthority_Uls_f32	0.300000012		
k_TurnsCntrAuthority_Uls_f32	0.300000012		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	110		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	371		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwPo	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthe	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	prity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
		HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_TurnsCntrValidit target_Pim_EOLVehCntrOffset	<del></del>	
		<del></del>	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	y_Cnt_u08	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name	target_Pim_EOLVehCntrOffset  Actual Value	y_Cnt_u08  Expected Value	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995	y_Cnt_u08  Expected Value  1527.19995 ± 0.009	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0	y_Cnt_u08  Expected Value  1527.19995 ± 0.009 0 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105	y_Cnt_u08  Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534	y_Cnt_u08  Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SflComHwPosStatus_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SflComHwPosStatus_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_Barn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffste_ThyDeg_M_f32  AbsHwPos_VehCntrOffste_ThyDeg_M_f32  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1 0 1600	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004  1600 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1	Expected Value  1527.19995 ± 0.009 0 ± 0.004 3378.58105 ± 0.1 65534 HWPOS_STATE_SENSORVALID 3378.58105 ± 0.1 65534 8814.7002 ± 0.1 0 ± 0.004 9802.53027 ± 0.1 1388.90002 ± 0.009 0 504 1 1 0 ± 0.004	Resu

1600

1600 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	150.699997	150.699997 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	550.690002		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	645.73999		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.400000006		
AbsHwPos_HwPosSource_Cnt_M_u16	112		
AbsHwPos_HwPosState_Cnt_M_enum	5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	157.300003		
AbsHwPos_ReIHwPos_HwDeg_M_f32	157.300003		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22065		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8708.74023		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1419.09998		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.40000006		
k HwPosOutputLPFError HwDeg f32	496.100006		
k I2CHwAuthority Uls f32	0.40000006		
k_MaxSensorlessAuthority_Uls_f32	0.129999995		
k MaxVehCntrOffDiff HwDeg f32	199.100006		
k MinSensorlessAuthority Uls f32	0.40000006		
k TurnsCntrAuthority Uls f32	0.40000006		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	139.699997		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	160.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.129999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	160.399994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	10		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	180.899994		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	495		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	target_AbsHwPos_Per2_DiagStatusHwPos	PeducedPorf Cnt Inc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr		
		_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	157.300003	157.300003 ± 0.0009	<b>~</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1677.34399	1677.34399 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPas ProvHandwhoolPosition HwPag M f32	1677 3/1300	1677 3/300 ± 0.1	

1677.34399

65535

0

0

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

1677.34399 ± 0.1

65535

0 ± 0.1

0 ± 0.004



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1419.09998	1419.09998 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.7 (Repeat Count = 1)	<u></u>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	691.429993
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-666.02002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	120
AbsHwPos_HwPosState_Cnt_M_enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	567.400024
AbsHwPos_RelHwPos_HwDeg_M_f32	567.400024
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.5
K_HwPosOutputLPFError_HwDeg_f32	525.799988
k_I2CHwAuthority_Uls_f32	0.5
k_MaxSensorlessAuthority_Uls_f32	0.140000001
k_MaxVehCntrOffDiff_HwDeg_f32	228.800003
k_MinSensorlessAuthority_Uls_f32	0.5
k_TurnsCntrAuthority_Uls_f32	0.5
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	169.399994
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.140000001
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	180.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	14
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	619
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority Uls f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
0	1. 0. 2



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	567.400024	567.400024 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997	120.199997 ± 0.1	<b>~</b>
AbsHwPos_HwPosSource_Cnt_M_u16	120	120	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997	120.199997 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137	22137	<b>~</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	120.199997	120.199997 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	120	120	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	22137	22137	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006	211.100006 ± 0.0009	<b>✓</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.8 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-832.169983
AbsHwPos HandwheelAuthority Uls M f32	0.60000024
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	686.299988
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.60000024
AbsHwPos HwPosSource Cnt M u16	128
AbsHwPos_HwPosState_Cnt_M_enum	7
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1587.5
AbsHwPos_RelHwPos_HwDeg_M_f32	1587.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22209
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8496.82031
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	140.300003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-960.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1.
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.600000024
k_HwPosOutputLPFError_HwDeg_f32	555.5
k_I2CHwAuthority_UIs_f32	0.600000024
k_MaxSensorlessAuthority_Uls_f32	0.150000006
k_MaxVehCntrOffDiff_HwDeg_f32	258.5
k_MinSensorlessAuthority_Uls_f32	0.600000024
k_TurnsCntrAuthority_UIs_f32	0.600000024
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	199.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.150000006
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	241.300003



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	743		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	ılid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_F	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1587.5	1587.5 ± 0.009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	358.699982	358.700012 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	358.699982	358.700012 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-960.900024	-960.900024 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	358.699982	358.700012 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.9 (Repeat Count = 1)	✓ ·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	972.909973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	706.580017
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	136
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1000.29999
AbsHwPos_RelHwPos_HwDeg_M_f32	-1000.29999
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22281
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	585.200012
k_I2CHwAuthority_Uls_f32	0.69999988



Name		Input Value		
k_MaxSensorlessAuthority_Uls_f32		0.159999996		
k_MaxVehCntrOffDiff_HwDeg_f32		288.200012		
k_MinSensorlessAuthority_Uls_f32		0.69999988		
k_TurnsCntrAuthority_UIs_f32		0.69999988		
k_UseTurnsCntr_Cnt_lgc		0		
A_bleehv@rdv@lftVeshi@btn@lfsetv_Desg_Dfs@LVehCntrOffset.E	OLVehC	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedF	erf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igo	value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.	value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.valu	e	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	2.value	0.159999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_	f32.value	220.699997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.v	/alue	26		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hv	wDeg_f32	271.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerfor	med_Cnt_u16	867		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_0	GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Dia	gStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatus	HwPosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Ha	ndwheelAuthority_Uls_f32	target_AbsHwPos_Per2_Handwhee	IAuthority_UIs_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Ha	ndwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_Handwhee	IPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Hw	PosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSou	ırce_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2C	CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbs	PosValid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2C	CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbs	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Ma	nufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMod	e_Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Ser	nsorlessAuthority_Uls_f32	target_AbsHwPos_Per2_Sensorless	Authority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Ser	nsorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_Sensorless	sHwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Srlv	ComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHw	PosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Srlv	ComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Tui	rnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntr\	/alidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffs	set	target_Pim_EOLVehCntrOffset		
Name		Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32		-1000.29999	-1000.29999 ± 0.009	✓
AbsHwPos HandwheelAuthority Uls M f32		0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f3	2	324.253998	324.253998 ± 0.1	✓Rte_Call
AbsHwPos_HwPosSource_Cnt_M_u16		65534	65534	<u> </u>
AbsHwPos_HwPosState_Cnt_M_enum		HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32		324.253998	324.253998 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16		65534	65534	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32		-8390.86035	-8390.86035 ± 0.1	✓
AbsHwPos T	OLVehCntrOffset.EOLVehC	: · ·	HwPos HwDeg M f32	



Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8284.88965		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.800000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	180.5		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	603.700012		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.800000012		
k_HwPosOutputLPFError_HwDeg_f32	614.900024		
k_I2CHwAuthority_Uls_f32	0.800000012		
k_MaxSensorlessAuthority_Uls_f32	0.170000002		
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994		
k_MinSensorlessAuthority_Uls_f32	0.800000012		
k_TurnsCntrAuthority_UIs_f32	0.800000012		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	258.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.170000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	32		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	301.700012 991		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		osReducedPerf Cnt Idc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-0.971984863	-0.972000003 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-0.971984863	-0.972000003 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	180.5	180.5 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	603.700012	603.700012 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-0.971984863	-0.972000003 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	301.700012	301.700012 ± 0.0009	•

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.11 (Repeat Count = 1)	Innut Value		
Name Abaltupas AlignodCumMtrDes Hurbag M #22	Input Value -1254.39001		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32			
AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos HandwheelPositionLPF M str.SV Uls f32	0.899999976 747.140015		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.89999976		
AbsHwPos_HwPosSource_Cnt_M_u16	152		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-960.900024		
AbsHwPos_RelHwPos_HwDeg_M_f32	-960.900024		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22425		
AbsHwPos SrlComHwPos HwDeg M f32	8178.93018		
AbsHwPos TargetHwAuthority Uls M f32	0.89999976		
AbsHwPos TempHwPos HwDeg M f32	200.600006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	633.900024		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target Rte Inst Ap AbsHwPos	iii o iioot_oot_iii o iiotatao_E.ii o i otatao	
k_HwPosAuthorityStep_Uls_f32	0.899999976		
k_HwPosOutputLPFError_HwDeg_f32	644.599976		
k_I2CHwAuthority_Uls_f32	0.89999976		
k_MaxSensorlessAuthority_Uls_f32	0.180000007		
k MaxVehCntrOffDiff HwDeg f32	347.600006		
k_MinSensorlessAuthority_Uls_f32	0.89999976		
k_TurnsCntrAuthority_UIs_f32	0.89999976		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	288.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.180000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	38		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	331.899994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1115		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-960.900024	-960.900024 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	255.253998	255.253998 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	255.253998	255.253998 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8178.93018	8178.93018 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	200.600006	200.600006 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	633.900024	633.900024 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target AbsHwPos Per2 HandwheelPosition HwDeg f32.value	255.253998	0 ± 0.004 255.253998 ± 0.1	
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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	331.899994	331.899994 ± 0.0009	✓

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.12 (Repeat Count = 1)			×
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1395.13		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	767.419983		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999		
AbsHwPos_HwPosSource_Cnt_M_u16	160		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1547.30005		
AbsHwPos_RelHwPos_HwDeg_M_f32	1547.30005		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22497		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8072.97021		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	220.699997		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.100000001		
k HwPosOutputLPFError HwDeg f32	674.299988		
k_I2CHwAuthority_UIs_f32	0.100000001		
k MaxSensorlessAuthority Uls f32	0.189999998		
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
k MinSensorlessAuthority Uls f32	0.100000001		
k_TurnsCntrAuthority_UIs_f32	0.100000001		
k UseTurnsCntr Cnt Igc	0		
k_VehCntrOffValidLimit_HwDeg_f32	317.899994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
	281		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.18999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	44		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	362.100006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1239		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosVa		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	713.913757	713.913818 ± 0.1	•
AbsHwPos HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	713.913757	713.913818 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	21845	21845	
AbsHwPos SrlComHwPos HwDen M f32	281	281 + 0.1	

281

0.100000001

281 ± 0.1

0.100000001 ± 0.004

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	281	281 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976	664.099976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	713.913757	713.913818 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	281	281 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.42 (Benest Count = 4)	
Test Step 2.13 (Repeat Count = 1)	·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.200000003
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.109999999
k_TurnsCntrAuthority_UIs_f32	0.109999999
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	347.600006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	722.072021	722.072021 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003	240.800003 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999791	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	722.072021	722.072021 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	168	168	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	-

Test Step 2.14 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1676.60999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.12999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.12999995
AbsHwPos_HwPosSource_Cnt_M_u16	176
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.119999997
k_HwPosOutputLPFError_HwDeg_f32	733.700012
k_I2CHwAuthority_Uls_f32	0.119999997
k_MaxSensorlessAuthority_Uls_f32	0.20999993
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012
k_MinSensorlessAuthority_Uls_f32	0.119999997
k_TurnsCntrAuthority_Uls_f32	0.119999997
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	377.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 



Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerl	f_( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	2 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	-

Τ			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

65535 ± 0.09

65535

Test Step 2.15 (Repeat Count = 1)	v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.10999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.319999993



Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	407		
k_MinSensorlessAuthority_Uls_f32	0.109999999		
k_TurnsCntrAuthority_UIs_f32	0.109999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos HandwheelAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>

target_rtte_mot_rtp_rtborrtwi oo.i im_Eoeveriona onoet	target_1 iiii_Lot venonti onoct		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	729.307983	729.307983 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	<b>✓</b>

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Nome	Input Value	
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1958.08997	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	848.539978	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006	
AbsHwPos_HwPosSource_Cnt_M_u16	192	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	126.699997	
AbsHwPos_RelHwPos_HwDeg_M_f32	126.699997	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22785	
AbsHwPos SrlComHwPos HwDeg M f32	269.209991	



Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVet	nCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.140000001		
k_HwPosOutputLPFError_HwDeg_f32	793.099976		
k_I2CHwAuthority_Uls_f32	0.140000001		
k_MaxSensorlessAuthority_Uls_f32	0.230000004		
k_MaxVehCntrOffDiff_HwDeg_f32	496.100006		
k_MinSensorlessAuthority_Uls_f32	0.140000001		
k_TurnsCntrAuthority_Uls_f32	0.140000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	436.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	482.899994		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	1735 2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		osPaducadParf Cnt Igo	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target_AbsHwPos_Per2_ManuliMode_Cit_endin target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	_
AbsHwPos HandwheelAuthority Uls M f32	0.150000006	0.150000006 ± 0.004	-
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006	301.100006 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	192	192	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006	301.100006 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024	784.900024 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.150000006	0.150000006 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	192	192	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
		269.209991 ± 0.1	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	269.209991 482.899994	209.209991 ± 0.1	-

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•



Fest Step 2.17 (Repeat Count = 1)	Input Value		
Name  AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2098.83008		
AbsHwPos HandwheelAuthority Uls M f32	0.159999996		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-868.820007		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.159999996		
AbsHwPos_HwPosSource_Cnt_M_u16	5200		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	139.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	139.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22857		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-409.950012		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.159999996		
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	815.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.150000006		
x_HwPosOutputLPFError_HwDeg_f32	822.799988		
x_I2CHwAuthority_Uls_f32	0.150000006		
x_MaxSensorlessAuthority_Uls_f32	0.23999995		
x_MaxVehCntrOffDiff_HwDeg_f32	525.799988		
x_MinSensorlessAuthority_Uls_f32	0.150000006		
x_TurnsCntrAuthority_Uls_f32	0.150000006		
x_UseTurnsCntr_Cnt_lgc	0		
(_VehCntrOffValidLimit_HwDeg_f32	466.399994		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value arget_AbsHwPos_Per2_l2CHwAbsPosValid_Cnt_lgc.value	0		
	482		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.23999995		
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1600		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	74		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	513.099976		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1859		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		sReducedPerf Cnt Igc	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAutho		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_h	lwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I	lwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	/_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Re
AbsHwPos_AlignedHwPos_HwDeg_M_f32	139.100006	139.100006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-473.808807	-473.808807 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-473.808807	-473.808807 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1600	1600 ± 0.1	
NbsHwPos_TargetHwAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	
NbsHwPos_TempHwPos_HwDeg_M_f32	1600	1600 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	815.099976	815.099976 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	504	*none*	
		*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0	*none*	
AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value		*none* 0.239999995 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0 0.239999995	0.239999995 ± 0.004	



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	513.099976	513.099976 ± 0.0009	<b>✓</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.18 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007		
AbsHwPos HandwheelAuthority Uls M f32	0.170000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_UIs_f32	0.170000002		
	208		
AbsHwPos_HwPosSource_Cnt_M_u16	HWPOS_STATE_SENSORINVALID		
NosHwPos_HwPosState_Cnt_M_enum	151.5		
hbsHwPos_PrevHandwheelPosition_HwDeg_M_f32			
hbsHwPos_RelHwPos_HwDeg_M_f32	151.5 22929		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	550.690002		
AbsHwPos_SrlComHwPos_HwDeg_M_f32			
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.170000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
\land Sharp Note On the One of the Control of	845.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	t-0#t-0-t0t	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	tronset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.159999996		
_HwPosOutputLPFError_HwDeg_f32	852.5		
_I2CHwAuthority_Uls_f32	0.159999996		
_MaxSensorlessAuthority_Uls_f32	0.25		
_MaxVehCntrOffDiff_HwDeg_f32	555.5		
_MinSensorlessAuthority_Uls_f32	0.159999996		
_TurnsCntrAuthority_Uls_f32	0.159999996		
_UseTurnsCntr_Cnt_lgc	1		
x_VehCntrOffValidLimit_HwDeg_f32	496.100006		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resi
AbsHwPos AlignedHwPos HwDeg M f32	151.5	151.5 ± 0.0009	i (CS)
TOSI IWI US_TRIIGITUWE US_I IWDEG_W_ISZ	101.0	0.25 + 0.004	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	0	0 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

lame	Input Value
bsHwPos AlignedCumMtrPos HwDeg M f32	2380.31006
bsHwPos HandwheelAuthority Uls M f32	0.180000007
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	909.380005
bshwPos HandwheelPositionLPF M str.K Uls f32	0.180000007
bsHwPos HwPosSource Cnt M u16	216
bsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID
bsHwPos PrevHandwheelPosition HwDeg M f32	163.899994
bsHwPos RelHwPos HwDeg M f32	163.899994
bsHwPos SrlComHwPosStatus Cnt M u16	23001
bsHwPos SrlComHwPos HwDeg M f32	691.429993
bsHwPos TargetHwAuthority Uls M f32	0.180000007
bsHwPos TempHwPos HwDeg M f32	6602.50977
_ ,	875.5
bsHwPos_VehCntrOffset_HwDeg_M_f32	1
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
bsHwPos_VehCntrValid_Cnt_M_lgc	
te_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)  te_Inst_Ap_AbsHwPos_	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.170000002 882.200012
_HwPosOutputLPFError_HwDeg_f32	0.170000002
_I2CHwAuthority_UIs_f32	0.25999999
_MaxSensorlessAuthority_Uls_f32	
_MaxVehCntrOffDiff_HwDeg_f32	585.200012
_MinSensorlessAuthority_UIs_f32	0.170000002
_TurnsCntrAuthority_Uls_f32	0.170000002
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	525.799988
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25999999
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	421.700012
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	86
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	573.5
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2107
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16



Name	Input Value
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value Expected Value		Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-500.600006	-500.600006 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-500.600006	-500.600006 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	$0.25 \pm 0.004$	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	$0.25 \pm 0.004$	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-500.600006	-500.600006 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-500.600006	-500.600006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>V</b>

Test Step 2.21 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2661.79004
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003
AbsHwPos_HwPosSource_Cnt_M_u16	232
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-941.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-941.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23145
AbsHwPos_SrlComHwPos_HwDeg_M_f32	972.909973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	941.599976
k_I2CHwAuthority_Uls_f32	0.189999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001



Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976		
k_MinSensorlessAuthority_Uls_f32	0.189999998		
k_TurnsCntrAuthority_Uls_f32	0.189999998		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	585.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	98		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2355		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf 0	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_el	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~

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Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-900	-900 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-900	-900 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-900	-900 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-900	-900 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024	935.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.189999998	0.189999998 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-900	-900 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-900	-900 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	633.900024	633.900024 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Name	Input Value	
	·	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2802.53003	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.209999993	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	970.219971	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.20999993	
AbsHwPos_HwPosSource_Cnt_M_u16	240	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-921.5	
AbsHwPos_RelHwPos_HwDeg_M_f32	-921.5	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23217	
AbsHwPos SrlComHwPos HwDeg M f32	1113.65002	



Name	Input Value		
AbsHwPos TargetHwAuthority Uls M f32	0.209999993		
AbsHwPos TempHwPos HwDeg M f32	7024.72998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	966.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.20000003		
k_HwPosOutputLPFError_HwDeg_f32	971.299988		
k_I2CHwAuthority_Uls_f32	0.20000003		
k_MaxSensorlessAuthority_Uls_f32	0.289999992		
k_MaxVehCntrOffDiff_HwDeg_f32	674.299988		
k_MinSensorlessAuthority_Uls_f32	0.200000003		
k_TurnsCntrAuthority_Uls_f32	0.20000003		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	614.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.289999992		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	104		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	664.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2479		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_F		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	/_CIIC_000	
	<u> </u>	Francisco d Value	Danulé
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-921.5	-921.5 ± 0.0009 0.200000003 ± 0.004	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	955.473755	955.473816 ± 0.1	
AbsHwPos_HwPosCotet_Cat_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos PrevHandwheelPosition HwDeq M f32	HWPOS_STATE_SENSORVALID 955.473755	HWPOS_STATE_SENSORVALID 955.473816 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	900	900 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.20000003	0.200000003 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	900	900 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	966.099976	966.099976 ± 0.0009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbshwPos_VenCntrValid Cnt M Igc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.20000003	0.200000003 ± 0.004	
		955.473816 ± 0.1	-
target AbsHwPos Per2 HandwheelPosition HwDeg f32 value	955.473755		_
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	955.473755 2	2	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2		
		2 21845 900 ± 0.1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>



Test Step 2.23 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2943.27002		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.219999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-990.5		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.219999999		
AbshwPos_HwPosSource_Cnt_M_u16	248		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	324.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23289		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1254.39001		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.219999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	996.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	hout-offset OstFree-Otstee Free-Otstee	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	nCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.209999993		
k_HwPosOutputLPFError_HwDeg_f32	1001 0.209999993		
k_I2CHwAuthority_Uls_f32	0.209999993		
k_MaxSensorlessAuthority_Uls_f32 k_MaxVehCntrOffDiff_HwDeg_f32	0.300000012		
<del>-</del>			
k_MinSensorlessAuthority_Uls_f32	0.209999993		
k_TurnsCntrAuthority_Uls_f32	0.209999993		
k_UseTurnsCntr_Cnt_lgc			
k_VehCntrOffValidLimit_HwDeg_f32	644.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.300000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	110		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	694.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2603		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Ci		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuti		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	324.5	324.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.209999993	0.209999993 ± 0.004	
AbeHwDoe HandwhoolDosition DE M. etc. SV 1116, 400	0	0 ± 0.1	
	2	2	•
AbsHwPos_HwPosSource_Cnt_M_u16	2 HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum			
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SriComHwPosStatus_Cnt_M_u16	HWPOS_STATE_SENSORVALID 0	HWPOS_STATE_SENSORVALID 0±0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	HWPOS_STATE_SENSORVALID 0 21845	HWPOS_STATE_SENSORVALID 0 ± 0.1 21845	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	HWPOS_STATE_SENSORVALID 0 21845 0	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.2099999993 ± 0.004	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0	HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.2099999993 ± 0.004 0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc	HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988	HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.209999993 ± 0.004 0 ± 0.1 996.299988 ± 0.0009	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc	HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0	HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.209999993 ± 0.004 0 ± 0.1 996.299988 ± 0.0009 0	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0 0	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 996.299988 0 0 504	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 996.299988 0 0 504	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*  *none*	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 996.299988 0 0 504 1	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*  *none*  *none*	
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0 0 504 1 1 0.2099999993	HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*  *none*  *none*  0.2099999993 ± 0.004	

21845

0

21845

0 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value\\ target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	694.299988	694.299988 ± 0.0009	<b>✓</b>

Τ				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.24 (Repeat Count = 1) Name	Input Value		
	-3084.01001		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.230000004		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1010.78003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.230000004		
AbsHwPos_HwPosSource_Cnt_M_u16	256		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	471.600006		
AbsHwPos_RelHwPos_HwDeg_M_f32	471.600006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23361		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.230000004		
AbsHwPos_TempHwPos_HwDeg_M_f32	7306.20996		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1026.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	t-Off t O-t	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	uronser_GeterrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.219999999		
k_HwPosOutputLPFError_HwDeg_f32	1030.69995		
k_I2CHwAuthority_UIs_f32	0.219999999		
k_MaxSensorlessAuthority_Uls_f32	0.310000002		
k_MaxVehCntrOffDiff_HwDeg_f32	733.700012		
k_MinSensorlessAuthority_Uls_f32	0.21999999		
k_TurnsCntrAuthority_UIs_f32	0.219999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	674.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.310000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	522.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	116		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	724.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2727		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4	Dadward Darf Out Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority	, <b>-</b> -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	JII_UU0	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	I=	
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	471.600006	471.600006 ± 0.0009	,
		0.040000000 . 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0.219999999 100.300003	0.219999999 ± 0.004 100.300003 ± 0.1	

HWPOS\_STATE\_SENSORVALID

100.300003

100.300003

0.219999999

100.300003

21845

 $AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32$ 

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

HWPOS\_STATE\_SENSORVALID

100.300003 ± 0.1

100.300003 ± 0.1

100.300003 ± 0.1

0.219999999 ± 0.004



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1026.5	1026.5 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.219999999	0.219999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	100.300003	100.300003 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	100.300003	100.300003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3224.75
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1031.06006
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.239999995
.bsHwPos_HwPosSource_Cnt_M_u16	264
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-488.100006
NbsHwPos_RelHwPos_HwDeg_M_f32	-488.100006
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	23433
.bsHwPos_SrlComHwPos_HwDeg_M_f32	1535.87
bsHwPos_TargetHwAuthority_Uls_M_f32	0.239999995
.bsHwPos_TempHwPos_HwDeg_M_f32	-7446.9502
bsHwPos_VehCntrOffset_HwDeg_M_f32	1056.69995
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.230000004
HwPosOutputLPFError_HwDeg_f32	1060.40002
I2CHwAuthority Uls f32	0.230000004
MaxSensorlessAuthority Uls f32	0.319999993
MaxVehCntrOffDiff HwDeg f32	763.400024
MinSensorlessAuthority_Uls_f32	0.230000004
TurnsCntrAuthority Uls f32	0.230000004
UseTurnsCntr Cnt Igc	0
VehCntrOffValidLimit HwDeg f32	704
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-350.200012
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.319999993
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	542.299988
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	122
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	754.700012
arget_PIM_EOLVENCHTOffset.EOLVENCHTOffset_HWDeg_132 arget_PIM_EOLVENCHTOffset.EOLHWPosTrimPerformed_Cnt_u16	2851
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_t	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_UIs_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-488.100006	-488.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.230000004	0.230000004 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.230000004	0.230000004 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1056.69995	1056.69995 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.230000004	0.230000004 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-350.200012	-350.200012 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-350.200012	-350.200012 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	754.700012	754.700012 ± 0.0009	<b>✓</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.26 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3365.48999
AbsHwPos HandwheelAuthority Uls M f32	0.25
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1051.33997
AbsHwPos HandwheelPositionLPF M str.K UIs f32	0.25
AbsHwPos_HwPosSource_Cnt_M_u16	272
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	765.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	765.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23505
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25
AbsHwPos_TempHwPos_HwDeg_M_f32	7587.68994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1086.90002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.239999995
k_HwPosOutputLPFError_HwDeg_f32	1090.09998
k_I2CHwAuthority_Uls_f32	0.239999995
k_MaxSensorlessAuthority_Uls_f32	0.330000013
k_MaxVehCntrOffDiff_HwDeg_f32	793.099976
k_MinSensorlessAuthority_Uls_f32	0.239999995
k_TurnsCntrAuthority_UIs_f32	0.239999995
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	733.700012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-330.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.330000013
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	562.400024
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	784.900024
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2975
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	0



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	765.799988	765.799988 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	705.880005	705.880005 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	HWPOS STATE SENSORVALID	•

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Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	765.799988	765.799988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	705.880005	705.880005 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	705.880005	705.880005 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-330.5	-330.5 ± 0.1	<b>~</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-330.5	-330.5 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1086.90002	1086.90002 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.239999995	0.239999995 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	705.880005	705.880005 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-330.5	-330.5 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.27 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_UIs_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_Uls_f32	0.159999996



			10010
Name	Input Value		
k_TurnsCntrAuthority_UIs_f32	0.159999996		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	496.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Output StatusHwPosReducedPerf\_Output StatusHwPosReduced$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•

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target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32		543.299988	543.299988 ± 0.0009		<b>✓</b>
Τ					✓
Actual Function	Count	Expected Function		Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_Checkpoint	Reached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_	GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag		1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetN	ΓCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_Checkpointl	Reached	1	•

0

504

0.25

21845

0

3

n

0

504

0.25 ± 0.004

0 ± 0.1

21845

0 ± 0.1

3

Name	Input Value	
AbsHwPos AlignedCumMtrPos HwDeg M f32	3646.96997	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.270000011	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1091.90002	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.270000011	
AbsHwPos_HwPosSource_Cnt_M_u16	2288	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1060	
AbsHwPos_RelHwPos_HwDeg_M_f32	1060	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23649	
AbsHwPos SrlComHwPos HwDeg M f32	1958.08997	

 $AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc$ 

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)
Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

 $target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value$ 

 $target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value$ 

target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc



Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.270000011		
AbsHwPos_TempHwPos_HwDeg_M_f32	-7869.16992		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	9238.54004		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.25999999		
k_HwPosOutputLPFError_HwDeg_f32	1149.5		
k_I2CHwAuthority_Uls_f32	0.25999999		
k_MaxSensorlessAuthority_Uls_f32	0		
k_MaxVehCntrOffDiff_HwDeg_f32	852.5		
k_MinSensorlessAuthority_Uls_f32	0.25999999		
k_TurnsCntrAuthority_Uls_f32	0.25999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	793.099976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.34999994		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	602.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	845.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3223		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2	DooDodysoodDoof Cot Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	,	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_Maintinvode_Crit_Entiriii target_Rte_Inst_Ap_AbsHwPos.AbsHwPos Per2_SensorlessAuthority_Uls_f32_	target_AbsHwPos_Per2_ManufMode_C target_AbsHwPos_Per2_SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	,	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1060	1060 ± 0.009	ixesuit
AbsHwPos HandwheelAuthority Uls M f32	0.25999999	0.25999999 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-712.636963	-712.636902 ± 0.1	
AbshwPos HwPosSource Cnt M u16	1	-7 12.030902 ± 0.1	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-712.636963	-712.636902 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5591.57031	-5591.56982 ± 0.1	<b>✓</b>
AbsHwPos TargetHwAuthority Uls M f32	0.25999999	0.25999999 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	-5591.57031	-5591.56982 ± 0.1	•
AbsHwPos VehCntrOffset HwDeg M f32	9238.54004	9238.54004 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Iqc	1	1	-
AbsHwPos VehCntrValid Cnt M lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	V
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25999999	0.25999999 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-712.636963	-712.636902 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	-
		04045	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	21845 -1600	-1600 ± 0.1	

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.29 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996		
AbsHwPos HandwheelAuthority Uls M f32	0.280000001		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1112.18005		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001		
AbsHwPos_HwPosSource_Cnt_M_u16	296		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_UIs_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	1		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_Uls_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3	- Dadward Darf Ont Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_t			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPos F		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	·-	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target_AbsHwPos_Per2_SensorlessAutho		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPo		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPosSt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
		Expected Value	Peau
Name AbeliwDes AlignedHwDes HwDeg M #32	Actual Value	1207 00008 + 0 000	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	622.700012 3	622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum			
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeq_M_f32	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
	622.700012	622.700012 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCatrOffeet_HwDeg_M_f32	622.700012	622.700012 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc  Pto_Call_Ap_AbsHwPos_NytrDiggMar_SetNTCStatus(NTC_Cat_T_equip)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	
targot_Abartwe ba_e etz_i ianuwneetAuthonty_ola_i32.Value		0.360000014 ± 0.004 622.700012 ± 0.1	
target AhsHwPos Par? HandwheelPosition HwPos f32 volus			
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	3 21845	3 21845	



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	875.5	875.5 ± 0.0009	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.30 (Repeat Count = 1) Name	Input Value		
	·		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1676.60999		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.129999995		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-807.97998		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995		
AbsHwPos_HwPosSource_Cnt_M_u16	176		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995		
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.119999997		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.20999993		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_Uls_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAutl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target_AbsHwPos_Per2_SensorlessAutI	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwF		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	5400	
		Expected Value	Parel
Name Abelly Dec Aligned by Dec Livy Dec M (99)	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	,
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbeHwDos ProvHandwheelDosition HwDog M f32	-1015 08685	1015 08601 ± 0.1	

-1015.08685

-2401.10986

0.119999997

21845

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosState\_Cnt\_M\_enum AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

-1015.08691 ± 0.1

-2401.11011 ± 0.1

0.119999997 ± 0.004

21845



Name			
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1501.30005	1501.30005 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.379999995	0.379999995 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	662.900024	662.900024 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	662.900024	662.900024 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	662.900024	662.900024 ± 0.1	~
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.379999995	0.379999995 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	662.900024	662.900024 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8920.66016	8920.66016 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value	0.379999995	0.379999995 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	662.900024	662.900024 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	662.900024	662.900024 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	935.900024	935.900024 ± 0.0009	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.32 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4209.93018
AbsHwPos HandwheelAuthority Uls M f32	0.310000002
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1173.02002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.310000002
AbsHwPos_HwPosSource_Cnt_M_u16	320
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-448.700012
AbsHwPos_RelHwPos_HwDeg_M_f32	-448.700012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23937
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.310000002
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.200000003
k_HwPosOutputLPFError_HwDeg_f32	1268.30005
k_I2CHwAuthority_UIs_f32	0.200000003
k_MaxSensorlessAuthority_Uls_f32	0.389999986
k_MaxVehCntrOffDiff_HwDeg_f32	1060.40002
k_MinSensorlessAuthority_Uls_f32	1
k_TurnsCntrAuthority_UIs_f32	0.200000003
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	911.900024
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-212.300003
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.389999986
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	683
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	966.099976
$target\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16$	3719
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	0

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32

AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) \\ Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) \\$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value$ 

Rte\_Call\_AbsHwPos\_Per2\_CP1\_CheckpointReached



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-448.700012	-448.700012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999	0.109999999 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3541.94092	3541.94092 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	320	320	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3541.94092	3541.94092 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~

8814.7002

8814.7002

0.109999999

504

1600

65534

1600

320

0 ± 0.004

504

320

65534

1600 ± 0.1

8814.7002 ± 0.1

8814.7002 ± 0.009

0.109999999 ± 0.004

1600 ± 0.1

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32		65535	65535 ± 0.09		~
T					V
Actual Function	Count	Expected Function		Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_Checkpoin	Reached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset	_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset	:_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag		1	<b>✓</b>
Pte Call An AheHwPos NytrDiagMar SetNTCStatus	1	Pte Call An AheHwPoe NytrDiagMar SetN	TCStatus	1	-

Rte\_Call\_AbsHwPos\_Per2\_CP1\_CheckpointReached

Test Step 2.33 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-4350.66992
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.319999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1193.30005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.319999993
AbsHwPos_HwPosSource_Cnt_M_u16	328
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-429
AbsHwPos_RelHwPos_HwDeg_M_f32	-429
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24009
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.319999993
AbsHwPos_TempHwPos_HwDeg_M_f32	-8708.74023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1208.73999
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.310000002
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_Uls_f32	0.310000002
k_MaxSensorlessAuthority_Uls_f32	0.40000006
k_MaxVehCntrOffDiff_HwDeg_f32	1090.09998



			1000
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.310000002		
k_TurnsCntrAuthority_Uls_f32	0.310000002		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	941.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-192.600006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.400000006		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	170		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	996.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3843		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	-429	-429 ± 0.0009	~
AbsHwPos HandwheelAuthority Uls M f32	0.40000006	0.400000006 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID	HWPOS STATE SENSORINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1208.73999	-1208.73999 ± 0.009	~

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32		996.299988	996.299988 ± 0.0009		~
T					<b>✓</b>
Actual Function	Count	Expected Function		Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_Checkpoin	tReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag		1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_Checkpoin	tReached	1	~

0.400000006

703.099976

703.099976

21845

0

0

504

0

0

\*none\*

\*none\*

21845

0.400000006 ± 0.004

703.099976 ± 0.1

703.099976 ± 0.1

Test Step 2.34 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4491.41016	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.330000013	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1213.57996	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.330000013	
AbsHwPos_HwPosSource_Cnt_M_u16	336	
AbsHwPos_HwPosState_Cnt_M_enum	7	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.300003	
AbsHwPos_RelHwPos_HwDeg_M_f32	147.300003	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24081	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2802.53003	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.330000013	

AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc



Name	Input Value		
AbsHwPos_TempHwPos_HwDeg_M_f32	8602.78027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8602.78027		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.319999993		
k_HwPosOutputLPFError_HwDeg_f32	1327.69995		
k_I2CHwAuthority_Uls_f32	0.319999993		
k_MaxSensorlessAuthority_Uls_f32	0.409999996		
k_MaxVehCntrOffDiff_HwDeg_f32	1119.80005		
k_MinSensorlessAuthority_Uls_f32	0.319999993		
k_TurnsCntrAuthority_Uls_f32	0.319999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	0		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-172.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.40999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	723.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	176		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1026.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3967		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Output and the property of the property$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	147.300003	147.300003 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3652.01611	3652.01611 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3652.01611	3652.01611 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	<b>→</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8602.78027	8602.78027 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	-
	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc		504	•
AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	00.	1
	504 0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0 0 0.0100000203	0 0 0.00999999978 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0 0 0.0100000203 1600	0 0 0.00999999978 ± 0.004 1600 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0 0 0.0100000203 1600 65534	0 0.00999999978 ± 0.004 1600 ± 0.1 65534	

T ✓						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>		
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>		
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~		



Test Step 2.35 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4632.1499		
AbsHwPos HandwheelAuthority Uls M f32	0.34000004		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1233.85999		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.340000004		
AbsHwPos_HwPosSource_Cnt_M_u16	344		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	156.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	156.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24153		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.340000004		
AbsHwPos_TempHwPos_HwDeg_M_f32	8496.82031		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8496.82031		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.330000013		
k_HwPosOutputLPFError_HwDeg_f32	1357.40002		
k_I2CHwAuthority_UIs_f32	0.330000013		
k_MaxSensorlessAuthority_Uls_f32	0.41999987		
k_MaxVehCntrOffDiff_HwDeg_f32	1149.5 0.330000013		
k_MinSensorlessAuthority_Uls_f32			
k_TurnsCntrAuthority_UIs_f32 k_UseTurnsCntr_Cnt_lgc	0.330000013		
k VehCntrOffValidLimit HwDeq f32	1600		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.419999987		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	743.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	182		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1056.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4091		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		osReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target AbsHwPos Per2 HandwheelPosi		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
	target_AbsHwPos_Per2_TurnsCntrValidit target_Pim_EOLVehCntrOffset		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	y_Cnt_u08	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name	target_Pim_EOLVehCntrOffset Actual Value	y_Cnt_u08  Expected Value	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994	y_Cnt_u08  Expected Value  156.899994 ± 0.0009	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046	y_Cnt_u08  Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994  0.009999999046  3703.2666  65534  HWPOS_STATE_SENSORINVALID	y_Cnt_u08  Expected Value  156.899994 ± 0.0009  0.00999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994  0.009999999046  3703.2666  65534	y_Cnt_u08  Expected Value  156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004  8496.82031 ± 0.1  8496.82031 ± 0.009	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Learn_Cnt_M_lgc	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004  8496.82031 ± 0.1  8496.82031 ± 0.009  1	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosSource_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Learn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004  8496.82031 ± 0.1  8496.82031 ± 0.009  1  0	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004  8496.82031 ± 0.1  8496.82031 ± 0.009  1  0  *none*	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Arm_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004  8496.82031 ± 0.1  8496.82031 ± 0.009  1  0  *none*  *none*	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504 0	Expected Value  156.899994 ± 0.0009  0.009999999978 ± 0.004  3703.26636 ± 0.1  65534  HWPOS_STATE_SENSORINVALID  3703.26636 ± 0.1  65534  2943.27002 ± 0.1  0 ± 0.004  8496.82031 ± 0.1  8496.82031 ± 0.009  1  0  *none*  *none*  *none*	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrVofstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504 0 0 0.009999999046	Expected Value  156.899994 ± 0.0009 0.00999999998 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.009999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 1 0 504 0 0 0.009999999046 1600	Expected Value  156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004 1600 ± 0.1	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TrempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffst_ear_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.009999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 1 0 504 0 0 0.009999999046 1600 65534	Expected Value  156.899994 ± 0.0009 0.009999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004 1600 ± 0.1 65534	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffste_ar_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  156.899994 0.009999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 1 0 504 0 0 0.009999999046 1600	Expected Value  156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004 1600 ± 0.1	Resu

1600

1600 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Τ				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.36 (Repeat Count = 1) Name	Input Value		
	-4772.89014		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	0.349999994		
	1254.14001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32			
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.34999994		
AbsHwPos_HwPosSource_Cnt_M_u16	352		
AbsHwPos_HwPosState_Cnt_M_enum	9		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	166.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	166.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24225		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.34999994		
AbsHwPos_TempHwPos_HwDeg_M_f32	-8390.86035		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1390.85999		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.340000004		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_UIs_f32	0.340000004		
k_MaxSensorlessAuthority_Uls_f32	0.430000007		
k_MaxVehCntrOffDiff_HwDeg_f32	1179.19995		
k_MinSensorlessAuthority_Uls_f32	0.340000004		
k TurnsCntrAuthority Uls f32	0.340000004		
k_UseTurnsCntr_Cnt_Igc	1		
k VehCntrOffValidLimit HwDeg f32	1030.69995		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.430000007		
	763.400024		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	188		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1086.90002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4215		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuti		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	166.5	166.5 ± 0.0009	1,000
AbshwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32			
	-2121.60986	-2121.61011 ± 0.1	
AbsHwPos_HwPosState_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INPLITINIVALID	65534	

HWPOS\_STATE\_INPUTINVALID

-2121.60986

65535

0

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

AbsHwPos\_HwPosState\_Cnt\_M\_enum

HWPOS\_STATE\_INPUTINVALID

-2121.61011 ± 0.1

65535

0 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1390.85999	-1390.85999 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

lame	Input Value
NosHwPos AlignedCumMtrPos HwDeg M f32	4913.62988
NosHwPos_AlighedCulfivitir-Os_HwDeg_ivi_132	0.360000014
	1274.42004
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	0.360000014
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	
AbsHwPos_HwPosSource_Cnt_M_u16	360
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID 176.100006
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	
\lambda SHwPos_RelHwPos_HwDeg_M_f32	176.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24297
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014
ubsHwPos_TempHwPos_HwDeg_M_f32	8284.88965
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.100000001
_HwPosOutputLPFError_HwDeg_f32	1416.80005
_I2CHwAuthority_Uls_f32	0.100000001
_MaxSensorlessAuthority_Uls_f32	0.43999998
_MaxVehCntrOffDiff_HwDeg_f32	1208.90002
_MinSensorlessAuthority_Uls_f32	0.100000001
_TurnsCntrAuthority_Uls_f32	0.100000001
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1060.40002
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1.
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.43999998
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	783.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	194
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4339
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorSta	atus 5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduced	Perf_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDe	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Ig	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f	



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	176.100006	176.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	783.5	783.5 ± 0.1	<b>~</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	783.5	783.5 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.439999998	0.439999998 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	783.5	783.5 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	783.5	783.5 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T ·					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 2.38 (Repeat Count = 1)	<u> </u>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5054.37012
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.370000005
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1294.69995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.370000005
AbsHwPos_HwPosSource_Cnt_M_u16	368
AbsHwPos_HwPosState_Cnt_M_enum	11
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	185.699997
AbsHwPos_RelHwPos_HwDeg_M_f32	185.699997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24369
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3365.48999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.370000005
AbsHwPos_TempHwPos_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.300000012
k_HwPosOutputLPFError_HwDeg_f32	1446.5
k_I2CHwAuthority_Uls_f32	0.300000012
k_MaxSensorlessAuthority_Uls_f32	0.449999988
k_MaxVehCntrOffDiff_HwDeg_f32	1238.59998
k_MinSensorlessAuthority_Uls_f32	0.300000012
k_TurnsCntrAuthority_Uls_f32	0.300000012
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1090.09998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2



Name	Input Value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.449999988			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	803.599976			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	200			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4463	4463		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatu	us 0	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedP	Perf(target_AbsHwPos_Per2_DiagStatusHw	target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	2 target_AbsHwPos_Per2_HandwheelAu	thority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_Position\_HwDe$	_f32 target_AbsHwPos_Per2_HandwheelPo	sition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	sValid_Cnt_lgc		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32$	target_AbsHwPos_Per2_I2CHwAbsPos	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0	Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAu	thority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_s	f32 target_AbsHwPos_Per2_SensorlessHw	/Pos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u	16 target_AbsHwPos_Per2_SrlComHwPos	sStatus_Cnt_u16		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	185.699997	185.699997 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0699999928	0.0700000003 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3841.86523	3841.86499 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3841.86523	3841.86499 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>	
AbsHwPos_TempHwPos_HwDeg_M_f32	8178.93018	8178.93018 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0699999928	0.0700000003 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~	
Annual Abellus Dee Deep Husbander Out and Combine	65534	65534	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1			
	65535	65535	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1111	65535 0 ± 0.1	<b>*</b>	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.39 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5195.10986
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.37999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1314.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.37999995
AbsHwPos_HwPosSource_Cnt_M_u16	376
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	195.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	195.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24441
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3506.22998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.37999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-8072.97021
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.370000005



Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	1476.19995		
k_I2CHwAuthority_UIs_f32	0.370000005		
k_MaxSensorlessAuthority_Uls_f32	0.460000008		
k_MaxVehCntrOffDiff_HwDeg_f32	1268.30005		
k_MinSensorlessAuthority_Uls_f32	0.370000005		
k_TurnsCntrAuthority_UIs_f32	0.370000005		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1119.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.460000008		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	823.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	206		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4587		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Output Ap\_AbsHwPosAbsHw$	erf_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	6 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos AlignedHwPos HwDeg M f32	195.300003	195.300003 ± 0.0009	•

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	195.300003	195.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	823.700012	823.700012 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	823.700012	823.700012 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000008	0.460000008 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	823.700012	823.700012 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	823.700012	823.700012 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.40 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-5335.8501	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.389999986	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1487	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.389999986	



	le const		
Name	Input Value		
AbsHwPos_HwPosSource_Cnt_M_u16	6384		
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_ProvI lend who all Position_HwPos_M_622	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_RelHwPos_HwDeg_M_f32	204.899994 204.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24513		
AbsHwPos SrlComHwPos HwDeg M f32	3646.96997		
AbsHwPos TargetHwAuthority Uls M f32	0.389999986		
AbsHwPos_TempHwPos_HwDeg_M_f32	7967.00977		
AbsHwPos VehCntrOffset HwDeg M f32	7967.00977		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLV	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.379999995		
k_HwPosOutputLPFError_HwDeg_f32	1505.90002		
k_I2CHwAuthority_Uls_f32	0.379999995		
k_MaxSensorlessAuthority_Uls_f32	0.469999999		
k_MaxVehCntrOffDiff_HwDeg_f32	1298		
k_MinSensorlessAuthority_Uls_f32	0.379999995		
k_TurnsCntrAuthority_UIs_f32	0.379999995		
k_UseTurnsCntr_Cnt_Igc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1149.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	562.400024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.469999999 843.799988		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	212		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1000.59998		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	4711		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPer		PosReducedPerf Cnt lac	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0	Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAu	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	/Pos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16000000000000000000000000000000000000$	target_AbsHwPos_Per2_SrlComHwPos	sStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	204.899994	204.899994 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4014.20361	4014.20386 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4014.20361	4014.20386 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	¥
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977	7967.00977 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	504	
	0	0	J
Dto Call An AbellinDoe NixtrDiagMar SotNTCStatus(Param Cat T 1.00)	T U		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0 00999999978 + 0 004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0 0.00999999046	0.00999999978 ± 0.004	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0 0.00999999046 1600	0.00999999978 ± 0.004 1600 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0 0.00999999046 1600 65534	$0.00999999978 \pm 0.004$ $1600 \pm 0.1$ 65534	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0 0.00999999046 1600	0.00999999978 ± 0.004 1600 ± 0.1	•



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.41 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5476.58984		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1507.09998		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40000006		
AbsHwPos_HwPosSource_Cnt_M_u16	392		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	214.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	214.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24585		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3787.70996		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	7861.0498		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.389999986		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998		
k_I2CHwAuthority_Uls_f32	0.389999986		
k_MaxSensorlessAuthority_Uls_f32	0.479999989		
k_MaxVehCntrOffDiff_HwDeg_f32	1327.69995		
k_MinSensorlessAuthority_Uls_f32	0.389999986		
k TurnsCntrAuthority Uls f32	0.389999986		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	1179.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	582.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.479999989		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	863.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	218		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-500.899994		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	4835		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		osReducedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_(	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	214.5	214.5 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	863.900024	863.900024 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	863.900024	863.900024 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	863.900024	863.900024 ± 0.1	
AbsHwPos TargetHwAuthority Llls M f32	0.470000080	0.47000080 + 0.004	

0.479999989

863.900024

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

0.479999989 ± 0.004

863.900024 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	863.900024	863.900024 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	863.900024	863.900024 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.42 (Repeat Count = 1)	· ·
	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1617.32996
	0.409999996
_ /	1527,19995
	0.409999996
	400
	HWPOS STATE SENSORVALID
	224.100006
	224.100006
	24657
AbsHwPos SrlComHwPos HwDeg M f32	-3928.44995
	0.409999996
	-7755.08984
_ , ,	-1455.08997
	1
AbsHwPos VehCntrValid Cnt M lgc	1
	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
, _ ,	target_Rte_Inst_Ap_AbsHwPos
	0.400000006
k HwPosOutputLPFError HwDeg f32	1565.30005
	0.40000006
	0.49000001
	0
	0.40000006
	0.40000006
k UseTurnsCntr Cnt Igc	1
k_VehCntrOffValidLimit_HwDeg_f32	1208.90002
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.49000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	884
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1268.09998
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4959
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	224.100006	224.100006 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2278.53882	-2278.53882 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984	-7755.08984 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	733.329956	733.330017 ± 0.0009	<b>✓</b>	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	733.329956	733.330017 ± 0.0009	~	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.43 (Repeat Count = 1)	▼ ·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-558.070007
AbsHwPos HandwheelAuthority Uls M f32	0.419999987
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	157.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.419999987
AbsHwPos_HwPosSource_Cnt_M_u16	408
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-389.600006
AbsHwPos_RelHwPos_HwDeg_M_f32	-389.600006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24729
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4069.18994
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.41999987
AbsHwPos_TempHwPos_HwDeg_M_f32	269.209991
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.409999996
k_HwPosOutputLPFError_HwDeg_f32	1595
k_I2CHwAuthority_Uls_f32	0.409999996
k_MaxSensorlessAuthority_Uls_f32	0.5
k_MaxVehCntrOffDiff_HwDeg_f32	1600
k_MinSensorlessAuthority_Uls_f32	0.409999996
k_TurnsCntrAuthority_Uls_f32	0.409999996
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1500.34998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	622.700012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.5
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	904.099976

 $target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value\\ target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value



Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	230		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1298.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5083		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Output StatusHwPosReducedPerf\_Output StatusHwPosReduced$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-389.600006	-389.600006 ± 0.0009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.419999987	0.419999987 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	269.209991	269.209991 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	408	408	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	269.209991	269.209991 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24729	24729	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4069.18994	4069.18994 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.419999987	0.419999987 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1462.16992	-1462.17004 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

0.419999987

269.209991

-1462.16992

408

24729

1600

0.419999987 ± 0.004

-1462.17004 ± 0.009

269.209991 ± 0.1

408

24729

1600 ± 0.1

Test Step 2.44 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	898.809998
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.430000007
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	567.400024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.430000007
AbsHwPos_HwPosSource_Cnt_M_u16	416
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	243.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	243.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24801
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4209.93018
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.430000007
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.419999987
k_HwPosOutputLPFError_HwDeg_f32	50.5999985



Name	Input Value		
k_I2CHwAuthority_UIs_f32	0.419999987		
k_MaxSensorlessAuthority_Uls_f32	0.50999999		
k_MaxVehCntrOffDiff_HwDeg_f32	1416.80005		
k_MinSensorlessAuthority_Uls_f32	0.419999987		
k_TurnsCntrAuthority_Uls_f32	0.419999987		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1268.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.50999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	924.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1328.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5207		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosf	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	243.300003	243.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	147.139496	147.139496 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534		



Input Value		
252.899994		
252.899994		
24873		
-4350.66992		
0.439999998		
550.690002		
-1437.21997		
0		
0		
target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
target_Rte_Inst_Ap_AbsHwPos		
0.430000007		
0		
0.430000007		
0.519999981		
1446.5		
0.430000007		
0.430000007		
0		
1298		
0		
	uity_Crit_u06	
<u> </u>	Evenante d Value	Danul
		Resul
		•
1		
	*none*	
	*none*	
1 0.420000000	0.420000000 ± 0.004	
0.439999998	0.439999998 ± 0.004	
0.439999998 1131.30359	1131.30359 ± 0.1	
0.439999998 1131.30359 424	1131.30359 ± 0.1 424	
0.439999998 1131.30359	1131.30359 ± 0.1	
	252.89994 252.899994 24873 -4350.66992 0.43999998 550.690002 -1437.21997 0 0 target_Rte_Call_Ap_AbsHwPos_EOLV6 target_Rte_inst_Ap_AbsHwPos 0.430000007 0.519999981 1446.5 0.430000007 0.519999981 0 0 662.900024 0 0.51999988 242 1358.69995 5331 1 ctarget_AbsHwPos_Per2_DiagStatusHw target_AbsHwPos_Per2_HandwheelAuf target_AbsHwPos_Per2_LandwheelPostarget_AbsHwPos_Per2_I2CHwAbsPostarget_AbsHwPos_Per2_I2CHwAbsPostarget_AbsHwPos_Per2_SensorlessAuf target_AbsHwPos_Per2_SensorlessAuf target_AbsHwPos_Per2_SensorlessHw target_AbsHwPos_Per2_SensorlessHwPos_Per2_Senso	252.899994 252.899994 252.899994 252.899994 24873 4350.66992 0.439999998 550.690002 -1-437.21997 0 0 0 target_Rte_Inst_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus target_Rte_Inst_Ap_AbsHwPos 0.430000007 0 0.430000007 0.519999981 1446.5 0.430000007 0 0.430000007 0 0 0.430000007 0 0 0.519999981 1446.5 1298 0 0 0 0 0.519999981 1446.5 11 11 11 11 11 11 11 11 11 11 11 11 11

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.46 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6180.29004		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.44999988		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos HandwheelPositionLPF M str.K Uls f32	-1000.29999 0.449999988		
	432		
AbsHwPos_HwPosSource_Cnt_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum	6		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	262.5 262.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	24945		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	4491.41016		
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority Uls M f32	0.449999988		
AbsHwPos TempHwPos HwDeg M f32	691.429993		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977		
	1		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehC	ntrOffeet CetErrorStatus ErrorStatus	
, , , ,	·	INITORISEL_GELETIOISIAIUS_ETIOISIAIUS	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<—HwPosAuthorityStep_Uls_f32 <—HwPosQutputl PEError, HwPog_f32	0.43999998		
<_HwPosOutputLPFError_HwDeg_f32 < I2CHwAuthority_Llls_f32	1620		
<_I2CHwAuthority_Uls_f32 <_MaySensorlessAuthority_Uls_f32	0.439999998 0.529999971		
<pre>&lt;_MaxSensorlessAuthority_Uls_f32</pre>	1476.19995		
<pre>&lt;_MaxVehCntrOffDiff_HwDeg_f32</pre> < MinSensorlessAuthority Uls f32	0.43999998		
	0.439999998		
c_TurnsCntrAuthority_Uls_f32			
<_UseTurnsCntr_Cnt_lgc	1		
<_VehCntrOffValidLimit_HwDeg_f32	1327.69995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	683		
rarget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.529999971		
rarget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	964.400024		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	248		
rarget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1388.90002		
rarget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5455		
rarget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2	D 1 1D 1 0 1 1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	,	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	<del></del>	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Re
AbsHwPos_AlignedHwPos_HwDeg_M_f32	262.5	262.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	691.429993	691.429993 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	691.429993	691.429993 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	691.429993	691.429993 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977	7331.25977 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
+ iss	0.00999999046	0.00999999978 ± 0.004	
arget AbsHwPos Per2 HandwheelAuthority Uls f32 value			
arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	691.429993	691.429993 ± 0.1	



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1388.90002	1388.90002 ± 0.009	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-6321.02979		
AbsHwPos HandwheelAuthority Uls M f32	0.460000008		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-980.599976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.46000008		
AbsHwPos_HwPosSource_Cnt_M_u16	6440		
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_ProvIdentwheelPasition_HwPos_M_622			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	272.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	272.100006 25017		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	4632.1499		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0.460000008		
AbsHwPos_TargetHwAuthority_UIs_M_f32			
AbsHwPos_TempHwPos_HwDeg_M_f32	-832.169983		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntrOπset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.44999988		
k_HwPosOutputLPFError_HwDeg_f32	139.699997		
k_I2CHwAuthority_Uls_f32	0.44999988		
k_MaxSensorlessAuthority_Uls_f32	0.540000021		
k_MaxVehCntrOffDiff_HwDeg_f32	1505.90002		
k_MinSensorlessAuthority_Uls_f32	0.44999988		
k_TurnsCntrAuthority_Uls_f32	0.44999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1357.40002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.540000021		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	984.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	254		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1419.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5579		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosP	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	272.100006	272.100006 ± 0.0009	
AbelluDes Hendubes Authority IIIs M 522	0.460000000	0.460000000000000	

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AbsHwPos_AlignedHwPos_HwDeg_M_f32	272.100006	272.100006 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-912.322144	-912.322205 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	6440	6440	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-912.322144	-912.322205 ± 0.1	~



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25017	25017	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4632.1499	4632.1499 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-832.169983	-832.169983 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998	7225.2998 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000008	0.460000008 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-912.322144	-912.322205 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	6440	6440	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	25017	25017	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1419.09998	1419.09998 ± 0.009	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Vame.	Immut Value
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6461.77002
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.46999999
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-960.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.469999999
AbsHwPos_HwPosSource_Cnt_M_u16	448
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	281.700012
AbsHwPos_RelHwPos_HwDeg_M_f32	281.700012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25089
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-4772.89014
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.469999999
AbsHwPos_TempHwPos_HwDeg_M_f32	972.909973
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996
hbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0
_HwPosOutputLPFError_HwDeg_f32	169.399994
_I2CHwAuthority_Uls_f32	0.460000008
_MaxSensorlessAuthority_Uls_f32	0.550000012
:_MaxVehCntrOffDiff_HwDeg_f32	1535.59998
_MinSensorlessAuthority_Uls_f32	0.46000008
_TurnsCntrAuthority_Uls_f32	0.460000008
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1387.09998
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	723.200012
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.550000012
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1004.59998
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	12
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-980.599976
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5703
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_Error	Status 4
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduc	cedPerf_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Us	s_f32 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_Hw	Deg_f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cn	t_lgc target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwD	



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	281.700012	281.700012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.469999999	0.469999999 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-52.0092773	-52.0093002 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-52.0092773	-52.0093002 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	972.909973	972.909973 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996	-1119.32996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.469999999	0.469999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-52.0092773	-52.0093002 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32

AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

Rte\_Call\_AbsHwPos\_Per2\_CP1\_CheckpointReached

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc



Name	Input Value		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1024.69995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	16		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-960.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5827		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1339.14807	1339.14795 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	456	456	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1339.14807	1339.14795 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25161	25161	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4913.62988	4913.62988 ± 0.1	<b>✓</b>

0.479999989

1113.65002

7013.37988

0.479999989

1339.14807

0

504

456 25161

1600

0.479999989 ± 0.004

1113.65002 ± 0.1

0

0

\*none\*

\*none\*

\*none\*

25161

1600 ± 0.1

7013.37988 ± 0.009

0.479999989 ± 0.004

1339.14795 ± 0.1

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32		-960.900024	-960.900024 ± 0.0009		~
T					V
Actual Function	Count	Expected Function		Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_Checkpoin	Reached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag		1	~

Rte\_Call\_AbsHwPos\_Per2\_CP1\_CheckpointReached

Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-6743.25
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.49000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1567.40002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.49000001
AbsHwPos_HwPosSource_Cnt_M_u16	464
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	300.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	300.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25233
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5054.37012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.49000001
AbsHwPos_TempHwPos_HwDeg_M_f32	-1254.39001
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.479999989
_HwPosOutputLPFError_HwDeg_f32	228.800003
k_I2CHwAuthority_Uls_f32	0.47999989



Name	Input Value		
k MaxSensorlessAuthority Uls f32	0.569999993		
k_MaxVehCntrOffDiff_HwDeg_f32	1595		
k_MinSensorlessAuthority_Uls_f32	0.479999989		
k_TurnsCntrAuthority_UIs_f32	0.479999989		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1446.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	763.400024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.569999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1044.80005		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-941.200012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5951		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	300.899994	300.899994 ± 0.0009	~

target_Rte_inst_Ap_AbsnwPos.Plin_EOLvenChtrOffset	target_Pim_EOLVenChironset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	300.899994	300.899994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	184.7229	184.7229 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	184.7229	184.7229 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-1254.39001	-1254.39001 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992	6907.41992 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000203	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	184.7229	184.7229 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.51 (Repeat Count = 1)	van de la companya d
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6883.99023
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	472
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID



Name	Input Value		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	310.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	310.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25305		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5195.10986		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5		
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	269.209991		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.49000001		
k_HwPosOutputLPFError_HwDeg_f32	258.5		
k_I2CHwAuthority_UIs_f32	0		
k_MaxSensorlessAuthority_Uls_f32	0.57999983		
k_MaxVehCntrOffDiff_HwDeg_f32	50.5999985		
k_MinSensorlessAuthority_Uls_f32	0.49000001		
k_TurnsCntrAuthority_Uls_f32	0.49000001		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1476.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	783.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.579999983		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1064.90002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	24		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-921.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6075		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1	a Dadwaad Darf Cat Jac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_HwPosSource_C target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_F		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target AbsHwPos Per2 SensorlessHwPo	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosSt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target AbsHwPos Per2 SrlComHwPos H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	310.5	310.5 ± 0.0009	- TOOUIC
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	<b>V</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-59.1499634	-59.1500015 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	2	2	•
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-59.1499634	-59.1500015 ± 0.1	•
AbsHwPos SrlComHwPosStatus Cnt M u16	21845	21845	
AbsHwPos SrlComHwPos HwDeg M f32	783.5	783.5 ± 0.1	_
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	783.5	783.5 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	269.209991	269.209991 ± 0.0009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	-
target AbsHwPos Per2 HandwheelPosition HwDeg f32.value	-59.1499634	-59.1500015 ± 0.1	<b>✓</b>
		2	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	4	
	2 21845	21845	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value			· ·

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.52 (Repeat Count = 1) Name	Input Value		
	7024.72998		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	0.50999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	114.300003		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.50999999		
AbsHwPos_HwPosSource_Cnt_M_u16	480		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	320.100006		
AbsHwPos RelHwPos HwDeg M f32	320.100006		
AbsHwPos_srlComHwPosStatus_Cnt_M_u16	25377		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5335.8501		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.50999999		
AbsHwPos TempHwPos HwDeg M f32	1535.87		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCi	otrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	IIIOIIset_GetEITorGtatus_EirorGtatus	
	0.5		
K_HwPosAuthorityStep_UIs_f32 K_HwPosOutputLPFError_HwDeg_f32	288.200012		
C_INVPOSOUIDUILPPETIOI_INVDeg_I32	1		
<pre>K_IZOHWAUTIORIY_UIS_I32 K_MaxSensorlessAuthority_UIs_f32</pre>	0.589999974		
k_MaxVehCntrOffDiff_HwDeg_f32	80.3000031		
MinSensorlessAuthority Uls f32	0.5		
C_TurnsCntrAuthority_UIs_f32	0.5		
<_TurnsCritt/Authority_ofs_132 <_UseTurnsCrit_Crit_lgc	0.3		
COSETURISCHIL_CHI_GC VehCntrOffValidLimit HwDeg f32	1505.90002		
	0		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.589999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1085		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	28		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-901.799988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6199		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	<del></del>	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStar		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	320.100006	320.100006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	465.843018	465.842987 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	465.843018	465.842987 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012	-409.950012 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
	1	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target AbsHwPos_Per2_HandwheelAuthority_Uls_f32_value		1 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1 465.843018	1 ± 0.004 465.842987 ± 0.1	





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	550.690002	550.690002 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	489.139954	489.140015 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	823.700012	823.700012 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-882.099976	-882.099976 ± 0.0009	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Took Story 2 54 (Bornock Count = 4)	
Test Step 2.54 (Repeat Count = 1)	·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7306.20996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.529999971
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	139.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.529999971
AbsHwPos_HwPosSource_Cnt_M_u16	9496
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	389.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	389.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25521
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5617.33008
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.529999971
AbsHwPos_TempHwPos_HwDeg_M_f32	1817.34998
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.519999981
k_HwPosOutputLPFError_HwDeg_f32	347.600006
k_I2CHwAuthority_Uls_f32	0.519999981
k_MaxSensorlessAuthority_Uls_f32	0.610000014
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997
k_MinSensorlessAuthority_Uls_f32	0.519999981
k_TurnsCntrAuthority_UIs_f32	0
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.610000014
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1125.19995
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-862.400024
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6447
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	389.200012	389.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4173.37207	-4173.37207 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4173.37207	-4173.37207 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993	691.429993 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.55 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	7446.9502
AbsHwPos HandwheelAuthority Uls M f32	0.540000021
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	151.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.540000021
AbsHwPos_HwPosSource_Cnt_M_u16	504
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-468.399994
AbsHwPos_RelHwPos_HwDeg_M_f32	-468.399994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25593
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5758.06982
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.540000021
AbsHwPos_TempHwPos_HwDeg_M_f32	1958.08997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-832.169983
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.529999971
k_HwPosOutputLPFError_HwDeg_f32	377.299988
k_I2CHwAuthority_Uls_f32	0.529999971
k_MaxSensorlessAuthority_Uls_f32	0.620000005
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994
k_MinSensorlessAuthority_Uls_f32	0.529999971
k_TurnsCntrAuthority_Uls_f32	1
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1595
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	863.900024
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.620000005
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1145.30005



Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-842.700012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6571		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	5		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerror and all the properties of the$	f_( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-468.399994	-468.399994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4540.41504	4540.41504 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4540.41504	4540.41504 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8279.12012	8279.12012 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	8279.12012	8279.12012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-832.169983	-832.169983 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AbsHwPos Per2 CP1 ChecknointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>V</b>	

-842.700012

1600

1600 ± 0.1

-842.700012 ± 0.0009

Test Step 2.56 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7587.68994
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.550000012
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	163.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.550000012
AbsHwPos_HwPosSource_Cnt_M_u16	512
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	784.099976
AbsHwPos_RelHwPos_HwDeg_M_f32	7844.1001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25665
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5898.81006
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.550000012
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2521.05005
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.540000021
k_HwPosOutputLPFError_HwDeg_f32	50.5999985
k_I2CHwAuthority_Uls_f32	0.540000021

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 



Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.629999995		
k_MaxVehCntrOffDiff_HwDeg_f32	199.100006		
k_MinSensorlessAuthority_Uls_f32	0.540000021		
k_TurnsCntrAuthority_Uls_f32	0.479999989		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	884		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.629999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1165.40002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-823		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6695		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7844.1001	7844.1001 ± 0.009	~

target_rate_met_rp_rberran een mi_Leelvenena eneet	tangot_1 mi		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7844.1001	7844.1001 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2860.40674	2860.40698 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2860.40674	2860.40698 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5066.63965	5066.64014 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.47999989 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5066.63965	5066.64014 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2521.05005	2521.05005 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.47999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.57 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7728.43018	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.560000002	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	176.300003	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.560000002	
AbsHwPos_HwPosSource_Cnt_M_u16	520	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	



Name	Input Value		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	966.200012		
AbsHwPos_RelHwPos_HwDeg_M_f32	9665.2002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25737		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6039.5498	6039.5498	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.560000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1261.79004		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.550000012		
k_HwPosOutputLPFError_HwDeg_f32	80.3000031		
k_I2CHwAuthority_UIs_f32	0.550000012		
k_MaxSensorlessAuthority_UIs_f32	0.639999986		
k_MaxVehCntrOffDiff_HwDeg_f32	228.800003		
k_MinSensorlessAuthority_UIs_f32	0.550000012		
k_TurnsCntrAuthority_UIs_f32	0.550000012		
k_UseTurnsCntr_Cnt_lgc	80.3000031		
k_VehCntrOffValidLimit_HwDeg_f32	0		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	100.099998		
	0		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.639999986		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1185.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	48		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-803.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		ReducedPart Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<del>-</del>	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	9665.2002	9665.2002 ± 0.009	-
AbsHwPos HandwheelAuthority Uls M f32	0.639999986	0.639999986 ± 0.004	<b>✓</b>
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	741.452026	741.452026 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos PrevHandwheelPosition HwDeq M f32	741.452026	741.452026 ± 0.1	<b>✓</b>
AbsHwPos SrlComHwPosStatus Cnt M u16	21845	21845	
AbsHwPos SrlComHwPos HwDeq M f32	1185.5	1185.5 ± 0.1	<b>✓</b>
AbsHwPos TargetHwAuthority Uls M f32	0.639999986	0.639999986 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	1185.5	1185.5 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1261.79004	-1261.79004 ± 0.009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.639999986	0.639999986 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	741.452026	741.452026 ± 0.1	-
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	-
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1185.5	1185.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-803.299988	-803.299988 ± 0.0009	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.58 (Repeat Count = 1) Name	Input Value		
	7869.16992		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	0.569999993		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-941.200012		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.569999993		
AbsHwPos_HwPosSource_Cnt_M_u16	528		
AbsHwPos_HwPosState_Cnt_M_enum	5		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-409.299988		
AbsHwPos RelHwPos HwDeg M f32	-409.299988		
AbsHwPos SrlComHwPosStatus Cnt M u16	25809		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos TargetHwAuthority Uls M f32	0.569999993		
AbsHwPos TempHwPos HwDeg M f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCr	ntrOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
(_HwPosAuthorityStep_Uls_f32	0.560000002		
CHWPosOutputLPFError_HwDeg_f32	110		
<_I2CHwAuthority_Uls_f32	0.560000002		
C_ISST MV tationty_OIS_IOZ C_MaxSensorlessAuthority_UIs_f32	0.649999976		
<_maxVehCntrOffDiff_HwDeg_f32	258.5		
MinSensorlessAuthority Uls f32	0.560000002		
<_TurnsCntrAuthority_Uls_f32	0.560000002		
<_UseTurnsCntr_Cnt_lgc	1		
VehCntrOffValidLimit HwDeg f32	110		
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1		
rarget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
rarget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.649999976		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1205.59998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	52		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-783.599976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		ReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthority Uls f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	<del></del>	
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthorit		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	,	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidity_		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Re
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-409.299988	-409.299988 ± 0.0009	1.0
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	952.06073	952.06073 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	952.06073	952.06073 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006	2380.31006 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003	2802.53003 ± 0.009	
AbsHwPos_VehCntrOfiset_HwDeg_M_i32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
NOTIVE OF VOTOTILIVATION OF IVE INC.	504	504	
	JUT		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	١٨	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)			



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-783.599976	-783.599976 ± 0.0009	✓

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.59 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8009.91016		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.579999983	0.579999983	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-921.5		
AbsHwPos HandwheelPositionLPF M str.K UIs f32	0.579999983		
AbsHwPos_HwPosSource_Cnt_M_u16	536		
AbsHwPos_HwPosState_Cnt_M_enum	6		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213		
AbsHwPos RelHwPos HwDeg M f32	213		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25881		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.579999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc  Pto_Call_Ab_AbsHwPos_EOLVebCntrOffcet_GetErrorStatus(ErrorStatus)		trOffeet GetErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	iiOnser_Gerenorstatus_enorstatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.569999993		
k_HwPosOutputLPFError_HwDeg_f32	139.699997		
k_I2CHwAuthority_Uls_f32	0.569999993		
k_MaxSensorlessAuthority_UIs_f32	0.660000026		
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.569999993		
k_TurnsCntrAuthority_Uls_f32	0.569999993		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	139.699997		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.660000026		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1225.69995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	56		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-763.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7067		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Other and the property of the property $	target_AbsHwPos_Per2_DiagStatusHwPosR	teducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	_	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	213	213 ± 0.0009	- tooult
Abolity Doc Hoody book Authority His M 622	0.570000003	0.570000003 + 0.004	

0.579999983

1075.17896

1075.17896

HWPOS\_STATE\_INPUTINVALID

536

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32 AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

0.579999983 ± 0.004

HWPOS\_STATE\_INPUTINVALID

1075.17896 ± 0.1

1075.17896 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25881	25881	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979	-6321.02979 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.579999983	0.579999983 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002	2943.27002 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.579999983	0.579999983 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1075.17896	1075.17896 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	536	536	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	25881	25881	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-763.900024	-763.900024 ± 0.0009	<b>~</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.60 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.589999974
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	324.5
AbsHwPos HandwheelPositionLPF M str.K UIs f32	0.589999974
AbsHwPos HwPosSource Cnt M u16	544
AbsHwPos HwPosState Cnt M enum	7
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25953
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002
AbsHwPos TargetHwAuthority Uls M f32	0.589999974
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos VehCntrValid Cnt M Igc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.579999983
k_HwPosOutputLPFError_HwDeg_f32	169.399994
k_I2CHwAuthority_Uls_f32	0.579999983
k_MaxSensorlessAuthority_Uls_f32	0.670000017
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994
k_MinSensorlessAuthority_Uls_f32	0.579999983
k_TurnsCntrAuthority_Uls_f32	0.579999983
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	169.399994
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	160.399994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.670000017
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1245.80005
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	60
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-744.200012
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7191
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-1437.41101	-1437.41113 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001	-1084.01001 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.61 (Repeat Count = 1)	<u> </u>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.600000024
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024
AbsHwPos_HwPosSource_Cnt_M_u16	7552
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26025
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.589999974
k_HwPosOutputLPFError_HwDeg_f32	199.100006
k_I2CHwAuthority_Uls_f32	0.589999974
k_MaxSensorlessAuthority_Uls_f32	0.680000007
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006
k_MinSensorlessAuthority_Uls_f32	0.589999974
k_TurnsCntrAuthority_UIs_f32	0.589999974
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	199.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.680000007

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value



Name	Input Value		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1265.90002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	64		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-724.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7315		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75	3224.75 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

504

1600

65534

65534

1600

65535

0.0100000501

\*none\*

\*none\*

\*none\*

65534

65534

1600 ± 0.1

1600 ± 0.1

65535 ± 0.09

0.00999999978 ± 0.004

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.610000014
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-488.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.610000014
AbsHwPos_HwPosSource_Cnt_M_u16	560
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	140.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	140.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26097
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.610000014
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3365.48999
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.600000024



Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	228.800003		
k_I2CHwAuthority_Uls_f32	0.600000024		
k_MaxSensorlessAuthority_Uls_f32	0.68999998		
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
k_MinSensorlessAuthority_Uls_f32	0.600000024		
k_TurnsCntrAuthority_Uls_f32	0.600000024		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.689999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1286		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1872.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7439		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AL H. D. Ali. H. D. H. D. A4 600	4.40.000000	440.000000 + 0.0000	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1605.03577	1605.03564 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1605.03577	1605.03564 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3365.48999	3365.48999 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.63 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8572.87012	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.620000005	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	765.799988	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.620000005	



		,	
Name	Input Value		
AbsHwPos_HwPosSource_Cnt_M_u16	568		
AbsHwPos_HwPosState_Cnt_M_enum	10		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	160.399994		
AbsHwPos_RelHwPos_HwDeg_M_f32	160.399994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26169		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.620000005		
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1506.22998		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_UIs_f32	0.610000014		
k_HwPosOutputLPFError_HwDeg_f32	258.5		
k_I2CHwAuthority_UIs_f32	0.610000014		
k_MaxSensorlessAuthority_Uls_f32	0.699999988 407		
k_MaxVehCntrOffDiff_HwDeg_f32 k MinSensorlessAuthority Uls f32	0.610000014		
k TurnsCntrAuthority UIs f32	0.610000014		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	258.5		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.69999988		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1306.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	72		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1902.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7563		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_InstAp\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_InstAp\_AbsHw$	( target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_TurnsCntrValidity_0 target_Pim_EOLVehCntrOffset	SIII_u06	
		F and ad Walter	D
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos HandwheelAuthority Uls M f32	160.399994	160.399994 ± 0.0009	
-	0.620000005	0.620000005 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos HwPosSource Cnt M u16	-1621.08228 568	-1621.08215 ± 0.1	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID	HWPOS STATE INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeq M f32	-1621.08228	-1621.08215 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	-1021.00220		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	26160	26160	
	26169 6883 99023	26169 6883 99023 ± 0.1	
	6883.99023	6883.99023 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32			
AbsHwPos_TargetHwAuthority_Uls_M_f32	6883.99023 0.620000005	6883.99023 ± 0.1 0.620000005 ± 0.004	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023 0.620000005 -3084.01001	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	6883.99023 0.620000005 -3084.01001 -1506.22998	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	6883.99023 0.620000005 -3084.01001 -1506.22998	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	6883.99023 0.620000005 -3084.01001 -1506.22998 1	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1	
AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	6883.99023 0.620000005 -3084.01001 -1506.22998 1 0 504	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1 0 *none*	
AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6883.99023 0.620000005 -3084.01001 -1506.22998 1 0 504	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1 0 *none* *none*	
AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	6883.99023 0.620000005 -3084.01001 -1506.22998 1 0 504 1	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1 0 *none* *none*	
AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value	6883.99023 0.620000005 -3084.01001 -1506.22998 1 0 504 1 1 0.620000005	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1 0 *none* *none* *none* 0.6200000005 ± 0.004	0
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	6883.99023 0.620000005 -3084.01001 -1506.22998 1 0 504 1 1 0.620000005 -1600	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1 0 *none* *none* *none* 0.620000005 ± 0.004 -1600 ± 0.1	0
AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value  target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	6883.99023 0.620000005 -3084.01001 -1506.22998 1 0 504 1 1 0.620000005 -1600 568	6883.99023 ± 0.1 0.620000005 ± 0.004 -3084.01001 ± 0.1 -1506.22998 ± 0.009 1 0 *none* *none* *none* 0.620000005 ± 0.004 -1600 ± 0.1	



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.64 (Repeat Count = 1) Name	Input Value		
	•		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8713.61035		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.629999995		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	912.900024		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.629999995		
AbsHwPos_HwPosSource_Cnt_M_u16	576		
AbsHwPos_HwPosState_Cnt_M_enum	11		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	180.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	180.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26241		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7024.72998		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.62999995		
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3646.96997		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.620000005		
k_HwPosOutputLPFError_HwDeg_f32	288.200012		
k_I2CHwAuthority_UIs_f32	0.620000005		
k_MaxSensorlessAuthority_Uls_f32	0.709999979		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.620000005		
k_TurnsCntrAuthority_Uls_f32	0.620000005		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	288.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.709999979		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1326.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	76		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1932.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7687		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		osReducedParf Cnt Idc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	180.5	180.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2369.36572	2369.36548 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	٠,
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2369.36572	2369.36548 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
	0	0 ± 0.1	
AbsHwPos SrlComHwPos HwDeg M f32	10		

3224.75

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

0 ± 0.004

3224.75 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3646.96997	3646.96997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1932.5	1932.5 ± 0.009	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.65 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9558.0498
AbsHwPos_HandwheelAuthority_Uls_M_f32	0
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-429
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.689999998
AbsHwPos_HwPosSource_Cnt_M_u16	624
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006
bsHwPos_RelHwPos_HwDeg_M_f32	301.100006
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	26313
.bsHwPos_SrlComHwPos_HwDeg_M_f32	1889.09998
AbsHwPos TargetHwAuthority Uls M f32	0.68999998
bsHwPos_TempHwPos_HwDeg_M_f32	4069.18994
bsHwPos_VehCntrOffset_HwDeg_M_f32	4491.41016
bsHwPos VehCntrOfstLearn Cnt M Igc	1
bsHwPos VehCntrValid Cnt M Igc	1
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
tte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.680000007
HwPosOutputLPFError_HwDeg_f32	466.399994
I2CHwAuthority Uls f32	0.680000007
MaxSensorlessAuthority Uls f32	0.76999981
MaxVehCntrOffDiff HwDeg f32	614.900024
_MinSensorlessAuthority_UIs_f32	0.680000007
TurnsCntrAuthority Uls f32	0.680000007
UseTurnsCntr Cnt Igc	1
VehCntrOffValidLimit HwDeg f32	466.399994
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994
	1
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	
irget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.769999981
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1446.80005
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2113.69995
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8431
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2674.75098	2674.75122 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2674.75098	2674.75122 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4069.18994	4069.18994 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4491.41016	4491.41016 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>~</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2113.69995	2113.69995 ± 0.009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.66 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	9698,79004
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	147.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos HwPosSource Cnt M u16	632
AbsHwPos HwPosState Cnt M enum	5
AbsHwPos PrevHandwheelPosition HwDeg M f32	321.26001
AbsHwPos RelHwPos HwDeg M f32	3421.26001
AbsHwPos SrlComHwPosStatus Cnt M u16	26385
AbsHwPos SrlComHwPos HwDeg M f32	909.380005
AbsHwPos TargetHwAuthority Uls M f32	0.69999988
AbsHwPos TempHwPos HwDeg M f32	4209.93018
AbsHwPos VehCntrOffset HwDeg M f32	4632.1499
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos VehCntrValid Cnt M lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.68999998
k_HwPosOutputLPFError_HwDeg_f32	496.100006
k_I2CHwAuthority_Uls_f32	0.68999998
k_MaxSensorlessAuthority_Uls_f32	0.779999971
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976
k_MinSensorlessAuthority_Uls_f32	0.68999998
k_TurnsCntrAuthority_Uls_f32	0.68999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	496.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.779999971
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1466.90002
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	104
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2143.8999



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8555		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorSta	tus 3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduced	Perf_( target_AbsHwPos_Per2_DiagStatu	usHwPosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3	target_AbsHwPos_Per2_Handwhe	eelAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg	g_f32 target_AbsHwPos_Per2_Handwhe	elPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSo	ource_Cnt_u16	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Ig$	c target_AbsHwPos_Per2_I2CHwAb	sPosValid_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f322AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_Per2\_f32AbsHwPos\_f32A$	target_AbsHwPos_Per2_I2CHwAb	sPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMo	de_Cnt_enum	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f3$	target_AbsHwPos_Per2_Sensorles	ssAuthority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_Results = 100000000000000000000000000000000000$	_f32 target_AbsHwPos_Per2_Sensorles	ssHwPos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_restrictions and the state of $	u16 target_AbsHwPos_Per2_SrlComH	wPosStatus_Cnt_u16	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_SrlComH	wPos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08$	target_AbsHwPos_Per2_TurnsCnt	rValidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	3421.26001	3421.26001 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988	0.69999988 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2991.14111	2991.14111 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	632	632	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2991.14111	2991.14111 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26385	26385	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988	0.699999988 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	4209.93018	4209.93018 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4632.1499	4632.1499 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.699999988	0.699999988 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	632	632	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26385	26385	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	909.380005	909.380005 ± 0.1	•

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.67 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9839.53027
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.709999979
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	156.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.709999979
AbsHwPos_HwPosSource_Cnt_M_u16	640
AbsHwPos_HwPosState_Cnt_M_enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	341.540009
AbsHwPos_RelHwPos_HwDeg_M_f32	341.540009
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26457
AbsHwPos_SrlComHwPos_HwDeg_M_f32	929.659973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.709999979
AbsHwPos_TempHwPos_HwDeg_M_f32	-4350.66992
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1572.89001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	525.799988
k_I2CHwAuthority_Uls_f32	0.69999988
k_MaxSensorlessAuthority_Uls_f32	0.790000021
k_MaxVehCntrOffDiff_HwDeg_f32	674.299988
k_MinSensorlessAuthority_Uls_f32	0.69999988



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Name	Input Value		
k_TurnsCntrAuthority_Uls_f32	0.69999988		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	525.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.790000021		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1487		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	108		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2174.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8679		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	thority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	341.540009	341.540009 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-3043.47461	-3043.47461 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID	HWPOS STATE INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-3043.47461	-3043.47461 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	-4350.66992	-4350.66992 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1572.89001	-1572.89001 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos VehCntrValid Cnt M Igc	0	0	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	504	
Die Cell As Abellin Des Abstraction (Control (Co			

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

0.00999999046

0.00999999978 ± 0.004

-1600 ± 0.1

65535 ± 0.09

65534

65535

0 ± 0.1

-1600

65534

65535

65535

Test Step 2.68 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9980.26953	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.720000029	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	166.5	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.720000029	
AbsHwPos_HwPosSource_Cnt_M_u16	1	
AbsHwPos_HwPosState_Cnt_M_enum	7	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	361.820007	
AbsHwPos_RelHwPos_HwDeg_M_f32	361.820007	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26529	

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

 $target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value$ target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value$ 



Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	949.940002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.720000029		
AbsHwPos_TempHwPos_HwDeg_M_f32	4491.41016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4913.62988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.709999979		
k_HwPosOutputLPFError_HwDeg_f32	555.5		
k_I2CHwAuthority_Uls_f32	0.709999979		
k_MaxSensorlessAuthority_Uls_f32	0.800000012		
k MaxVehCntrOffDiff HwDeg f32	704		
k_MinSensorlessAuthority_Uls_f32	0.709999979		
	0.709999979		
k_TurnsCntrAuthority_UIs_f32	0.709999979		
k_UseTurnsCntr_Cnt_lgc			
k_VehCntrOffValidLimit_HwDeg_f32	555.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.800000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1507.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	112		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2204.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8803		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutI	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl	Pos HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	,	
Name	Actual Value	Expected Value	Result
		· · · · · · · · · · · · · · · · · · ·	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	361.820007	361.820007 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.720000029	0.720000029 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3280.43555	3280.4353 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3280.43555	3280.4353 ± 0.1	•
AbsHwPosSrlComHwPosStatus_Cnt_M_u16	26529	3280.4353 ± 0.1 26529	•
			•
AbsHwPosSrlComHwPosStatus_Cnt_M_u16	26529	26529	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	26529 949.940002	26529 949.940002 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	26529 949.940002 0.720000029	26529 949.940002 ± 0.1 0.720000029 ± 0.004	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	26529 949.940002 0.720000029 4491.41016	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	26529 949.940002 0.720000029 4491.41016 4913.62988	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	26529 949.940002 0.720000029 4491.41016 4913.62988 0	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	26529 949.940002 0.720000029 4491.41016 4913.62988 0	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	26529 949.940002 0.720000029 4491.41016 4913.62988 0 0	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0 0 *none*	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	26529 949.940002 0.720000029 4491.41016 4913.62988 0 0 504	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0 0 *none* *none*	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	26529 949.940002 0.720000029 4491.41016 4913.62988 0 0 504	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0 0 *none* *none* *none* 0.720000029 ± 0.004	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstt_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	26529 949.940002 0.720000029 4491.41016 4913.62988 0 0 504 1 1 0.720000029 1600	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0 0 *none* *none* *none*	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstt_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	26529 949.940002 0.720000029 4491.41016 4913.62988 0 0 504 1 1 0.720000029 1600	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0 *none* *none* *none* 0.720000029 ± 0.004 1600 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	26529 949.940002 0.720000029 4491.41016 4913.62988 0 0 504 1 1 0.720000029 1600	26529 949.940002 ± 0.1 0.720000029 ± 0.004 4491.41016 ± 0.1 4913.62988 ± 0.009 0 0 *none* *none* *none* 0.720000029 ± 0.004 1600 ± 0.1	

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.69 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9874.30957		
AbsHwPos HandwheelAuthority Uls M f32	0.730000019		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	176.100006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.730000019		
AbsHwPos_HwPosSource_Cnt_M_u16	65534		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	382.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	382.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26601		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	970.219971		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.730000019		
AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VebCstrOffset_HwDeg_M_f32	4632.1499		
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_tgc	5054.37012		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.720000029		
k_HwPosOutputLPFError_HwDeg_f32	585.200012		
k_I2CHwAuthority_Uls_f32	0.720000029		
k_MaxSensorlessAuthority_Uls_f32	0.810000002		
k_MaxVehCntrOffDiff_HwDeg_f32	733.700012		
k_MinSensorlessAuthority_Uls_f32	0.720000029		
k_TurnsCntrAuthority_UIs_f32	0.720000029		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	585.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.810000002		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1527.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	116		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2234.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8927		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	prity_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_HandwheelPositi	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_F		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32			
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		s_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosSt	os_HwDeg_f32 atus_Cnt_u16	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16\\target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_b	os_HwDeg_f32 atus_Cnt_u16 dwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity	os_HwDeg_f32 atus_Cnt_u16 dwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset	os_HwDeg_f32 atus_Cnt_u16 HwDeg_f32 /_Cnt_u08	Rasuk
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_H target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value	os_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 y_Cnt_u08  Expected Value	Result
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_H target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006	s_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009	•
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046	s_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009 0.00999999978 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_H target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006	s_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166	s_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009 0.00999999978 ± 0.004 3429.0166 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166 65534	ss_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009 0.00999999978 ± 0.004 3429.0166 ± 0.1 65534	•
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166 65534 HWPOS_STATE_INPUTINVALID	s_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009 0.00999999978 ± 0.004 3429.0166 ± 0.1 65534 HWPOS_STATE_INPUTINVALID	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166 65534 HWPOS_STATE_INPUTINVALID 3429.0166	ss_HwDeg_f32 atus_Cnt_u16 dwDeg_f32 /_Cnt_u08 Expected Value 382.100006 ± 0.0009 0.00999999978 ± 0.004 3429.0166 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 3429.0166 ± 0.1	
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target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166 65534 HWPOS_STATE_INPUTINVALID 3429.0166 65535 0 0 4632.1499 5054.37012 1 0 504	atus_Cnt_u16 atus_Cnt_u16 atus_Cnt_u16 atus_Cnt_u08  Expected Value  382.100006 ± 0.0009  0.00999999978 ± 0.004  3429.0166 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  3429.0166 ± 0.1  65535  0 ± 0.1  0 ± 0.004  4632.1499 ± 0.1  5054.37012 ± 0.009  1  0  504  1  1  0.009999999978 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_earr_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166 65534 HWPOS_STATE_INPUTINVALID 3429.0166 65535 0 0 4632.1499 5054.37012 1 0 504 1	atus_Cnt_u16 atus_Cnt_u16 atus_Cnt_u16 atus_Cnt_u08  Expected Value  382.100006 ± 0.0009  0.00999999978 ± 0.004  3429.0166 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  3429.0166 ± 0.1  65535  0 ± 0.1  0 ± 0.004  4632.1499 ± 0.1  5054.37012 ± 0.009  1  0  504  1  1  0.009999999978 ± 0.004  1600 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_gc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_SrlComHwPosSt target_AbsHwPos_Per2_SrlComHwPos_t target_AbsHwPos_Per2_TurnsCntrValidity target_Pim_EOLVehCntrOffset Actual Value 382.100006 0.00999999046 3429.0166 65534 HWPOS_STATE_INPUTINVALID 3429.0166 65535 0 0 4632.1499 5054.37012 1 0 504	atus_Cnt_u16 atus_Cnt_u16 atus_Cnt_u16 atus_Cnt_u08  Expected Value  382.100006 ± 0.0009  0.00999999978 ± 0.004  3429.0166 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  3429.0166 ± 0.1  65535  0 ± 0.1  0 ± 0.004  4632.1499 ± 0.1  5054.37012 ± 0.009  1  0  504  1  1  0.009999999978 ± 0.004	Result

0

0 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.70 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9768.34961		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.74000001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	185.699997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.74000001		
AbsHwPos_HwPosSource_Cnt_M_u16	1024		
AbsHwPos_HwPosState_Cnt_M_enum	9		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	402.380005		
AbsHwPos RelHwPos HwDeg M f32	402.380005		
AbsHwPos SrlComHwPosStatus Cnt M u16	26673		
AbsHwPos SrlComHwPos HwDeg M f32	-990.5		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.74000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	-4772.89014		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5195.10986		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.730000019		
k_HwPosOutputLPFError_HwDeg_f32	614.900024		
k_I2CHwAuthority_Uls_f32	0.730000019		
k_MaxSensorlessAuthority_Uls_f32	0.819999993		
k_MaxVehCntrOffDiff_HwDeg_f32	763.400024		
k MinSensorlessAuthority Uls f32	0.730000019		
k_TurnsCntrAuthority_Uls_f32	0.730000019		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	614.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-330.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.819999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1547.30005		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	120		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2264.69995		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9051		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		ReducedPerf Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	402.380005	402.380005 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0.74000001	0.74000001 ± 0.004	-
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-3483.65698	-3483.65649 ± 0.1	-
AbsHwPas HwPasSource Cnt M u16	1024	1024	

HWPOS\_STATE\_INPUTINVALID

-3483.65698

26673

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 HWPOS\_STATE\_INPUTINVALID

-3483.65649 ± 0.1

26673



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.74000001	0.74000001 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-4772.89014	-4772.89014 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5195.10986	5195.10986 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.74000001	0.74000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1024	1024	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26673	26673	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-990.5	-990.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2264.69995	2264.69995 ± 0.009	<b>✓</b>

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.71 (Repeat Count = 1)	
Name	Input Value
	9662.38965
	0.75
	195.300003
	0.75
	1236
	HWPOS STATE SENSORVALID
	422.660004
	422.660004
	26745
	1010.78003
	0.75
	4913.62988
_ , = -	8284.88965
	1
= 0	1
	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
,	target_Rte_Inst_Ap_AbsHwPos
	0.74000001
	644.599976
k_I2CHwAuthority_Uls_f32	0.74000001
	0.829999983
k MaxVehCntrOffDiff HwDeg f32	793.099976
k_MinSensorlessAuthority_Uls_f32	0.74000001
k_TurnsCntrAuthority_UIs_f32	0.74000001
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	644.599976
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-310.799988
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.829999983
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1567.40002
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	124
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2294.8999
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9175
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	422.660004	422.660004 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75	0.75 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3734.04761	3734.04761 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	1236	1236	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3734.04761	3734.04761 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003	1010.78003 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75	0.75 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4913.62988	4913.62988 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965	8284.88965 ± 0.009	<b>~</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75	0.75 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1236	1236	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1010.78003	1010.78003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2294.8999	2294.8999 ± 0.009	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.72 (Repeat Count = 1)	💌
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9556.42969
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	204.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75999999
AbsHwPos_HwPosSource_Cnt_M_u16	1348
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	442.940002
AbsHwPos_RelHwPos_HwDeg_M_f32	442.940002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.75
k_HwPosOutputLPFError_HwDeg_f32	674.299988
k_I2CHwAuthority_Uls_f32	0.75
k_MaxSensorlessAuthority_Uls_f32	0.839999974
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988
k_MinSensorlessAuthority_Uls_f32	0.75
k_TurnsCntrAuthority_Uls_f32	0.75
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	674.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75999999	0.75999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	123.400002	123.400002 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001	2325.1001 ± 0.009	<b>✓</b>

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.73 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9450.46973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	214.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.769999981
AbsHwPos_HwPosSource_Cnt_M_u16	1460
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	463.220001
AbsHwPos_RelHwPos_HwDeg_M_f32	463.220001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26889
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.769999981
AbsHwPos_TempHwPos_HwDeg_M_f32	5195.10986
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.75999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.75999999
k_MaxSensorlessAuthority_Uls_f32	0.850000024
k_MaxVehCntrOffDiff_HwDeg_f32	852.5



		MAC	11 Cat
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.75999999		
k_TurnsCntrAuthority_UIs_f32	0.75999999		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	704		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	132		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	463.220001	463.220001 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4049.56958	4049.56958 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4049.56958	4049.56958 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	5195.10986	5195.10986 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997	-1072.96997 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
	1.		

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

0.00999999046

1600

65534

65534

65535

-248.300003

0.00999999978 ± 0.004 1600 ± 0.1

-248.300003 ± 0.1

65535 ± 0.09

65534

65534

Test Step 2.74 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9662.38965	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	195.300003	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75	
AbsHwPos_HwPosSource_Cnt_M_u16	1236	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	422.660004	
AbsHwPos_RelHwPos_HwDeg_M_f32	422.660004	

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)
Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 



Name	Input Value			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26745			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75			
AbsHwPos_TempHwPos_HwDeg_M_f32	4913.62988			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
k_HwPosAuthorityStep_UIs_f32	0.74000001			
k_HwPosOutputLPFError_HwDeg_f32	644.599976			
k_I2CHwAuthority_Uls_f32	0.74000001			
k_MaxSensorlessAuthority_Uls_f32	0.829999983			
k_MaxVehCntrOffDiff_HwDeg_f32	793.099976			
k_MinSensorlessAuthority_UIs_f32	0.74000001			
k_TurnsCntrAuthority_UIs_f32	0.74000001			
k_UseTurnsCntr_Cnt_lgc k_VehCntrOffValidLimit_HwDeg_f32	644.599976			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-310.799988			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1			
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.829999983			
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1567.40002			
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	124			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2294.8999			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9175			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPos.Abs$	target_AbsHwPos_Per2_DiagStatusHwPosl	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDog_f32	target_AbsHwPos_Per2_SensorlessHwPos			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPosStat target_AbsHwPos_Per2_SrlComHwPos_Hw			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValidity			
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	oni_uoo		
Name	Actual Value	Expected Value	Result	
	422.660004	422.660004 ± 0.0009	Kesuit	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	0.829999983	0.829999983 ± 0.004		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1224.375	1224.375 ± 0.1		
AbsHwPos HwPosSource Cnt M u16	3	3	<b>V</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS STATE SENSORINVALID	-	
AbsHwPos PrevHandwheelPosition HwDeg M f32	1224.375	1224.375 ± 0.1	•	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	-	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.1	•	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.829999983	0.829999983 ± 0.004	-	
AbsHwPos_TempHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.1	•	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>~</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.829999983	0.829999983 ± 0.004		
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1224.375	1224.375 ± 0.1	<b>V</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	<b>V</b>	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	Ž	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1567.40002 2294.8999	1567.40002 ± 0.1 2294.8999 ± 0.009	~	
	7734 0999	ZZ34.0999 ± U.UU9	~	

T				<b>~</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•



Test Step 2.75 (Repeat Count = 1) Name	Input Value		
	Input Value 9344.5		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	0.779999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	224.100006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.779999971		
AbsHwPos_HwPosSource_Cnt_M_u16	1572		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-11725		
AbsHwPos RelHwPos HwDeg M f32	-600		
AbsHwPos SrlComHwPosStatus Cnt M u16	26961		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13		
AbsHwPos TargetHwAuthority Uls M f32	0.779999971		
AbsHwPos TempHwPos HwDeg M f32	-5335.8501		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehC	ntrOffeet GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	Thi Oliset_GetEfforstatus_Efforstatus	
<—HwPosAuthorityStep_Uls_f32 <—HwPosOutputLPFError_HwDeg_f32	0.769999981 733.700012		
C_nwPosOutputEPFEffor_nwDeg_i32 C_l2CHwAuthority_Uls_f32	0.769999981		
<pre>&lt;_IZCHWAUthority_Uis_i32</pre> <pre>&lt;_MaxSensorlessAuthority_Uls_f32</pre>	0.860000014		
<_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k MinSensorlessAuthority Uls f32	0.769999981		
<ul> <li>TurnsCntrAuthority Uls f32</li> </ul>	0.769999981		
<_TurnsCntr_Cnt_lgc	0		
Cose runischit_one_ige VehCntrOffValidLimit HwDeg f32	733.700012		
	0		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997 1		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	136		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2385.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5	D 1 1D 10 11	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	<del></del>	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	·	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-600	-600 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971	0.779999971 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4112.66113	-4112.66113 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4112.66113	-4112.66113 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26961	26961	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.779999971	0.779999971 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	-5335.8501	-5335.8501 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977	7967.00977 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
	0.779999971	0.779999971 ± 0.004	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.779999971 -1600	0.779999971 ± 0.004 -1600 ± 0.1	



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26961	26961	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1395.13	1395.13 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2385.5	2385.5 ± 0.009	✓

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.76 (Repeat Count = 1) Name	Input Value			
	•			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9238.54004			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.790000021			
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-389.600006 0.790000021			
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32				
AbsHwPos_HwPosSource_Cnt_M_u16	1684			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725			
AbsHwPos_RelHwPos_HwDeg_M_f32	6008			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27033			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1535.87			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.790000021			
AbsHwPos_TempHwPos_HwDeg_M_f32	5476.58984			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
k_HwPosAuthorityStep_Uls_f32	0.779999971			
k_HwPosOutputLPFError_HwDeg_f32	763.400024			
k_I2CHwAuthority_Uls_f32	0.779999971			
k_MaxSensorlessAuthority_Uls_f32	0.870000005			
k_MaxVehCntrOffDiff_HwDeg_f32	911.900024			
k_MinSensorlessAuthority_Uls_f32	0.779999971			
k_TurnsCntrAuthority_UIs_f32	0.779999971			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	763.400024			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-232			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAutl	hority Uls f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	,		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target AbsHwPos Per2 SensorlessAutl			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwf	·		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPos			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValid	<del> </del>		
		ity_Ont_uoo		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	l=	-	
Name	Actual Value	Expected Value	Resu	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	6008	6008 ± 0.009		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4244.68994	4244.68994 ± 0.1		
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID		
Abellu Pos ProvHendu beel Position Hu Pos M #22	4244 69004	4244 69004 + 0.4		

4244.68994

65535

0

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32
AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

4244.68994 ± 0.1

65535

0 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5476.58984	5476.58984 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498	7861.0498 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9132.58008
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	243.300003
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.800000012
AbsHwPos HwPosSource Cnt M u16	1796
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0
AbsHwPos RelHwPos HwDeg M f32	7150
ubsHwPos SrlComHwPosStatus Cnt M u16	27105
AbsHwPos SrlComHwPos HwDeg M f32	-1676.60999
.bsHwPos TargetHwAuthority Uls M f32	0.800000012
ubsHwPos TempHwPos HwDeg M f32	5617.33008
AbsHwPos VehCntrOffset HwDeg M f32	-1455.08997
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.79000021
: HwPosOutputLPFError HwDeg f32	793.099976
: I2CHwAuthority Uls f32	0.790000021
MaxSensorlessAuthority Uls f32	0.87999995
MaxVehCntrOffDiff HwDeg f32	941.599976
MinSensorlessAuthority Uls f32	0.790000021
TurnsCntrAuthority Uls f32	0.790000021
UseTurnsCntr Cnt lgc	0
VehCntrOffValidLimit HwDeq f32	793.099976
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-212.300003
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.879999995
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-941.200012
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	144
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2445.8999
arget Pim EOLVehChtrOffset.EOLHwPosTrimPerformed Cnt u16	9795
arget_Fili_LOEVenCritionset.EOENWF0sTrillinFenormeu_Crit_u10 arget_Rte_Call_Ap_AbsHwPos_EOEVehCntrOffset_GetErrorStatus_ErrorSt	
arget_Rte_lnst_Ap_AbsHwPos_EOLVerlChironset_GetEnbiotatus_Enbiot arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduce	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_	
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAdtrionty_bis_ arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDe	
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheeiPosition_HwDe arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_inst_ap_abshwpos.abshwpos_per2_nwpos5ource_crit_u16 arget_Rte_inst_ap_abshwpos.abshwpos_per2_12ChwabsposValid_cnt_1	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f3	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_	target_AbsHwPos_Per2_ManufMode_Cnt_enum f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7150	7150 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012	0.800000012 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4542.52393	4542.52393 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1796	1796	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4542.52393	4542.52393 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999	-1676.60999 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.800000012	0.800000012 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5617.33008	5617.33008 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1455.08997	-1455.08997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.800000012	0.800000012 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1796	1796	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2445.8999	2445.8999 ± 0.009	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.78 (Repeat Count = 1)	<b>√</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9026.62012
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.810000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	252.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.810000002
AbsHwPos_HwPosSource_Cnt_M_u16	1908
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	544.340027
AbsHwPos_RelHwPos_HwDeg_M_f32	544.340027
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27177
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1817.34998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.810000002
AbsHwPos_TempHwPos_HwDeg_M_f32	-5758.06982
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.800000012
k_HwPosOutputLPFError_HwDeg_f32	822.799988
k_I2CHwAuthority_Uls_f32	0.800000012
k_MaxSensorlessAuthority_Uls_f32	0.88999986
k_MaxVehCntrOffDiff_HwDeg_f32	971.299988
k_MinSensorlessAuthority_Uls_f32	0.800000012
k_TurnsCntrAuthority_Uls_f32	0.800000012
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	822.799988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-192.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.889999986
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-921.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	148



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2476.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9919		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	544.340027	544.340027 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	544.340027	544.340027 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4615.98535	-4615.98584 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4615.98535	-4615.98584 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-5758.06982	-5758.06982 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014	7649.14014 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2476.1001	2476.1001 ± 0.009	<b>✓</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.79 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8920.66016
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.819999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	262.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.819999993
AbsHwPos_HwPosSource_Cnt_M_u16	2020
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-564.619995
AbsHwPos_RelHwPos_HwDeg_M_f32	-564.619995
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27249
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1958.08997
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.819999993
AbsHwPos_TempHwPos_HwDeg_M_f32	5898.81006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.810000002
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_Uls_f32	0.810000002



Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.89999976		
k_MaxVehCntrOffDiff_HwDeg_f32	1001		
k_MinSensorlessAuthority_Uls_f32	0.810000002		
k_TurnsCntrAuthority_UIs_f32	0.810000002		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	852.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-172.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.899999976		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-901.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	152		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2506.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10043		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-564.619995	-564.619995 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976	0.89999976 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988	-901.799988 ± 0.1	~

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Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-564.619995	-564.619995 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976	0.899999976 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988	-901.799988 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.899999976	0.899999976 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018	7543.18018 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.899999976	0.899999976 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-901.799988	-901.799988 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-901.799988	-901.799988 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2506.30005	2506.30005 ± 0.009	~	

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 2.80 (Repeat Count = 1)		🗸
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8814.7002	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.829999983	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	272.100006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.829999983	
AbsHwPos_HwPosSource_Cnt_M_u16	2132	
AbsHwPos_HwPosState_Cnt_M_enum	5	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	584.900024	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27321	



Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.829999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.819999993		
k_HwPosOutputLPFError_HwDeg_f32	882.200012		
k_I2CHwAuthority_Uls_f32	0.819999993		
k_MaxSensorlessAuthority_Uls_f32	0.910000026		
k_MaxVehCntrOffDiff_HwDeg_f32	1030.69995		
k_MinSensorlessAuthority_Uls_f32	0.81999993		
k_TurnsCntrAuthority_Uls_f32	0.819999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	882.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1 461 800004		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	461.899994		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.910000026		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-882.099976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	156		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2536.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10167		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		PosReducedPerf Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.09	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5059.0835	5059.0835 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5059.0835	5059.0835 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498	6039.5498 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997	-1437.21997 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1	1	
Pto Call An AbellyPoe NytrDiagMar SetNTCStatus(Param Cat T ::00)	[ 1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1 0 0000000078 + 0 004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.00999999046 1600	0.00999999978 ± 0.004 1600 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0.00999999046 1600 65534	0.00999999978 ± 0.004 1600 ± 0.1 65534	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.00999999046 1600	0.00999999978 ± 0.004 1600 ± 0.1	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•



Test Step 2.81 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8708.74023
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.839999974
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.839999974
AbsHwPos_HwPosSource_Cnt_M_u16	2244
AbsHwPos_HwPosState_Cnt_M_enum	6



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27393	27393	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2566.69995	2566.69995 ± 0.009	✓

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.82 (Repeat Count = 1)			
	Inner A Walter		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8602.78027		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.850000024		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-350.200012		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.850000024		
AbsHwPos_HwPosSource_Cnt_M_u16	2356		
AbsHwPos_HwPosState_Cnt_M_enum	7		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	625.460022		
AbsHwPos_RelHwPos_HwDeg_M_f32	0		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27465		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2380.31006		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.850000024		
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.839999974		
k_HwPosOutputLPFError_HwDeg_f32	941.599976		
k_I2CHwAuthority_Uls_f32	0.839999974		
k_MaxSensorlessAuthority_Uls_f32	0.930000007		
k_MaxVehCntrOffDiff_HwDeg_f32	1090.09998		
k_MinSensorlessAuthority_Uls_f32	0.839999974		
k_TurnsCntrAuthority_Uls_f32	0.839999974		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	941.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.930000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-842.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	164		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2596.8999		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	10415		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (		teducedParf Cnt Ido	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_ei		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDog_f32	target_AbsHwPos_Per2_SensorlessHwPos_	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0	0 ± 0.000009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-5425.40527	-5425.40527 ± 0.1	~

65534

65535

0

-5425.40527

HWPOS\_STATE\_INPUTINVALID

65534

65535

0 ± 0.1

-5425.40527 ± 0.1

HWPOS\_STATE\_INPUTINVALID

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979	-6321.02979 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998	7225.2998 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	8496.82031
AbsHwPos HandwheelAuthority Uls M f32	0.860000014
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	300.899994
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.860000014
AbsHwPos HwPosSource Cnt M u16	2468
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INIT
LbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	645.73999
AbsHwPos RelHwPos HwDeg M f32	645.73999
ubsHwPos SrlComHwPosStatus Cnt M u16	27537
AbsHwPos SrlComHwPos HwDeg M f32	2521.05005
ubsHwPos TargetHwAuthority Uls M f32	0.860000014
ubsHwPos TempHwPos HwDeg M f32	6461.77002
AbsHwPos VehCntrOffset HwDeg M f32	-1119.32996
AbsHwPos VehCntrOfstLearn Cnt M lqc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.850000024
HwPosOutputLPFError HwDeg f32	1060.40002
: I2CHwAuthority Uls f32	0.850000024
MaxSensorlessAuthority Uls f32	0.93999998
MaxVehCntrOffDiff HwDeg f32	1119.80005
MinSensorlessAuthority Uls f32	0.850000024
TurnsCntrAuthority Uls f32	0.850000024
UseTurnsCntr Cnt lqc	0
: VehCntrOffValidLimit HwDeq f32	971.299988
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	522.200012
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.93999998
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-823
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	168
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-488.100006
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	10539
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorS	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduca	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwD	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f	
arget_Rte_irist_Ap_AbsHwPos.AbsHwPos_Fet2_12CHwAbsFos_HwDeg_i arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls	



Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	645.73999	645.73999 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5599.24854	5599.24805 ± 0.1	<b>~</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>~</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5599.24854	5599.24805 ± 0.1	<b>~</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996	-1119.32996 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-488.100006	-488.100006 ± 0.0009	~	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Test Step 2.84 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8390.86035
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.870000005
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	310.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.870000005
AbsHwPos_HwPosSource_Cnt_M_u16	2580
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-666.02002
AbsHwPos_RelHwPos_HwDeg_M_f32	-666.02002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27609
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.870000005
AbsHwPos_TempHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1.
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.860000014
C_HwPosOutputLPFError_HwDeg_f32	1090.09998
C_I2CHwAuthority_Uls_f32	0.860000014
	0.94999988
c_MaxVehCntrOffDiff_HwDeg_f32	1149.5
C_MinSensorlessAuthority_Uls_f32	0.860000014
C_TurnsCntrAuthority_Uls_f32	0.860000014
<pre>C_UseTurnsCntr_Cnt_lgc</pre>	1
<pre>v_VehCntrOffValidLimit_HwDeg_f32</pre>	1001
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.94999988
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-803.299988
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	172



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-468.399994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10663		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_el	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-666.02002	-666.02002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5784.54834	5784.54883 ± 0.1	~

target_rite_inst_Ap_Absrivir os.i iiii_EoEvenontronset	target_r ini_Locatenoniionact		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-666.02002	-666.02002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5784.54834	5784.54883 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5784.54834	5784.54883 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988	7013.37988 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-468.399994	-468.399994 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.85 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8284.88965
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.87999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	320.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.87999995
AbsHwPos_HwPosSource_Cnt_M_u16	2692
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	686.299988
AbsHwPos_RelHwPos_HwDeg_M_f32	686.299988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27681
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.87999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.870000005
k_HwPosOutputLPFError_HwDeg_f32	1119.80005
k_I2CHwAuthority_Uls_f32	0.870000005



Name	Input Value		
k MaxSensorlessAuthority Uls f32	0.959999979		
k MaxVehCntrOffDiff HwDeg f32	1179.19995		
k MinSensorlessAuthority Uls f32	0.870000005		
k TurnsCntrAuthority Uls f32	0.870000005		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	1030.69995		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	562.400024		
target AbsHwPos Per2 ManufMode Cnt enum.value	2		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.959999979		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-783.599976		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	176		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-448.700012		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	10787		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	3		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (		ReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthorit		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target_AbsHwPos_Per2_HandwheelPosition		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	Cnt lgc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos Hw		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt e	num	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthorit	y Uls f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos	HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	686.299988	686.299988 ± 0.0009	~
AbsHwPos HandwheelAuthority Uls M f32	0.879999995	0.879999995 ± 0.004	<b>~</b>

target_itte_inst_Ap_Absrive os.Fiiii_EOE verionitionset	target_Filli_LOLVellOllitoliset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	686.299988	686.299988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.879999995	0.879999995 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-5895.64795	-5895.64795 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	2692	2692	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-5895.64795	-5895.64795 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27681	27681	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.879999995	0.879999995 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-6743.25	-6743.25 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992	6907.41992 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.879999995	0.879999995 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2692	2692	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27681	27681	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-448.700012	-448.700012 ± 0.0009	<b>✓</b>

Τ			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.86 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8178.93018	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.889999986	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-507.799988	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.889999986	
AbsHwPos_HwPosSource_Cnt_M_u16	2804	
AbsHwPos_HwPosState_Cnt_M_enum	11	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	706.580017	
AbsHwPos_RelHwPos_HwDeg_M_f32	706.580017	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27753	



Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.889999986		
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6801.45996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.879999995		
k_HwPosOutputLPFError_HwDeg_f32	1149.5		
k_I2CHwAuthority_Uls_f32	0.879999995		
k_MaxSensorlessAuthority_Uls_f32	0.970000029		
k_MaxVehCntrOffDiff_HwDeg_f32	1208.90002		
k_MinSensorlessAuthority_Uls_f32	0.879999995		
k_TurnsCntrAuthority_Uls_f32	0.879999995		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1060.40002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	582.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.970000029		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-763.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	180		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-429		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10911		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target AbsHwPos Per2 DiagStatusHwP	osReducedPerf Cnt lgc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidit		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	,···_	
Name	Actual Value	Expected Value	Result
	706.580017	706.580017 ± 0.0009	Kesuit
AbsHwPos_AlignedHwPos_HwDeg_M_f32			-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6070.89307	6070.89307 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	6070.89307	6070.89307 ± 0.1	<b>Y</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>V</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>V</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>•</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023	6883.99023 ± 0.1	<b>V</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6801.45996	6801.45996 ± 0.009	<b>V</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>V</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>V</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>V</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0099999046	0.00999999978 ± 0.004	<b>4</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>V</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>V</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.87 (Repeat Count = 1)	Input Value		
Name Abol hulbon Aligned Curp MtrDon Lhu Don M 622	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8072.97021		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.899999976		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	389.200012		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.899999976		
AbsHwPos_HwPosSource_Cnt_M_u16	2916		
AbsHwPos_HwPosState_Cnt_M_enum	12		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-726.859985		
AbsHwPos_RelHwPos_HwDeg_M_f32	-726.859985		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27825		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.89999976		
AbsHwPos_TempHwPos_HwDeg_M_f32	7024.72998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6695.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0-1-0#1 0-1F01-1 F01-1	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	Untroffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<—HwPosAuthorityStep_Uls_f32 < HwPosOutput REference (1)	0.889999986		
<_HwPosOutputLPFError_HwDeg_f32	1179.19995		
<_I2CHwAuthority_Uls_f32 <_MaySepperlessAuthority_Uls_f32	0.889999986		
<_MaxSensorlessAuthority_Uls_f32	0.980000019		
k_MaxVehCntrOffDiff_HwDeg_f32	1238.59998		
MinSensorlessAuthority_Uls_f32     To Colon Authority_Uls_f32	0.88999986		
C_TurnsCntrAuthority_Uls_f32	0.88999986		
<_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	1090.09998		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.980000019		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-744.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	184		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-409.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11035		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi	on_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	ılid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_F	lwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosSt		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_h	HwDeg f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Re
NbsHwPos_AlignedHwPos_HwDeg_M_f32	-726.859985	-726.859985 ± 0.0009	146
NosHwPos HandwheelAuthority Uls M f32	0.899999976	0.899999976 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	6361.17676	6361.17676 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	2916	2916	
AbsHwPos HwPosState Cnt M enum		HWPOS STATE INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	HWPOS_STATE_INPUTINVALID	6361.17676 ± 0.1	
	6361.17676		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27825	27825	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.899999976	0.899999976 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	7024.72998	7024.72998 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6695.5	6695.5 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.89999976	0.89999976 ± 0.004	
arget_Abstrive os_Fet2_trandwheetAuthority_ofs_152.value			
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27825	27825	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-409.299988	-409.299988 ± 0.0009	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.88 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7967.00977		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.910000026		
	-468.399994		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0.910000026		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32			
AbsHwPos_HwPosSource_Cnt_M_u16	3028		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	747.140015		
AbsHwPos_RelHwPos_HwDeg_M_f32	747.140015		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27897		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.910000026		
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1589.54004		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.89999976		
k_HwPosOutputLPFError_HwDeg_f32	1208.90002		
k_I2CHwAuthority_UIs_f32	0.89999976		
k_MaxSensorlessAuthority_Uls_f32	0.99000001		
k_MaxVehCntrOffDiff_HwDeg_f32	1268.30005		
k_MinSensorlessAuthority_Uls_f32	0.89999976		
k TurnsCntrAuthority Uls f32	0.89999976		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1119.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.99000001		
	-724.5		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	188		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-389.600006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11159		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStar	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	747.140015	747.140015 ± 0.0009	Nesu
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.0099999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6478.42188	6478.42188 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	

HWPOS\_STATE\_INPUTINVALID

6478.42188

65535

0

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

HWPOS\_STATE\_INPUTINVALID

6478.42188 ± 0.1

65535

0 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021	7165.47021 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1589.54004	-1589.54004 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

lame	Input Value
NosHwPos AlignedCumMtrPos HwDeg M f32	7861.0498
AbsHwPos HandwheelAuthority Uls M f32	0.920000017
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	784.099976
bsHwPos HandwheelPositionLPF M str.K Uls f32	0.920000017
ubsHwPos_HwPosSource_Cnt_M_u16	3140
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INPUTINVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	767.419983
bshwPos RelHwPos HwDeg M f32	767.419983
bsHwPos SrlComHwPosStatus Cnt M u16	27969
	-3084.01001
bsHwPos_SrlComHwPos_HwDeg_M_f32	0.920000017
bsHwPos_TargetHwAuthority_UIs_M_f32	
bsHwPos_TempHwPos_HwDeg_M_f32	-7306.20996
.bsHwPos_VehCntrOffset_HwDeg_M_f32	6483.58008
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.910000026
_HwPosOutputLPFError_HwDeg_f32	1238.59998
_I2CHwAuthority_Uls_f32	0.910000026
_MaxSensorlessAuthority_Uls_f32	0.100000001
_MaxVehCntrOffDiff_HwDeg_f32	1298
_MinSensorlessAuthority_Uls_f32	0.910000026
_TurnsCntrAuthority_Uls_f32	0.910000026
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	1149.5
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
rget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-704.799988
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	192
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-369.899994
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11283
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorSt	tatus 1
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduce	edPerf_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_	
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDo	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f3	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls	



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	767.419983	767.419983 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.920000017	0.920000017 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-6658.98535	-6658.98535 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3140	3140	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-6658.98535	-6658.98535 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27969	27969	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	~
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.920000017	0.920000017 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7306.20996	-7306.20996 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6483.58008	6483.58008 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.920000017	0.920000017 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3140	3140	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27969	27969	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-369.899994	-369.899994 ± 0.0009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Test Step 2.90 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7755.08984
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.930000007
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	966.200012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.930000007
AbsHwPos_HwPosSource_Cnt_M_u16	3252
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	787.700012
AbsHwPos_RelHwPos_HwDeg_M_f32	787.700012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.930000007
AbsHwPos_TempHwPos_HwDeg_M_f32	5828.25977
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6377.62012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	0.920000017
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	1268.30005
<_I2CHwAuthority_Uls_f32	0.920000017
<_MaxSensorlessAuthority_Uls_f32	0.109999999
<_MaxVehCntrOffDiff_HwDeg_f32	1327.69995
<_MinSensorlessAuthority_Uls_f32	0.920000017
<_TurnsCntrAuthority_Uls_f32	0.920000017
<_UseTurnsCntr_Cnt_lgc	1
<_VehCntrOffValidLimit_HwDeg_f32	1179.19995
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	662.900024
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.109999999
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-685.099976
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	196



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-350.200012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11407		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	787.700012	787.700012 ± 0.0009	~
Abelly Dec Handy bealt when it He M 522	0.0000000046	0.0000000070 + 0.004	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	787.700012	787.700012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5487.91553	5487.91602 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5487.91553	5487.91602 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	5828.25977	5828.25977 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6377.62012	6377.62012 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-350.200012	-350.200012 ± 0.0009	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.91 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7649.14014
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.939999998
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-409.299988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.939999998
AbsHwPos_HwPosSource_Cnt_M_u16	3364
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-807.97998
AbsHwPos_RelHwPos_HwDeg_M_f32	-807.97998
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
AbsHwPos_SrlComHwPos_HwDeg_M_f32	202.529999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.939999998
AbsHwPos_TempHwPos_HwDeg_M_f32	848.539978
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1271.65002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.930000007
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_Uls_f32	0.930000007



Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.119999997		
k_MaxVehCntrOffDiff_HwDeg_f32	1357.40002		
k_MinSensorlessAuthority_Uls_f32	0.930000007		
k_TurnsCntrAuthority_UIs_f32	0.930000007		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1208.90002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	683		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-665.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	200		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-330.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11531		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwPosl	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-807.97998	-807.97998 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.93999998	0.939999998 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	773.069519	773.06958 ± 0.1	~

- 3	1 . 3		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-807.97998	-807.97998 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.93999998	0.939999998 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	773.069519	773.06958 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3364	3364	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	773.069519	773.06958 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.93999998	0.939999998 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	848.539978	848.539978 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1271.65002	-1271.65002 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.93999998	0.939999998 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	773.069519	773.06958 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3364	3364	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	202.529999	202.529999 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-330.5	-330.5 ± 0.0009	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.92 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7543.18018	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.949999988	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	213	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.949999988	
AbsHwPos_HwPosSource_Cnt_M_u16	3476	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	828.26001	
AbsHwPos_RelHwPos_HwDeg_M_f32	5828.25977	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	28969	



AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	Name	Input Value		
Abashar   Termethries    Montage   M.   D2	AbsHwPos_SrlComHwPos_HwDeg_M_f32	9802.53027		
Abata-Rev_NetCommonder ame_Ore JU Digs	AbsHwPos_TargetHwAuthority_Uls_M_f32	0.949999988		
Abstractive VenCortORise.em Dot. M. 100	AbsHwPos_TempHwPos_HwDeg_M_f32	-868.820007		
Marchard   Marchan   Mar	AbsHwPos_VehCntrOffset_HwDeg_M_f32	6165.7002		
Insect and Ap. Abatherions   ECUVenchrofother GetErrorStatus   Enro/Status   Enro/St	AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
Ris   Inst. Ap. AbethWeb   Light Peakulum Step   Light Step	AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
New Procedure   New Process	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
	Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
K.M.Sar-Grant Charlom V, Us. 72	k_HwPosAuthorityStep_Uls_f32	0.93999998		
K. Mas-Senories Authority, Us. 52 k. Min-Senories Authority, Us. 52 k. Min-Senories Authority, Us. 52 k. Jin-Senories Authority, Us. 52 kergel, Abstantive, Park, Jin-Senories Authority, Us. 52 kergel, Abstantive, Park, Senories Authority, Us. 52 kergel, Abstantive, Park, Senories Authority, Us. 52 kergel, Park, Disk, Verdentification, Control of the Senories Authority, Us. 52 kergel, Park, Disk, Verdentification, Control of the Senories Authority, Us. 52 kergel, Park, Disk, Abstantive, David, Danis, Senories Authority, Us. 52 kergel, Park, Disk, Abstantive, Danis, Senories Authority, Us. 52 kergel, Park, Disk, Abstantive, Abstantive, Park, Danis, Senories Authority, Us. 52 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Danis, Heing, 124 kergel, Park, Inst, Ap, Abstantive, Abstantive, Park, Dani	k_HwPosOutputLPFError_HwDeg_f32	1327.69995		
Manison   Mani	k_I2CHwAuthority_Uls_f32	0.93999998		
K_UmerCarteAuthority_UB_122  L_UseTureCrit_Crt_Lgc	k_MaxSensorlessAuthority_Uls_f32	0.129999995		
Libration   Librator	k_MaxVehCntrOffDiff_HwDeg_f32	1387.09998		
1238.8998   1238.89998   1238.89998   1238.89998   1238.89998   1238.89998   1238.89998   1238.899999   1238.8999999   1238.899999999999999999999999999999999999	k_MinSensorlessAuthority_Uls_f32	0.93999998		
Recommendation   National State   1982   1	k_TurnsCntrAuthority_Uls_f32	0.93999998		
targeq_Absh-Mong_Per_Z_DigShatas-MongReducePer_C Cnt_lgo; value targeq_Absh-Mong_Per_Z_DicAmbaPong_Hove_g_132 value targeq_Absh-Mong_Per_Z_DicAmbaPong_Hove_g_132 value targeq_Absh-Mong_Per_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Turnscrinv-Mong_132 value targeq_Absh-Mong_Per_Z_Turnscrinv-Mong_132 value targeq_Absh-Mong_Per_Z_Turnscrinv-Mong_132 value targeq_Absh-Mong_Per_Z_Turnscrinv-Mong_132 value targeq_Absh-Mong_Per_Z_Turnscrinv-Mong_132 value targeq_Absh-Mong_Absh-Mong_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Absh-Mong_Absh-Mong_Absh-Mong_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Absh-Mong_Absh-Mong_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Absh-Mong_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Absh-Mong_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Ber_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Sensories-Senduriny_Us_132 value targeq_Absh-Mong_Per_Z_Sensories-Senduriny	k_UseTurnsCntr_Cnt_lgc	1		
Integral AbsHarbors   Periz   CitchMashSport   Hoberg   12 value   1   10   10   10   10   10   10   10	k_VehCntrOffValidLimit_HwDeg_f32	1238.59998		
target_Abel+Wres_Per2_Clot-MachaPos_Per0_Clot-enum value target_Abel+Wres_Per2_SensoriessAuthority_Uis_132_value target_Abel+Wres_Per2_SensoriessAuthority_Uis_132_value target_Pers_Clot-Verichtoritofiset_Coult-wichion_Cnt_uide_value target_Pers_Clot-Verichtoritofiset_Verichtoritofiset_value target_Pers_Clot-Verichtoritofiset_Verichtoritofiset_value target_Pers_Clot-Verichtoritofiset_value target_Pers_Clot-Verichtoritoritoritoritoritoritoritoritoritori	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
Integred_Abst-HWPos_PerZ_Sensoriessatuhronty_Uis_f32_value				
Integral AbstHWPDs   PerZ   SensortiessAuthority   Uis   22 value   2-46-5700012   2-24   2-46-5700012   2-24   2-46-5700012   2-24   2-46-5700012   2-24   2-46-5700012   2-24   2-46-5700012   2-24   2-46-5700012   2-24   2-				
Integred_AbsHHMPDs_PerZ_SensorlessHWPDs_HMDeg_152   1979, PerZ_Despitatish Properties   1979, PerZ_D				
target   AbsHwPos Per 2   TurnsCntrValidity   Cnt   U08 value   204				
1855   1855				
Integred   Ric   Inst   Ap   AbsHrWPos   EDULVENCINOTIONESE   GetErroStatus   Frontiers	0			
target_Rie_Inst_Ap_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Ct_target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheeRuthroity_Uis_1322 target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheeRuthroity_Uis_1322 target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheeRuthroity_Uis_1322 target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheeRuthroity_Uis_1322 target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_Uis_MabsPos_Per2_HandwheeRuthroity_Uis_1322 target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewhsPos_Per2_HandwheeRuthroity_Uis_1322 target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewhsPos_IndewhsPos_Indew				
target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_Per2_HandwheelPosition_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U2CHwAbsPos_Valid_Cnt_Uge   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U2CHwAbsPos_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U2CHwAbsPos_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U2CHwAbsPos_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U3CHwAbsPos_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U3chwAbsPos_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_U3chwAbsPos_HwDeg_132   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_SincomHwPosStatus_Cnt_U08   target_AbsHwPos_Per2_HandwheelPosition_HwDeg_M_132   target_AbsHwPos_Per2_HandwheelPosition_HwDeg_M_132   target_AbsHwPos_Per2_HandwheelPosition_HwDeg_M_132   target_AbsHwPos_Per2_HandwheelPosition_HwDeg_M_132   target_AbsHwPos_Per2_HandwheelPosition_HwDeg			Andread Dark Out Inc	
target_Rie_Inst_Ap_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_HwPosSource_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LiCHwAbsPos_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_LiCHwAbsPos_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LiCHwAbsPos_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_SensoriessAlmonity_Uis_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_Rie_Inst_Ap_AbsHwPos_Per2_Instructivation_Uis_f12   target_Rie_Inst_Ap_AbsHwPos_Per2_Instructivation_Uis_f12   target_Rie_Inst_Ap_AbsHwPos_Per2_Instructivation_Uis_f12   target_Rie_Inst_Ap_AbsHwPos_Per2_Instructivation_Uis_f12   target_Rie_Inst_Ap_AbsHwPos_Per2_Instructivation_Uis_f12   target_Rie_Inst_Ap_AbsHwPos_Per2_Instructivation_Uis_f12   target_AbsHwPos_Per2_Instructivation_Uis_f12   target_AbsHwPos_Per2				
target_Rie_Inst_Ap_AbsHwPos_Per2_HwPosSource_Cnt_u16   target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsHwPos_Per2_IdcHwAbsPos_Per2_IdcHwAbsHwPos_Pe			/	
target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_Manufflode_Cnt_enum   target_Rie_Inst_Ap_AbsHwPos_Per2_SensoriessAuthority_Uis_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SensoriessAuthority_Uis_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SensoriessAuthority_Uis_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_HwDeg_I32   target_Rie_Inst_Ap_AbsHwPos_Per2_SirComHwPos_SirComHwPos_Per2_SirComHwPo				
target_Rte_Inst_Ap_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_ManufMode_Cnt_enum   target_AbsHwPos_Per2_SensoriessAtthrofty_Uis_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAtwPos_Per2_Catter_Per2_Per2_Per2_Per2_Per2_Per2_Per2_Pe				
target_Rte_Inst_Ap_AbsHwPos_Pat2_ManufMode_Cnt_enum         target_Rte_Inst_Ap_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32         target_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32         target_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32         target_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Pat2_SensoriessAuthority_Uils_f32         target_AbsHwPos_Pat2_SinComHwPos_HwDeg_f32         target_AbsHwPos_Pat2_SinComHwPos_HwDeg_f32_SinComHwPos_HwDeg_f32_SinComHwPos_HwDeg_f32_SinComHwPos_HwDeg_f32_SinComHwDeg_f32_SinComHwDeg_f32_SinComHwDeg_f32_SinComHwDeg_f32_SinComHwDeg_f32_SinComHwDeg_f32_SinComHwDeg_f32_SinCo				
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Status_Cnt_u16   target_Rte_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Status_Cnt_u16   target_Rte_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Status_Cnt_u16   target_Rte_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Inv_Deg_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_SriComHwPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Inv_Deg_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_Ap_AbsHwPos_Per2_SriComHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U08   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt_U16   target_Inst_AbsHwPos_Per2_TurnsCntrValidity_Cnt			- <del>-</del>	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_Status_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_AbsHwPos_fared_AbsHwPos_Per2_SriComHwPos_HwDeg_f32				
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPosStatus_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_Inst_Ap_AbsHwPos_Pim_EOLVehCntrOffset         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           Name         Actual Value         Expected Value         Result           AbsHwPos_HignedHwPos_HwDeg_M_f32         5828_25977         5828_25977 ± 0.009         40           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.00999999046         0.0099999978 ± 0.004         40           AbsHwPos_HandwheelPostition_HwDeg_M_f32         4914_729004         4914_729004 ± 0.1         40           AbsHwPos_HwPosSource_Cnt_M_u16         65534         65534         65534           AbsHwPos_HandwheelPostition_HwDeg_M_f32         4914_729004         814_729004 ± 0.1         40           AbsHwPos_State_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID         40           AbsHwPos_Frey_HandwheelPostition_HwDeg_M_f32         914_729004         914_729004 ± 0.1         914_729004         914_729004 ± 0.1         914_729004         914_729004 ± 0.1				
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SriComHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_Inst_Ap_AbsHwPos_Pim_EDLVehcTntOffset         target_Pim_EDLVehCntrOffset           Name         Actual Value         Expected Value         Result           AbsHwPos_AlignedHwPos_HwDeg_M_f32         5828.25977         5828.25977 ± 0.009         484.729004           AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32         0.00999999046         0.0099999978 ± 0.004         484.729004           AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32         -814.729004         -814.729004         -814.729004           AbsHwPos_HwPosSource_Cnt_M_u16         65534         65534         65534           AbsHwPos_HwPosSource_Ort_M_u16         65535         65535         65535           AbsHwPos_SriComHwPos HwDeg_M_f32         -814.729004         -814.729004 ± 0.1         -814.729004 ± 0.1           AbsHwPos_TerpHwPos_HwDeg_M_f32         0         0         0 ± 0.1         -80.820007           AbsHwPos_TerpHwPos_HwDeg_M_f32         0         0         0 ± 0.0         -80.820007 ± 0.00         -80.820007 ± 0.0         -80.820007 ± 0.0         -80.820007 ± 0.0         -80.820007 ± 0.0         -80.820007 ± 0.0         -80.820007 ±				
target_Rte_Inst_Ap_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_Rte_Inst_Ap_AbsHwPos_Pirin_EOLVehCntrOffset           Name         Actual Value         Expected Value         Result           AbsHwPos_AlignedHwPos_HwDeg_M_f32         5828.25977         5828.25977 ± 0.009         4828.25977           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.00999999046         0.0099999978 ± 0.004         484.729004 ± 0.1           AbsHwPos_HandwheelPosition_IPF_M_str.SV_Uls_f32         -814.729004         -814.729004 ± 0.1         65534           AbsHwPos_HwPosSource_Cnt_M_u16         65534         65534         65534           AbsHwPos_HwPosStatus_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID         484.729004 ± 0.1           AbsHwPos_FrevHandwheelPosition_HwDeg_M_f32         -814.729004         -814.729004 ± 0.1         65534           AbsHwPos_SriComHwPosStatus_Cnt_M_u16         65535         65535         65535         65535           AbsHwPos_TargetHwAuthority_Uls_M_f32         0         0 ± 0.01         40.00         40.00           AbsHwPos_TempHwPos_HwDeg_M_f32         668.820007         -868.820007 ± 0.00         4665.7002 ± 0.009         4665.7002 ± 0.009         4665.7002 ± 0.009         4665.7002 ± 0.009         4665.7002 ± 0.009         4665.7002 ± 0.009         4666.7002 ± 0.009         4666.7002 ± 0.009         4666.7002 ± 0.				
Rarget_Rie_inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset				
Name         Actual Value         Expected Value         Result           AbsHwPos_AlignedHwPos_HwDeg_M_f32         5828.25977         5828.25977 ± 0.009         484.759004         5828.25977 ± 0.009         484.759004         484.759004         484.759004         484.759004         484.759004 ± 0.10         484.759004 ± 0.10         484.759004 ± 0.10         484.759004 ± 0.11			_	
AbsHwPos_AlignedHwPos_HwDeg_M_f32       5828.25977       5828.25977 ± 0.009         AbsHwPos_HandwheelAuthority_Uls_M_f32       0.00999999046       0.00999999978 ± 0.004         AbsHwPos_HandwheelPosition_LPF_M_str.SV_Uls_f32       -814.729004       -814.729004 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       65534       65534         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       -814.729004       -814.729004 ± 0.1         AbsHwPos_STComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -868.820007       -868.820007 ± 0.10         AbsHwPos_VehCntrOffst_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrOfst_Learn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_Per2_H			Expected Value	Result
AbsHwPos_HandwheelAuthority_Uis_M_f32			•	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32				•
AbsHwPos_HwPosSource_Cnt_M_u16         65534         65534           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         -814.729004         -814.729004 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         65535         65535           AbsHwPos_SrlComHwPos_HwDeg_M_f32         0         0 ± 0.0           AbsHwPos_TargetHwAuthority_Uls_M_f32         0         0 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         -868.820007         -868.820007 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         6165.7002         6165.7002 ± 0.009           AbsHwPos_VehCntrOffset_HwDeg_M_f32         1         1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         6165.7002         6165.7002 ± 0.009           AbsHwPos_VehCntrOfset_HwDeg_M_f32         1         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           1 arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value         0.00999999046         0.0099999978 ± 0.004           4 arget_AbsHwPos_Per2_HindwheelPosition_HwDeg_f32.value         65534         65535	·			_
AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         -814.729004         -814.729004 ± 0.1           AbsHwPos_SrlComHwPos_Status_Cnt_M_u16         65535         65535           AbsHwPos_SrlComHwPos_HwDeg_M_f32         0         0 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0         0 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         -868.820007         -868.820007 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         6165.7002         6165.7002 ± 0.009           AbsHwPos_VehCntrOfstl.earn_Cnt_M_lgc         1         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value         0.00999999046         0.0099999978 ± 0.004           target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value         65534         65534           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value <td></td> <td></td> <td></td> <td>•</td>				•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       -814.729004       -814.729004 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -868.820007       -868.820007 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1				-
AbsHwPos_SrIComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrIComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_UIs_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -868.820007       -868.820007 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrValid_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       1       1         1       2       4				•
AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -868.820007       -868.820007 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         1 arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.009999999046       0.00999999978 ± 0.004         1 arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         1 arget_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         1 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         1 target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1			65535	~
AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -868.820007       -868.820007 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         1 arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.009999999046       0.00999999978 ± 0.004         1 arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         1 arget_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         1 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         1 target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos SrlComHwPos HwDeg M f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32       -868.820007       -868.820007 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.009999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1		0	0 ± 0.004	-
AbsHwPos_VehCntrOffset_HwDeg_M_f32       6165.7002       6165.7002 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.009999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1		-868.820007	-868.820007 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_igc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_VehCntrOffset_HwDeg_M_f32	6165.7002	6165.7002 ± 0.009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
Rte Call Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1		0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value       0.009999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -814.729004       -814.729004 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value 65534 65534 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 65535 65535 target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0 0 ± 0.1	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0099999046	0.0099999978 ± 0.004	-
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 65535 65535 atarget_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0 0 ± 0.1	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-814.729004	-814.729004 ± 0.1	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0±0.1	target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	-
	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 $65535$ $65535 \pm 0.09$				~
	target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.93 (Repeat Count = 1) Name	Input Value		
	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7437.22021		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.959999979		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-369.899994		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.959999979		
AbsHwPos_HwPosSource_Cnt_M_u16	3588		
AbsHwPos_HwPosState_Cnt_M_enum	5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	848.539978		
AbsHwPos_RelHwPos_HwDeg_M_f32	848.539978		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	29467		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0		
AbsHwPos_TempHwPos_HwDeg_M_f32	1889.09998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6059.74023		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	.0" .0.5	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.94999988		
x_HwPosOutputLPFError_HwDeg_f32	1357.40002		
c_I2CHwAuthority_Uls_f32	0.94999988		
<_MaxSensorlessAuthority_Uls_f32	0.140000001		
k_MaxVehCntrOffDiff_HwDeg_f32	1416.80005		
k_MinSensorlessAuthority_Uls_f32	0.949999988		
x_TurnsCntrAuthority_Uls_f32	0.94999988		
<_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	1268.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	723.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.140000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-626		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	208		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-291.100006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11779		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		ReducedPart Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		·	
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	- <del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	wDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Re
AbsHwPos_AlignedHwPos_HwDeg_M_f32	848.539978	848.539978 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1889.09998	1889.09998 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3588	3588	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	1889.09998	1889.09998 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	29467	29467	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	1889.09998	1889.09998 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6059.74023	6059.74023 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
	0.00999999046	0.00999999978 ± 0.004	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0033333040		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	29467	29467	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	•
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-291.100006	-291.100006 ± 0.0009	<b>✓</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.94 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7331.25977		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.970000029		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	120.199997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.970000029		
AbsHwPos_HwPosSource_Cnt_M_u16	3700		
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_ProvIdentivibesIPseifien_HwPos_M_622	6		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-868.820007		
AbsHwPos_RelHwPos_HwDeg_M_f32	-868.820007		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	29965		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	120.199997		
AbsHwPos_TargetHwAuthority_Uls_M_f32	1		
AbsHwPos_TempHwPos_HwDeg_M_f32	909.380005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1446.94995		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.959999979		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_Uls_f32	0.959999979		
k_MaxSensorlessAuthority_Uls_f32	0.150000006		
k_MaxVehCntrOffDiff_HwDeg_f32	1446.5		
k_MinSensorlessAuthority_Uls_f32	0.959999979		
k_TurnsCntrAuthority_Uls_f32	0.959999979		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1298		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	743.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.150000006		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-606.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	212		
	-271.399994		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11903		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0	Dadward Darf Out Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-868.820007	-868.820007 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	885.704651	885.70459 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
ASSET WI CO_T WI COCCUTO_CITE_WI_UTC		00004	

HWPOS\_STATE\_INPUTINVALID

885.704651

65535

0

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

HWPOS\_STATE\_INPUTINVALID

885.70459 ± 0.1

65535

0 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1446.94995	-1446.94995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	885.704651	885.70459 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

lame	Input Value
ubsHwPos AlignedCumMtrPos HwDeg M f32	7225.2998
ubshwPos_AlignedCuminitiPos_nwDeg_w_132	0.980000019
	140.300003
hbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	
hbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.980000019
hbsHwPos_HwPosSource_Cnt_M_u16	3812 7
bsHwPos_HwPosState_Cnt_M_enum	·
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	889.099976
lbsHwPos_RelHwPos_HwDeg_M_f32	1889.09998
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	30463
bsHwPos_SrlComHwPos_HwDeg_M_f32	140.300003
bsHwPos_TargetHwAuthority_Uls_M_f32	0.980000019
.bsHwPos_TempHwPos_HwDeg_M_f32	929.659973
.bsHwPos_VehCntrOffset_HwDeg_M_f32	7587.68994
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.970000029
_HwPosOutputLPFError_HwDeg_f32	1416.80005
_l2CHwAuthority_Uls_f32	0.970000029
_MaxSensorlessAuthority_Uls_f32	0.159999996
_MaxVehCntrOffDiff_HwDeg_f32	1476.19995
_MinSensorlessAuthority_Uls_f32	0.970000029
_TurnsCntrAuthority_Uls_f32	0.970000029
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	1327.69995
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	763.400024
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
irget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.159999996
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-586.599976
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	216
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-251.699997
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12027
rget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	1
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority Uls f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1889.09998	1889.09998 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.980000019	0.980000019 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	929.659973	929.659973 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3812	3812	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	929.659973	929.659973 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	30463	30463	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.980000019	0.980000019 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	929.659973	929.659973 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7587.68994	7587.68994 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.980000019	0.980000019 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	929.659973	929.659973 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3812	3812	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	30463	30463	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	140.300003	140.300003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-251.699997	-251.699997 ± 0.0009	~

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.96 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7119.33008
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.99000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	160.399994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.99000001
AbsHwPos_HwPosSource_Cnt_M_u16	3924
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	909.380005
AbsHwPos_RelHwPos_HwDeg_M_f32	909.380005
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	30961
AbsHwPos_SrlComHwPos_HwDeg_M_f32	160.399994
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.99000001
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7728.43018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.980000019
k_HwPosOutputLPFError_HwDeg_f32	1446.5
k_I2CHwAuthority_UIs_f32	0.980000019
k_MaxSensorlessAuthority_Uls_f32	0.170000002
k_MaxVehCntrOffDiff_HwDeg_f32	1505.90002
k_MinSensorlessAuthority_Uls_f32	0.980000019
k_TurnsCntrAuthority_Uls_f32	0.980000019
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1357.40002
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	783.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.170000002
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-566.900024
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	220



Nome	Input Value		
Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5000.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12151		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11606.1465	-11606.1465 ± 0.1	~
AbsHwPos HwPosSource Cot M u16	65534	65534	-

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11606.1465	-11606.1465 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11606.1465	-11606.1465 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7728.43018	7728.43018 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5000.6001	5000.6001 ± 0.009	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.97 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7013.37988
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	180.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	4036
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	929.659973
AbsHwPos_RelHwPos_HwDeg_M_f32	929.659973
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31459
AbsHwPos_SrlComHwPos_HwDeg_M_f32	180.5
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1369.17004
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.99000001
k_HwPosOutputLPFError_HwDeg_f32	1476.19995
k_I2CHwAuthority_Uls_f32	0.99000001



Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.180000007		
k_MaxVehCntrOffDiff_HwDeg_f32	1535.59998		
k_MinSensorlessAuthority_Uls_f32	0.99000001		
k_TurnsCntrAuthority_UIs_f32	0.99000001		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1387.09998		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.180000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-547.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5120.3999		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12275		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	929.659973	929.659973 ± 0.0009	~

target_Inte_inst_Ap_AbsniwFos.Fini_EoEvenonitionset	target_Fini_LOLVenontionset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	929.659973	929.659973 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1334.95007	1334.94995 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	4036	4036	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1334.95007	1334.94995 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31459	31459	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	180.5	180.5 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1369.17004	-1369.17004 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1334.95007	1334.94995 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4036	4036	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	31459	31459	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	180.5	180.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5120.3999	5120.3999 ± 0.009	•

Т					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 2.98 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6907.41992	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	200.600006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003	
AbsHwPos_HwPosSource_Cnt_M_u16	4148	
AbsHwPos_HwPosState_Cnt_M_enum	10	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	949.940002	
AbsHwPos_RelHwPos_HwDeg_M_f32	949.940002	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31957	



Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	200.600006		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.200000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2804		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.100000001		
k_HwPosOutputLPFError_HwDeg_f32	1505.90002		
k_I2CHwAuthority_Uls_f32	0.100000001		
k_MaxSensorlessAuthority_Uls_f32	0.18999998		
k_MaxVehCntrOffDiff_HwDeg_f32	1565.30005		
k_MinSensorlessAuthority_Uls_f32	0.100000001		
k_TurnsCntrAuthority_Uls_f32	0.100000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1416.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	823.700012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-527.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	228		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5240.2002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12399		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		PoducodPorf Cat Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_t			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_UIs_f32	target_AbsHwPos_Per2_HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HandwheelPositio		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_HwPosSource_Cr target_AbsHwPos_Per2_I2CHwAbsPosVal	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	•
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos PrevHandwheelPosition HwDeg M f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	-
AbsHwPos SrlComHwPos HwDeg M f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos VehCntrOffset HwDeg M f32	2804	2804 ± 0.009	-
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim FOI VehCntrOffset FOI VehCntrOffset HwDeg f32	65535	65535 + 0.09	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

65535 ± 0.09

65535

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 



Test Step 2.99 (Repeat Count = 1)	Input Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6801.45996		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.300000012		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	220.699997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.300000012		
AbsHwPos_HwPosSource_Cnt_M_u16	4260		
AbsHwPos_HwPosState_Cnt_M_enum	11		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	970.219971		
AbsHwPos_RelHwPos_HwDeg_M_f32	970.219971		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32455		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	220.699997		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2916		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntroffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.109999999		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998		
k_I2CHwAuthority_Uls_f32	0.109999999		
k_MaxSensorlessAuthority_Uls_f32	0.200000003		
k_MaxVehCntrOffDiff_HwDeg_f32	1595		
k_MinSensorlessAuthority_Uls_f32	0.109999999		
k_TurnsCntrAuthority_Uls_f32	0.109999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1446.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.20000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-507.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	232		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5360		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12523		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos Hv	vDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	,	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
	Actual Value	Expected Value	Doo
Name		Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	970.219971	970.219971 ± 0.0009	
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.300000012	0.300000012 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1037.47107	1037.47095 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	4260	4260	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1037.47107	1037.47095 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32455	32455	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	220.699997	220.699997 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.30000012	0.300000012 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2916	2916 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
	0.300000012	0.300000012 ± 0.004	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.300000012		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1037.47107	1037.47095 ± 0.1	



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	32455	32455	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	220.699997	220.699997 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	5360	5360 ± 0.009	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.100 (Repeat Count = 1) Name	Input Value		
	·		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6695.5		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	240.800003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40000006		
AbsHwPos_HwPosSource_Cnt_M_u16	4372		
AbsHwPos_HwPosState_Cnt_M_enum	12		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-990.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	-990.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32953		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	240.800003		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3028		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.119999997		
k_HwPosOutputLPFError_HwDeg_f32	1565.30005		
k_I2CHwAuthority_Uls_f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.209999993		
k_MaxVehCntrOffDiff_HwDeg_f32	50.5999985		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_Uls_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1476.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	863.900024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.209999993		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-488.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5479.7998		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12647		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0		sReducedPerf Cnt lac	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAutho		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target_AbsHwPos_Per2_HandwheelPosition		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource C		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt		
		_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDog_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_CIII_uU8	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	0.280000001 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1089.12402	-1089.12402 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbaHwBoo BrowHandwhoolBooition HwDog M f22	1080 12402	1090 12402 + 0.1	

-1089.12402

65535

0

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32
AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

-1089.12402 ± 0.1

65535

0 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3028	3028 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.280000001	0.280000001 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1089.12402	-1089.12402 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Took Ston 2 404 (Bonock Count = 4)	
Test Step 2.101 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	200
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	260.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	4484
AbsHwPos_HwPosState_Cnt_M_enum	13
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1010.78003
AbsHwPos_RelHwPos_HwDeg_M_f32	1010.78003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33451
AbsHwPos_SrlComHwPos_HwDeg_M_f32	260.899994
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5
AbsHwPos_TempHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	f.
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k HwPosAuthorityStep Uls f32	0.129999995
k HwPosOutputLPFError HwDeg f32	1595
k I2CHwAuthority Uls f32	0.129999995
k_MaxSensorlessAuthority_UIs_f32	0.219999999
k MaxVehCntrOffDiff HwDeg f32	80.3000031
k MinSensorlessAuthority Uls f32	0.129999995
k TurnsCntrAuthority Uls f32	0.129999995
k UseTurnsCntr Cnt Igc	0
k VehCntrOffValidLimit HwDeg f32	1505.90002
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	884
target AbsHwPos Per2 ManufMode Cnt enum.value	0
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.219999999
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-468.399994
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	240
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	5599.6001
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12771
target_Pim_EOLVenChitOfiset.EOLPwPosTrimPerformed_Chi_uTo target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus_	1
·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1010.78003	1010.78003 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	4484	4484	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33451	33451	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	260.899994	260.899994 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	668.400024	668.400024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-248.300003	-248.300003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4484	4484	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	33451	33451	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	260.899994	260.899994 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	668.400024	668.400024 ± 0.0009	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>~</b>

Test Step 2.102 (Repeat Count = 1)	variable in the second of the
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1000.25
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.60000024
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024
AbsHwPos_HwPosSource_Cnt_M_u16	4596
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1031.06006
AbsHwPos_RelHwPos_HwDeg_M_f32	123.400002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33949
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.140000001
k_HwPosOutputLPFError_HwDeg_f32	1149.5
k_I2CHwAuthority_Uls_f32	0.140000001
k_MaxSensorlessAuthority_Uls_f32	0.230000004
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.140000001
k_TurnsCntrAuthority_UIs_f32	0.140000001
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1535.59998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004



Name	Input Value			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-448.700012	448.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	244			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12895			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000038	0.460000008 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos PrevHandwheelPosition HwDeg M f32	1395.13	1395.13 ± 0.1	<b>✓</b>	

- 3-2 12 - 12 - 13 - 14 - 15 - 15 - 15 - 15 - 15 - 15 - 15	· · · · · · · · · · · · · · · · · · ·		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000038	0.460000008 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1395.13	1395.13 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1448.94995	1448.94995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000038	0.460000008 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1395.13	1395.13 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999	5719.3999 ± 0.009	<b>~</b>

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.103 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	524.210022
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.699999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.699999988
AbsHwPos_HwPosSource_Cnt_M_u16	4708
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1051.33997
AbsHwPos_RelHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	34447
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.699999988
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.150000006



Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_UIs_f32	0.150000006		
k_MaxSensorlessAuthority_Uls_f32	0.239999995		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.150000006		
k_TurnsCntrAuthority_UIs_f32	0.150000006		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.239999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-429		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	248		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5839.2002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13019		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos AlianedHwPos HwDea M f32	-248.300003	-248.300003 ± 0.0009	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988	0.69999988 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1535.87	1535.87 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	4708	4708	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1535.87	1535.87 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988	0.69999988 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87	1535.87 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	953.210022	953.210022 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.69999988	0.69999988 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1535.87	1535.87 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4708	4708	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	953.210022	953.210022 ± 0.0009	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.104 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-324.119995	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	321.26001	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.800000012	
AbsHwPos_HwPosSource_Cnt_M_u16	4820	



Name	Input Value
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1071.62
AbsHwPos_RelHwPos_HwDeg_M_f32	147.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	34945
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.80000012
AbsHwPos_TempHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1000.59998
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.105 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-400.209991		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.89999976		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	341.540009		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.89999976		
AbsHwPos_HwPosSource_Cnt_M_u16	4932		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1091.90002		
AbsHwPos_RelHwPos_HwDeg_M_f32	-991.700012		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	35443		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.89999976		
AbsHwPos TempHwPos HwDeg M f32	1817.34998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-500.899994		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	a onot octano otatus_Enorotatus	
k_HwPosAuthorityStep_Uls_f32	0.170000002		
k_HwPosOutputLPFError_HwDeg_f32	1238.59998		
k_I2CHwAuthority_UIs_f32	0.170000002		
k_MaxSensorlessAuthority_Uls_f32	0.25999999		
k_MaxVehCntrOffDiff_HwDeg_f32	199.100006		
k_MinSensorlessAuthority_Uls_f32	0.170000002		
k_TurnsCntrAuthority_Uls_f32	0.170000002		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	160.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-389.600006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	10		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	6078.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13267		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
		PoducodPorf Cot Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	-991.700012	-991.700012 ± 0.0009	
	0.729999959	0.730000019 ± 0.004	
AbsHwPos_HandwheelAuthority_UIs_M_f32			
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-316.485992	-316.485992 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	_
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	_
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-316.485992	-316.485992 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•

-389.600006

0.25999999

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

 $AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32$ 

-389.600006 ± 0.1

0.25999999 ± 0.004



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-389.600006	-389.600006 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-10.6099854	-10.6099997 ± 0.00009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.729999959	0.730000019 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-316.485992	-316.485992 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-389.600006	-389.600006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-10.6099854	-10.6099997 ± 0.00009	<b>✓</b>

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.106 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6059.74023
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	361.820007
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999
AbsHwPos_HwPosSource_Cnt_M_u16	5044
AbsHwPos_HwPosState_Cnt_M_enum	5
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1112.18005
AbsHwPos_RelHwPos_HwDeg_M_f32	894.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	35941
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999
AbsHwPos_TempHwPos_HwDeg_M_f32	1958.08997
NbsHwPos_VehCntrOffset_HwDeg_M_f32	3700
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
\bsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.180000007
_HwPosOutputLPFError_HwDeg_f32	1268.30005
_I2CHwAuthority_Uls_f32	0.180000007
_MaxSensorlessAuthority_Uls_f32	0.270000011
_MaxVehCntrOffDiff_HwDeg_f32	228.800003
_MinSensorlessAuthority_Uls_f32	0.180000007
	0.180000007
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	80.3000031
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	180.5
arget AbsHwPos Per2 ManufMode Cnt enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.270000011
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-369.899994
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	20
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	6198.6001
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	13391
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority Uls f32
	, , , , , , , , , , , , , , , , , , , ,
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SerisonessAdunonty_Ois_i32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	894.5	894.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1958.08997	1958.08997 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1958.08997	1958.08997 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	1958.08997	1958.08997 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3700	3700 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6198.6001	6198.6001 ± 0.009	<b>✓</b>

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.107 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-5953.77881
AbsHwPos HandwheelAuthority Uls M f32	0.119999997
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	382.100006
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.119999997
AbsHwPos HwPosSource Cnt M u16	5156
AbsHwPos HwPosState Cnt M enum	6
AbsHwPos PrevHandwheelPosition HwDeg M f32	-1132.45996
AbshwPos RelHwPos HwDeg M f32	5641
AbsHwPos SrlComHwPosStatus Cnt M u16	36439
	-6743.25
AbsHwPos_SrlComHwPos_HwDeg_M_f32	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3812
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_Uls_f32	0.18999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001
k_MaxVehCntrOffDiff_HwDeg_f32	258.5
k_MinSensorlessAuthority_Uls_f32	0.18999998
k_TurnsCntrAuthority_Uls_f32	0.18999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	110
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-350.200012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	30
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6318.3999



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13515		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3.	2 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuti	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwf	Pos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	5641	5641 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	588.107605	588.107605 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	5156	5156	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	588.107605	588.107605 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36439	36439	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25	-6743.25 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3812	3812 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	588.107605	588.107605 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	5156	5156	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	36439	36439	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 2.108 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5847.81982
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	402.380005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995
AbsHwPos_HwPosSource_Cnt_M_u16	5268
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1152.73999
AbsHwPos_RelHwPos_HwDeg_M_f32	150.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36937
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3924
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.200000003
k_HwPosOutputLPFError_HwDeg_f32	1327.69995
k_I2CHwAuthority_Uls_f32	0.200000003
k_MaxSensorlessAuthority_Uls_f32	0.289999992



Name	Input Value
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012
k_MinSensorlessAuthority_Uls_f32	0.200000003
k_TurnsCntrAuthority_Uls_f32	0.200000003
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	139.699997
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.289999992
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-330.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6438.2002
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13639
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf 0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset



Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	37435		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7024.72998		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.140000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4036		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.209999993		
k_HwPosOutputLPFError_HwDeg_f32	1357.40002		
k_I2CHwAuthority_Uls_f32	0.209999993		
k_MaxSensorlessAuthority_Uls_f32	0.300000012		
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994		
k_MinSensorlessAuthority_UIs_f32	0.20999993		
k_TurnsCntrAuthority_UIs_f32	0.209999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	169.399994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.300000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-310.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6558		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13763		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3	D D I ID ( 0 I I	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_ManufMode_C target_AbsHwPos_Per2_SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	any_oni_uoo	
Name	Actual Value	Expected Value	Populé
		•	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-620 0.209999993	-620 ± 0.0009 0.209999993 ± 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32			
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1705.85986	1705.85999 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_onum	HWPOS STATE SENSORVALID		J
AbsHwPos_HwPosState_Cnt_M_enum		HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos SrlComHwPosStatus Cnt M u16	1705.85986 21845	21845	
		21043	
		1705 95000 + 0 1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1705.85986	1705.85999 ± 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	1705.85986 0.209999993	0.209999993 ± 0.004	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	1705.85986 0.209999993 1705.85986	0.209999993 ± 0.004 1705.85999 ± 0.1	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	1705.85986 0.209999993 1705.85986 4036	0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1705.85986 0.209999993 1705.85986 4036	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1705.85986 0.209999993 1705.85986 4036 1	0.20999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1705.85986 0.209999993 1705.85986 4036 1 0	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$ 1 0 504	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1705.85986 0.209999993 1705.85986 4036 1 0 504	0.20999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1705.85986 0.209999993 1705.85986 4036 1 0 504 1	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$ $1$ $0$ $504$ $1$	0
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$ $1$ $0$ $504$ $1$ $1$ $0.2099999993 \pm 0.004$	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993 1600	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$ $1$ $0$ $504$ $1$ $1$ $0.2099999993 \pm 0.004$ $1600 \pm 0.1$	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993 1600 1	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$ $1$ $0$ $504$ $1$ $1$ $0.2099999993 \pm 0.004$ $1600 \pm 0.1$	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993 1600	$0.209999993 \pm 0.004$ $1705.85999 \pm 0.1$ $4036 \pm 0.009$ $1$ $0$ $504$ $1$ $1$ $0.2099999993 \pm 0.004$ $1600 \pm 0.1$	



T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.110 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5635.8999		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725	-11725	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006		
AbsHwPos_HwPosSource_Cnt_M_u16	5492		
AbsHwPos_HwPosState_Cnt_M_enum	9		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1193.30005		
AbsHwPos_RelHwPos_HwDeg_M_f32	201.199997		
AbsHwPos SrlComHwPosStatus Cnt M u16	37933		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006		
AbsHwPos TempHwPos HwDeg M f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4148		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	al onsot_octEnorolatas_Enorolatas	
k HwPosAuthorityStep Uls f32	0.219999999		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_Uls_f32	0.219999999		
	0.310000002		
k_MaxSensorlessAuthority_Uls_f32			
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006		
k_MinSensorlessAuthority_Uls_f32	0.219999999		
k_TurnsCntrAuthority_UIs_f32	0.219999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	199.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.310000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	60		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6677.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13887		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStati		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	_	
Name	Actual Value	Expected Value	Resul
	201.199997	201.199997 ± 0.0009	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2521.05005	2521.05005 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	¥
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	

0

0

0 ± 0.1

0 ± 0.004

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4148	4148 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

lame	Input Value
	-5529.93018
bsHwPos_AlignedCumMtrPos_HwDeg_M_f32 bsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996
	11725
psHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-
psHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.159999996
bsHwPos_HwPosSource_Cnt_M_u16	5604
lbsHwPos_HwPosState_Cnt_M_enum	10
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1213.57996
lbsHwPos_RelHwPos_HwDeg_M_f32	64.9800034
bsHwPos_SriComHwPosStatus_Cnt_M_u16	38431
bsHwPos_SrlComHwPos_HwDeg_M_f32	7306.20996
bsHwPos_TargetHwAuthority_Uls_M_f32	0.159999996
lbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004
.bsHwPos_VehCntrOffset_HwDeg_M_f32	4260
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.230000004
_HwPosOutputLPFError_HwDeg_f32	1416.80005
_I2CHwAuthority_Uls_f32	0.230000004
_MaxSensorlessAuthority_Uls_f32	0.319999993
_MaxVehCntrOffDiff_HwDeg_f32	377.299988
_MinSensorlessAuthority_Uls_f32	0.230000004
_TurnsCntrAuthority_Uls_f32	0.230000004
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	228.800003
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.319999993
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-271.399994
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	70
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6797.6001
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14011
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
irget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	64.9800034	64.9800034 ± 0.00009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996	0.159999996 ± 0.004	<b>~</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	9423.11328	9423.11328 ± 0.1	<b>~</b>
AbsHwPos_HwPosSource_Cnt_M_u16	5604	5604	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	9423.11328	9423.11328 ± 0.1	<b>~</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38431	38431	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7306.20996	7306.20996 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.159999996	0.159999996 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4260	4260 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.159999996	0.159999996 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	5604	5604	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	38431	38431	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6797.6001	6797.6001 ± 0.009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.112 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	5423.97998
AbsHwPos HandwheelAuthority Uls M f32	0.170000002
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	0
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.170000002
AbsHwPos HwPosSource Cnt M u16	5716
AbsHwPos HwPosState Cnt M enum	11
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1233.85999
AbsHwPos_RelHwPos_HwDeg_M_f32	8888
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7446.9502
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	949.940002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4372
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.23999995
k_HwPosOutputLPFError_HwDeg_f32	1446.5
k_I2CHwAuthority_Uls_f32	0.23999995
k_MaxSensorlessAuthority_Uls_f32	0.330000013
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.23999995
k_TurnsCntrAuthority_UIs_f32	0.23999995
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	258.5
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.330000013
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-251.699997
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6917.3999



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14135		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_InstAp\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_InstAp\_AbsHw$	_( target_AbsHwPos_Per2_DiagStatusHwPo	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	8888	8888 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002	949.940002 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	949.940002	949.940002 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	$0 \pm 0.004$	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4372	4372 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	$0 \pm 0.004$	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	949.940002	949.940002 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	N		



Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.340000004		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012	882.200012	
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_UIs_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	~
AbsHwPos HandwheelAuthority I lls M f32	0.360000014	0.360000014 + 0.004	<b>✓</b>

target_Inst_Ap_AbsniwFos.Filli_EOEvenChitOnset	target_Fini_EOEvenonitionset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.114 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-5212.05908	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-564.619995	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.189999998	
AbsHwPos_HwPosSource_Cnt_M_u16	5940	
AbsHwPos_HwPosState_Cnt_M_enum	13	



Name	Input Value		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1274.42004		
AbsHwPos_RelHwPos_HwDeg_M_f32	-471		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	39925		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7728.43018		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998		
AbsHwPos_TempHwPos_HwDeg_M_f32	-990.5		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4596		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.25999999		
k_HwPosOutputLPFError_HwDeg_f32	1505.90002		
k_I2CHwAuthority_Uls_f32	0.25999999		
k_MaxSensorlessAuthority_Uls_f32	0.349999994		
k_MaxVehCntrOffDiff_HwDeg_f32	466.399994		
k_MinSensorlessAuthority_Uls_f32	0.25999999		
k_TurnsCntrAuthority_UIs_f32	0.25999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	317.899994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	341.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.349999994		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-212.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7157		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14383		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_ManufMode_Cnt_		
	target_AbsHwPos_Per2_SensorlessAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SensorlessHwPos target AbsHwPos Per2 SrlComHwPosSta		
	target AbsHwPos Per2 SrlComHwPos H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		- <del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_TurnsCntrValidity_ target Pim EOLVehCntrOffset	_CIII_u08	
	·	Even este d Volus	Daguilé
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-471	-471 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-645.53717	-645.53717 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>*</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID -645.53717	HWPOS_STATE_INPUTINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32		-645.53717 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>V</b>
AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	-990.5	0 ± 0.004 -990.5 ± 0.1	<b>✓</b>
AbshwPos_TemphwPos_HwDeg_M_f32 AbshwPos_VehCntrOffset_HwDeg_M_f32	-990.5 4596	-990.5 ± 0.1 4596 ± 0.009	Ĭ
	1	1	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	
Pto Call An Abelia/Doc NytrDiogMar CotNTCCtct.cc/Docom Cot T	V		-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0 -645.53717	0 ± 0.004 -645.53717 ± 0.1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0 -645.53717 65534	0 ± 0.004 -645.53717 ± 0.1 65534	\ \frac{1}{2}
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	0 -645.53717 65534 65535	0 ± 0.004 -645.53717 ± 0.1 65534 65535	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0 -645.53717 65534	0 ± 0.004 -645.53717 ± 0.1 65534	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.115 (Repeat Count = 1) Name	Input Value		
	•		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5106.08984		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	484		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0		
AbsHwPos_HwPosSource_Cnt_M_u16	6052		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1294.69995		
AbsHwPos_RelHwPos_HwDeg_M_f32	4649		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40423		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7869.16992		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.20000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	1010.78003		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4708		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998		
k_I2CHwAuthority_UIs_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	0.360000014		
k_MaxVehCntrOffDiff_HwDeg_f32	496.100006		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_Uls_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-192.600006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	110		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7276.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14507		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosSta		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
		Expected Value 4649 ± 0.009	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	4649		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1010.78003	1010.78003 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	6052	6052	1
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1010.78003	1010.78003 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40423	40423	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Llls_M_f32	-7869.16992	-7869.16992 ± 0.1	
	0.20000003		

0.200000003

1010.78003

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

0.200000003 ± 0.004

1010.78003 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4708	4708 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.20000003	0.200000003 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1010.78003	1010.78003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	6052	6052	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	40423	40423	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7276.7998	7276.7998 ± 0.009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.116 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5000.12988		
AbsHwPos HandwheelAuthority Uls M f32	0.209999993		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-564.299988		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	1		
AbsHwPos HwPosSource Cnt M u16	6164		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	1314.97998		
AbsHwPos_RelHwPos_HwDeg_M_f32	234		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40921		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.209999993		
AbsHwPos_TempHwPos_HwDeg_M_f32	123.400002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1687		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.280000001		
k_HwPosOutputLPFError_HwDeg_f32	1565.30005		
k_I2CHwAuthority_Uls_f32	0.280000001		
k_MaxSensorlessAuthority_UIs_f32	0.370000005		
k_MaxVehCntrOffDiff_HwDeg_f32	525.799988		
k_MinSensorlessAuthority_Uls_f32	0.280000001		
k_TurnsCntrAuthority_Uls_f32	0.280000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.370000005		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-172.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	120		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7396.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14631		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Ci	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
		Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_SrlComHwPos_ target_AbsHwPos_Per2_TurnsCntrValid	HwDeg_f32	
		HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	HwDeg_f32	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset	HwDeg_f32 ty_Cnt_u08	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value	_HwDeg_f32 ty_Cnt_u08 Expected Value	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value 234	_HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value 234 0	_HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009 0 ± 0.0004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value 234 0 123.400002	HwDeg_f32 ty_Cnt_u08 <b>Expected Value</b> 234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value 234 0 123.400002 65534	HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	target_AbsHwPos_Perz_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID	HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_AbsHwPos_Perz_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002	HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 123.400002 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535	HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 123.400002 ± 0.1 65535	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0	HwDeg_f32 ty_Cnt_u08 Expected Value 234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 123.400002 ± 0.1 65535 0 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value  234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004  123.400002 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value  234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 123.400002	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004  123.400002 ± 0.1  1657 ± 0.009	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Learn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004  123.400002 ± 0.1  1687 ± 0.009  1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687 1	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004  123.400002 ± 0.1  1687 ± 0.009  1  0	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Ard_Digc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687 1 0 504	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004  123.400002 ± 0.1  1687 ± 0.009  1  0  504	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffst_BruDeg_M_f32 AbsHwPos_VehCntrOffst_BruDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687 1 0 504	Expected Value  234 ± 0.0009  0 ± 0.004  123.400002 ± 0.1  65534  HWPOS_STATE_INPUTINVALID  123.400002 ± 0.1  65535  0 ± 0.1  0 ± 0.004  123.400002 ± 0.1  1687 ± 0.009  1  0  504	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrVofstLearn_Cnt_M_lgc AbsHwPos_VehCntrVolid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687 1 0 504	HwDeg_f32  ty_Cnt_u08  Expected Value  234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 123.400002 ± 0.1 65535 0 ± 0.1 0 ± 0.004 123.400002 ± 0.1 1687 ± 0.009 1 0 504 1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_AbsHwPos_Perz_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687 1 0 504 1 1 0 123.400002	HwDeg_f32  ty_Cnt_u08  Expected Value  234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 123.400002 ± 0.1 65535 0 ± 0.1 0 ± 0.004 123.400002 ± 0.1 1687 ± 0.009 1 0 504 1 1 0 ± 0.004 123.400002 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TrargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrVofft_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Perz_TurnsCntrValid target_Pim_EOLVehCntrOffset  Actual Value 234 0 123.400002 65534 HWPOS_STATE_INPUTINVALID 123.400002 65535 0 0 123.400002 1687 1 0 504 1	HwDeg_f32  ty_Cnt_u08  Expected Value  234 ± 0.0009 0 ± 0.004 123.400002 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 123.400002 ± 0.1 65535 0 ± 0.1 0 ± 0.004 123.400002 ± 0.1 1687 ± 0.009 1 0 504 1 1 0 ± 0.004	Resu

0

0 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.117 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9556.42969		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-941.200012		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.569999993		
AbsHwPos_HwPosSource_Cnt_M_u16	1348		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-409.299988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-409.299988		
AbsHwPos SrlComHwPosStatus Cnt M u16	41419		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.569999993		
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_vehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Call_Ap_AbsHwPos_EOLVellCll target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.75		
k_HwPosOutputLPFError_HwDeg_f32	674.299988		
k_I2CHwAuthority_UIs_f32	0.75		
	0.839999974		
k_MaxSensorlessAuthority_Uls_f32			
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988 0.75		
k_MinSensorlessAuthority_Uls_f32			
k_TurnsCntrAuthority_UIs_f32	0.75		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	674.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	130		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-409.299988	-409.299988 ± 0.0009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.569999993	0.569999993 ± 0.004	•

AbshwPos_AlignedHwPos_HwDeg_M_f32	-409.299988	-409.299988 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.569999993	0.569999993 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	952.06073	952.06073 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	952.06073	952.06073 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41419	41419	<b>✓</b>



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004	6180.29004 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.569999993	0.569999993 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006	2380.31006 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003	2802.53003 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.569999993	0.569999993 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	952.06073	952.06073 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	41419	41419	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.118 (Repeat Count = 1)	<b>→</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-514.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-921.5
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.579999983
AbsHwPos HwPosSource Cnt M u16	1460
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213
AbsHwPos RelHwPos HwDeg M f32	213
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41917
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979
AbsHwPos TargetHwAuthority UIs M f32	0.579999983
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005
AbsHwPos VehCntrOffset HwDeg M f32	2943,27002
AbsHwPos VehCntrOfstLearn Cnt M lqc	1
AbsHwPos VehCntrValid Cnt M Igc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k HwPosAuthorityStep Uls f32	0.75999999
k HwPosOutputLPFError HwDeg f32	704
k_I2CHwAuthority_UIs_f32	0.75999999
k_MaxSensorlessAuthority_Uls_f32	0.850000024
k MaxVehCntrOffDiff HwDeg f32	852.5
k MinSensorlessAuthority Uls f32	0.75999999
k TurnsCntrAuthority Uls f32	0.75999999
k_UseTurnsCntr_Cnt_lgc	1
k VehCntrOffValidLimit HwDeg f32	704
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-271.399994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.850000024 -1000.29999
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	· ·
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	213	213 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1075.17896	1075.17896 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1075.17896	1075.17896 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	485.97998	485.980011 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1075.17896	1075.17896 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	485.97998	485.980011 ± 0.0009	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.119 (Repeat Count = 1)	<b>▼</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-874.22998
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.589999974
AbsHwPos_HwPosSource_Cnt_M_u16	1572
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42415
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.769999981
k_HwPosOutputLPFError_HwDeg_f32	733.700012
k_I2CHwAuthority_Uls_f32	0.769999981
k_MaxSensorlessAuthority_Uls_f32	0.860000014
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.769999981
k_TurnsCntrAuthority_UIs_f32	0.769999981
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	733.700012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1



Name	Input Value		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2385.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9547		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPer	rf(target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	32 target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3.	2 target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1437.41101	-1437.41113 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	106.369995	106.370003 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.589999974	0.589999974 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	~
target AbsHwPos Per2 SrlComHwPosStatus Cnt u16.value	65534	65534	<b>✓</b>
target_Abstract os_retz_StreothirtwrosStatus_Citt_u to.value			
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.120 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-325.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.790000021
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.60000024
AbsHwPos_HwPosSource_Cnt_M_u16	1684
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42913
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos



Name	Input Value			
k_HwPosAuthorityStep_Uls_f32	0.779999971			
k_HwPosOutputLPFError_HwDeg_f32	763.400024	763.400024		
k_I2CHwAuthority_Uls_f32	0.779999971			
k_MaxSensorlessAuthority_Uls_f32	0.870000005			
k_MaxVehCntrOffDiff_HwDeg_f32	911.900024			
k_MinSensorlessAuthority_Uls_f32	0.779999971			
k_TurnsCntrAuthority_UIs_f32	0.779999971			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	763.400024			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-232			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	6 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009		

target_Rte_inst_Ap_AbsHwPos.Plm_EOLVenCntrOffset	target_PIm_EOLVenCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	635.580017	635.580017 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995	2415.69995 ± 0.009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.121 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-514.320007	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-921.5	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.579999983	



Name	Input Value		
AbsHwPos_HwPosSource_Cnt_M_u16	1460		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213		
AbsHwPos_RelHwPos_HwDeg_M_f32	213		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41917		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.579999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_UIs_f32	0.75999999		
k_HwPosOutputLPFError_HwDeg_f32	704		
k_I2CHwAuthority_UIs_f32	0.75999999 0.850000024		
k_MaxSensorlessAuthority_Uls_f32 k_MaxVehCntrOffDiff_HwDeg_f32	852.5		
k MinSensorlessAuthority Uls f32	0.75999999		
k TurnsCntrAuthority Uls f32	0.75999999		
k UseTurnsCntr Cnt lgc	1		
k VehCntrOffValidLimit HwDeg f32	704		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_I$	_( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	<i>/</i>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid		
		any_crit_uoo	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	Evenante d Value	Danul
Name AbsHwPos AlignedHwPos HwDeg M f32	Actual Value 213	Expected Value 213 ± 0.0009	Result
AbshwPos_AlignedhwPos_hwDeg_ivi_132  AbshwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	1075.17896	1075.17896 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID	HWPOS STATE INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	1075.17896	1075.17896 ± 0.1	•
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	485.97998	485.980011 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc		1	•
AbsHwPos VehCntrValid Cnt M Igc	1		
Abs I W 03_VCI OIL Valid_OIL_W_ige	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)			-
	1	1	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 504	1 504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1 504 1 1 0.00999999046	1 504 1 1 0.00999999978 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1 504 1 1 0.00999999046 1075.17896	1 504 1 1 0.009999999978 ± 0.004 1075.17896 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 504 1 1 0.00999999046 1075.17896 65534	1 504 1 1 0.009999999978 ± 0.004 1075.17896 ± 0.1 65534	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	1 504 1 1 0.00999999046 1075.17896 65534 65535	1 504 1 1 1 0.009999999978 ± 0.004 1075.17896 ± 0.1 65534 65535	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 504 1 1 0.00999999046 1075.17896 65534	1 504 1 1 0.009999999978 ± 0.004 1075.17896 ± 0.1 65534	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.122 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-874.22998		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	324.5		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.589999974		
AbsHwPos_HwPosSource_Cnt_M_u16	1572		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42415		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974		
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.76999981		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.769999981		
k_MaxSensorlessAuthority_Uls_f32	0.860000014		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.769999981		
k TurnsCntrAuthority Uls f32	0.769999981		
k_UseTurnsCntr_Cnt_lgc	0		
k VehCntrOffValidLimit HwDeg f32	733.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150		
	2385.5		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	9547		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		a Dadwaad Darf Cat Jac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosVa		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	/_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.58999974	0.589999974 ± 0.004	•
	-1437.41101	-1437.41113 ± 0.1	
ADSTRUPOS HANDWINEEIPOSITIONLEP IVI ST.SV UIS 132		1572	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32  AbsHwPos HwPosSource Cnt M u16	1572		•
AbsHwPos_HwPosSource_Cnt_M_u16	1572 HWPOS STATE SENSORINVALID		
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_SENSORINVALID -1437.41101	HWPOS_STATE_SENSORINVALID -1437.41113 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	

0.589999974

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

0.589999974 ± 0.004



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	106.369995	106.370003 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.589999974	0.589999974 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	106.369995	106.370003 ± 0.0009	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.123 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-325,320007
AbsHwPos HandwheelAuthority Uls M f32	0.790000021
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.60000024
AbsHwPos HwPosSource Cnt M u16	1684
AbsHwPos HwPosState Cnt M enum	3
AbsHwPos PrevHandwheelPosition HwDeg M f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42913
AbsHwPos SrlComHwPos HwDeg M f32	6602.50977
AbsHwPos TargetHwAuthority Uls M f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VenChtrOfistLearn Cnt M lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0.779999971
k_HwPosOutputLPFError_HwDeg_f32	763.400024
k_I2CHwAuthority_Uls_f32	0.779999971
k MaxSensorlessAuthority Uls f32	0.870000005
k MaxVehCntrOffDiff HwDeg f32	911,900024
k MinSensorlessAuthority Uls f32	0.779999971
k TurnsCntrAuthority Uls f32	0.779999971
k UseTurnsCntr Cnt Igc	1
k VehCntrOffValidLimit HwDeg f32	763.400024
	1
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	-232
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	2
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16



Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>	
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	635.580017	635.580017 ± 0.0009	<b>✓</b>	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	635.580017	635.580017 ± 0.0009	<b>✓</b>	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Case 3: Path Test



#### **Description** Vector Description:

```
TS3.1'
 if ( (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) || (AbsHwPos_HwPosState_Cnt_M_enum ==
#WPOS_STATE_INPUTINVALID) )=>False
if (AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc == TRUE)=>False
if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >=
k_HwPosOutputLPFError_HwDeg_f32)=>True
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=>False
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=>True
if (k_UseTurnsCntr_Cnt_lgc == FALSE)=>True
if (l2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False'
TS3.2"
 if (Diagoslation Wrosineduceur ein [__in_c__in_c] = 1100_j=1100 if (AbsHwPos_VehCntrOfstLearn_Cnt_M_igc == TRUE)=>True if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>False if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>True
if( NvMStatus_Cnt_T_enum != NVM_REQ_PENDING )=>True"
if ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) =>False||
(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID)=>True)
if (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE)=>False"
TS3 4'
163.4 if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>True if (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NvMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>True"
if( (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NyMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>False"
 if( (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) >
(NvMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>False"
TS3 7"
if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>False
if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)=>False else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False"
if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)=>False else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=>False
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=>False"
TS3.9"
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=>True if (k_UseTurnsCntr_Cnt_Igc == TRUE)=>True if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)=>True"
TS3.10'
if (k_UseTurnsCntr_Cnt_lgc == TRUE)=>False
if (l2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False"
TS3.11
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True"
TS3.12"
else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>False"
TS3.13"
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>True"
if (k_UseTurnsCntr_Cnt_lgc == FALSE)=>False
if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>True
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>True"
 TS3.15"
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True"
TS3.16"
 if (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => False"
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>False
if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>True if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>False"
if (Sensorle
               essAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>False'
TS3 19"
else if (AbsHwPos HwPosState Cnt M enum == HWPOS STATE SENSORINVALID)=>True
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE) )=>True"
TS3.21"
if ( (k_UseTurnsCntr_Cnt_lgc == FALSE) &&
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE) )=>False
if (k_UseTurnsCntr_Cnt_lgc == TRUE)=>True
if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) <
FLT_EPSILON)=>False'
TS3 22"
if (SensorlessAuthority Uls T f32 >= k MaxSensorlessAuthority Uls f32)=>False"
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>True"
```



TS3.26"  $if (Sensorless Authority\_Uls\_T\_f32 >= k\_MinSensorless Authority\_Uls\_f32) => False"$ TS3 27" if (SensorlessAuthority\_Uls\_T\_f32 >= k\_MaxSensorlessAuthority\_Uls\_f32)=>False"

if( NvMStatus\_Cnt\_T\_enum != NVM\_REQ\_PENDING )=>False"

(AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32>=D\_MAXAUTHORITY\_ULS\_F32)=>True"
TS3.30"(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=False,(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=True,

(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=True,
(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=False)
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCnt\_Lgc == TRUE)=False)
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=True,(k\_UseTurnsCnt\_Lgc == TRUE)=True,(k\_UseTurnsCnt\_Lgc

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos HandwheelAuthority Uls M f32	0		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-1600		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0		
AbsHwPos HwPosSource Cnt M u16	1		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-1600		
AbsHwPos RelHwPos HwDeg M f32	-10125		
AbsHwPos SrlComHwPosStatus Cnt M u16	21845		
AbsHwPos SrlComHwPos HwDeg M f32	-11725		
	0		
AbsHwPos_TargetHwAuthority_UIs_M_f32			
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725 -1600		
AbsHwPos_VehCntrOffset_HwDeg_M_f32			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0		
<hr/> k_HwPosOutputLPFError_HwDeg_f32	0		
x_I2CHwAuthority_Uls_f32	0		
x_MaxSensorlessAuthority_Uls_f32	0		
<_MaxVehCntrOffDiff_HwDeg_f32	0		
<_MinSensorlessAuthority_Uls_f32	0		
k_TurnsCntrAuthority_Uls_f32	0		
<_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	0		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900		
arget AbsHwPos Per2 ManufMode Cnt enum.value	0		
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0		
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-1600		
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	0		
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-1600		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		PeducedPorf Cnt Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	·-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resi
AbsHwPos AlignedHwPos HwDog M f32	10125	10125 ± 0.00	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1600	-1600 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1600	-1600 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

ame	Input Value
psHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125
psHwPos_HandwheelAuthority_Uls_M_f32	1
	1600
psHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	
psHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1
psHwPos_HwPosSource_Cnt_M_u16	65534
osHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
psHwPos_PrevHandwheelPosition_HwDeg_M_f32	1600
osHwPos_RelHwPos_HwDeg_M_f32	10125
psHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
osHwPos_SrlComHwPos_HwDeg_M_f32	11725
sHwPos_TargetHwAuthority_Uls_M_f32	1
sHwPos_TempHwPos_HwDeg_M_f32	11725
psHwPos_VehCntrOffset_HwDeg_M_f32	65535
psHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
psHwPos_VehCntrValid_Cnt_M_lgc	1
e_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
e_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep_Uls_f32	1
HwPosOutputLPFError_HwDeg_f32	1620
I2CHwAuthority_Uls_f32	1
MaxSensorlessAuthority_Uls_f32	1
MaxVehCntrOffDiff_HwDeg_f32	1600
MinSensorlessAuthority_Uls_f32	1
TurnsCntrAuthority_Uls_f32	1
UseTurnsCntr_Cnt_lgc	1
VehCntrOffValidLimit_HwDeg_f32	1600
rget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
rget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
get_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900
get_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
get_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
rget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600
rget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	255
rget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535
rget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	43690
rget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerl	( target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc
get Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority UIs f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f3	
get Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
get Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
get Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
rget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Fer2_SensorlessHwPos_HwDeg_f32 rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	1
rget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Fer2_srlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	9556.42969
AbsHwPos HandwheelAuthority Uls M f32	0.75999999
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	204.899994
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.75999999
AbsHwPos HwPosSource Cnt M u16	1348
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	442.940002
AbsHwPos_RelHwPos_HwDeg_M_f32	442.940002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.75
k_HwPosOutputLPFError_HwDeg_f32	674.299988
k_I2CHwAuthority_Uls_f32	0.75
k_MaxSensorlessAuthority_Uls_f32	0.83999974
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988
k_MinSensorlessAuthority_Uls_f32	0.75
k_TurnsCntrAuthority_Uls_f32	0.75
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	674.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerror and all the properties of the$	f_( target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3$	target_AbsHwPos_Per2_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75999999	0.75999999 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	•
		400 400000 + 0.4	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	123.400002	123.400002 ± 0.1	•

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

T (0) 04/D (0) (4)	
Test Step 3.4 (Repeat Count = 1)	<u> </u>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1617.32996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40999996
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1527.19995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40999996
AbsHwPos_HwPosSource_Cnt_M_u16	400
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	224.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	224.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24657
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3928.44995
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40999996
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1555.08997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.40000006
k_HwPosOutputLPFError_HwDeg_f32	1565.30005
k_I2CHwAuthority_Uls_f32	0.40000006
k_MaxSensorlessAuthority_Uls_f32	0.49000001
k_MaxVehCntrOffDiff_HwDeg_f32	0
k_MinSensorlessAuthority_Uls_f32	0.40000006



		TW	DOTTAL
Name	Input Value		
k_TurnsCntrAuthority_Uls_f32	0.40000006		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1208.90002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.49000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	884		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1268.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4959		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthority UIs f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	224.100006	224.100006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID	HWPOS STATE INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-2278.53882	-2278.53882 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	-7755.08984	-7755.08984 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	733.329956	733.330017 ± 0.0009	

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2278.53882	-2278.53882 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984	-7755.08984 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	733.329956	733.330017 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	733.329956	733.330017 ± 0.0009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 3.5 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	898.809998	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.430000007	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	567.400024	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.430000007	
AbsHwPos_HwPosSource_Cnt_M_u16	416	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	243.300003	
AbsHwPos_RelHwPos_HwDeg_M_f32	243.300003	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24801	



Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4209.93018		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.430000007		
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.419999987		
k_HwPosOutputLPFError_HwDeg_f32	50.5999985		
k_I2CHwAuthority_Uls_f32	0.419999987		
k_MaxSensorlessAuthority_Uls_f32	0.50999999		
k_MaxVehCntrOffDiff_HwDeg_f32	1416.80005		
k_MinSensorlessAuthority_Uls_f32	0.419999987		
k_TurnsCntrAuthority_UIs_f32	0.419999987		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1268.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.50999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	924.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1328.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5207		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	·-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Uni_u06	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	Formate d Walter	D14
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	243.300003	243.300003 ± 0.0009	<b>Y</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	147.139496	147.139496 ± 0.1	<b>*</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.139496	147.139496 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	_
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>V</b>
AbsHwPos_TargetHwAuthority_UIs_M_f32		0 ± 0.004	~
AbsHwPos_TempHwPos_HwDog_M_f32	-409.950012	-409.950012 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-25.3900146 1	-25.3899994 ± 0.00009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc	1	1	
	504	504	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000203	0.00999999978 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	147.139496	147.139496 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	
target AbsHwPos Per2 SrlComHwPosStatus Cnt u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0±0.1	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1328.5	1328.5 ± 0.009	-

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 3.6 (Repeat Count = 1)	Innut Value		
Name AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	Input Value 1000.25		
AbsHwPos HandwheelAuthority Uls M f32	0.600000024		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	281		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.600000024		
AbsHwPos_HwPosSource_Cnt_M_u16	4596		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1031.06006		
AbsHwPos_RelHwPos_HwDeg_M_f32	123.400002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33949		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.600000024		
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.140000001		
C_HwPosOutputLPFError_HwDeg_f32	1149.5		
c_I2CHwAuthority_Uls_f32	0.140000001		
x_MaxSensorlessAuthority_Uls_f32	0.230000004		
c_MaxVehCntrOffDiff_HwDeg_f32	110		
C_MinSensorlessAuthority_Uls_f32	0.140000001		
C_TurnsCntrAuthority_Uls_f32	0.140000001		
C_UseTurnsCntr_Cnt_lgc	1		
<ul><li>VehCntrOffValidLimit_HwDeg_f32</li></ul>	1535.59998		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-448.700012		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	244		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12895		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		as Body and Borf. Cat. Igo	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_ arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32			
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cn		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target AbsHwPos Per2 SensorlessAutho		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target_AbsHwPos_Per2_SensorlessHwPos	·	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPosS		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<del></del>	
Name	Actual Value	Expected Value	Resi
AbsHwPos AlignedHwPos HwDeg M f32	123.400002	123.400002 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.46000038	0.460000008 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1395.13	1395.13 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1448.94995	1448.94995 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
		0.460000008 ± 0.004	
arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000038	0.700000000 ± 0.007	
arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.460000038	1395.13 ± 0.1	
·			



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EQLVehCntrOffset.EQLVehCntrOffset HwDeg f32	5719.3999	5719.3999 ± 0.009	<b>✓</b>

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	269.209991		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20000003		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	605.179993		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003		
AbsHwPos HwPosSource Cnt M u16	96		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1507.09998		
AbsHwPos RelHwPos HwDeg M f32	1507.09998		
AbsHwPos SrlComHwPosStatus Cnt M u16	21921		
AbsHwPos SrlComHwPos HwDeg M f32	8920.66016		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.20000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	436.700012		
k_I2CHwAuthority_Uls_f32	0.20000003		
k_MaxSensorlessAuthority_Uls_f32	0.109999999		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k MinSensorlessAuthority Uls f32	0.200000003		
k_TurnsCntrAuthority_UIs_f32	0.200000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.10999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	120.5		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	247		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	HwDeg f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1507.09998	1507.09998 ± 0.009	~
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	<b>V</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	524.650024	524.650024 ± 0.1	-
1			

HWPOS\_STATE\_SENSORINVALID

524.650024

65534

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 HWPOS\_STATE\_SENSORINVALID

524.650024 ± 0.1

65534



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	8920.66016 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	1358.69995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	524.650024	524.650024 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	120.5	120.5 ± 0.0009	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value
Northe Nos AlignedCumMtrPos HwDeg M f32	691.429993
AbsHwPos HandwheelAuthority Uls M f32	0.5
	-666.02002
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	120
AbsHwPos_HwPosState_Cnt_M_enum	1
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	567.400024
AbsHwPos_RelHwPos_HwDeg_M_f32	567.400024
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.5
.bsHwPos_TempHwPos_HwDeg_M_f32	120.199997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.5
_HwPosOutputLPFError_HwDeg_f32	525.799988
_I2CHwAuthority_Uls_f32	0.5
_MaxSensorlessAuthority_Uls_f32	0.140000001
_MaxVehCntrOffDiff_HwDeg_f32	228.800003
_MinSensorlessAuthority_Uls_f32	0.5
_TurnsCntrAuthority_Uls_f32	0.5
_UseTurnsCntr_Cnt_lgc	0
VehCntrOffValidLimit_HwDeg_f32	169.399994
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	180.5
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.140000001
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	180.5
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	14
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	211.100006
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	619
arget_rin_EGEvenorinorisct.EGEriwi ostrimi enormed_cnt_uto arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	-
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_inst_Ap_AbshwPos.AbshwPos_Pet2_HandwheelPosition_HwDeg_f3	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	567.400024	567.400024 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997	120.199997 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	120	120	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997	120.199997 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137	22137	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	120.199997	120.199997 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	120	120	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	22137	22137	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006	211.100006 ± 0.0009	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 3.9 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	128.470001
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	584.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	88
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1487
AbsHwPos_RelHwPos_HwDeg_M_f32	1487
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21849
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.100000001
k_HwPosOutputLPFError_HwDeg_f32	407
k_I2CHwAuthority_UIs_f32	0.100000001
k_MaxSensorlessAuthority_Uls_f32	0.100000001
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.100000001
k_TurnsCntrAuthority_Uls_f32	0.100000001
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0

 $AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc$ 

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	90.3000031		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	123		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1487	1487 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	806.663025	806.663025 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	806.663025	806.663025 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012	9026.62012 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5	1328.5 ± 0.009	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

0

0

504

1

0

806.663025

90.3000031

65534

65534

1600

0

0

1

1

0 ± 0.004

65534

65534

1600 ± 0.1

806.663025 ± 0.1

90.3000031 ± 0.00009

504

Test Step 3.10 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	972.909973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	706.580017
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	136
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1000.29999
AbsHwPos_RelHwPos_HwDeg_M_f32	-1000.29999
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22281
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	585.200012
k_I2CHwAuthority_Uls_f32	0.69999988
k_MaxSensorlessAuthority_Uls_f32	0.159999996



Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.69999988		
k_TurnsCntrAuthority_UIs_f32	0.69999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.159999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	26		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	867		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.009	~
AbsHwPos HandwheelAuthority I lls M f32	0	0 + 0 004	<b>9</b>

	10. g		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.253998	324.253998 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.253998	324.253998 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	324.253998	324.253998 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5	271.5 ± 0.0009	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.11 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1395.13	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	767.419983	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999	
AbsHwPos_HwPosSource_Cnt_M_u16	160	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1547.30005	
AbsHwPos_RelHwPos_HwDeg_M_f32	1547.30005	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22497	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8072.97021	



Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	220.699997		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	<del>_</del>	
k_HwPosAuthorityStep_Uls_f32	0.100000001		
k_HwPosOutputLPFError_HwDeg_f32	674.299988		
k_I2CHwAuthority_Uls_f32	0.100000001		
k_MaxSensorlessAuthority_Uls_f32	0.189999998		
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
k_MinSensorlessAuthority_Uls_f32	0.100000001		
k_TurnsCntrAuthority_UIs_f32	0.100000001		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	317.899994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	44		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	362.100006		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1239		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target Pim EOLVehCntrOffset	7	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.009	- 100 LIII
AbsHwPos HandwheelAuthority Uls M f32	0.100000001	0.100000001 ± 0.004	-
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	713.913757	713.913818 ± 0.1	-
AbsHwPos HwPosSource Cnt M u16	2	2	_
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS STATE SENSORVALID	_
AbsHwPos PrevHandwheelPosition HwDeg M f32	713.913757	713.913818 ± 0.1	-
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	_
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281	281 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	_
AbsHwPos_TempHwPos_HwDeg_M_f32	281	281 ± 0.1	<b>✓</b>
AbsHwPos VehCntrOffset HwDeg M f32	664.099976	664.099976 ± 0.0009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	•
AbsHwPos VehCntrValid Cnt M Igc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
	0.100000001	0.100000001 ± 0.004	
	1	713.913818 ± 0.1	-
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	713.913757		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	713.913757 2		_
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value			•

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 3.12 (Repeat Count = 1)	Input Value		
Name Abellu Dec Aligned Cum Mt-Dec Llu Dec M 522	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-409.950012		
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0.300000012 625.460022		
	0.300000012		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32  AbsHwPos HwPosSource Cnt M u16	104		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	1527.19995		
AbsHwPos_RelHwPos_HwDeg_M_f32	1527.19995		
AbsHwPos SrlComHwPosStatus Cnt M u16	21993		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.30000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	nCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.300000012		
k_HwPosOutputLPFError_HwDeg_f32	466.399994		
k_I2CHwAuthority_Uls_f32	0.300000012		
k_MaxSensorlessAuthority_Uls_f32	0.119999997		
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994		
k_MinSensorlessAuthority_Uls_f32	0.300000012		
k_TurnsCntrAuthority_Uls_f32	0.300000012		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	110		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	371		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi		
toward Discharge Annalysis Programmer Collysis Collysis Controller		ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	-	
Name	target_Pim_EOLVehCntrOffset Actual Value	Expected Value	Resu
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995	<b>Expected Value</b> 1527.19995 ± 0.009	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0	Expected Value 1527.19995 ± 0.009 0 ± 0.004	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105	Expected Value 1527.19995 ± 0.009 0 ± 0.004 3378.58105 ± 0.1	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534	Expected Value 1527.19995 ± 0.009 0 ± 0.004 3378.58105 ± 0.1 65534	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1 0 1600	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004  1600 ± 0.1	
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004	Resu

1600

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

1600 ± 0.1



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	150.699997	150.699997 ± 0.0009	<b>✓</b>

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	-7306.20996		
AbsHwPos HandwheelAuthority Uls M f32	0.529999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	139.100006		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.529999971		
AbsHwPos_HwPosSource_Cnt_M_u16	9496		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	389.200012		
AbsHwPos_RelHwPos_HwDeg_M_f32	389.200012		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25521		
AbsHwPos SrlComHwPos HwDeg M f32	5617.33008		
AbsHwPos TargetHwAuthority Uls M f32	0.529999971		
NosHwPos TempHwPos HwDeg M f32	1817.34998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
	1		
\lambda SHwPos_VehCntrValid_Cnt_M_lgc		OntrOffeet GetErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehi	Sintonset_GetEnorstatus_Enorstatus	
kte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.519999981 347.600006		
_HwPosOutputLPFError_HwDeg_f32			
_I2CHwAuthority_UIs_f32	0.519999981		
MaxSensorlessAuthority_Uls_f32	0.610000014		
_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
_MinSensorlessAuthority_Uls_f32	0.519999981		
_TurnsCntrAuthority_Uls_f32	0		
_UseTurnsCntr_Cnt_lgc	1		
x_VehCntrOffValidLimit_HwDeg_f32 x_seet_Abel_lwDea_Reg_ DiagClatual.hvDeaReducedDeaf_Cat_leavelue	1565.30005		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.610000014		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1125.19995		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-862.400024		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6447		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4	B 1 1 1 1 1 0 1 1	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	target_AbsHwPos_Per2_DiagStatusHwPo		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	<del></del>	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	/_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
lame	Actual Value	Expected Value	Res
bsHwPos_AlignedHwPos_HwDeg_M_f32	389.200012	389.200012 ± 0.0009	
.bsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4173.37207	-4173.37207 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4173.37207	-4173.37207 ± 0.1	
	21045	21045	

21845

0

-7997.64014

21845

0 ± 0.004

-7997.64014 ± 0.1

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993	691.429993 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

lame	Input Value
	-1676.60999
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0.12999995
AbsHwPos_HandwheelAuthority_Uls_M_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	176
AbsHwPos_HwPosSource_Cnt_M_u16	
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_SENSORVALID -901.799988
NoshwPos_PievHandwheeirosiiion_nwDeg_iw_i32	-901.799988
ubsHwPos SrlComHwPosStatus Cnt M u16	22641
NbsHwPos SrlComHwPos HwDeg M f32	7861.0498
AbsHwPos TargetHwAuthority Uls M f32	0.12999995
AbsHwPos TempHwPos HwDeg M f32	260.899994
NbSHwPos VehCntrOffset HwDeg M f32	724.5
AbsHwPos VehCntrOfstLearn Cnt M lqc	1
AbsHwPos VehCntrValid Cnt M Igc	1
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
: HwPosAuthorityStep Uls f32	0.119999997
_HwPosOutputLPFError_HwDeg_f32	733.700012
: I2CHwAuthority Uls f32	0.119999997
MaxSensorlessAuthority Uls f32	0.20999993
MaxVehCntrOffDiff HwDeg f32	436.700012
MinSensorlessAuthority Uls f32	0.119999997
TurnsCntrAuthority Uls f32	0.119999997
UseTurnsCntr Cnt Igc	1
VehCntrOffValidLimit HwDeg f32	377.299988
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	321,200012
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	1
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	422.5
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1487
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus Error	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosRedu	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority UI	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_Hw	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cn	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UI	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwD	-



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	<b>~</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.15 (Repeat Count = 1)	·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2661.79004
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003
AbsHwPos_HwPosSource_Cnt_M_u16	232
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-941.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-941.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23145
AbsHwPos_SrlComHwPos_HwDeg_M_f32	972.909973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	941.599976
k_I2CHwAuthority_Uls_f32	0.189999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976
k_MinSensorlessAuthority_Uls_f32	0.189999998
k_TurnsCntrAuthority_UIs_f32	0.189999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	585.200012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001



Name	Input Value			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	461.899994			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	98			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2355			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~	
			1	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-900	-900 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024	935.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.189999998	0.189999998 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-900	-900 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-900	-900 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024	633.900024 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.16 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1958.08997
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	848.539978
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006
AbsHwPos_HwPosSource_Cnt_M_u16	192
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	126.699997
AbsHwPos_RelHwPos_HwDeg_M_f32	126.699997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22785
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.140000001
k_HwPosOutputLPFError_HwDeg_f32	793.099976
k_I2CHwAuthority_Uls_f32	0.140000001



Name	Input Value			
k_MaxSensorlessAuthority_Uls_f32	0.230000004			
k_MaxVehCntrOffDiff_HwDeg_f32	496.100006			
k_MinSensorlessAuthority_Uls_f32	0.140000001			
k_TurnsCntrAuthority_Uls_f32	0.140000001			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	436.700012	436.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1735			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosR	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	~	
=				

target_Inst_Ap_Absniwr os.rim_EOEvenOntrollset	target_Fini_EOEvenOntiOnset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	192	192	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024	784.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.150000006	0.150000006 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	301.100006	301.100006 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	192	192	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	269.209991	269.209991 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994	482.899994 ± 0.0009	<b>✓</b>

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 3.17 (Repeat Count = 1)		V
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001	
AbsHwPos_HwPosSource_Cnt_M_u16	296	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998	



	I		
Name AboliuDos PolitiuDos HivDos M #22	Input Value 1207.09998		
AbsHwPos_RelHwPos_HwDeg_M_f32 AbsHwPos SrlComHwPosStatus Cnt M u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos TargetHwAuthority Uls M f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_Uls_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	1		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_Uls_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	CIII_u06	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	Fyrnantad Value	Danulé
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004 622.700012 ± 0.1	_
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	622.700012	622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	JUMPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	622.700012	HWPOS_STATE_SENSORVALID 622.700012 ± 0.1	
ADSHWF05_FIEVHAIIUWIIEEIF0SIII0II_HWDeg_IVI_I32	022.700012	022.700012 I U.1	
AbelliuDes CrlComlliuDesCtatus Cat M v.46	24045	24045	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	2098.83008 0.360000014	2098.83008 ± 0.1 0.360000014 ± 0.004	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008 0.360000014 622.700012	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	2098.83008 0.360000014 622.700012 -1132.57996	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	2098.83008 0.360000014 622.700012 -1132.57996	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	2098.83008 0.360000014 622.700012 -1132.57996 0	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 0	0
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2098.83008 0.360000014 622.700012 -1132.57996 0 0	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 0 0 504	0
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2098.83008 0.360000014 622.700012 -1132.57996 0 0 504	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 0 504	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	2098.83008 0.360000014 622.700012 -1132.57996 0 0 504	2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 0 504 1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	2098.83008 0.360000014 622.700012 -1132.57996 0 0 504 1 1 0.360000014	$2098.83008 \pm 0.1$ $0.360000014 \pm 0.004$ $622.700012 \pm 0.1$ $-1132.57996 \pm 0.009$ 0 0 504 1 1 0.360000014 $\pm$ 0.004	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	2098.83008 0.360000014 622.700012 -1132.57996 0 0 504 1 1 0.360000014 622.700012	$2098.83008 \pm 0.1$ $0.360000014 \pm 0.004$ $622.700012 \pm 0.1$ $-1132.57996 \pm 0.009$ 0 0 504 1 1 0.360000014 $\pm$ 0.004 622.700012 $\pm$ 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2098.83008 0.360000014 622.700012 -1132.57996 0 0 504 1 1 0.360000014 622.700012 3	$2098.83008 \pm 0.1$ $0.360000014 \pm 0.004$ $622.700012 \pm 0.1$ $-1132.57996 \pm 0.009$ 0 0 504 1 1 0.360000014 $\pm$ 0.004 622.700012 $\pm$ 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	2098.83008 0.360000014 622.700012 -1132.57996 0 0 504 1 1 0.360000014 622.700012	$2098.83008 \pm 0.1$ $0.360000014 \pm 0.004$ $622.700012 \pm 0.1$ $-1132.57996 \pm 0.009$ 0 0 504 1 1 0.360000014 $\pm$ 0.004 622.700012 $\pm$ 0.1	

au				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Step 3.18 (Repeat Count = 1) Name	Input Value		
	•		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4209.93018		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.310000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1173.02002 0.310000002		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	320		
AbsHwPos_HwPosStoto_Cnt_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_ProvHendwhoolPosition_HwPog_M_f22	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-448.700012 -448.700012		
AbsHwPos_RelHwPos_HwDeg_M_f32	23937		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.310000002		
AbsHwPos_TempHwPos_HwDog_M_f32	8814.7002 8814.7002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc		atroffeet CetErrerCtetus ErrerCtetus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntronset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	1268.30005		
k_I2CHwAuthority_UIs_f32	0.20000003		
k_MaxSensorlessAuthority_Uls_f32	0.38999986		
k_MaxVehCntrOffDiff_HwDeg_f32	1060.40002		
k_MinSensorlessAuthority_UIs_f32	1		
k_TurnsCntrAuthority_UIs_f32	0.200000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	911.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-212.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.389999986		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	683		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	966.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3719		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0	- ID (0.11	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	<i>'</i>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	/ <b>-</b> -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-448.700012	-448.700012 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999	0.109999999 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3541.94092	3541.94092 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	320	320	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3541.94092	3541.94092 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002	8814.7002 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002	8814.7002 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)			
	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		1 0.109999999 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1		



Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	✓

Τ	Τ				
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 3.19 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	787.700012		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.119999997		
AbsHwPos_HwPosSource_Cnt_M_u16	168		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002		
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569		
AbsHwPos SrlComHwPos HwDeg M f32	7967.00977		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997		
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	atrOffeet GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	ili Oliset_GetEllorGtatus_EllorGtatus	
k_HwPosAuthorityStep_Uls_f32	0.109999999		
k_HwPosOutputLPFError_HwDeg_f32	704		
k_I2CHwAuthority_Uls_f32	0.109999999		
k MaxSensorlessAuthority Uls f32	0.200000003		
k MaxVehCntrOffDiff HwDeg f32	407		
- · · ·	0.109999999		
k_MinSensorlessAuthority_Uls_f32			
k_TurnsCntrAuthority_UIs_f32	0.109999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>
AbelliuDee HendubeelDesition DE M. etc. CV 111e 522	720 207092	700 207002 + 0.4	

729.307983

HWPOS\_STATE\_SENSORINVALID

AbsHwPos\_HwPosSource\_Cnt\_M\_u16 AbsHwPos\_HwPosState\_Cnt\_M\_enum

AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32

HWPOS\_STATE\_SENSORINVALID

729.307983 ± 0.1



Name	Actual Value	Expected Value	Result
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	<b>~</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Name Same	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2802.53003
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	970.219971
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.20999993
AbsHwPos_HwPosSource_Cnt_M_u16	240
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-921.5
AbsHwPos_RelHwPos_HwDeg_M_f32	-921.5
AbsHwPos SrlComHwPosStatus Cnt M u16	23217
AbsHwPos SrlComHwPos HwDeg M f32	1113.65002
bsHwPos TargetHwAuthority Uls M f32	0.209999993
AbsHwPos TempHwPos HwDeg M f32	7024.72998
AbsHwPos VehCntrOffset HwDeg M f32	966.099976
AbsHwPos VehCntrOfstLearn Cnt M Igc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
: HwPosAuthorityStep Uls f32	0.200000003
HwPosOutputLPFError HwDeg f32	971.299988
z_I2CHwAuthority_Uls_f32	0.20000003
MaxSensorlessAuthority Uls f32	0.289999992
MaxVehCntrOffDiff HwDeg f32	674.299988
MinSensorlessAuthority Uls f32	0.20000003
TurnsCntrAuthority Uls f32	0.200000003
UseTurnsCntr Cnt Igc	0
VehCntrOffValidLimit HwDeg f32	614.900024
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	900
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.289999992
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	482
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	104
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	664.099976
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2479
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt lgc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority Uls f32



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-921.5	-921.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	955.473755	955.473816 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	✓



Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_( target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	2 target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target AbsHwPos Per2 HwPosSource Cnt u16.value	3	3	<b>✓</b>
	-	-	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>~</b>

65535

0 ± 0.1

65535 ± 0.09

Test Step 3.22 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1676.60999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995
AbsHwPos_HwPosSource_Cnt_M_u16	176
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.119999997

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 



Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.209999993		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_Uls_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	2 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

target_r ini_Locvenontronset		
Actual Value	Expected Value	Result
-901.799988	-901.799988 ± 0.0009	~
0.119999997	0.119999997 ± 0.004	<b>✓</b>
-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
1	1	<b>✓</b>
HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>~</b>
-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
21845	21845	<b>~</b>
-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
0.119999997	0.119999997 ± 0.004	~
-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
724.5	724.5 ± 0.0009	~
0	0	<b>✓</b>
1	1	~
504	504	<b>✓</b>
1	1	~
1	1	<b>✓</b>
0.119999997	0.119999997 ± 0.004	~
-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
1	1	~
21845	21845	<b>✓</b>
-1600	-1600 ± 0.1	~
422.5	422.5 ± 0.0009	<b>~</b>
	Actual Value -901.799988 0.119999997 -1015.08685 1 HWPOS_STATE_SENSORVALID -1015.08685 21845 -2401.10986 0.119999997 -2401.10986 724.5 0 1 504 1 1 0.119999997 -1015.08685 1 21845 -1600	Actual Value         Expected Value           -901.799988         -901.799988 ± 0.0009           0.11999997         0.11999997 ± 0.004           -1015.08685         -1015.08691 ± 0.1           1         1           HWPOS_STATE_SENSORVALID         HWPOS_STATE_SENSORVALID           -1015.08685         -1015.08691 ± 0.1           21845         21845           -2401.10986         -2401.11011 ± 0.1           0.119999997         0.119999997 ± 0.004           -2401.10986         -2401.11011 ± 0.1           724.5         724.5 ± 0.0009           0         0           1         1           504         1           1         1           0.119999997         0.119999997 ± 0.004           -1015.08685         -1015.08691 ± 0.1           1         1           21845         -21845           -1600         -1600 ± 0.1

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 3.23 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001	
AbsHwPos_HwPosSource_Cnt_M_u16	296	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	



Name	Input Value		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998		
AbshwPos RelHwPos HwDeg M f32	1207.09998		
AbsHwPos SrlComHwPosStatus Cnt M u16	23721		
AbsHwPos SrlComHwPos HwDeq M f32	2098.83008		
AbsHwPos TargetHwAuthority Uls M f32	0.280000001		
AbsHwPos TempHwPos HwDeq M f32	8009.91016		
AbsHwPos VehCntrOffset HwDeg M f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_Uls_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	0.340000004		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_UIs_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100 875.5		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	3		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		ReducedPart Cnt Inc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthori		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	rus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>•</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>V</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	<b>V</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg_f32	1600 65535	1600 ± 0.1	
target_1 iii_2OEverionitionset.EOEverionitionset_Hwbeg_132	00000	65535 ± 0.09	



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007			
AbsHwPos HandwheelAuthority Uls M f32	0.170000002			
	889.099976			
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32				
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002			
AbsHwPos_HwPosSource_Cnt_M_u16	208			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5			
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002			
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	CntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
k HwPosAuthorityStep Uls f32	0.159999996			
k HwPosOutputLPFError HwDeg f32	852.5			
k_I2CHwAuthority_Uls_f32	0.159999996			
k_MaxSensorlessAuthority_Uls_f32	0.25			
k MaxVehCntrOffDiff HwDeg f32	555.5			
k MinSensorlessAuthority Uls f32	0.159999996			
k_TurnsCntrAuthority_UIs_f32	0.159999996			
k UseTurnsCntr Cnt Igc	1			
k VehCntrOffValidLimit HwDeg f32	496.100006			
	0			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value				
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Other and the property of the property $	target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	rity_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Ci	nt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	id_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthor			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	/ <del>-</del> -		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target AbsHwPos Per2 SrlComHwPos H			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	<del></del>		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	_0.11_430		
		Function Value	Post	
Name	Actual Value	Expected Value	Resu	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1		
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1		

21845

0

0.25

21845

0 ± 0.1

0.25 ± 0.004

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Stan 2 25 (Panest Count = 4)	
Test Step 3.25 (Repeat Count = 1)	la ava
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
K HwPosAuthorityStep Uls f32	0.159999996
HwPosOutputLPFError HwDeg f32	852.5
C I2CHwAuthority Uls f32	0.159999996
<pre>C_ISST Mathematical Control Contr</pre>	0.25
MaxVehCntrOffDiff HwDeg f32	555.5
MinSensorlessAuthority Uls f32	0.159999996
< TurnsCntrAuthority Uls f32	0.159999996
CTUTISCHITAUTIONY_DIS_132  UseTurnsCntr Cnt Igc	1
Cose rumschit_Chi_gc     VehCntrOffValidLimit HwDeg f32	496.100006
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
karget_nte_mat_np_nuarriwr va.nuarriwr va_reiz_aneuninwrva_nwbeg_i32	Langer_About WE 03_F 612_OHOOHII WE 03_HWD6Y_132



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>~</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	<b>~</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>~</b>

Test Step 3.26 (Repeat Count = 1)	<b>√</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.200000003
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.409999996
k_TurnsCntrAuthority_Uls_f32	0.109999999
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	347.600006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363



Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u	target_AbsHwPos_Per2_DiagStatusHwPosf	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	<b>✓</b>
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID	HWPOS STATE SENSORINVALID	~

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	722.072021	722.072021 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003	240.800003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999791	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	722.072021	722.072021 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	168	168	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 3.27 (Repeat Count = 1)	, and the second se
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.319999993
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.109999999
k_TurnsCntrAuthority_UIs_f32	0.109999999



Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_( target_AbsHwPos_Per2_DiagStatusHwF	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	32 target_AbsHwPos_Per2_HandwheelPos	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3.	2 target_AbsHwPos_Per2_SensorlessHwF	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	1567.40002	1567.40002 ± 0.009	
AbsHwPos HandwheelAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	729.307983	729.307983 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	3	3	<b>✓</b>
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	729.307983	729.307983 ± 0.1	<b>✓</b>
AbsHwPos SrlComHwPosStatus Cnt M u16	21845	21845	
AbsHwPos SrlComHwPos HwDeg M f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos TargetHwAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	301.100006	301.100006 ± 0.1	•
AbsHwPos VehCntrOffset HwDeg M f32	694.299988	694.299988 ± 0.0009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos VehCntrValid Cnt M Igc	0	0	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	_
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	-
5			

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

729.307983

301.100006

392.299988

21845

729.307983 ± 0.1

301.100006 ± 0.1

392.299988 ± 0.0009

3

21845

Test Step 3.28 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1600	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1600	
AbsHwPos_RelHwPos_HwDeg_M_f32	10125	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	

target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value



		(MAC)	COL
Name	Input Value		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCr	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	1		
k HwPosOutputLPFError HwDeg f32	1620		
k_I2CHwAuthority_UIs_f32	1		
k MaxSensorlessAuthority Uls f32	1		
k_MaxVehCntrOffDiff_HwDeg_f32	1600		
k MinSensorlessAuthority Uls f32	1		
k TurnsCntrAuthority Uls f32	1		
	1		
k_UseTurnsCntr_Cnt_lgc	1600		
k_VehCntrOffValidLimit_HwDeg_f32			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u	( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.09	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	
AbsHwPos VehCntrOfstLearn Cnt M lgc	1	1	•
AbsHwPos VehCntrValid Cnt M Igc	1	1	-
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	504	•
, , _ , _ , _ , , _ , , , , ,			

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>~</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>~</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	<b>~</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>~</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

✓
Input Value
7024.72998



			71040	
Name	Input Value			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.50999999			
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	114.300003			
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.50999999			
AbsHwPos_HwPosSource_Cnt_M_u16	480			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	320.100006			
AbsHwPos_RelHwPos_HwDeg_M_f32	320.100006			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25377			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5335.8501			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.50999999			
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVeh0	CatrOffeet CetErrorStatus ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	ChilOnset_GetErrorStatus_ErrorStatus		
k_HwPosAuthorityStep_Uls_f32	0.5			
k_HwPosOutputLPFError_HwDeg_f32	288.200012			
k_I2CHwAuthority_Uls_f32	1			
k MaxSensorlessAuthority Uls f32	0.589999974			
k MaxVehCntrOffDiff HwDeg f32	80.3000031			
k MinSensorlessAuthority Uls f32	0.5			
k TurnsCntrAuthority Uls f32	0.5			
k_UseTurnsCntr_Cnt_lgc	0.5			
k_VehCntrOffValidLimit_HwDeg_f32	1505.90002			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.589999974			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1085			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	28			
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-901.799988			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6199			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	lwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	320.100006	320.100006 ± 0.0009	•	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	465.843018	465.842987 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	465.843018	465.842987 ± 0.1	•	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	•	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	•	
AbsHwPos_TempHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	•	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012	-409.950012 ± 0.0009	•	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	•	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	465.843018	465.842987 ± 0.1	•	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	803.599976	803.599976 ± 0.1	•	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-901.799988	-901.799988 ± 0.0009	•	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.30 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002		
AbsHwPos_HwPosSource_Cnt_M_u16	208		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929		
.bsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
.bsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.159999996		
C_HwPosOutputLPFError_HwDeg_f32	852.5		
z_I2CHwAuthority_Uls_f32	0.159999996		
_MaxSensorlessAuthority_Uls_f32	0.25		
_MaxVehCntrOffDiff_HwDeg_f32	555.5		
_MinSensorlessAuthority_Uls_f32	0.159999996		
_TurnsCntrAuthority_Uls_f32	0.159999996		
_UseTurnsCntr_Cnt_lgc	1		
_VehCntrOffValidLimit_HwDeg_f32	496.100006		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	( target AbsHwPos Per2 DiagStatusHwPo	sReducedPerf Cnt lac	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	<del>-</del>	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target_AbsHwPos_Per2_SensorlessAutho		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPo		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosSta		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	00	
		Expected Value	Das
lame	Actual Value	Expected Value	Res
bsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	
lbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	
.bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
.bsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
		0 ± 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0		
bsHwPos_SrlComHwPos_HwDeg_M_f32 bsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	

0

65535

0 ± 0.1

65535 ± 0.09

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988	543.299988 ± 0.0009	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~