

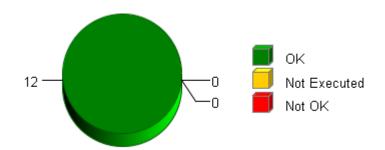
Summary

Overall Test Object Results (including Coverage)

Total Test Objects: 12

Successful: 12
Failed: 0
Not Executed: 0

Date: 2014-10-14 Time: 18:25:00+0530



Selected Project Items

Test Object "CBD UnitTest/DigColPs/ComputeRoughTurns"

Test Object "CBD_UnitTest/DigColPs/ConstrainOneRev"

Test Object "CBD_UnitTest/DigColPs/DiagnosticThreshold"

Test Object "CBD_UnitTest/DigColPs/DigColPs_Init1"

Test Object "CBD_UnitTest/DigColPs/DigColPs_Per1"

Test Object "CBD_UnitTest/DigColPs/DigColPs_Per2"

Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_CustClrTrim"

Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_CustSetTrim"

Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_NxtClrTrim"

Test Object "CBD_UnitTest/DigColPs/DigColPs_SCom_NxtSetTrim"

Test Object "CBD_UnitTest/DigColPs/OddParityFault"

Test Object "CBD_UnitTest/DigColPs/VernierLookup"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

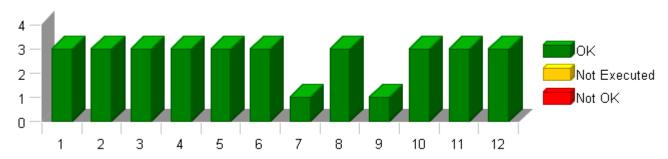
Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Modified Condition / Decision Coverage,

Multiple Condition Coverage

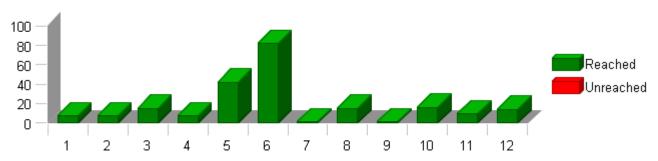


Test Case Results for Each Test Object (without Coverage)



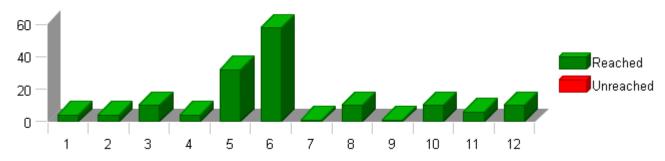
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

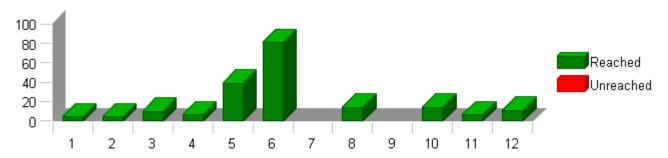
Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.



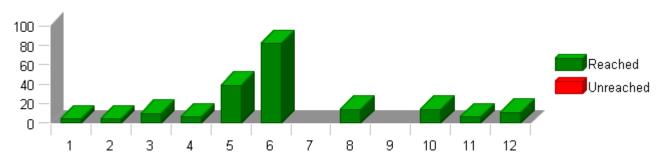
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	MC/DC	MCC	Test Cases Result
	DigColPs	100 %	100 %	100 %	100 %	32 of 32 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	32 of 32 passed
	DigColPs	100 %	100 %	100 %	100 %	32 of 32 passed
1	<u>ComputeRoughTurns</u>	100 %	100 %	100 %	100 %	3 of 3 passed
2	<u>ConstrainOneRev</u>	100 %	100 %	100 %	100 %	3 of 3 passed ✓
3	<u>DiagnosticThreshold</u>	100 %	100 %	100 %	100 %	3 of 3 passed ✓
4	DigColPs Init1	100 %	100 %	100 %	100 %	3 of 3 passed
5	DigColPs Per1	100 %	100 %	100 %	100 %	3 of 3 passed
6	<u>DigColPs Per2</u>	100 %	100 %	100 %	100 %	3 of 3 passed
7	DigColPs SCom CustClrTrim	100 %	100 %	-	-	1 of 1 passed
8	<u>DigColPs SCom CustSetTrim</u>	100 %	100 %	100 %	100 %	3 of 3 passed
9	DigColPs SCom NxtClrTrim	100 %	100 %	-	-	1 of 1 passed
10	DigColPs SCom NxtSetTrim	100 %	100 %	100 %	100 %	3 of 3 passed
11	<u>OddParityFault</u>	100 %	100 %	100 %	100 %	3 of 3 passed
12	<u>VernierLookup</u>	100 %	100 %	100 %	100 %	3 of 3 passed

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 Project
 DigColPs

 Module
 DigColPs

 Test Object
 ConstrainOneRev

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date: 10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested. Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code. Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac."

Attributes			
Name	Value		
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5		
Float Precision	9		



Attributes				
Name	Value			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl			
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2			
Time Unit	Cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 41.00 Cycles Longest Execution Path TS1.2 9.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path =>

(Input_Deg_T_f32 < D_ZERO_ULS_F32)=>TRUE"
TS1.2 "Shortest Execution Path =>
(Input_Deg_T_f32 > D_ONEREV_DEGREESPREV_F32)=>FALSE
(Input_Deg_T_f32 < D_ZERO_ULS_F32)=>FALSE"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Deg_T_f32	-1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	*

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
Input_Deg_T_f32	0		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	~

Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 41.00 Cycles TS2.2 42.00 Cycles TS2.3 9.00 Cycles TS2.4 16.00 Cycles TS2.5 21.00 Cycles

Description VECTOR DESCRIPTION:

TS2.1 Input_Deg_T_f32=Min TS2.2 Input_Deg_T_f32=Max TS2.3 Input_Deg_T_f32=Zero TS2.4 Input_Deg_T_f32=Pos TS2.5 Input_Deg_T_f32=Neg

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
Input_Deg_T_f32	-1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	0	0 ± 0.00048828125	~

Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
Input_Deg_T_f32	1800		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	360	360 ± 0.00048828125	~

Test Step 2.3 (Repeat Count = 1)				
Name	Input Value			
Input_Deg_T_f32	0			
Name	Actual Value	Expected Value	Result	
ConstrainOneRev()	0	0 ± 0.00048828125	~	



Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
Input_Deg_T_f32	800.5		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	80.5	80.5 ± 0.00048828125	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
Input_Deg_T_f32	-750.2		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	329.799988	329.8 ± 0.00048828125	~

Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 3.00 Cycles TS3.2 11.00 Cycles

Description

VECTOR DESCRIPTION:

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Test Step 3.1 (Repeat Count = 1)			~
Name	Input Value		
Input_Deg_T_f32	500		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	140	140 ± 0.00048828125	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
Input_Deg_T_f32	-500		
Name	Actual Value	Expected Value	Result
ConstrainOneRev()	220	220 ± 0.00048828125	~

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DiagnosticThreshold

 Project
 DigColPs

 Module
 DigColPs

 Test Object
 DiagnosticThreshold

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
indule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested. Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro "Redundant_Format_1_m" to imitate the source code. Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac_""

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2014-10-14, 18:04:58+0530



DiagnosticThreshold

Attributes		
Name	Value	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 19.00 Cycles Longest Execution Path TS1.2 8.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path => if (FaultPresent_Cnt_T_lgc == TRUE)=>True if (DiagFailed_m(*AccumulatorPtr_Cnt_T_u16, DiagSettings_Cnt_T_str) == TRUE)=>True" TS1.2 "Shortest Execution Path => if (FaultPresent_Cnt_T_lgc == TRUE)=>False"

Test Step 1.1 (Repeat Count = 1)			✓	
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	65535			
DiagSettings_Cnt_T_str.PStep	65535			
DiagSettings_Cnt_T_str.NStep	65535	65535		
FaultPresent_Cnt_T_lgc	1	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	1	1	✓	
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	✓	

Test Step 1.2 (Repeat Count = 1)			✓	
Name	Input Value			
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16			
DiagSettings_Cnt_T_str.Threshold	100			
DiagSettings_Cnt_T_str.PStep	48			
DiagSettings_Cnt_T_str.NStep	852	852		
FaultPresent_Cnt_T_lgc	0	0		
tgt_AccumulatorPtr_Cnt_T_u16	1			
Name	Actual Value	Expected Value	Result	
DiagnosticThreshold()	0	0	~	
tgt_AccumulatorPtr_Cnt_T_u16	0	0	✓	



Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

7.00 Cycles
19.00 Cycles
8.00 Cycles
20.00 Cycles
8.00 Cycles
19.00 Cycles
19.00 Cycles
8.00 Cycles
20.00 Cycles
8.00 Cycles
19.00 Cycles
20.00 Cycles
20.00 Cycles
20.00 Cycles
19.00 Cycles
20.00 Cycles
20.00 Cycles
20.00 Cycles
19.00 Cycles
20.00 Cycles
8.00 Cycles
19.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11 TS2.12 TS2.13 TS2.14 TS2.15 TS2.16

Description

VECTOR DESCRIPTION:

TS2.1 All Min

TS2.2 All Max
TS2.3 FaultPresent_Cnt_T_lgc=Min
TS2.4 FaultPresent_Cnt_T_lgc=Max
TS2.5 FacumulatorPtr_Cnt_T_u16=Max
TS2.5 AccumulatorPtr_Cnt_T_u16=Max
TS2.7 AccumulatorPtr_Cnt_T_u16=Pos
TS2.8 DiagSettings_Cnt_T_str.Threshold=Min
TS2.9 DiagSettings_Cnt_T_str.Threshold=Max
TS2.10 DiagSettings_Cnt_T_str.Threshold=Pos
TS2.11 DiagSettings_Cnt_T_str.Pstep=Min
TS2.12 DiagSettings_Cnt_T_str.Pstep=Max
TS2.13 DiagSettings_Cnt_T_str.Pstep=Pos
TS2.14 DiagSettings_Cnt_T_str.Nstep=Min
TS2.15 DiagSettings_Cnt_T_str.Nstep=Max
TS2.16 DiagSettings_Cnt_T_str.Nstep=Pos

T (0) 04/D (0)			
Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	0		
DiagSettings_Cnt_T_str.PStep	0		
DiagSettings_Cnt_T_str.NStep	0		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tot AccumulatorPtr Cnt T u16	0	0	•

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	✓
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	~

Test Step 2.3 (Repeat Count = 1)			~
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	100		
DiagSettings_Cnt_T_str.PStep	48		
DiagSettings_Cnt_T_str.NStep	852		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	1		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt AccumulatorPtr Cnt T u16	0	0	✓



Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	200		
DiagSettings_Cnt_T_str.PStep	82		
DiagSettings_Cnt_T_str.NStep	1020		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	24		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt AccumulatorPtr Cnt T u16	106	106	✓

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	300		
DiagSettings_Cnt_T_str.PStep	116		
DiagSettings_Cnt_T_str.NStep	1188		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	400		
DiagSettings_Cnt_T_str.PStep	150		
DiagSettings_Cnt_T_str.NStep	1356		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	400	400	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	500		
DiagSettings_Cnt_T_str.PStep	184		
DiagSettings_Cnt_T_str.NStep	1524		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	54		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tqt AccumulatorPtr Cnt T u16	0	0	✓

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16	i	
DiagSettings_Cnt_T_str.Threshold	0		
DiagSettings_Cnt_T_str.PStep	218		
DiagSettings_Cnt_T_str.NStep	1692		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	95		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	-
tgt AccumulatorPtr Cnt T u16	0	0	✓



DiagnosticThreshold

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	252		
DiagSettings_Cnt_T_str.NStep	1860		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	136		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1023		
DiagSettings_Cnt_T_str.PStep	286		
DiagSettings_Cnt_T_str.NStep	2028		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	177		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	463	463	✓

Test Step 2.11 (Repeat Count = 1)			· ·
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	1		
DiagSettings_Cnt_T_str.PStep	0		
DiagSettings_Cnt_T_str.NStep	2196		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	218		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	•
tgt_AccumulatorPtr_Cnt_T_u16	0	0	•

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	488		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	2364		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	259		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	488	488	•

Test Step 2.13 (Repeat Count = 1)		✓	
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	928		
DiagSettings_Cnt_T_str.PStep	1045		
DiagSettings_Cnt_T_str.NStep	2532		
FaultPresent_Cnt_T_lgc	0		
tgt_AccumulatorPtr_Cnt_T_u16	300		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	0	0	~
tgt_AccumulatorPtr_Cnt_T_u16	0	0	~





Test Step 2.14 (Repeat Count = 1)		✓
Name	Input Value	
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16	
DiagSettings_Cnt_T_str.Threshold	1368	
DiagSettings_Cnt_T_str.PStep	645	
DiagSettings_Cnt_T_str.NStep	0	
FaultPresent_Cnt_T_lgc	1	
tgt_AccumulatorPtr_Cnt_T_u16	341	
Name	Actual Value Expect	ted Value Result
DiagnosticThreshold()	0 0	✓
tgt_AccumulatorPtr_Cnt_T_u16	986 986	✓

Test Step 2.15 (Repeat Count = 1)		✓
Name	Input Value	
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16	
DiagSettings_Cnt_T_str.Threshold	1808	
DiagSettings_Cnt_T_str.PStep	152	
DiagSettings_Cnt_T_str.NStep	65535	
FaultPresent_Cnt_T_lgc	0	
tgt_AccumulatorPtr_Cnt_T_u16	382	
Name	Actual Value Expected Value	ue Result
DiagnosticThreshold()	0 0	✓
tgt_AccumulatorPtr_Cnt_T_u16	0	✓

Test Step 2.16 (Repeat Count = 1)		✓	
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	2248		
DiagSettings_Cnt_T_str.PStep	8524		
DiagSettings_Cnt_T_str.NStep	2046		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	423		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	2248	2248	✓

Test Case 3:	Path Test
Specification	Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

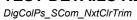
TS3.1 19.00 Cycles TS3.2 8.00 Cycles TS3.3 20.00 Cycles TS3.4 9.00 Cycles

VECTOR DESCRIPTION: Description

TS3.1 "if (FaultPresent_Cnt_T_lgc == TRUE)=>True if (DiagFailed_m(*AccumulatorPtr_Cnt_T_u16, DiagSettings_Cnt_T_str) == TRUE)=>True" TS3.2 if (FaultPresent_Cnt_T_lgc == TRUE)=>False TS3.3 "if (FaultPresent_Cnt_T_lgc == TRUE)=>True if (DiagFailed_m(*AccumulatorPtr_Cnt_T_u16, DiagSettings_Cnt_T_str) == TRUE)=>False" TS3.4 ((*AccumulatorPtr_Cnt_T_u16)>(DiagSettings_Cnt_T_str.NStep))=>TRUE

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
AccumulatorPtr_Cnt_T_u16	tgt_AccumulatorPtr_Cnt_T_u16		
DiagSettings_Cnt_T_str.Threshold	65535		
DiagSettings_Cnt_T_str.PStep	65535		
DiagSettings_Cnt_T_str.NStep	65535		
FaultPresent_Cnt_T_lgc	1		
tgt_AccumulatorPtr_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
DiagnosticThreshold()	1	1	~
tgt_AccumulatorPtr_Cnt_T_u16	65535	65535	✓

2014-10-14, 18:19:26+0530





Project DigColPs

Module DigColPs

Test Object DigColPs_SCom_NxtClrTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

lame	Text
Name Nodule 'DigColPs'	Text

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd

2014-10-14, 18:19:26+0530





Attributes	
Name	Value
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



DigColPs_SCom_NxtClrTrim

Description

Test Case 1: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 18.00 Cycles Vector Description:

TS1.1 Clear All The Trim Variables

Test Step 1.1 (Repeat Count = 1)			✓
Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~

OddParityFault

2014-10-14, 18:22:35+0530



Project	DigColPs
Module	DigColPs
Test Object	OddParityFault

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date: 10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested. Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code. Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2014-10-14, 18:22:35+0530

OddParityFault



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 168.00 Cycles Longest Execution Path TS1.2 10.00 Cycles Shortest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Longest Execution Path => while (Input_Cnt_T_u16 > 0U)=>TRUE if ((Input_Cnt_T_u16 & 1U)!= 0U)=>TRUE if ((Parity_Cnt_T_u08 & 1U) == 0U)=>TRUE" TS1.2 "Shortest Execution Path => while (Input_Cnt_T_u08 & 1U) == 0U)=>FALSE if ((Parity_Cnt_T_u08 & 1U) == 0U)=>TRUE"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 1.2 (Repeat Count = 1)			~
Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 10.00 Cycles TS2.2 168.00 Cycles TS2.3 58.00 Cycles

Description

VECTOR DESCRIPTION:

TS2.1 Input_Cnt_T_u16=Min TS2.2 Input_Cnt_T_u16=Max TS2.3 Input_Cnt_T_u16=Pos

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 2.3 (Repeat Count = 1)		~	
Name	Input Value		
Input_Cnt_T_u16	44		
Name	Actual Value	Expected Value	Result
OddParityFault()	0	0	~



Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 10.00 Cycles TS3.2 168.00 Cycles TS3.3 33.00 Cycles TS3.4 1.00 Cycles

VECTOR DESCRIPTION: Description

TS3.1 "while (Input_Cnt_T_u16 > 0U)=>FALSE if ((Parity_Cnt_T_u08 & 1U) == 0U)=>TRUE" TS3.2 "while (Input_Cnt_T_u16 > 0U)=>TRUE if ((Input_Cnt_T_u16 & 1U) != 0U)=>TRUE" TS3.3 if ((Input_Cnt_T_u16 & 1U) != 0U)=>FALSE TS3.4 if ((Parity_Cnt_T_u08 & 1U) != 0U)=>FALSE

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	0		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	✓

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
Input_Cnt_T_u16	65535		
Name	Actual Value	Expected Value	Result
OddParityFault()	1	1	~

Test Step 3.3 (Repeat Count = 1)		1
Name	Input Value	

2014-10-14, 18:16:51+0530



DigColPs_SCom_CustClrTrim

Project DigColPs
Module DigColPs

Test Object DigColPs_SCom_CustClrTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Name Module 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1 : Inline functions defined in GlobalMacro.h are not unit tested.
	Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code. Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.
	Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' macro

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>
Linker File	\$(PROJECTROOT)\UnitTestEnv\static build files\sys link.cmd

2014-10-14, 18:16:51+0530



DigColPs_SCom_CustClrTrim

Attributes	
Name	Value
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



DigColPs_SCom_CustClrTrim

Test Case 1: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

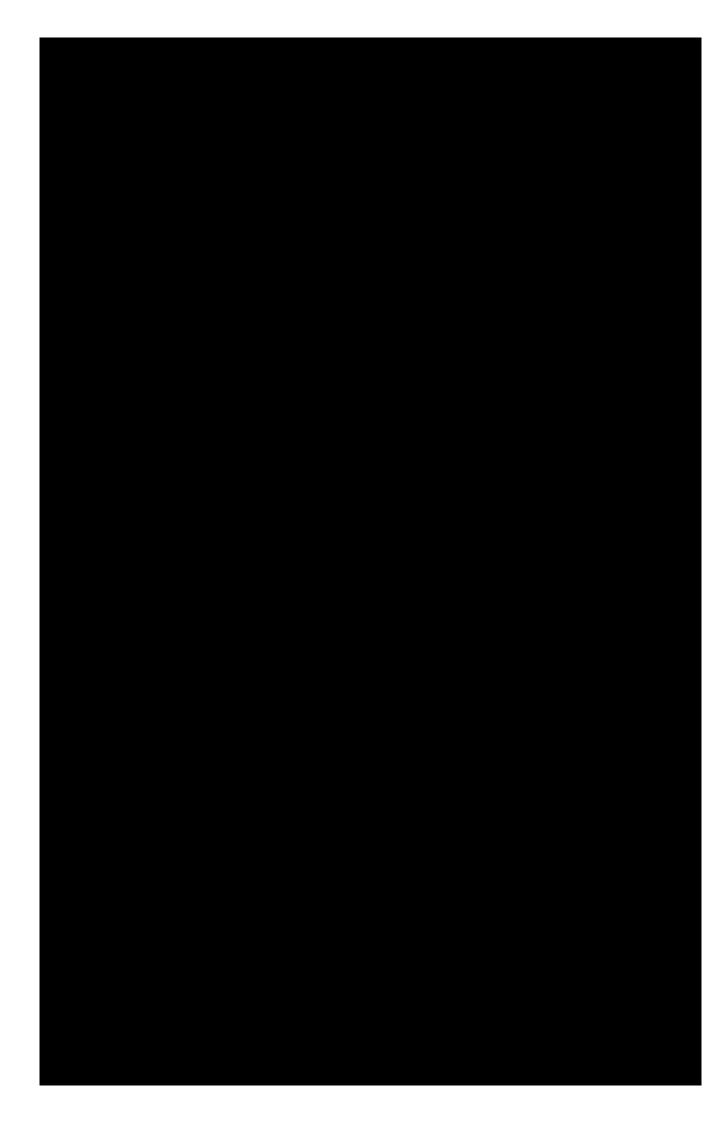
CPU Cycles:

TS1.1 515.00 Cycles

Description Vector Description:

TS1.1 Clear all the Trim variables

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		



2014-10-14, 18:16:06+0530

DigColPs_Per2



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



```
Test Case 1: Metrics Test
Specification
                                                              Performance Metrics:
(With "None" instrumentation and WithPS
Environment)
                                                                                           8951.00 Cycles Longest Execution Path 12143.00 Cycles Shortest Execution Path
                                                               TS1.2
Description
                                                              VECTOR DESCRIPTION:
                                                              TS1.1 "Longest Execution Path =>
                                                            TS1.1 "Longest Execution Path =>
if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08) &&
(I2CColSensorFault_Cnt_T_lgc == FALSE) &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE) &&
(TrimCompleteEOL_Cnt_T_lgc == TRUE) )=>True
if (HwAVernCorrFault_Cnt_T_lgc == FALSE)=>True
if (HwAVernCorrFault_Cnt_T_lgc == FALSE)=>True
                                                             (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) )=>False
if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(DigColPs_VernierAngleOORange_Cnt_M_lgc == TRUE) )=>False
if ((DigColPs_VernierAngleOORange_Cnt_M_u16 == 0U) && (DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 == 0U))=>False
if ((2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>False
if ((ColParityError_Cnt_T_lgc == TRUE) ||
(SpurParityError_Cnt_T_lgc == TRUE) ||
(I2CSensCommFlts_Cnt_T_u08 != 0U) )=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE) >>False
else if ((I2CColSensorFault_Cnt_T_lgc == TRUE) )=>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == TRUE) >>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) >>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) >>False
if ((ErrorDataReady_Cnt_T_lgc == TRUE) >>False
if (DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(I2CColSensorFaultAcc_Cnt_M_u16 == 0U)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
                                                             if ( (ErrorDataReady_Cnt_T_lgc == TRUE) && (ParityOrCommFault_Cnt_T_lgc == FALSE) && (12CSpurSensorFault_Cnt_T_lgc == TRUE) )=>False
                                                           (i2CSpurSensorFault_Cnt_T_igc == TRUE) )=>False
if (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U)=>True"
TS1.2 "Shortest Execution Path =>
if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>True
if ((12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08) &&
(i2CColSensorFault_Cnt_T_igc == FALSE) &&
(i2CSpurSensorFault_Cnt_T_igc == FALSE) &&
(i2CSpurSensorFault_Cnt_T_igc == FALSE) &&
(i2CSpurSensorFault_Cnt_T_igc == TRUE) )=>False
if (k_SelectFromColumn_Cnt_igc == TRUE) )=>False
if ((AbsVernDiagError_Deg_T_f32) *_VernCorrerrorThresh_Deg_f32) && (AngleDataAvailable_Cnt_T_igc == TRUE))=>False
if ((AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
(AngleDataAvailable_Cnt_T_igc == TRUE) &&
(DigColPs_PrevAngleDataAvailable_Cnt_M_igc == TRUE) )=>False
if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) ==
TRUE)=>>True
                                                           (SpurParityError_Cht_I_gc == IRUE) ||
(I2CSensCommFits_Cnt_T_u08 != 0U) )=>False
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == FALSE) &&
(I2CColSensorFault_Cnt_T_lgc == TRUE) =>>True
if ((ErrorDataReady_Cnt_T_lgc == TRUE) &&
(ParityOrCommFault_Cnt_T_lgc == TRUE) &&
(I2CSpurSensorFault_Cnt_T_lgc == TRUE) =>>True
```

Test Step 1.1 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	255	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_ColTrimStatic_Deg_M_f32	259.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	46069	
DigColPs_I2CHwColAngle_Deg_M_f32	360	

2014-10-14, 18:16:06+0530





Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	33.3
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	l'
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1]	
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5

2014-10-14, 18:16:06+0530



DigColPs_Per2

DigColPs_Per2	MACIL
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
Γ2_ColSpurVernierLUT_Cnt_s16[3][14]	7 4
F2_ColSpurVernierLUT_Cnt_s16[3][15]	17
<pre>F2_ColSpurVernierLUT_Cnt_s16[3][16] F2 DualSpurVernierLUT Cnt s16[0][0]</pre>	-396
Γ2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
F2_DualSpurVernierLUT_Cnt_s16[0][1]	-324
Γ2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
Γ2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
Γ2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
Γ2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
C2_DualSpurVernierLUT_Cnt_s16[0][11]	0
Γ2_DualSpurVernierLUT_Cnt_s16[0][12]	36
Γ2_DualSpurVernierLUT_Cnt_s16[0][13]	72
Γ2_DualSpurVernierLUT_Cnt_s16[0][14]	108
Γ2_DualSpurVernierLUT_Cnt_s16[0][15]	144
Γ2_DualSpurVernierLUT_Cnt_s16[0][16]	180
Γ2_DualSpurVernierLUT_Cnt_s16[0][17]	216
⁷ 2_DualSpurVernierLUT_Cnt_s16[0][18]	252
[2_DualSpurVernierLUT_Cnt_s16[0][19]	288
[2_DualSpurVernierLUT_Cnt_s16[0][20]	324
F2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
Γ2_DualSpurVernierLUT_Cnt_s16[1][4]	3
Γ2 DualSpurVernierLUT Cnt s16[1][5]	4
Γ2_DualSpurVernierLUT_Cnt_s16[1][6]	5
Γ2_DualSpurVernierLUT_Cnt_s16[1][7]	6
Γ2_DualSpurVernierLUT_Cnt_s16[1][8]	7
Γ2_DualSpurVernierLUT_Cnt_s16[1][9]	8
Γ2_DualSpurVernierLUT_Cnt_s16[1][10]	9
Γ2_DualSpurVernierLUT_Cnt_s16[1][11]	0
Γ2_DualSpurVernierLUT_Cnt_s16[1][12]	1
Γ2_DualSpurVernierLUT_Cnt_s16[1][13]	2
Γ2_DualSpurVernierLUT_Cnt_s16[1][14]	3
Γ2_DualSpurVernierLUT_Cnt_s16[1][15]	4
C2_DualSpurVernierLUT_Cnt_s16[1][16]	5
Γ2_DualSpurVernierLUT_Cnt_s16[1][17]	6
C2_DualSpurVernierLUT_Cnt_s16[1][18]	7
C2_DualSpurVernierLUT_Cnt_s16[1][19]	8
72_DualSpurVernierLUT_Cnt_s16[1][20]	9
[2_DualSpurVernierLUT_Cnt_s16[1][21]	0
[2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1 2
F2_DualSpurVernierLUT_Cnt_s16[2][2] F2_DualSpurVernierLUT_Cnt_s16[2][3]	3
2_DualSpurVernierLUT_Cnt_s16[2][3] 72_DualSpurVernierLUT_Cnt_s16[2][4]	4
2_DualSpurVernierLUT_Cnt_s16[2][4] 72_DualSpurVernierLUT_Cnt_s16[2][5]	5
2_DualSpurVernierLUT_Cnt_s16[2][6]	6
² _DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
Γ2_DualSpurVernierLUT_Cnt_s16[2][9]	9
Γ2_DualSpurVernierLUT_Cnt_s16[2][10]	10
Γ2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4

DigColPs_Per2

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7 8		
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	17 19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	55		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	3.54		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	1=	1_
Name	Actual Value	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	-
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371.030273	371.0302938 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs_PrevColPos_Deg_M_f32	360 5	360 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_ReqizCSnsrDataType_Cnt_M_uu8 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4	4	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	4	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-528.969727	-528.9697062 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	
NTC	0x6E	0x6E	
Param	0x00	0x00	
Status	0x00	0x00	
NTC	0x6F	0x6F	
Param	0x00	0x00	
Status	0x00	0x00	



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

lame	Input Value
igColPsInt_GetCustData()	414
ligColPs_ColParityError_Cnt_M_lgc	0
ligColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	360
)igColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
higColPs_I2CHwTrimTransCnts_Uls_M_u08	0
0igColPs_I2CSensCommFlts_Cnt_M_u08	0
higColPs_I2CSpurSensorFault_Cnt_M_lgc	1
higColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs SpurParityError Cnt M lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_Igc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
	-99
2_ColSpurVernierLUT_Cnt_s16[0][2]	
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
'2_ColSpurVernierLUT_Cnt_s16[1][12] '2_ColSpurVernierLUT_Cnt_s16[1][13]	2

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15] 0 T2_ColSpurVernierLUT_Cnt_s16[1][16] 4 T2_ColSpurVernierLUT_Cnt_s16[2][0] 0 T2_ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 2 T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 7 T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 8 T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2 ColSpurVernierLUT Cnt s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2 ColSpurVernierLUT Cnt s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 -324 T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 -252 T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2 DualSpurVernierLUT Cnt s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2 DualSpurVernierLUT Cnt s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2 DualSpurVernierLUT Cnt s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 2 T2_DualSpurVernierLUT_Cnt_s16[1][13]

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8

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2 DualSpurVernierLUT Cnt s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
	4		
T2_DualSpurVernierLUT_Cnt_s16[2][4]			
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
	22		
T2_DualSpurVernierLUT_Cnt_s16[3][0] T0_DualSpurVernierLUT_Cnt_s16[3][0]			
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cst_s16[3][21]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	100		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	102	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
Discolled 12Cl by Col Angle For Trim, Dog M 422	0	0 + 0 00040000405	

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

0 ± 0.00048828125

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	✓
Param	0x04	0x04	✓
Status	0x01	0x01	✓
NTC	0x6E	0x6E	~
Param	0x7F	0x7F	~
Status	0x01	0x01	~
NTC	0x6F	0x6F	✓
Param	0x7F	0x7F	~
Status	0x01	0x01	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

DigColPs_Per2

2014-10-14, 18:16:06+0530



Test Case 2: Boundary Test

2014-10-14, 18:16:06+0530



Specification

DigColPs_Per2

Performance Metrics: (With "None" instrumentation and WithPS Environment) CPU Cycles: 12132.00 Cycles 5985.00 Cycles 5849.00 Cycles 5935.00 Cycles TS2 1 TS2.2 TS2.3 TS2.4 5906.00 Cycles 5921.00 Cycles 2806.00 Cycles TS2.5 TS2.6 TS2.7 2658.00 Cvcles TS2.8 5843.00 Cycles 5773.00 Cycles 8997.00 Cycles TS2.9 TS2.10 TS2.11 2696.00 Cycles 8863.00 Cycles 8857.00 Cycles 8868.00 Cycles TS2.12 TS2.13 TS2.14 TS2.15 8856.00 Cycles 8849.00 Cycles 8854.00 Cycles 5822.00 Cycles TS2.16 TS2.17 TS2.18 TS2.19 5822.00 Cycles
12189.00 Cycles
12185.00 Cycles
5712.00 Cycles
3039.00 Cycles
3080.00 Cycles
2696.00 Cycles
8857.00 Cycles
8857.00 Cycles
8857.00 Cycles
8856.00 Cycles
8854.00 Cycles
8854.00 Cycles
8854.00 Cycles TS2.20 TS2.21 TS2.22 TS2.23 TS2.24 TS2.25 TS2.26 TS2.27 TS2.28 TS2.29 TS2.30 TS2.31 TS2.32 TS2.33 TS2.34 TS2.35 5822.00 Cycles 12189.00 Cycles 12185.00 Cycles 5712.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles 2696.00 Cycles 8863.00 Cycles 8857.00 Cycles 8856.00 Cycles 8856.00 Cycles TS2.36 TS2.37 TS2.38 TS2.39 TS2.40 TS2.41 TS2.42 8856.00 Cycles 8849.00 Cycles 8854.00 Cycles 5822.00 Cycles 12189.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles TS2.43 TS2.44 TS2.45 TS2.46 TS2.46 TS2.47 TS2.48 TS2.49 TS2.50 8951.00 Cycles 12143.00 Cycles 12132.00 Cycles 5985.00 Cycles 5945.00 Cycles 5935.00 Cycles 5906.00 Cycles 5921.00 Cycles TS2.52 TS2.53 TS2.54 TS2.55 TS2.56 TS2.57 TS2.58 5921.00 Cycles 2806.00 Cycles 2658.00 Cycles 5843.00 Cycles 5773.00 Cycles 2696.00 Cycles 8863.00 Cycles 8865.00 Cycles 8865.00 Cycles 8856.00 Cycles 8854.00 Cycles 8854.00 Cycles 8854.00 Cycles 2822.00 Cycles 12189.00 Cycles TS2.59 TS2.60 TS2.61 TS2.62 TS2.63 TS2.64 TS2.65 TS2.66 TS2.66 TS2.68 TS2.69 TS2.70 TS2.71 TS2.72 TS2.73 TS2.74 TS2.75 12189.00 Cycles 12185.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles 2696.00 Cycles 8863.00 Cycles TS2.76 TS2.77 TS2.78 8857.00 Cycles 8868.00 Cycles 8856.00 Cycles 8849.00 Cycles TS2.79 TS2.80 TS2.81 TS2.82 TS2.83 TS2.84 TS2.85 8854.00 Cycles 5822.00 Cycles 12189.00 Cycles 12185.00 Cycles TS2.86 TS2.87 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles TS2.88 TS2.89 3080.00 Cycles 2696.00 Cycles 8863.00 Cycles 8857.00 Cycles 8868.00 Cycles 8856.00 Cycles TS2.90 TS2.91 TS2.92 TS2.93 TS2.94 TS2.95 TS2.96 TS2.97 8849.00 Cycles 8854.00 Cycles 5822.00 Cycles 5822.00 Cycles 12189.00 Cycles 12185.00 Cycles 5712.00 Cycles 3039.00 Cycles 3080.00 Cycles 3039.00 Cycles 3080.00 Cycles 3039.00 Cycles 3080.00 Cycles TS2.98 TS2.99 TS2.100 TS2.101 TS2.102 TS2 103 TS2.103 TS2.104 TS2.105 TS2.106 3080.00 Cycles 5712.00 Cycles TS2 107

TS2.108 5712.00 Cyc

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2014-10-14, 18:16:06+0530

DigColPs_Per2



TS2.109 3039.00 Cycles TS2.110 3080.00 Cycles



DigColPs_Per2 Description

VECTOR DESCRIPTION:

TS2.1All Min TS2.2All Max TS2.3DigColPs_I2CColSensorFault_Cnt_M_lgc=Min TS2.4DigColPs_I2CColSensorFault_Cnt_M_lgc=Max TS2.5DigColPs_I2CSpurSensorFault_Cnt_M_lgc=Min TS2.6DigColPs_I2CSpurSensorFault_Cnt_M_lgc=Max TS2.7DigColPs_ColParityError_Cnt_M_lgc=Min TS2.8DigColPs_ColParityError_Cnt_M_lgc=Max TS2.9DigColPs_SpurParityError_Cnt_M_lgc=Min TS2.10DigColPs_SpurParityError_Cnt_M_lgc=Max TS2.11DigColPs_I2CHwColAngle_Cnt_M_u16=Min TS2.12DigColPs_I2CHwColAngle_Cnt_M_u16=Max TS2.13DigColPs_I2CHwColAngle_Cnt_M_u16=Pos TS2.14DigColPs_I2CHwSpurAngle_Cnt_M_u16=Min TS2.15DigColPs_I2CHwSpurAngle_Cnt_M_u16=Max TS2.16DigColPs_I2CHwSpurAngle_Cnt_M_u16=Max
TS2.16DigColPs_I2CHwSpurAngle_Cnt_M_u16=Pos
TS2.17DigColPs_I2CHwDataType_Cnt_M_u08=Min
TS2.18DigColPs_I2CHwDataType_Cnt_M_u08=Max
TS2.19DigColPs_I2CHwDataType_Cnt_M_u08=Pos
TS2.20DigColPs_I2CSensCommFlts_Cnt_M_u08=Min
TS2.21DigColPs_I2CSensCommFlts_Cnt_M_u08=Max TS2.22DigColPs_I2CSensCommFlts_Cnt_M_u08=Pos TS2.23DigColPs_I2CHwColAngleTrim_Deg_M_f32=Min TS2.24DigColPs_I2CHwColAngleTrim_Deg_M_f32=Max TS2.25DigColPs | I2CHwColAngleTrim Deg M f32=Pos TS2.26DigColPs | I2CHwColAngleTrim Deg M f32=Neg TS2.27DigColPs | I2CHwColAngleTrim Deg M f32=Zero TS2.28DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Min TS2.28DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Min TS2.29DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Max TS2.30DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Pos TS2.31DigColPs_I2CHwSpurAngleTrim_Deg_M_f32=Neg TS2.32DigCoIPs_I2CHwSpurAngleTrim_Deg_M_f32=Zero TS2.33Rte_Pim_DigCoIPsEOL.TrimComp_Cnt_u16=Min TS2.34Rte_Pim_DigCoIPsEOL.TrimComp_Cnt_u16=Max TS2.35Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Pos TS2.36k_SelectFromColumn_Cnt_lgc=Min TS2.37k_SelectFromColumn_Cnt_lgc=Max TS2.38k_VernCorrErrorThresh_Deg_f32=Min TS2.39k_VernCorrErrorThresh_Deg_f32=Max TS2.40k_VernCorrErrorThresh_Deg_f32=Pos TS2.41DigColPs_VernCorrDetectAcc_Cnt_M_u16=Min TS2.42DigColPs_VernCorrDetectAcc_Cnt_M_u16=Max TS2.43DigColPs_VernCorrDetectAcc_Cnt_M_u16=Pos TS2.44DigColPs_PrevVernierLevelNo_Cnt_M_u08=Min TS2.44DigColPs_PrevVernierLevelNo_Cnt_M_u08=Max
TS2.46DigColPs_PrevVernierLevelNo_Cnt_M_u08=Pos
TS2.47DigColPs_PrevVernierLevelNo_Cnt_M_u08=Pos
TS2.47DigColPs_PrevAngleDataAvailable_Cnt_M_lgc=Min
TS2.48DigColPs_PrevAngleDataAvailable_Cnt_M_lgc=Max
TS2.49DigColPs_SkipStepFltDetectAcc_Cnt_M_u16=Min
TS2.50DigColPs_SkipStepFltDetectAcc_Cnt_M_u16=Max TS2.51DigColPs_SkipStepFltDetectAcc_Cnt_M_u16=Pos TS2.52DigColPs_PrevColPos_Deg_M_f32=Min TS2.53DigColPs_PrevColPos_Deg_M_f32=Max TS2.54DigColPs_PrevColPos_Deg_M_f32=Pos TS2.55DigColPs_VernierAngleOORange_Cnt_M_lgc=Min TS2.56DigColPs_VernierAngleOORange_Cnt_M_lgc=Max TS2.57DigColPs_ColSensorFaultAcc_Cnt_M_u16=Min TS2.58DigColPs_ColSensorFaultAcc_Cnt_M_u16=Min TS2.58DigColPs_ColSensorFaultAcc_Cnt_M_u16=Pos TS2.60DigColPs_SpurSensorFaultAcc_Cnt_M_u16=Min TS2.61DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16=Max TS2.62DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16=Pos TS2.63k_VernCorrErrorDiag_Cnt_str.Pstep=Min TS2.64k_VernCorrErrorDiag_Cnt_str.Pstep=Max TS2.65k_VernCorrErrorDiag_Cnt_str.Pstep=Pos TS2.66k_VernCorrErrorDiag_Cnt_str.Nstep=Min TS2.67k_VernCorrErrorDiag_Cnt_str.Nstep=Max TS2.68k_VernCorrErrorDiag_Cnt_str.Nstep=Pos TS2.69k_VernCorrErrorDiag_Cnt_str.Threshold=Min TS2.70k_VernCorrErrorDiag_Cnt_str.Threshold=Max TS2.71k_VernCorrErrorDiag_Cnt_str.Threshold=pos TS2.72k_SkipStepErrDiag_Cnt_str.Threshold=Min TS2.73k_SkipStepErrDiag_Cnt_str.Threshold=Max TS2.73k_SkipStepErrDiag_Cnt_str.Threshold=Pos TS2.75k_SkipStepErrDiag_Cnt_str.Pstep=Min TS2.76k_SkipStepErrDiag_Cnt_str.Pstep=Max TS2.77k_SkipStepErrDiag_Cnt_str.Pstep=Pos TS2.78k_SkipStepErrDiag_Cnt_str.Nstep=Min TS2.76k_SkipStepErrDiag_Cnt_str.Nstep=Miax TS2.86k_SkipStepErrDiag_Cnt_str.Nstep=Max TS2.86k_SkipStepErrDiag_Cnt_str.Nstep=Pos TS2.81k_VernOORangeThresh_Deg_f32=Miax TS2.82k_VernOORangeThresh_Deg_f32=Max TS2.83k_VernOORangeThresh_Deg_f32=Pos TS2.84MecState_Cnt_enum=>ProductionMode TS2.85MecState_Cnt_enum=>ManufacturingMode TS2.86MecState_Cnt_enum=>EngineeringMode
TS2.87DigCoIPs_CoITrimStatic_Deg_M_f32=>Min
TS2.88DigCoIPs_CoITrimStatic_Deg_M_f32=>Max TS2.89DigColPs_ColTrimStatic_Deg_M_f32=>Pos TS2.90DigColPs_ColTrimStatic_Deg_M_f32=>Neg TS2.91DigColPs_ColTrimStatic_Deg_M_f32=>Zero TS2.92DigColPs_SpurTrimStatic_Deg_M_f32=>Min TS2.93DigColPs_SpurTrimStatic_Deg_M_f32=>Max TS2.94DigColPs_SpurTrimStatic_Deg_M_f32=>Pos



TS2.95DigColPs_SpurTrimStatic_Deg_M_f32=>Neg
TS2.96DigColPs_SpurTrimStatic_Deg_M_f32=>Zero
TS2.97DigColPs_TrimCompStatic_Cnt_M_u16=>Min
TS2.98DigColPs_TrimCompStatic_Cnt_M_u16=>Max
TS2.99DigColPs_TrimCompStatic_Cnt_M_u16=>Pos
TS2.10DDigColPs_I2CHwColAngle_Deg_M_f32=>Min
TS2.101DigColPs_I2CHwColAngle_Deg_M_f32=>Max
TS2.102DigColPs_I2CHwColAngle_Deg_M_f32=>Pos
TS2.103DigColPs_I2CHwSpurAngle_Deg_M_f32=>Min
TS2.104DigColPs_I2CHwSpurAngle_Deg_M_f32=>Min
TS2.104DigColPs_I2CHwSpurAngle_Deg_M_f32=>Pos
TS2.105DigColPs_I2CHwSpurAngle_Deg_M_f32=>Pos
TS2.105DigColPs_I2CHwSpurAngle_Deg_M_f32=>Pos
TS2.105DigColPs_HwAVernCorrFault_Cnt_M_lgc=>Min
TS2.107DigColPs_HwAVernCorrFault_Cnt_M_lgc=>Max
TS2.108DigColPs_I2CHwTrimTransCnts_Uls_M_u08=>Min
TS2.109DigColPs_I2CHwTrimTransCnts_Uls_M_u08=>Pos

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs ColSensorFaultAcc Cnt M u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs I2CHwColAngle Deg M f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs VernCorrDetectAcc Cnt M u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4

DigColPs_Per2

2014-10-14, 18:16:06+0530



DigCoiPs_Per2		
Name	Input Value	
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0	
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8	
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6	
T2 ColSpurVernierLUT Cnt s16[2][3]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9	
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5	
T2_ColSpurVernierEUT_Cnt_s16[2][9]	3	
T2 ColSpurVernierLUT Cnt s16[2][10]	1	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10	
	8	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	6	
T2_ColSpurVernierLUT_Cnt_s16[2][13]		
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2	
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11	
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8	
Γ2_ColSpurVernierLUT_Cnt_s16[3][4]	5	
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15	
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12	
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16	
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13	
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4	
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17	
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396	
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360	
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324	
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288	
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252	
	-216	
T2_DualSpurVernierLUT_Cnt_s16[0][5]		
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72	
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
Γ2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
Γ2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
Γ2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
Γ2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
Γ2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
Γ2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
Γ2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
Γ2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7	
	8	
T2_DualSpurVernierLUT_Cnt_s16[1][9]		
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	

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16

2014-10-14, 18:16:06+0530



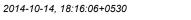


Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep	0		
k VernCorrErrorDiag Cnt str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C tgt_Pim_DigColPsEOL	nt_igc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	Result
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	
DigColPs_I2CHwColAnglet 011111_beg_w_132 DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs PrevColPos Deg M f32	0	0 ± 0.0001220703125	

0

DigColPs_PrevColPos_Deg_M_f32

0 ± 0.0001220703125





Name	Actual Value	Expected Value	Result
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	~
NTC	0x6E	0x6E	~
Param	0x00	0x00	~
Status	0x00	0x00	~
NTC	0x6F	0x6F	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt GetCustData()	511
DigColPs ColParityError Cnt M Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs ColTrimStatic Deg M f32	360
DigColPs HwAVernCorrFault Cnt M Igc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs I2CHwColAngle Deg M f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229

2014-10-14, 18:16:06+0530



Name	Input Value	
	· ·	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261 294	
T2_ColSpurVernierLUT_Cnt_s16[0][14]		
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359	
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4	
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3	
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2	
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4	
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3	
T2 ColSpurVernierLUT Cnt s16[1][8]	2	
Γ2_ColSpurVernierLUT_Cnt_s16[1][9]	1	
Γ2_ColSpurVernierLUT_Cnt_s16[1][10]	0	
Γ2_ColSpurVernierLUT_Cnt_s16[1][11]	4	
Γ2_ColSpurVernierLUT_Cnt_s16[1][12]	3	
	2	
Γ2_ColSpurVernierLUT_Cnt_s16[1][13]		
[2_ColSpurVernierLUT_Cnt_s16[1][14]	1	
[2_ColSpurVernierLUT_Cnt_s16[1][15]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4	
Γ2_ColSpurVernierLUT_Cnt_s16[2][0]	0	
Γ2_ColSpurVernierLUT_Cnt_s16[2][1]	8	
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2	
Γ2_ColSpurVernierLUT_Cnt_s16[2][5]	0	
Γ2_ColSpurVernierLUT_Cnt_s16[2][6]	9	
Γ2_ColSpurVernierLUT_Cnt_s16[2][7]	7	
Γ2_ColSpurVernierLUT_Cnt_s16[2][8]	5	
Γ2_ColSpurVernierLUT_Cnt_s16[2][9]	3	
	1	
Γ2_ColSpurVernierLUT_Cnt_s16[2][10]		
Γ2_ColSpurVernierLUT_Cnt_s16[2][11]	10	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8	
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6	
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2	
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11	
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8	
T2 ColSpurVernierLUT Cnt s16[3][4]	5	
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2	
Γ2_ColSpurVernierLUT_Cnt_s16[3][6]	15	
Γ2_ColSpurVernierLUT_Cnt_s16[3][7]	12	
	9	
Γ2_ColSpurVernierLUT_Cnt_s16[3][8]		
[2_ColSpurVernierLUT_Cnt_s16[3][9]	6	
Γ2_ColSpurVernierLUT_Cnt_s16[3][10]	3	
Γ2_ColSpurVernierLUT_Cnt_s16[3][11]	16	
[2_ColSpurVernierLUT_Cnt_s16[3][12]	13	
C2_ColSpurVernierLUT_Cnt_s16[3][13]	10	
Γ2_ColSpurVernierLUT_Cnt_s16[3][14]	7	
[2_ColSpurVernierLUT_Cnt_s16[3][15]	4	
[2_ColSpurVernierLUT_Cnt_s16[3][16]	17	
Γ2_DualSpurVernierLUT_Cnt_s16[0][0]	-396	
Γ2_DualSpurVernierLUT_Cnt_s16[0][1]	-360	
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324	
² _DualSpurVernierLUT_Cnt_s16[0][3]	-288	
2_DualSpurVernierLUT_Cnt_s16[0][3]	-252	
	-252 -216	
C2_DualSpurVernierLUT_Cnt_s16[0][5]		
72_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
[2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
Γ2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72	
Γ2_DualSpurVernierLUT_Cnt_s16[0][10]	-36	
2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
Γ2_DualSpurVernierLUT_Cnt_s16[0][13]	72	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
	180	
T2_DualSpurVernierLUT_Cnt_s16[0][16]		
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17] T3_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19] T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cat_s48[2][12]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9] T2_DualSpurVernierLUT_Cnt_s16[3][10]	18 20
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	255
k_SkipStepErrDiag_Cnt_str.PStep	50

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value k_SkipStepErrDiag_Cnt_str.NStep 50 $k_VernCorrErrorDiag_Cnt_str.Threshold$ 100 k_VernCorrErrorDiag_Cnt_str.PStep 50 k_VernCorrErrorDiag_Cnt_str.NStep 50 k_VernCorrErrorThresh_Deg_f32 100 k_VernOORangeThresh_Deg_f32 1800 tgt_DigColPs_Per2_MecState_Cnt_enum.value 2 tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 360 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 360 4488 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt Rte Inst Sa DigColPs.Pim DigColPsEOL tgt Pim DigColPsEOL

tgt_Rte_Inst_3a_bigColes.Filit_bigColesEOL	tgt_Filli_bigColFSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	•

Test Step 2.3 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4





1 tgt_Rte_Inst_Sa_DigColPs -163 -131 -99 -66 -33 0
-163 -131 -99 -66 -33
-131 -99 -66 -33
-99 -66 -33
-66 -33
-33
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98
130
163
196
229
261
294
327
359
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-396
-360
-324

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2 DualSpurVernierLUT Cnt s16[1][4]	3
T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2 DualSpurVernierLUT Cnt s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
566.566.466.0016.10[2][7]	•





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.14		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_H	wDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_en	ium	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DisColDo Lluch\/ornCorrEquit Cat M Iso	4	4	

tgt_rtte_inst_oa_bigooii 3.1 iiii_bigooii 3EOE	tgt_i iiii_bigooii 3LOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x04	0x04	~
Status	0x01	0x01	✓

T ·				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.4 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	105	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50	
DigColPs_ColTrimStatic_Deg_M_f32	14.8	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	24432	
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406	

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs I2CHwTrimTransCnts Uls M u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	105
	6.9
DigColPs_SpurTrimStatic_Deg_M_f32	
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	4
T2_ColSpurVernierLUT_Cnt_s16[1][6]	3
T2_ColSpurVernierLUT_Cnt_s16[1][7]	
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T3_ColSpurVernierLUT_Cnt_s16[2][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2 ColCourt/conjort LIT Cot e40(3)(4)	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T3_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11

2014-10-14, 18:16:06+0530



		1 1000
Name	Input Value	
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15	
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12	
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16	
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13	
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4	
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17	
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396	
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360	
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324	
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288	
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216	
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72	
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	0 1	

2014-10-14, 18:16:06+0530



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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2 DualSpurVernierLUT Cnt s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2 DualSpurVernierLUT Cnt s16[3][4]	8		
T2 DualSpurVernierLUT Cnt s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2 DualSpurVernierLUT Cnt s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2 DualSpurVernierLUT Cnt s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.57		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt Pim DigColPsEOL.ColTrim Deg f32	274.3637406		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid Cnt loc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt DigColPs Per2 TrimComp Cn		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt Pim DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	, , , ,
DigColPs I2CHwColAngleForTrim Deg M f32	981.818176	981.8181818 ± 0.00048828125	
DigColPs I2CHwTrimTransCnts Uls M u08	0	0	
DigColPs PrevAngleDataAvailable Cnt M Iqc	0	0	
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	10	10	
DigColPs Regl2CSnsrDataType Cnt M u08	1	1	
DigColPs SkipStepFltDetectAcc Cnt M u16	2	2	
DigColPa VernCorrDetectAce Cnt M u16	2	2	

Name	Actual value	Expected value	Kesuit
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	

T ✓				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~



Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs ColSensorFaultAcc Cnt M u16	101
DigColPs_ColTrimStatic_Deg_M_f32	25
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.4548138
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	80
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2 20
DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	80
DigColPs_TrimCompStatic_Cnt_M_u16	196
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2] T3_ColSpurVernierLUT_Cnt_s16[0][2]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][3] T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[1][0]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T0_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T3_ColSpurVernierLUT_Cnt_s16[2][40]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10
12_00/0pui voimoi 201_0m_310[2][11]	iv .





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 9 T2_DualSpurVernierLUT_Cnt_s16[2][20] T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2 DualSpurVernierLUT Cnt s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2 DualSpurVernierLUT Cnt s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2 DualSpurVernierLUT Cnt s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k SelectFromColumn Cnt lgc 0 k_SkipStepErrDiag_Cnt_str.Threshold 214 38 k_SkipStepErrDiag_Cnt_str.PStep k_SkipStepErrDiag_Cnt_str.NStep 23 66 k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep 39 k_VernCorrErrorDiag_Cnt_str.NStep 90 55352902 $k_VernCorrErrorThresh_Deg_f32$ k_VernOORangeThresh_Deg_f32 803.11 $tgt_DigColPs_Per2_MecState_Cnt_enum.value$ tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 226.4548138 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 143 9507322 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL Name **Actual Value Expected Value** Result ${\tt DigColPs_HwAVernCorrFault_Cnt_M_lgc}$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 1443.65869 $1443.658758 \pm 0.00048828125$ DigColPs_I2CHwTrimTransCnts_Uls_M_u08 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 1440 ± 0.0001220703125 ${\tt DigColPs_PrevColPos_Deg_M_f32}$ 1440 DigColPs_PrevVernierLevelNo_Cnt_M_u08 14 14 DigColPs Reql2CSnsrDataType Cnt M u08 1 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs VernCorrDetectAcc Cnt M u16 1 1 DigColPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value 0 0

543.658691

0x6C

0x0C

0x01

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tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt DigColPs Per2 TrimComp Cnt Igc.value

NTC Param

Status

543.6587581 ± 0.0009

0x6C

0x0C

0x01



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	-

Test Step 2.6 (Repeat Count = 1)	م
	Invest Value
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	9.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	232
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T3_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T3_ColSpurVernierLUT_Cnt_s16[1][11]	
T2_ColSpurVernierLUT_Cnt_s16[1][12] T3_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	4

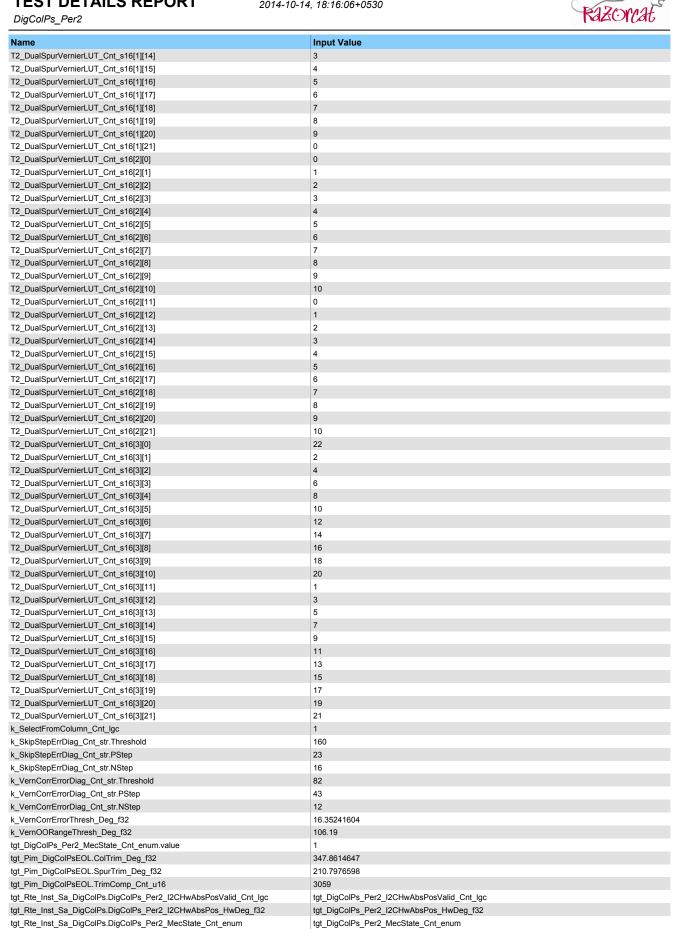




Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	7 5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2 DualSpurVernierLUT Cnt s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18] T3_DualSpurVernierLUT_Cnt_s16[0][18]	252 288
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	324 360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

2014-10-14, 18:16:06+0530





Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	~

tgt_Pim_DigColPsEOL

tgt_DigColPs_Per2_TrimComp_Cnt_lgc

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL

 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	312.661438	312.6614647 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-587.338562	-587.3385353 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

- 101 0-17	
Test Step 2.7 (Repeat Count = 1)	·
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs_ColTrimStatic_Deg_M_f32	45.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	14286
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921
DigColPs_I2CHwSpurAngle_Deg_M_f32	10.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	814.3879313
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	10.2
DigColPs_TrimCompStatic_Cnt_M_u16	268
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
	12

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360





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2014-10-14, 18:16:06+0530



Name	Input Value
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	298.7894
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt DigColPs Per2 MecState Cnt enum

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T3_ColSpurVernierLUT_Cnt_s46[4][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][0]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s46[3][40]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
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2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	

2014-10-14, 18:16:06+0530



DigColPs_Per2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	191		
k_SkipStepErrDiag_Cnt_str.PStep	16		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	24		
k_VernCorrErrorDiag_Cnt_str.PStep	21		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	67.6606307		
k_VernOORangeThresh_Deg_f32	664.42		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	199.9994296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	301.9312882		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2922		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cr	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C	:nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	504.399445	504.3994296 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-395.600555	-395.6005704 ± 0.0009	~
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Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	✓

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Test Step 2.9 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	149	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103	
DigColPs_ColTrimStatic_Deg_M_f32	65.8	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	15468	
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410	
DigColPs_I2CHwSpurAngle_Deg_M_f32	12.4	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	23	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	569.7636028	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20	
DigColPs_SpurParityError_Cnt_M_lgc	0	

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value DigColPs_SpurSensorFaultAcc_Cnt_M_u16 149 DigColPs_SpurTrimStatic_Deg_M_f32 12.4 340 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 19 ${\tt DigColPs_VernierAngleOORange_Cnt_M_lgc}$ Rte_Inst_Sa_DigColPs tgt_Rte_Inst_Sa_DigColPs T2_ColSpurVernierLUT_Cnt_s16[0][0] -163 T2_ColSpurVernierLUT_Cnt_s16[0][1] -131 T2_ColSpurVernierLUT_Cnt_s16[0][2] -99 T2_ColSpurVernierLUT_Cnt_s16[0][3] -66 T2_ColSpurVernierLUT_Cnt_s16[0][4] -33 T2_ColSpurVernierLUT_Cnt_s16[0][5] 0 T2_ColSpurVernierLUT_Cnt_s16[0][6] 32 T2 ColSpurVernierLUT Cnt s16[0][7] 65 T2_ColSpurVernierLUT_Cnt_s16[0][8] 98 T2_ColSpurVernierLUT_Cnt_s16[0][9] 130 T2_ColSpurVernierLUT_Cnt_s16[0][10] 163 T2_ColSpurVernierLUT_Cnt_s16[0][11] 196 T2_ColSpurVernierLUT_Cnt_s16[0][12] 229 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][7] 3 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2 ColSpurVernierLUT Cnt s16[1][9] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2 ColSpurVernierLUT Cnt s16[1][12] 3 T2_ColSpurVernierLUT_Cnt_s16[1][13] 2 T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15] 0 T2_ColSpurVernierLUT_Cnt_s16[1][16] 4 T2_ColSpurVernierLUT_Cnt_s16[2][0] 0 T2 ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 9 T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2 ColSpurVernierLUT Cnt s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2 ColSpurVernierLUT Cnt s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15] 4

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_S10[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	35		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.55		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPe	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPe	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M lgc	1	1	•

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	153.27533	153.2753346 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-746.72467	-746.7246654 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	~
Status	0x01	0x01	✓

au				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	76





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08 DigColPs_I2CHwSpurAngle_Cnt_M_u16	1 53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	13.5
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0
DigColPs I2CSensCommFlts Cnt M u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	13.5 376
DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs VernierAngleOORange Cnt M Igc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11]	163 196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3] T3_ColSpurVernierLUT_Cnt_s16[2][4]	4 2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2 DualSpurVernierLUT Cnt s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[2][4]	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

2014-10-14, 18:16:06+0530



DigColPs_Per2

		(0)	1000
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2 DualSpurVernierLUT Cnt s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.16		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_F	HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_e	num	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs HwAVernCorrFault Cnt M lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1065.17773	1065.177819 ± 0.00048828125	
DigColPs 12CHwTrimTransCnts Uls M u08	0	0	
DigColPs PrevAngleDataAvailable Cnt M Igc	1	1	
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	
DigColPs Reql2CSnsrDataType Cnt M u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	
D: 0 ID 1/4 0 D 1 1/4 0 1 1/4 10			

0

165.177734

165.177819 ± 0.0009

DigColPs_VernCorrDetectAcc_Cnt_M_u16

DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_l2CHwAbsPosValid_Cnt_lgc.value tgt_DigColPs_Per2_l2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.11 (Repeat Count = 1)	
	Input Value
Name	Input Value
DigColPsInt_GetCustData()	120
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	86.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	325.6206695
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	157.2728202
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	14.6
DigColPs_TrimCompStatic_Cnt_M_u16	412
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSputVernierLUT_Cnt_s10[0][12] T2 ColSputVernierLUT Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	359
T2_ColSpurVernierLUT_Cnt_s16[0][16]	
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpur/ernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	99		
k_SkipStepErrDiag_Cnt_str.PStep k_SkipStepErrDiag_Cnt_str.NStep	13		
k_VernCorrErrorDiag_Cnt_str.Threshold	74		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k VernCorrErrorDiag Cnt str.NStep	6		
k VernCorrErrorThresh Deg f32	78.75594592		
k_VernOORangeThresh_Deg_f32	1151.77		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~

1309.09082

0

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

 ${\tt DigColPs_I2CHwTrimTransCnts_Uls_M_u08}$

1309.090909 ± 0.00048828125

0





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1319.42065	1319.42067 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	419.420654	419.4206695 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T ·				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
bigColPs ColSensorFaultAcc Cnt M u16	175
DigColPs ColTrimStatic Deg M f32	96.4
bigColPs HwAVernCorrFault Cnt M lgc	0
bigColPs I2CColSensorFault Cnt M Igc	1
bigColPs_I2CHwColAngle_Cnt_M_u16	65535
ligColPs_I2CHwColAngle_Cit_M_t12	115.010748
ligColPs_I2CHwDataType_Cnt_M_u08	4
igColPs_I2CHwSpurAngle_Cnt_M_u16	7129
igColPs I2CHwSpurAngle Deg M f32	15.7
igColPs I2CHwTrimTransCnts UIs M u08	2
igColPs I2CSensCommFlts Cnt M u08	6
	0
igColPs_I2CSpurSensorFault_Cnt_M_lgc	0
ligColPs_PrevAngleDataAvailable_Cnt_M_lgc	1464.024646
igColPs_PrevColPos_Deg_M_f32	
igColPs_PrevVernierLevelNo_Cnt_M_u08	9
higColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
igColPs_SpurParityError_Cnt_M_lgc	0
igColPs_SpurSensorFaultAcc_Cnt_M_u16	127
bigColPs_SpurTrimStatic_Deg_M_f32	15.7
DigColPs_TrimCompStatic_Cnt_M_u16	448
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
ligColPs_VernierAngleOORange_Cnt_M_lgc	1
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T3_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][5]	6
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2 DualSpurVernierLUT Cnt s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21] T2_DualSpurVernierLUT_Cnt_s16[3][0]	10 22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	13 15
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	70
k_SkipStepErrDiag_Cnt_str.PStep	47
k_SkipStepErrDiag_Cnt_str.NStep	44
k_VernCorrErrorDiag_Cnt_str.Threshold	88
k_VernCorrErrorDiag_Cnt_str.PStep	0
k_VernCorrErrorDiag_Cnt_str.NStep	38
k_VernCorrErrorThresh_Deg_f32	78.63725519
k_VernOORangeThresh_Deg_f32	1720.3
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	115.010748

DigColPs_Per2





Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1458.61072	1458.610748 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.13 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	124	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185	
DigColPs_ColTrimStatic_Deg_M_f32	106.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	25526	
DigColPs_I2CHwColAngle_Deg_M_f32	216.7759984	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	674	
DigColPs_I2CHwSpurAngle_Deg_M_f32	16.8	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	840.5093411	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124	
DigColPs_SpurTrimStatic_Deg_M_f32	16.8	
DigColPs_TrimCompStatic_Cnt_M_u16	484	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131	
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0	

2014-10-14, 18:16:06+0530



32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
2 1 0 4 3 2 1 0 4 3
1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 2 1 0 4 3 2
4 3 2 1 0 4 3
3 2 1 0 4 3
2 1 0 4 3 2
1 0 4 3 2
0 4 3 2
4 3 2
3 2
2
1
0
4
0
8
6
4
2
0
9
7
5
3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
4
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36





Input Value
0
36
72
108
144
180
216
252
288
324
360 9
0
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14 16
16
18 20
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3 5
3 5 7
3 5

DigColPs_VernCorrDetectAcc_Cnt_M_u16
DigColPs_VernierAngleOORange_Cnt_M_lgc

 $tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value$

tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 19 T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k SelectFromColumn_Cnt_lgc 1 k_SkipStepErrDiag_Cnt_str.Threshold 74 k_SkipStepErrDiag_Cnt_str.PStep 2 $k_SkipStepErrDiag_Cnt_str.NStep$ 33 k_VernCorrErrorDiag_Cnt_str.Threshold 99 $k_VernCorrErrorDiag_Cnt_str.PStep$ 38 k_VernCorrErrorDiag_Cnt_str.NStep 17 48.37198949 k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32 269.58 tgt DigColPs Per2 MecState Cnt enum.value n tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 216.7759984 90.56395859 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ 2243 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL $tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL$ **Actual Value Expected Value** ${\tt DigColPs_HwAVernCorrFault_Cnt_M_lgc}$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 818.181763 818.1818182 ± 0.00048828125 DigColPs_I2CHwTrimTransCnts_Uls_M_u08 2 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 830.176025 830.1759984 ± 0.0001220703125 ${\tt DigColPs_PrevVernierLevelNo_Cnt_M_u08}$ 9 9 DigColPs_Reql2CSnsrDataType_Cnt_M_u08 1 1 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	✓

-69.8239746

0

0

0

0

0

-69.82400159 ± 0.00009

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	116.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	17.9
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	17.9
DigColPs_TrimCompStatic_Cnt_M_u16	520
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_gc	1
Rte_Inst_Sa_DigColPs T3_CalSaud (arrival LIT_Cat_a46/0/0)	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierEUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][1]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_00 0 u ve e 0 _0 [_0 0] 10	TH.





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2 DualSpurVernierLUT Cnt s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16] T3_DualSpurVernierLUT_Cnt_s16[2][17]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17] T3_DualSpurVernierLUT_Cnt_s16[2][18]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21] T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[2][21] T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

DigColPs_Per2





Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	0			
k_SkipStepErrDiag_Cnt_str.Threshold	46			
k_SkipStepErrDiag_Cnt_str.PStep	49			
k_SkipStepErrDiag_Cnt_str.NStep	17			
k_VernCorrErrorDiag_Cnt_str.Threshold	53			
k_VernCorrErrorDiag_Cnt_str.PStep	26			
k_VernCorrErrorDiag_Cnt_str.NStep	9			
k_VernCorrErrorThresh_Deg_f32	74.78180027			
k_VernOORangeThresh_Deg_f32	1199.29			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosV	alid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_I	HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_e	enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_	_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~	
DigColPs I2CHwColAngleForTrim Deg M f32	818.181763	818.1818182 ± 0.00048828125	✓	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓	
DigColPs_PrevColPos_Deg_M_f32	818.811279	818.8112897 ± 0.0001220703125	~	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1		

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwColAngle_Deg_M_f32	276.8997883
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	19
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	625.0201091
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	5
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	19 556
DigCoIPs_TrimCompStatic_Cnt_M_u16 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5] T2_ColSpurVernierLUT_Cnt_s16[0][6]	0 32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7] T2_ColSpurVernierLUT_Cnt_s16[1][8]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15] T3_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][0]	4 0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 4 T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 -396 T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108 T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2 DualSpurVernierLUT Cnt s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2 DualSpurVernierLUT Cnt s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 3 T2_DualSpurVernierLUT_Cnt_s16[1][4] T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2 DualSpurVernierLUT Cnt s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] n T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2 DualSpurVernierLUT Cnt s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 2 T2 DualSpurVernierLUT Cnt s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2 DualSpurVernierLUT Cnt s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2

2014-10-14, 18:16:06+0530



DiaColPs Per2

DigColPs_Per2		(Va)2	2016ab
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2 DualSpurVernierLUT Cnt s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	225		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	96		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	57.46032691		
k_VernOORangeThresh_Deg_f32	1341.97		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	276.8997883		
tgt_Pim_DigColPsEOL.CorTill_Deg_l32	232.8930412		
	1		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		Nolid Cat Igo	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPose tgt_DigColPs_Per2_I2CHwAbsPose		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Ci	nt_igc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	l=	1_
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	,
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	720.957642	720.9577085 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	,
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	720.957642	720.9577085 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-179.042358	-179.0422915 ± 0.0009	✓
tgt DigColPs Per2 TrimComp Cnt lgc.value	1	1	~



T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
	128
DigColPo. ColPorityError Cot M. Igo	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	137.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	258.0886749
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	12329
DigColPs_I2CHwSpurAngle_Deg_M_f32	20.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	30
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	87.60431278
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	20.1
DigColPs_TrimCompStatic_Cnt_M_u16	592
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
	-33
T2_ColSpurVernierLUT_Cnt_s16[0][4]	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4-
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

DigColPs_Per2

2014-10-14, 18:16:06+0530





Name	Immut Value		
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
	20		
T2_DualSpurVernierLUT_Cnt_s16[3][10] T0_DualSpurVernierLUT_Cnt_s16[3][10]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][11]			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	254		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	47		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	17.45087004		
k_VernOORangeThresh_Deg_f32	517.23		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	258.0886749		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.1939993		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1851		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPsEOL tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
		Funnatad Value	B
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•

490.909088

5

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

DigColPs_I2CHwTrimTransCnts_Uls_M_u08

490.9090909 ± 0.00048828125

5

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	480.888672	480.8886749 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7	7	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-419.111328	-419.1113251 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T ✓				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

- 101 0.15 to 10	
Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	163
DigColPs_ColTrimStatic_Deg_M_f32	147.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	5894
DigColPs_I2CHwColAngle_Deg_M_f32	248.0463682
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64194
DigColPs_I2CHwSpurAngle_Deg_M_f32	21.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1737.555742
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	21.2
DigColPs_TrimCompStatic_Cnt_M_u16	628
DigColPs_VernCorrDetectAcc_Cnt_M_u16	5
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5 6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2 DualSpurVernierLUT Cnt s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_S16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	111
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	48
k_VernCorrErrorDiag_Cnt_str.Threshold	75
k_VernCorrErrorDiag_Cnt_str.PStep	34
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	32.15087152
k_VernOORangeThresh_Deg_f32	1098.48
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	248.0463682

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 Name
 Input Value

 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32
 112.600146

 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16
 530

 tgt_Rte_Inst_Sa_DigColPs.DigColPs.DigColPs.Per2_I2CHwAbsPosValid_Cnt_lgc
 tgt_DigColPs.DigColPs.DigColPs.DigColPs.DigColPs.DigColPs.DigColPs.DigColPs.Per2_I2CHwAbsPos_HwDeg_f32
 tgt_DigColPs.D

Name Actual Value Expected Value Result DigColPs_HwAVernCorrFault_Cnt_M_lgc 1 1 1 DigColPs_12CHwColAngleForTrim_Deg_M_f32 818.181763 818.1818182 ± 0.00048828125 V DigColPs_12CHwTrimTransCnts_Uls_M_u08 0 0 0 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0 0 DigColPs_PrevOelPos_Deg_M_f32 820.646362 820.6463682 ± 0.0001220703125 V DigColPs_PrevVernierLevelNo_Cnt_M_u08 9 9 V DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 1 DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 1 1 1 DigColPs_VernierAngleOORange_Cnt_M_u16 1 1 1 DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 0 V tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value -79.3536377 -79.35363182 ± 0.00009 V tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value 0 0 0 NTC 0x6C 0x6C 0x6C Param 0x04 0x04 0x04	tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
DigCoIPs_I2CHwColAngleForTrim_Deg_M_f32 818.181763 818.1818182 ± 0.00048828125 DigCoIPs_I2CHwTrimTransCnts_Uls_M_u08 0 0 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0 DigCoIPs_PrevCoIPos_Deg_M_f32 820.646362 820.6463682 ± 0.0001220703125 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 9 9 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigCoIPs_Per2_TrimComp_Cnt_lgc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	Name	Actual Value	Expected Value	Result
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 0 0 0 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0 0 DigCoIPs_PrevCoIPos_Deg_M_f32 820.646362 820.6463682 ± 0.0001220703125 0 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 9 9 9 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 1 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 0 tgt_DigCoIPs_Per2_TrimComp_Cnt_lgc.value 0 0 0 0 NTC 0x6C 0x6C 0x6C 0x6C 0x04 0x04	DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0 DigCoIPs_PrevCoIPos_Deg_M_f32 820.646362 820.6463682 ± 0.0001220703125 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 9 9 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigCoIPs_Per2_TrimComp_Cnt_lgc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigCoIPs_PrevCoIPos_Deg_M_f32 820.646362 820.6463682 ± 0.0001220703125 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 9 9 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigCoIPs_Per2_TrimComp_Cnt_Igc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 9 9 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigCoIPs_Per2_TrimComp_Cnt_Igc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 1 1 1 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value 0 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 0 tgt_DigCoIPs_Per2_TrimComp_Cnt_Igc.value 0 0 0 NTC 0x6C 0x6C 0x6C Param 0x04 0x04 0x04	DigColPs_PrevColPos_Deg_M_f32	820.646362	820.6463682 ± 0.0001220703125	✓
DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigCoIPs_Per2_TrimComp_Cnt_Igc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 1 1 1 DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 0 0 tgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 0 tgt_DigCoIPs_Per2_TrimComp_Cnt_lgc.value 0 0 0 NTC 0x6C 0x6C 0x6C Param 0x04 0x04 0x04	DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigCoIPs_VernierAngleOORange_Cnt_M_lgc 1 1 1 tgt_DigCoIPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 0 vgt_DigCoIPs_Per2_I2CHwAbsPos_HwDeg_I32.value -79.3536377 -79.35363182 ± 0.00009 vgt_DigCoIPs_Per2_TrimComp_Cnt_lgc.value 0 0 vgt_DigCoIPs_Der2_TrimComp_Cnt_lgc.value 0 0 0 0 0 <td>DigColPs_SkipStepFltDetectAcc_Cnt_M_u16</td> <td>1</td> <td>1</td> <td>•</td>	DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value 0 0 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	DigCoIPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value -79.3536377 -79.35363182 ± 0.00009 tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value 0 0 NTC 0x6C 0x6C Param 0x04 0x04	DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value 0 0 • NTC 0x6C 0x6C • Param 0x04 0x04 0x04	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
NTC 0x6C 0x6C Param 0x04 0x04	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-79.3536377	-79.35363182 ± 0.00009	~
Param 0x04 0x04	tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
	NTC	0x6C	0x6C	✓
Status 0x01 0x01	Param	0x04	0x04	✓
	Status	0x01	0x01	✓

au				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)	🗸
Name	Input Value
DigColPsInt_GetCustData()	205
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	157.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	21646
DigColPs_I2CHwColAngle_Deg_M_f32	274.5377293
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	22.3
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1064.526832
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205
DigColPs_SpurTrimStatic_Deg_M_f32	22.3
DigColPs_TrimCompStatic_Cnt_M_u16	664
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0





32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 2 1 0 4 3 2
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-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
	252
T2_DualSpurVernierLUT_Cnt_s16[0][18]	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2 DualSpurVernierLUT Cnt s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][21]	
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
	1
T2_DualSpurVernierLUT_Cnt_s16[3][11]	
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13

DigColPs_SkipStepFltDetectAcc_Cnt_M_u16

 $tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value$

tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

DigColPs_VernCorrDetectAcc_Cnt_M_u16
DigColPs_VernierAngleOORange_Cnt_M_lgc

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 19 T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k SelectFromColumn_Cnt_lgc 0 k_SkipStepErrDiag_Cnt_str.Threshold 200 k_SkipStepErrDiag_Cnt_str.PStep 24 $k_SkipStepErrDiag_Cnt_str.NStep$ 31 k_VernCorrErrorDiag_Cnt_str.Threshold 95 $k_VernCorrErrorDiag_Cnt_str.PStep$ 48 k_VernCorrErrorDiag_Cnt_str.NStep 10 47.7859745 k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32 674.82 tgt DigColPs Per2 MecState Cnt enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 274.5377293 86.89214289 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ 655 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL $tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL$ **Actual Value Expected Value** ${\tt DigColPs_HwAVernCorrFault_Cnt_M_lgc}$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 490.909088 490.9090909 ± 0.00048828125 DigColPs_I2CHwTrimTransCnts_Uls_M_u08 0 0 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 476.937714 476.9377293 ± 0.0001220703125 ${\tt DigColPs_PrevVernierLevelNo_Cnt_M_u08}$ 6 6 DigColPs_Reql2CSnsrDataType_Cnt_M_u08 4 4

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

-409.090912

1

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0

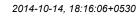
1

0

0

-409.0909091 ± 0.0009

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	210
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158
DigColPs_ColTrimStatic_Deg_M_f32	167.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	56914
DigColPs_I2CHwColAngle_Deg_M_f32	93.15782326
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	23.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	496.3249275
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	210





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	23.4
DigColPs_TrimCompStatic_Cnt_M_u16	700 0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17

2014-10-14, 18:16:06+0530



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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7]	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2 DualSpurVernierLUT Cnt s16[0][18]	252
T2 DualSpurVernierLUT Cnt s16[0][19]	288
	324
T2_DualSpurVernierLUT_Cnt_s16[0][20]	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[2][3]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2 DualSpurVernierLUT Cnt s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
	8
T2_DualSpurVernierLUT_Cnt_s16[3][4]	0
T2_DualSpurVernierLUT_Cnt_s16[3][4] T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10 12

2014-10-14, 18:16:06+0530



DigCoiPs_Per2		l OLE	CILAIU
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	13		
k_SkipStepErrDiag_Cnt_str.PStep	33		
k_SkipStepErrDiag_Cnt_str.NStep	14		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	9		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	90.42534328		
k_VernOORangeThresh_Deg_f32	855.99		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	93.15782326		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	317.1723412		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1412		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	645.357788	645.3578233 ± 0.0001220703125	✓

<u> </u>	13 3		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	645.357788	645.3578233 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-254.642212	-254.6421767 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	-
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	220
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	178
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0





Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	16977
DigColPs_I2CHwColAngle_Deg_M_f32	198.4525095
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43743
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	845.2340471
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	15
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	220
DigColPs_SpurTrimStatic_Deg_M_f32	24.5
DigColPs_TrimCompStatic_Cnt_M_u16	736
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2 ColSpurVernierLUT Cnt s16[0][1]	-163 -131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8] T2_ColSpurVernierLUT_Cnt_s16[1][9]	2
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	2
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	1 14
12_0010pui voimioi 201_0m_310[0][1]	· ·

2014-10-14, 18:16:06+0530



Name T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	Input Value 11 8
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	
T2_ColSpurVernierLUT_Cnt_s16[3][4]	8
T2_ColSpurVernierLUT_Cnt_s16[3][5]	5
	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2 ColSpurVernierLUT Cnt s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
	3
T2_ColSpurVernierLUT_Cnt_s16[3][10]	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
	5
T2_DualSpurVernierLUT_Cnt_s16[1][6]	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2 DualSpurVernierLUT Cnt s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[1][18]	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[2][12]	

2014-10-14, 18:16:06+0530



DigColPs_Per2

DigColPs_Per2		(VA	acitati
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2 DualSpurVernierLUT Cnt s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	199		
k_SkipStepErrDiag_Cnt_str.PStep	6		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	76		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	87.62320375		
k_VernOORangeThresh_Deg_f32	1774.591192		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	198.4525095		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	46.80067945		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4447	folial Cost Inc.	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosV		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cnt	_igc	
	tgt_Pim_DigColPsEOL	Function Value	Deculé
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0 1472.72717	0 1472.727273 ± 0.00048828125	
DigColPs_I2CHwColArigleFofThm_Deg_M_i32 DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	•
DigColPs PrevColPos Deg M f32	1460.45251	1460.45251 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs SkipStepFltDetectAcc Cnt M u16	1	1	-
DigColPs VernCorrDetectAcc Cnt M u16	1	1	
DigColPs VernierAngleOORange Cnt M Igc	0		

0

560.452515

0

0

560.4525095 ± 0.0009

DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value



T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

T4 04 0 04 (B4 04)	
Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	214
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs_ColTrimStatic_Deg_M_f32	188.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	17009
DigColPs_I2CHwColAngle_Deg_M_f32	183.5
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	9502
DigColPs_I2CHwSpurAngle_Deg_M_f32	25.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	601.2839711
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	214
DigColPs_SpurTrimStatic_Deg_M_f32	25.6
DigColPs_TrimCompStatic_Cnt_M_u16	772
DigColPs_VernCorrDetectAcc_Cnt_M_u16	9
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2 ColSpurVernierLUT Cnt s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_GolopurVernierLUT_Gnt_gnt[r][7] T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s10[1][2] T2 ColSpurVernierLUT Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][7] T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T3_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T3_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	1 2

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2 DualSpurVernierLUT Cnt s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	101		
k_SkipStepErrDiag_Cnt_str.Threshold	191 46		
k_SkipStepErrDiag_Cnt_str.PStep k_SkipStepErrDiag_Cnt_str.NStep	46		
k_VernCorrErrorDiag_Cnt_str.Threshold	43		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	8		
k VernCorrErrorThresh Deg f32	39.43172193		
k VernOORangeThresh Deg f32	1755.401681		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	183.5		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	205.6963653		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2712		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPc	ps_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Ci	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs 12CHwColAngleForTrim Deg M f32	1636 36353	1636 363636 + 0 00048828125	

1636.36353

3

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

DigColPs_I2CHwTrimTransCnts_Uls_M_u08

1636.363636 ± 0.00048828125

3

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1795.30005	1795.3 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Т	T ✓			
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

laura.	Innut Value
lame	Input Value
DigColPsInt_GetCustData()	
igColPs_ColParityError_Cnt_M_lgc	0
igColPs_ColSensorFaultAcc_Cnt_M_u16	144
igColPs_ColTrimStatic_Deg_M_f32	198.4
igColPs_HwAVernCorrFault_Cnt_M_lgc	0
igColPs_I2CColSensorFault_Cnt_M_Igc	0
igColPs_I2CHwColAngle_Cnt_M_u16	11710 204.045151
igColPs_I2CHwColAngle_Deg_M_f32	
igColPs_I2CHwDataType_Cnt_M_u08	1
igColPs_I2CHwSpurAngle_Cnt_M_u16	16894
igColPs_I2CHwSpurAngle_Deg_M_f32	26.7
igColPs_I2CHwTrimTransCnts_Uls_M_u08	5
igColPs_I2CSensCommFlts_Cnt_M_u08	8
igColPs_I2CSpurSensorFault_Cnt_M_lgc	0
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
igColPs_PrevColPos_Deg_M_f32	1513.739851
igColPs_PrevVernierLevelNo_Cnt_M_u08	11
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	10
igColPs_SpurParityError_Cnt_M_lgc	0
gColPs_SpurSensorFaultAcc_Cnt_M_u16	152
igColPs_SpurTrimStatic_Deg_M_f32	26.7
igColPs_TrimCompStatic_Cnt_M_u16	808
igColPs_VernCorrDetectAcc_Cnt_M_u16	11
igColPs_VernierAngleOORange_Cnt_M_lgc	1
te_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T3_DualSpurVernierLUT_Cnt_s16[1][20]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22 2
T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	27
k_SkipStepErrDiag_Cnt_str.PStep	20
k_SkipStepErrDiag_Cnt_str.NStep	4
k_VernCorrErrorDiag_Cnt_str.Threshold	15
k_VernCorrErrorDiag_Cnt_str.PStep	49
k_VernCorrErrorThreeh_Dog_f32	10 97.54986858
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	97.54986858 1151.320251
k_vernOORange1nresn_Deg_r32 tgt_DigCoIPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	204.045151
.g.,sigooii ococ.comiii_bog_ioc	25 5 . 5 . 5 .

2014-10-14, 18:16:06+0530



Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	219.1047057		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	730.725098	730.7251338 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	720	720 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8	8	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	27	27	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-169.274902	-169.2748662 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	•
NTC	0x6C	0x6C	✓
Param	0x0E	0x0E	•
Status	0x01	0x01	•

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.23 (Repeat Count = 1)	🗸
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	208.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22738
DigColPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1851
DigColPs_I2CHwSpurAngle_Deg_M_f32	27.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	8
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1273.742756
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	27.8
DigColPs_TrimCompStatic_Cnt_M_u16	844
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

2014-10-14, 18:16:06+0530



32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
2 1 0 4 3 2 1 0 4 3
1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
0 4 3 2 1 0 4 3 2 2 1 0 4 3 2
4 3 2 1 0 4 3
3 2 1 0 4 3
2 1 0 4 3 2
1 0 4 3 2
0 4 3 2
4 3 2
3 2
2
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10
1
14
11
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15
12
9
6
3
16
13
10
7
4
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36

2014-10-14, 18:16:06+0530



DigColPs_Per2		Razorcat
Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
[2_DualSpurVernierLUT_Cnt_s16[0][13]	72	
72_DualSpurVernierLUT_Cnt_s16[0][14]	108 144	
'2_DualSpurVernierLUT_Cnt_s16[0][15] '2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
2_DualSpurVernierLUT_Cnt_s16[0][17] 72_DualSpurVernierLUT_Cnt_s16[0][18]	252	
2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
2_DualSpurVernierLUT_Cnt_s16[0][19] 72_DualSpurVernierLUT_Cnt_s16[0][20]	324	
2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
2 DualSpurVernierLUT Cnt s16[1][6]	5	
2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
2_BualSpurVernierLUT Cnt s16[1][12]	1	
2_badsparvernierLUT Cnt s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
72_DualSpurVernierLUT_Cnt_s16[2][1]	1	
Γ2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
Γ2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
[2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
Γ2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
Γ2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
Γ2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
72 DualSpurVernierLUT Cnt s16[2][8]	8	
Γ2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
2 DualSpurVernierLUT Cnt s16[2][10]	10	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0	
2 DualSpurVernierLUT Cnt s16[2][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
2_DualSpurVernierLUT_Cnt_s16[3][2]	4	
2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
2_DualSpurVernierLUT_Cnt_s16[3][7]	14	
2_DualSpurVernierLUT_Cnt_s16[3][8]	16	
[2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
Γ2_DualSpurVernierLUT_Cnt_s16[3][10]	20	
Γ2_DualSpurVernierLUT_Cnt_s16[3][11]	1	
Γ2_DualSpurVernierLUT_Cnt_s16[3][12]	3	
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7	
2_DualSpurVernierLUT_Cnt_s16[3][15]	9	
2_DualSpurVernierLUT_Cnt_s16[3][16]	11	
Γ2_DualSpurVernierLUT_Cnt_s16[3][17]	13	

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87

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	150
k_SkipStepErrDiag_Cnt_str.PStep	8
k_SkipStepErrDiag_Cnt_str.NStep	29
k_VernCorrErrorDiag_Cnt_str.Threshold	35
k_VernCorrErrorDiag_Cnt_str.PStep	37
k_VernCorrErrorDiag_Cnt_str.NStep	6
k_VernCorrErrorThresh_Deg_f32	51.31432509
k_VernOORangeThresh_Deg_f32	1014.951933
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220.2809907
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1.
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLOT_Cnt_s16[2][11]	10
T2_ColSpurVernierLOT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLOT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLOT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2 ColSpurVernierLUT Cnt s16[2][14]	2
T2_ColSpurVernierLUT_Cnt_S16[2][15] T2_ColSpurVernierLUT_Cnt_S16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	1 14
T2_ColSpurVernierLUT_Cnt_s16[3][1]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2]	8
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
F2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
F2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5]	-292
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	2 3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][5]	5
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 0 k_SelectFromColumn_Cnt_lgc $k_SkipStepErrDiag_Cnt_str.Threshold$ 16 k_SkipStepErrDiag_Cnt_str.PStep 4 $k_SkipStepErrDiag_Cnt_str.NStep$ 47 $k_VernCorrErrorDiag_Cnt_str.Threshold$ 98 $k_VernCorrErrorDiag_Cnt_str.PStep$ 3 k_VernCorrErrorDiag_Cnt_str.NStep 99 41426611 $k_VernCorrErrorThresh_Deg_f32$ k_VernOORangeThresh_Deg_f32 359.5822154 tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 360 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 250.4857173 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum tgt DigColPs Per2 MecState Cnt enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt Rte Inst Sa DigColPs.Pim DigColPsEOL tgt Pim DigColPsEOL

(9.1. 1.0_1.10(_04_5.1906.1 0.1 1.15.1906.1 02.02	tgt_1D.goo 02-02		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	501.200012	501.2 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x04	0x04	~
Status	0x01	0x01	✓

Τ			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

Test Step 2.25 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	50
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131
DigColPs_ColTrimStatic_Deg_M_f32	229
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1





Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	48650
DigColPs_I2CHwColAngle_Deg_M_f32	126.5327979 3
DigColPs_I2CHwDataType_Cnt_M_u08 DigColPs_I2CHwSpurAngle_Cnt_M_u16	5 51339
DigColPs I2CHwSpurAngle Deg M f32	30
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	373.8183561
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50
DigColPs_SpurTrimStatic_Deg_M_f32	30 916
DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12]	196 229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][4]	4 2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	1 14

2014-10-14, 18:16:06+0530



<u> </u>	
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
	12
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cst_s46[3][7]	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	6
T2_ColSpurVernierLUT_Cnt_s16[3][9]	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
	180
T2_DualSpurVernierLUT_Cnt_s16[0][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
	-
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	8 9
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	8 9 10
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	8 9 10 0
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	8 9 10

DigColPs_Per2

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2 DualSpurVernierLUT Cnt s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2 DualSpurVernierLUT Cnt s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	123		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	64		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	45.68451142		
k VernOORangeThresh Deg f32	735.0528789		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	126.5327979		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	306.8582928		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid Cnt loc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs Per2 MecState Cn		
tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc	tgt_DigColPs_Per2_TrimComp_C	_	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Feiz_Trimcomp_ci	·9°	
Name	Actual Value	Expected Value	Doord
	1	1	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc		1 365.0644124 ± 0.00048828125	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	365.064392		
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	3	3	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tot DiaColPs Per? I2CHwAhsPosValid Cnt Iac value	n	10	

0

-540

0x6C

0x0C

0x01

-540 ± 0.0009

0x6C

0x0C

0x01

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

NTC

Param

Status



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	30
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs I2CHwColAngle Deg M f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	28.9
DigColPs I2CHwTrimTransCnts Uls M u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs PrevVernierLevelNo Cnt M u08	7
DigColPs SkipStepFltDetectAcc Cnt M u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	30
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs TrimCompStatic Cnt M u16	880
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs VernierAngleOORange Cnt M lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4.
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





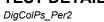
Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

DigColPs_Per2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	0		
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2 DualSpurVernierLUT Cnt s16[2][0]	0		
T2_DualSpurVernierEUT_GNt_s16[2][1]	1		
T2_DualSpurVernierEUT_GNt_316[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
Γ2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2 DualSpurVernierLUT Cnt s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
<pre>x_SelectFromColumn_Cnt_lgc</pre>	0		
<pre><_SkipStepErrDiag_Cnt_str.Threshold</pre>	16		
x_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
<_VernOORangeThresh_Deg_f32 gt_DigCoIPs_Par2_MasState_Cnt_enum value	359.5822154		
gt_DigColPs_Per2_MecState_Cnt_enum.value	1 -74.24		
gt_Pim_DigColPsEOL.ColTrim_Deg_f32 gt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
gt_Pim_DigColPsEOL.SpurTrim_Deg_r32 gt_Pim_DigColPsEOL.TrimComp_Cnt_u16	250.4857173		
gt_Plin_bigcolPsEOL.Tillicomp_clit_u16 gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	ant lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_Valid_Cft_gc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHWAbsPos_HWDeg_t32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	9_102	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Fer2_Trifficomp_Crit_igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
	1	1	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc			





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	501.200012	501.2 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓
Param	0x04	0x04	~
Status	0x01	0x01	✓

T v				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	152
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	124
DigColPs_ColTrimStatic_Deg_M_f32	208.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs I2CHwColAngle Cnt M u16	22738
DigColPs I2CHwColAngle Deg M f32	0
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs I2CHwSpurAngle Cnt M u16	1851
DigColPs I2CHwSpurAngle Deg M f32	27.8
DigColPs I2CHwTrimTransCnts Uls M u08	6
DigColPs I2CSensCommFlts Cnt M u08	8
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_FrevAngleBataAvailable_Crit_ivi_igc DigColPs PrevColPos Deg M f32	1273.742756
DigColPs PrevVernierLevelNo Cnt M u08	12/3.742/30
	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc	152
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	27.8
DigColPs_SpurTrimStatic_Deg_M_f32	844
DigColPs_TrimCompStatic_Cnt_M_u16	7
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
	-30
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324

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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2 DualSpurVernierLUT Cnt s16[2][0]	0
T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2 DualSpurVernierLUT Cnt s16[2][6]	6
T2 DualSpurVernierLUT Cnt s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2 DualSpurVernierLUT Cnt s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2 DualSpurVernierLUT Cnt s16[3][2]	4
T2 DualSpurVernierLUT Cnt s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	-

DigColPs_Per2



Name	Input Value
k_VernCorrErrorDiag_Cnt_str.NStep	6
k_VernCorrErrorThresh_Deg_f32	51.31432509
k_VernOORangeThresh_Deg_f32	1014.951933
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220.2809907
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

Actual Value	Expected Value	Result
0	0	~
730.69043	730.6904588 ± 0.00048828125	~
5	5	•
0	0	~
720	720 ± 0.0001220703125	~
8	8	~
1	1	~
1	1	~
1	1	~
0	0	•
0	0	•
-169.30957	-169.3095412 ± 0.0009	~
1	1	~
	0 730.69043 5 0 720 8 1 1 1 0 0	0 0 730.69043 730.6904588 ± 0.00048828125 5 5 0 0 720 720 ± 0.0001220703125 8 8 1 1 1 1 1 1 0 0 0 0 0 0

au			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.28 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt GetCustData()	101
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	100
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.8777566
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	31.1
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359 359
T2_ColSpurVernierLUT_Cnt_S10[0][10] T2_ColSpurVernierLUT_Cnt_S10[0][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	5
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2 ColSpurVernierLUT Cnt s16[2][11]	10
T2 ColSpurVernierLUT Cnt s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14] T3_ColSpurVernierLUT_Cnt_s16[3][15]	7 4
T2_ColSpurVernierLUT_Cnt_s16[3][15] T3_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-390 -360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-300 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
	-216
T2 DualSpurVernierLUT Cnt s16f0lf5l	
T2_DualSpurVernierLUT_Cnt_s16[0][5] T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180 -144





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2 DualSpurVernierLUT Cnt s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

DigColPs_Per2

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	12		
k_SkipStepErrDiag_Cnt_str.NStep	41		
k_VernCorrErrorDiag_Cnt_str.Threshold	48		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	78.9135704		
k_VernOORangeThresh_Deg_f32	1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1077.77271	1077.772727 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

T			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	144	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	182	
DigColPs_ColTrimStatic_Deg_M_f32	249.4	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	18257	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	21803	
DigColPs_I2CHwSpurAngle_Deg_M_f32	32.2	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	845.517553	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	15	
DigColPs_SpurParityError_Cnt_M_lgc	1	





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
DigColPs_SpurTrimStatic_Deg_M_f32	32.2
DigColPs_TrimCompStatic_Cnt_M_u16	988
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_gc	0
Rte_Inst_Sa_DigColPs	
	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T3_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1]	
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T3_ColSpurVernierLUT_Cnt_s16[2][11]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11] T3_ColSpurVernierLUT_Cnt_s16[2][12]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T0_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
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2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duaiopui veitiieteu i Ott 510[3][3]	10

2014-10-14, 18:16:06+0530





DigCoiPs_Per2			MACIEMU .
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	179		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	11		
k_VernCorrErrorDiag_Cnt_str.Threshold	8		
k_VernCorrErrorDiag_Cnt_str.PStep	1		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	31.8570087		
k_VernOORangeThresh_Deg_f32	390.7995283		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_Hv	wDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_en	um	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lg	jc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	14.6363688	14.63636364 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value		1 1		~
Т				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

-885.363647

0

2

0

-885.3636364 ± 0.0009

Test Step 2.30 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	105	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150	
DigColPs_ColTrimStatic_Deg_M_f32	259.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	46069	
DigColPs_I2CHwColAngle_Deg_M_f32	360	

DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc

 $tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value$

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

2014-10-14, 18:16:06+0530



-	
Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	33.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2]	14 11
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T0_ColOpurV(sprind UT_Cot_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	4
T2 ColSpurVernierLUT Cnt s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][4]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16] T0_DualSpurVernierLUT_Cnt_s16[0][47]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18] T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14] T0_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
TO Development of LIT Ont. 1070Y157	
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1	DesMalid Out Inc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	_	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	_CIII_Igc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	l=	I
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	371.030273	371.0302938 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-528.969727	-528.9697062 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	1	1	•
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	•
Status	0x01	0x01	→



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.31 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100
DigColPs ColTrimStatic Deg M f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.8777566
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	31.1
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs I2CSpurSensorFault Cnt M Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs VernCorrDetectAcc Cnt M u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2 ColSpurVernierLUT Cnt s16[0][15]	327
T2_GolSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
12_0010pdi verillerE01_011[_310[1][13]	4





Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2		
T2 DualSpurVernierLUT Cnt s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2 DualSpurVernierLUT Cnt s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	12		
k_SkipStepErrDiag_Cnt_str.NStep	41		
k_VernCorrErrorDiag_Cnt_str.Threshold	48		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorThrash_Dog_f32	3 78 0135704		
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	78.9135704 1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-74.24		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	nt Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
		· ·	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

DigColPs_ColParityError_Cnt_M_igc 144
DigColPsInt_GetCustData() 144 DigColPs_ColParityError_Cnt_M_lgc 0 DigColPs_ColSensorFaultAcc_Cnt_M_u16 182 DigColPs_ColTrimStatic_Deg_M_f32 249.4 DigColPs_HwAVermCorrFault_Cnt_M_lgc 1 DigColPs_12CColSensorFault_Cnt_M_lgc 0 DigColPs_12CHwColAngle_Cnt_M_u16 18257 DigColPs_12CHwColAngle_Deg_M_f32 0 DigColPs_12CHwSpurAngle_Cnt_M_u08 0 DigColPs_12CHwSpurAngle_Cnt_M_u16 21803 DigColPs_12CHwSpurAngle_Deg_M_f32 32.2 DigColPs_12CHwTrimTransCnts_Uls_M_u08 3 DigColPs_12CSensCommFits_Cnt_M_u08 3 DigColPs_12CSpurSensorFault_Cnt_M_u08 24 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigColPs_PrevVenipe_Deg_M_f32 845.517553 DigColPs_PrevVeniperLevelNo_Cnt_M_u08 13 DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 15 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 15 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigColPs_ColParityError_Cnt_M_lgc 0 DigColPs_ColSensorFaultAcc_Cnt_M_u16 182 DigColPs_ColTrimStatic_Deg_M_f32 249.4 DigColPs_HwAVernCorrFault_Cnt_M_lgc 1 DigColPs_12CColSensorFault_Cnt_M_lgc 0 DigColPs_12CHwColAngle_Cnt_M_u16 18257 DigColPs_12CHwColAngle_Deg_M_f32 0 DigColPs_12CHwDataType_Cnt_M_u08 0 DigColPs_12CHwSpurAngle_Cnt_M_u16 21803 DigColPs_12CHwSpurAngle_Deg_M_f32 32.2 DigColPs_12CHwTrimTransCnts_UIs_M_u08 3 DigColPs_12CSensCommFits_Cnt_M_u08 3 DigColPs_12CSpurSensorFault_Cnt_M_lgc 1 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigColPs_PrevVernierLevelNo_Cnt_M_u08 13 DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 15 DigColPs_SpurParityError_Cnt_M_lgc 1 DigColPs_SpurParityError_Cnt_M_u16 15 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 182 DigCoIPs_ColTrimStatic_Deg_M_f32 249.4 DigCoIPs_HwAVemCorrFault_Cnt_M_lgc 1 DigCoIPs_I2CCOISensorFault_Cnt_M_lgc 0 DigCoIPs_I2CHwColAngle_Cnt_M_u16 18257 DigCoIPs_I2CHwColAngle_Deg_M_f32 0 DigCoIPs_I2CHwDataType_Cnt_M_u08 0 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFits_Cnt_M_u08 24 DigCoIPs_I2CSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevVoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVermierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFillDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_u6 1 DigCoIPs_SpurParityError_Cnt_M_u16 144
DigCoIPs_ColTrimStatic_Deg_M_f32 249.4 DigCoIPs_HwAVernCorrFault_Cnt_M_lgc 1 DigCoIPs_I2CColSensorFault_Cnt_M_lgc 0 DigCoIPs_I2CHwColAngle_Cnt_M_u16 18257 DigCoIPs_I2CHwColAngle_Deg_M_f32 0 DigCoIPs_I2CHwDataType_Cnt_M_u08 0 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_u08 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVenierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_u16 15 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_HwAVernCorrFault_Cnt_M_Igc 1 DigCoIPs_I2CColSensorFault_Cnt_M_Igc 0 DigCoIPs_I2CHwColAngle_Cnt_M_u16 18257 DigCoIPs_I2CHwColAngle_Deg_M_f32 0 DigCoIPs_I2CHwDataType_Cnt_M_u08 0 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFits_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFitDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigColPs_I2CColSensorFault_Cnt_M_u16 18257 DigColPs_I2CHwColAngle_Cnt_M_u16 18257 DigColPs_I2CHwDataType_Cnt_M_u08 0 DigColPs_I2CHwDataType_Cnt_M_u08 0 DigColPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigColPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigColPs_I2CHwTrimTransCnts_Uls_M_u08 3 DigColPs_I2CSensCommFlts_Cnt_M_u08 24 DigColPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigColPs_PrevColPos_Deg_M_f32 845.517553 DigColPs_PrevVernierLevelNo_Cnt_M_u08 13 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigColPs_SpurParityError_Cnt_M_lgc 1 DigColPs_SpurParityError_Cnt_M_lgc 1 DigColPs_SpurParityError_Cnt_M_u16 15 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CHwCoIAngle_Cnt_M_u16 18257 DigCoIPs_I2CHwDataType_Cnt_M_u08 0 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_Igc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_Igc 1 DigCoIPs_SpurParityError_Cnt_M_Igc 1 DigCoIPs_SpurParityError_Cnt_M_u16 15 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CHwCoIAngle_Deg_M_f32 0 DigCoIPs_I2CHwDataType_Cnt_M_u08 0 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CHwDataType_Cnt_M_u08 0 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFits_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 21803 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs 2CHwSpurAngle_Deg_M_f32 32.2 DigCoIPs 2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs 2CSensCommFlts_Cnt_M_u08 24 DigCoIPs 2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 3 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CSensCommFlts_Cnt_M_u08 24 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc 1 DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_PrevAngleDataAvailable_Cnt_M_lgc 1 DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigCoIPs_PrevCoIPos_Deg_M_f32 845.517553 DigCoIPs_PrevVernierLevelNo_Cnt_M_u08 13 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigCoIPs_SpurParityError_Cnt_M_lgc 1 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 15 DigColPs_SpurParityError_Cnt_M_lgc 1 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigColPs_SpurParityError_Cnt_M_lgc 1 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 144
DigColPs_SpurSensorFaultAcc_Cnt_M_u16 144
•
DigCoIPs SpurTrimStatic Deg M f32 32.2
DigCoIPs_TrimCompStatic_Cnt_M_u16 988
DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 4
DigCoIPs_VernierAngleOORange_Cnt_M_lgc 0
Rte_Inst_Sa_DigColPs tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0] -163
T2_ColSpurVernierLUT_Cnt_s16[0][1] -131
T2_ColSpurVernierLUT_Cnt_s16[0][2] -99
T2_ColSpurVernierLUT_Cnt_s16[0][3] -66
T2_ColSpurVernierLUT_Cnt_s16[0][4] -33
T2_ColSpurVernierLUT_Cnt_s16[0][5] 0
T2_ColSpurVernierLUT_Cnt_s16[0][6] 32
T2_ColSpurVernierLUT_Cnt_s16[0][7] 65
T2_ColSpurVernierLUT_Cnt_s16[0][8] 98
T2_ColSpurVernierLUT_Cnt_s16[0][9] 130
T2_ColSpurVernierLUT_Cnt_s16[0][10] 163
T2_ColSpurVernierLUT_Cnt_s16[0][11] 196
T2_ColSpurVernierLUT_Cnt_s16[0][12] 229
T2_ColSpurVernierLUT_Cnt_s16[0][13] 261
T2_ColSpurVernierLUT_Cnt_s16[0][14] 294
T2_ColSpurVernierLUT_Cnt_s16[0][15] 327
T2_ColSpurVernierLUT_Cnt_s16[0][16] 359
T2_ColSpurVernierLUT_Cnt_s16[1][0] 0
T2_ColSpurVernierLUT_Cnt_s16[1][1] 4
T2_ColSpurVernierLUT_Cnt_s16[1][2] 3

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7] T3_DualSpurVernierLUT_Cnt_s16[1][9]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2 DualSpurVernierLUT Cnt s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14 16
T2_DualSpurVernierLUT_Cnt_s16[3][8]	18
T2_DualSpurVernierLUT_Cnt_s16[3][9] T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2 DualSpurVernierLUT Cnt s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	179
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	11
k_VernCorrErrorDiag_Cnt_str.Threshold	8
k_VernCorrErrorDiag_Cnt_str.PStep	1
k_VernCorrErrorDiag_Cnt_str.NStep	2
k_VernCorrErrorThresh_Deg_f32	31.8570087
k_VernOORangeThresh_Deg_f32	390.7995283
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1

2014-10-14, 18:16:06+0530



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	14.636363	14.63636364 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-885.363647	-885.3636364 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.33 (Repeat Count = 1)	<u></u>
Name	Input Value
DigColPsInt GetCustData()	106
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	141
DigColPs ColTrimStatic Deg M f32	269.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26533
DigColPs_I2CHwColAngle_Deg_M_f32	60.248
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36379
DigColPs_I2CHwSpurAngle_Deg_M_f32	34.4
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	284.4795149
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106
DigColPs_SpurTrimStatic_Deg_M_f32	34.4
DigColPs_TrimCompStatic_Cnt_M_u16	1060
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7] T3_ColSpurVernierLUT_Cnt_s46[1][9]	2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T3_ColSpurVernierLUT_Cnt_s46[4][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T3_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
	I .





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpur/orpicsLUT_Cnt_s16[2][18]	
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	8 9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	22
k_SkipStepErrDiag_Cnt_str.PStep	8
k_SkipStepErrDiag_Cnt_str.NStep	16
k_VernCorrErrorDiag_Cnt_str.Threshold	4
k_VernCorrErrorDiag_Cnt_str.PStep	38
k_VernCorrErrorDiag_Cnt_str.NStep	7
k_VernCorrErrorThresh_Deg_f32	88.97686696
k_VernOORangeThresh_Deg_f32	706.5625857
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2





1 tgt_Rte_Inst_Sa_DigColPs -163 -131 -99 -66 -33 0
-163 -131 -99 -66 -33
-131 -99 -66 -33
-99 -66 -33
-66 -33
-33
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32
65
98
130
163
196
229
261
294
327
359
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1
0
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3
2
1
0
4
3
2
1
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4
0
8
6
4
2
0
9
7
5
3
1
10
8
6
4
2
10
1
14
11
8
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15
12
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6
3
16
13
10
7
4
17
-396
-360
-324

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2 DualSpurVernierLUT Cnt s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][10]	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
	10
	1 10
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

DigColPs_Per2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	131		
k_SkipStepErrDiag_Cnt_str.PStep	40		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	40		
k_VernCorrErrorDiag_Cnt_str.PStep	34		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	58.9241991		
k_VernOORangeThresh_Deg_f32	866.7677131		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	11.56588054		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsI	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsI	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

igi_rtte_mat_oa_bigoon a.r im_bigoon aloc	tgt_i iii_bigooii 3LOL	tgt_i iii_bigooii sede		
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	~	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~	
DigColPs_PrevColPos_Deg_M_f32	811.565918	811.5658805 ± 0.0001220703125	✓	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	✓	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	11	11	~	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	~	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~	
NTC	0x6C	0x6C	~	
Param	0x0C	0x0C	✓	
Status	0x01	0x01	~	

T ✓				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	151	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	145	
DigColPs_ColTrimStatic_Deg_M_f32	290.2	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	48613	
DigColPs_I2CHwColAngle_Deg_M_f32	136.3651175	

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36961
DigColPs_I2CHwSpurAngle_Deg_M_f32	36.6
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	28
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1505.659877
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
	0
DigColPs_SpurParityError_Cnt_M_lgc	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151
DigColPs_SpurTrimStatic_Deg_M_f32	36.6
DigColPs_TrimCompStatic_Cnt_M_u16	1132
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T0_ColOpurV(sprint UT_Cot_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	4
T2 ColSpurVernierLUT Cnt s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][4]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16] T0_DualSpurVernierLUT_Cnt_s16[0][47]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18] T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14] T0_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
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T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	184		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	26		
k_VernCorrErrorDiag_Cnt_str.Threshold	20		
k_VernCorrErrorDiag_Cnt_str.PStep	46		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	87.16203666		
k_VernOORangeThresh_Deg_f32	1105.319018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	136.3651175		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2271		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_F	HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_e	num	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_I	lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resu
DigColPs HwAVernCorrFault Cnt M lgc	0	0	
DigColPs I2CHwColAngleForTrim Deg M f32	1636.36353	1636.363636 ± 0.00048828125	•
DigColPs I2CHwTrimTransCnts Uls M u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1646.16504	1646.165117 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	16	16	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
5.goo. ooqizoonorbata iypo_ont_ivi_uoo		'	

DigColPs_I2CHwColAngleFolTiffi_Deg_ivi_i32	1030.30333	1030.303030 ± 0.00040020123	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1646.16504	1646.165117 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
7			

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•



Test Step 2.36 (Repeat Count = 1)	🔻
Name	Input Value
DigColPsInt_GetCustData()	165
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	300.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	28682
DigColPs_I2CHwColAngle_Deg_M_f32	49.7053827
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16 DigColPs_I2CHwSpurAngle_Deg_M_f32	13341 37.7
DigColPs_12CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	7
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1225.322705
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
DigColPs_SpurTrimStatic_Deg_M_f32	37.7
DigColPs_TrimCompStatic_Cnt_M_u16	1168
DigColPs_VernCorrDetectAcc_Cnt_M_u16	9
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs T2 ColSpurVernierLUT Cnt s16[0][0]	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14] T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	5
LZ LAUGOULVEUDEU III UUT CIKIZUXI	J
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	3





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2 DualSpurVernierLUT Cnt s16[2][10]	10		
T2 DualSpurVernierLUT Cnt s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	223		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	36		
k_VernCorrErrorDiag_Cnt_str.Threshold	11		
k_VernCorrErrorDiag_Cnt_str.PStep	6		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	81.95902205		
k_VernOORangeThresh_Deg_f32	1527.852543		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	49.7053827		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	341		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1542		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	829.30542	829.3053827 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-81.8182373	-81.81818182 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	

0x0C

0x01

0x0C

0x01

Param Status



T -				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.37 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
DigColPsInt_GetCustData()	175
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs ColSensorFaultAcc Cnt M u16	128
DigColPs_ColTrimStatic_Deg_M_f32	310.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	5110
DigColPs I2CHwColAngle Deg M f32	236.1581588
DigColPs I2CHwDataType Cnt M u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	13604
DigColPs_I2CHwSpurAngle_Deg_M_f32	38.8
DigColPs I2CHwTrimTransCnts Uls M u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1393.487479
DigColPs PrevVernierLevelNo Cnt M u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175
DigColPs_SpurTrimStatic_Deg_M_f32	38.8
DigColPs TrimCompStatic Cnt M u16	1204
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
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Input Value 1 0 4 0
4
0
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-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
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2014-10-14, 18:16:06+0530



DigColPs_Per2

DigColFs_Fer2			TOLOTO
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7 8		
T2_DualSpurVernierLUT_Cnt_s16[1][19] T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	8 9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	8 9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8] T2_DualSpurVernierLUT_Cnt_s16[3][9]	16 18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	17 19		
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	175		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	59		
k_VernCorrErrorDiag_Cnt_str.PStep	46		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	77.78657174		
k_VernOORangeThresh_Deg_f32	566.6271515 1		
tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	236.1581588		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	156.2506101		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPo	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPe		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	105

654.54541

654.5454545 ± 0.00048828125

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	645.558167	645.5581588 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-254.441833	-254.4418412 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.38 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	185
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs ColSensorFaultAcc Cnt M u16	168
DigColPs ColTrimStatic Deg M f32	320.8
DigColPs HwAVernCorrFault Cnt M Igc	1
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26911
DigColPs I2CHwColAngle Deg M f32	55.3166151
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs I2CHwSpurAngle Cnt M u16	23900
DigColPs_I2CHwSpurAngle_Deg_M_f32	39.9
DigColPs I2CHwTrimTransCnts UIs M u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs PrevAngleDataAvailable Cnt M lgc	0
DigColPs_PrevColPos_Deg_M_f32	93.47087908
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	185
DigColPs_SpurTrimStatic_Deg_M_f32	39.9
DigColPs_TrimCompStatic_Cnt_M_u16	1240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2 DualSpurVernierLUT Cnt s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][3]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	96
k_SkipStepErrDiag_Cnt_str.PStep	42
k_SkipStepErrDiag_Cnt_str.NStep	34
k_VernCorrErrorDiag_Cnt_str.Threshold	98
k_VernCorrErrorDiag_Cnt_str.PStep	34
k_VernCorrErrorDiag_Cnt_str.NStep	11
	1
k_VernCorrErrorThresh_Deg_f32	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	1574.365275

DigColPs_Per2

2014-10-14, 18:16:06+0530



Name
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32
301.781571
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16
tgt_Pim_DigColPs_DigColPs_Per2_l2CHwAbsPosValid_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs_DigColPs_Per2_l2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs_DigColPs_Per2_l2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL

Input Value
tgt_DigColPs_Per2_12CHwAbsPos
tgt_DigColPs_Per2_12CHwAbsPos_Valid_Cnt_lgc
tgt_DigColPs_Per2_12CHwAbsPosValid_Cnt_lgc
tgt_DigColPs_Per2_12CHwAbsPos_HwDeg_f32
tgt_DigColPs_Per2_12CHwAbsPos_HwDeg_f32
tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Pim_DigColPsEOL

tgt_Pim_DigColPsEOL		
Actual Value	Expected Value	Result
1	1	~
818.181763	818.1818182 ± 0.00048828125	~
2	2	~
0	0	~
814.516602	814.5166151 ± 0.0001220703125	~
9	9	~
4	4	~
1	1	~
1	1	~
0	0	~
0	0	~
-81.8182373	-81.81818182 ± 0.00009	•
0	0	~
	Actual Value 1 818.181763 2 0 814.516602 9 4 1 1 0 0	Actual Value Expected Value 1 1 818.181763 818.1818182 ± 0.00048828125 2 2 0 0 814.516602 814.5166151 ± 0.0001220703125 9 9 4 4 1 1 1 1 0 0 0 0

au				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.39 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	195
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_ColTrimStatic_Deg_M_f32	331
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	22241
DigColPs_I2CHwColAngle_Deg_M_f32	71.4783923
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	37586
DigColPs_I2CHwSpurAngle_Deg_M_f32	141
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	11
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	414.2750131
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	195
DigColPs_SpurTrimStatic_Deg_M_f32	141
DigColPs_TrimCompStatic_Cnt_M_u16	1276
DigColPs_VernCorrDetectAcc_Cnt_M_u16	16
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7] T3_ColSpurVernierLUT_Cnt_s16[1][9]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	112

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DigCoiPs_Per2		Tazolar
Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]		



DigColPs_Per2	7 14, 10.10.50 .0000	Razorcat
Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21	
k_SelectFromColumn_Cnt_lgc	1	
k_SkipStepErrDiag_Cnt_str.Threshold	67	
k_SkipStepErrDiag_Cnt_str.PStep	39	
k_SkipStepErrDiag_Cnt_str.NStep	27	
k_VernCorrErrorDiag_Cnt_str.Threshold	5	
k_VernCorrErrorDiag_Cnt_str.PStep	39	
k_VernCorrErrorDiag_Cnt_str.NStep	15	
k_VernCorrErrorThresh_Deg_f32	100	
k_VernOORangeThresh_Deg_f32	245.4025523	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	71.4783923	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.98159581	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1481	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	

tgt_rttc_mot_ou_bigoon on im_bigoon obot	tgt_i iii_bigooii sece	tgt_i iii_bigooii seese	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	820.478394	820.4783923 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-79.5216064	-79.5216077 ± 0.00009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	✓

τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.40 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123	
DigColPs_ColTrimStatic_Deg_M_f32	1.5	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	46566	
DigColPs_I2CHwColAngle_Deg_M_f32	135.5191227	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22556	
DigColPs_I2CHwSpurAngle_Deg_M_f32	42.1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1072.03711	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	14	
DigColPs_SpurParityError_Cnt_M_lgc	1	





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	42.1
DigColPs_TrimCompStatic_Cnt_M_u16	1312
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLOT_Cnt_S16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_\$16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierEUT_Cnt_S16[1][2] T2_ColSpurVernierEUT_Cnt_S16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1,
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLOT_Cnt_\$16[2][13] T2_ColSpurVernierLUT_Cnt_\$16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
	13
T2_ColSpurVernierLUT_Cnt_s16[3][12]	
	10
12_ColSpurVernierLU1_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duaiopui veitiieteu i Ott 510[3][3]	10





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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2 DualSpurVernierLUT Cnt s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt Igc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	71		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	38		
k VernCorrErrorDiag Cnt str.Threshold	7		
k_VernCorrErrorDiag_Cnt_str.PStep	12		
k_VernCorrErrorDiag_Cnt_str.NStep	5		
k VernCorrErrorThresh Deg f32	83.48664141		
k_VernOORangeThresh_Deg_f32	744.223277		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	135.5191227		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	263.9402983		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	302		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsl	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	494.019104	494.0191227 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DiaColDo VernCorrDotectAco Cot M v16	1	1	

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	494.019104	494.0191227 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.41 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	152
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146
DigColPs_ColTrimStatic_Deg_M_f32	5.6

2014-10-14, 18:16:06+0530



Name	
	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	20466
	34.50624543
DigColPs_I2CHwColAngle_Deg_M_f32	
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	34618
DigColPs_I2CHwSpurAngle_Deg_M_f32	43.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs I2CSpurSensorFault Cnt M Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs PrevColPos Deg M f32	1779.91482
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	18
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	43.2
DigColPs_TrimCompStatic_Cnt_M_u16	1348
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
	0
T2_ColSpurVernierLUT_Cnt_s16[2][0]	
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
	2
T2 ColSpurVernier LIT Cnt e16(2)(15)	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0]	10

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T. C. OSSAVVANICUT, CM 1907 1	Nama	Input Value
12_CoSquirement_Cot_statp[0] 11	Name	Input Value
P. Colispa/remontall_Colstiligid 5 12 Colispa/remontall_Colstiligid 5 12 Colispa/remontall_Colstiligid 5 15 Colispa/remontall_Colstiligid 5 15 Colispa/remontall_Colstiligid 15 Colispa/remontall_Colstiligid 16 Colispa/remontall_Colstiligid 16 Colispa/remontall_Colstiligid 17 Colispa/remontall_Colstiligid 17 Colispa/remontall_Colstiligid 18		
T. Collego/vernient U. Col. 4 (1978)		
12_00spx/remail_Coll_Statistics 2		
T2_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 12_COSQAVeniment_U_Cnt_visit(SIS) 13_COSQAVeniment_U_Cnt_visit(SIS) 14_COSQAVeniment_U_Cnt_visit(SIS) 15_COSQAVeniment_U_Cnt_visit(SIS) 16_COSQAVeniment_U_Cnt_visit(SIS) 17_COSQAVeniment_U_Cnt_visit(SIS) 18_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 19_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit(SIS) 10_COSQAVeniment_U_L_Cnt_visit		
12, Osspar/weinstall, Cot, 14(3)(1)		
Tz. COSSpur/venseLT. Cot. s163789 6 Tz. COSSpur/venseLT. Cot. s163719 6 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 18 Tz. COSSpur/venseLT. Cot. s163719 19 Tz. COSSpur/v		
To Conspired ment LT, Det. 310(19) To Conspired ment LT, Det. 510(19) To Conspired ment LT, Det. 510		
To Colsput/went LT Ont \$180]119 10		
T. Colsput/ment.U. Col.; st (20)112 13 13 13 13 13 13 13	T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[14] T2_CoSport/emetU_Cot_st (50)[15]	T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T. C. OSBOUYMONE LT. COL. \$16(3)*14 7 7 7 7 7 7 7 7 7	T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_Colspar/membut/D_cnt_st093115 4	T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
17, CoSpur/wentUT, Cott, \$103115	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_CosSpurVement UT_Cnt_s160[110] 388 12_DusSpurVement UT_Cnt_s160[11] 380 12_DusSpurVement UT_Cnt_s160[12] 384 12_DusSpurVement UT_Cnt_s160[13] 384 12_DusSpurVement UT_Cnt_s160[13] 384 12_DusSpurVement UT_Cnt_s160[14] 382 12_DusSpurVement UT_Cnt_s160[16] 388 12_DusSpurVement UT_Cnt_s160[16] 380 12_DusSpurVement UT_Cnt_s160[16] 380 12_DusSpurVement UT_Cnt_s160[17] 380 12_DusSpurVement UT_Cnt_s160[18] 380 12_DusSpurVement UT_Cnt_s160[18	T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
12. DualSparVermicht UT. Cit.; 1490191 396 172. DualSparVermicht UT. Cit.; 1490191 356 173. DualSparVermicht UT. Cit.; 1490191 324 173. DualSparVermicht UT. Cit.; 1490191 252 173. DualSparVermicht UT. Cit.; 1490191 252 173. DualSparVermicht UT. Cit.; 1490191 252 173. DualSparVermicht UT. Cit.; 1490191 316 174. DualSparVermicht UT. Cit.; 1490191 316 175. DualSparVermicht UT. Cit.; 1490191 316 177. DualSparVermicht UT. Cit.; 1490191 317 177. DualSparVermicht UT. Cit.; 1490191 317 177. DualSparVermicht UT. Cit.; 1490191 318 177. DualSparVermicht UT. Cit.; 1490191 319 177. DualSparVermicht UT. Cit.; 1490191 319 177. DualSparVermicht UT. Cit.; 1490191 319 177. DualSparVermicht UT. Cit.; 1490191 322 177. DualSparVermicht UT. Cit.; 3490191 326 177. DualSparVermicht UT. Cit.; 3490191 326 177. DualSparVermicht UT. Cit.; 3490191 326 177. DualSparVermicht UT. Cit.; 3490191 327 177. DualSparVermicht UT. Cit.; 3490191 327 177. DualSparVermicht UT. Cit.; 3490191 329 177. DualSparVermicht U	T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
12_DasSgru/FemerU_T_Crt_s160[12]	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_DasSgru/FemerU_T_Crt_s160[12]	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2, DuaSpurVermiet.UT_Cnt_s160[15] 288		-360
T. DualSparVermicLUT_Cnt_s180[H] 252	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_Dus SpurVernieLUT_Cnt_s16[0][5] 216 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -144 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -144 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -148 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -148 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -72 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -36 T2_Dus SpurVern		-288
T2_Dus SpurVernieLUT_Cnt_s16[0][5] 216 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -144 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -144 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -148 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -148 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -72 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -36 T2_Dus SpurVern		
12, DualSparVermetLUT_Cnt_s16(0)(8) .189		
12. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -108 17. DuaSgurVemet.UT_Cnt_s160[18] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -72 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -73 17. DuaSgurVemet.UT_Cnt_s160[11] -74 17. DuaSgurVemet.UT_Cnt_s160[12] -74 17. DuaSgurVemet.UT_Cnt_s160[13] -74 17. DuaSg		
12_DusSpurVermetUT_Cnt_s160[18] -102 T2_DusSpurVermetUT_Cnt_s160[10] -72 T2_DusSpurVermetUT_Cnt_s160[10] -36 T2_DusSpurVermetUT_Cnt_s160[11] 0 T2_DusSpurVermetUT_Cnt_s160[11] 0 T2_DusSpurVermetUT_Cnt_s160[11] 36 T2_DusSpurVermetUT_Cnt_s160[11] 36 T2_DusSpurVermetUT_Cnt_s160[11] 108 T2_DusSpurVermetUT_Cnt_s160[11] 108 T2_DusSpurVermetUT_Cnt_s160[11] 108 T2_DusSpurVermetUT_Cnt_s160[11] 109 T2_DusSpurVermetUT_Cnt_s160[11] 109 T2_DusSpurVermetUT_Cnt_s160[11] 109 T2_DusSpurVermetUT_Cnt_s160[11] 109 T2_DusSpurVermetUT_Cnt_s160[12] 360 T2_DusSpurVermetUT_Cnt_s160[12] 360 T2_DusSpurVermetUT_Cnt_s160[12] 360 T2_DusSpurVermetUT_Cnt_s160[12] 360 T2_DusSpurVermetUT_Cnt_s160[12] 360 T2_DusSpurVermetUT_Cnt_s160[12] 109 T2_DusSpurVermetUT_Cnt_s160[12] 109 T2_DusSpurVermetUT_Cnt_s160[13] 109 T2_DusSpu		
12. DualSpur/vernietUT_Cnt_s16(0) 10 36 36 36 36 36 36 36 3		
12. DualSpur/vernierLUT_Cnt_s16()[11] 0 0 0 0 0 0 0 0 0		
12 DusiSpur/VernietUT_Cnt, 1610[11] 12 2 2 2 2 2 2 2 2		
12 DuaiSpurVernierLUT_Cnt_s16[0][12] 36 72 72 73 73 74 74 74 74 74 74		
T2 DualSpurVermierLUT_Cnt_s16[0][14] 108		
172 DuaiSpurVernierLUT_Cnt_sticip[14] 108 172 DuaiSpurVernierLUT_Cnt_sticip[16] 144 172 DuaiSpurVernierLUT_Cnt_sticip[17] 126 172 DuaiSpurVernierLUT_Cnt_sticip[17] 126 172 DuaiSpurVernierLUT_Cnt_sticip[17] 126 172 DuaiSpurVernierLUT_Cnt_sticip[18] 125 172 DuaiSpurVernierLUT_Cnt_sticip[18] 126 173 DuaiSpurVernierLUT_Cnt_sticip[18] 127 174 DuaiSpurVernierLUT_Cnt_sticip[18] 136 175 DuaiSpurVernierLUT_Cnt_sticip[18] 147 175 DuaiSpurVernierLUT_Cnt_sticip[18] 14		
T2 DualSpurVermict.UT Cnt s16(0) 15 144 T2 DualSpurVermict.UT Cnt s16(0) 16 180 T2 DualSpurVermict.UT Cnt s16(0) 17 216 T2 DualSpurVermict.UT Cnt s16(0) 17 216 T2 DualSpurVermict.UT Cnt s16(0) 18 252 T3 DualSpurVermict.UT Cnt s16(0) 20 324 T2 DualSpurVermict.UT Cnt s16(0) 20 324 T2 DualSpurVermict.UT Cnt s16(1) 10 9 T3 DualSpurVermict.UT Cnt s16(1) 10 9 T2 DualSpurVermict.UT Cnt s16(1) 10 9 T3 DualSpurVermict.UT Cnt s16(1) 10 1 T4 DualSpurVermict.UT Cnt s16(1) 10 1 T5 DualSpurVermict.UT Cnt s16(1) 10 1 T5 DualSpurVermict.UT Cnt s16(1) 10 3 T2 DualSpurVermict.UT Cnt s16(1) 10 5 T2 DualSpurVermict.UT Cnt s16(1) 10 5 T3 DualSpurVermict.UT Cnt s16(1) 10 5 T4 DualSpurVermict.UT Cnt s16(1) 10 6 T5 DualSpurVermict.UT Cnt s16(1) 10 7 T5 DualSpurVermict.UT Cnt s16(1) 10 8 T5 DualSpurVermict.UT Cnt s16(1) 10 9 T5 DualSpurVermict.UT Cnt s16(1) 10 9 T5 DualSpurVermict.UT Cnt s16(1) 10 9 T6 DualSpurVermict.UT Cnt s16(1) 10 9 T7 DualSpurVermict.UT Cnt s16(1) 10 9 T8 DualSpurVermict.UT Cnt s16(1) 10 9 T7 DualSpurVermict.UT Cnt s16(1) 10		
12		
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T2_DualSpurVemiert.UT_Cnt_s16[1][8] 7 T2_DualSpurVemiert.UT_Cnt_s16[1][9] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][10] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][12] 1 T2_DualSpurVemiert.UT_Cnt_s16[1][13] 2 T2_DualSpurVemiert.UT_Cnt_s16[1][14] 3 T2_DualSpurVemiert.UT_Cnt_s16[1][15] 4 T2_DualSpurVemiert.UT_Cnt_s16[1][16] 5 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][20] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 0 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][2] 2 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 3 T2_DualSpurVemiert.UT_Cnt_s16[2][4] 4 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 5 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 6 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemier.UT_Cnt_s16[2][7] 7	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
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T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 2 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][15] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		9
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T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
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T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
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T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] 9	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] 10	T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] 0	T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] 1	T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2 DualSpurVernierLUT Cnt s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2 DualSpurVernierLUT Cnt s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
T2 DualSpurVernierLUT Cnt s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	228		
k_SkipStepErrDiag_Cnt_str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	40		
k_VernCorrErrorDiag_Cnt_str.PStep	32		
k VernCorrErrorDiag Cnt str.NStep	5		
k_VernCorrErrorThresh_Deg_f32	23.81406522		
k_VernOORangeThresh_Deg_f32	1525.900935		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	34.50624543		
tgt Pim DigColPsEOL.SpurTrim Deg f32	324.5753602		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2878		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs HwAVernCorrFault Cnt M lgc	0	0	, todu
DigColPs I2CHwColAngleForTrim Deg M f32	1472.72717	1472.727273 ± 0.00048828125	
DigColPs I2CHwTrimTransCnts Uls M u08	5	5	
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1468.90625	1468.906245 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	15	15	
DigColPa Pagl2CSparDataTypa Cat. M u09	1	1	

0	0 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1468.90625	1468.906245 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	568.90625	568.9062454 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	~
Status	0x00	0x00	✓



T .				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	-

Test Step 2.42 (Repeat Count = 1)	v
Name	Input Value
DigColPsInt_GetCustData()	163
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158
DigColPs_ColTrimStatic_Deg_M_f32	9.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	52348
DigColPs_I2CHwColAngle_Deg_M_f32	222.1544354
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27884
DigColPs I2CHwSpurAngle Deg M f32	44.3
DigColPs I2CHwTrimTransCnts UIs M u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1405.727187
DigColPs PrevVernierLevelNo Cnt M u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	163
DigColPs_SpurTrimStatic_Deg_M_f32	44.3
DigColPs TrimCompStatic Cnt M u16	1384
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Input Value 1 0 4 0
4
0
8
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16
13
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17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
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9
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1
2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2] T3_DualSpurVernierLUT_Cnt_s16[2][2]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2 DualSpurVernierLUT Cnt s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	222		
k_SkipStepErrDiag_Cnt_str.PStep	8		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	68		
k_VernCorrErrorDiag_Cnt_str.PStep	30		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k_VernCorrErrorThresh_Deg_f32	28.48486996		
k_VernOORangeThresh_Deg_f32	1595.635967		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	222.1544354		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	78.57416618 1		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt_Pim_DigColPs_DigColPs_Pim_ColPs_Pim_2 12CHwAhsPosValid_Cnt_Inc		ant lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPosValid_C tgt_DigColPs_Per2_I2CHwAbsPos_HwDe		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	9_102	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
THE PARTY OF THE P		· ·	Kesuii
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	68	68	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	50	50	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

lame	Input Value	
DigColPsInt GetCustData()	142	
DigColPs ColParityError Cnt M Igc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	
DigColPs ColTrimStatic Deg M f32	13.8	
DigColPs HwAVernCorrFault Cnt M Igc	0	
DigColPs I2CColSensorFault Cnt M Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	9945	
DigColPs_I2CHwColAngle_Deg_M_f32	253.5686912	
DigCoIPs I2CHwDataType Cnt M u08	3	
DigColPs I2CHwSpurAngle Cnt M u16	37553	
DigColPs_I2CHwSpurAngle_Deg_M_f32	45.4	
igColPs_I2CHwTrimTransCnts_Uls_M_u08	1	
bigColPs_I2CSensCommFlts_Cnt_M_u08	4	
bigColPs I2CSpurSensorFault Cnt M Igc	0	
	0	
ligColPs_PrevAngleDataAvailable_Cnt_M_lgc ligColPs_PrevColPos_Deg_M_f32	319.1410994	
	11	
igColPs_PrevVernierLevelNo_Cnt_M_u08 igColPs_SkipStepFltDetectAcc_Cnt_M_u16	7	
	0	
igColPs_SpurParityError_Cnt_M_lgc	142	
igColPs_SpurSensorFaultAcc_Cnt_M_u16	45.4	
bigColPs_SpurTrimStatic_Deg_M_f32	1420	
igColPs_TrimCompStatic_Cnt_M_u16	10	
higColPs_VernCorrDetectAcc_Cnt_M_u16	1	
higColPs_VernierAngleOORange_Cnt_M_lgc		
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163 -131	
2_ColSpurVernierLUT_Cnt_s16[0][1]	-	
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66	
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33	
2_ColSpurVernierLUT_Cnt_s16[0][5]	0	
2_ColSpurVernierLUT_Cnt_s16[0][6]	32	
2_ColSpurVernierLUT_Cnt_s16[0][7]	65	
2_ColSpurVernierLUT_Cnt_s16[0][8]	98	
2_ColSpurVernierLUT_Cnt_s16[0][9]	130	
2_ColSpurVernierLUT_Cnt_s16[0][10]	163	
2_ColSpurVernierLUT_Cnt_s16[0][11]	196	
2_ColSpurVernierLUT_Cnt_s16[0][12]	229	
2_ColSpurVernierLUT_Cnt_s16[0][13]	261	
2_ColSpurVernierLUT_Cnt_s16[0][14]	294	
2_ColSpurVernierLUT_Cnt_s16[0][15]	327	
2_ColSpurVernierLUT_Cnt_s16[0][16]	359	
2_ColSpurVernierLUT_Cnt_s16[1][0]	0	
2_ColSpurVernierLUT_Cnt_s16[1][1]	4	
2_ColSpurVernierLUT_Cnt_s16[1][2]	3	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[7][10]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_\$16[0][15] T2_DualSpurVernierLUT_Cnt_\$16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
12_50010pur vormore01_0nt_310[0][10]	288
T2 DualSpurVernierLUT Cnt s16f01f191	120
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	360





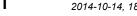
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15] T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	5
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	124
k_SkipStepErrDiag_Cnt_str.PStep	50
k_SkipStepErrDiag_Cnt_str.NStep	31
k_VernCorrErrorDiag_Cnt_str.Threshold	80
k_VernCorrErrorDiag_Cnt_str.PStep	3
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	1
k_VernOORangeThresh_Deg_f32	414.5643529
1 1 0.11 0 0 1 tall g 0 1 1 1 0 0 1 2 0 g _ 1 0 2	

DigColPs_Per2

Param

Status

2014-10-14, 18:16:06+0530





Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	253.5686912		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.5532733		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2452		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1319.76868	1319.768691 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6	6	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	409.09082	409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	•

0x0C

0x01

0x0C

Test Step 2.44 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	158
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	17.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	37674
DigColPs_I2CHwColAngle_Deg_M_f32	266.7729402
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64843
DigColPs_I2CHwSpurAngle_Deg_M_f32	46.5
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	12
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1117.242519
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158
DigColPs_SpurTrimStatic_Deg_M_f32	46.5
DigColPs_TrimCompStatic_Cnt_M_u16	1456
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

2014-10-14, 18:16:06+0530



Name		
T. Codes/weinestall Cert 9-100 52	Name	Input Value
T. Cossy-version UP_OL_seg07 65	T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T. Codepartment(T. Cut Strollip) 150	T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 13_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 13_OSB_WarrenicLD_CB_SBUP[10] 14_OSB_WarrenicLD_CB_SBUP[10] 15_OSB_WarrenicLD_CB_SBUP[10] 16_OSB_WarrenicLD_CB_SBUP[10] 17_OSB_WarrenicLD_CB_SBUP[10] 18_OSB_WarrenicLD_CB_SBUP[10] 18_OSB_WarrenicLD_CB_SBUP[10] 19_OSB_WarrenicLD_CB_SBUP[10] 19_OSB_WarrenicLD_CB_SBUP[10] 10_OSB_WarrenicLD_CB_SBUP[10] 11_OSB_WarrenicLD_CB_SBUP[10] 11_OSB	T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 13_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 12_OSB_WarrenicLD_CB_SBUP[10] 13_OSB_WarrenicLD_CB_SBUP[10] 14_OSB_WarrenicLD_CB_SBUP[10] 15_OSB_WarrenicLD_CB_SBUP[10] 16_OSB_WarrenicLD_CB_SBUP[10] 17_OSB_WarrenicLD_CB_SBUP[10] 18_OSB_WarrenicLD_CB_SBUP[10] 18_OSB_WarrenicLD_CB_SBUP[10] 19_OSB_WarrenicLD_CB_SBUP[10] 19_OSB_WarrenicLD_CB_SBUP[10] 10_OSB_WarrenicLD_CB_SBUP[10] 11_OSB_WarrenicLD_CB_SBUP[10] 11_OSB	T2 ColSpurVernierLUT Cnt s16[0][8]	98
12, Collego/wome.Lift Col., 18(9)(1)		
12. CoSSparVersiteLTU, Cot. 1-90[011] 12. CoSSparVersiteLTU, Cot. 1-90[013] 12. CoSSparVersiteLTU, Cot. 1-90[013] 12. CoSSparVersiteLTU, Cot. 1-90[015] 12. CoSSparVersiteLTU Cot. 1-90[015] 12. CoSSparVersiteLTU Cot. 1-90[015] 12. CoSSparVersiteLTU Cot. 1-90[015] 13. CoSSparVersiteLTU, Cot. 1-90[016] 13. CoSSparVersiteLTU, Cot. 1-90[016] 14. CoSSparVersiteLTU, Cot. 1-90[017] 15. CoSSparVersiteLTU, Cot. 1-90[017] 16. CoSSparVersiteLTU, Cot. 1-90[017] 17. CoSSparVersiteLTU, Cot. 1-90[017] 18. CoSSparVersiteLTU, Cot. 1-90[017] 18. CoSSparVersiteLTU, Cot. 1-90[017] 18. CoSSparVersiteLTU, Cot. 1-90[017] 18. CoSSparVersiteLTU, Cot. 1-90[017] 19. CoSSparVersiteLTU, Cot. 1-90[01		
12_CoSsylvement_U_Cst_Stigl_19 12_CoSsylvement_U_Cst_Stigl_19 12_CoSsylvement_U_Cst_Stigl_19 12_CoSsylvement_U_Cst_Stigl_19 13_CoSsylvement_U_Cst_Stigl_19 14_CoSsylvement_U_Cst_Stigl_19 15_CoSsylvement_U_Cst_Stigl_19 16_CoSsylvement_U_Cst_Stigl_19 17_CoSsylvement_U_Cst_Stigl_19 18_CoSsylvement_U_Cst_Stigl_19 18_CoSsylvement_U_Cst_Stigl_19 18_CoSsylvement_U_Cst_Stigl_19 18_CoSsylvement_U_Cst_Stigl_19 18_CoSsylvement_U_Cst_Stigl_19 19_CoSsylvement_U_Cst_Stigl_19 19_C		
12. CoSpa/verbinetUT Cut 190 119 284 12. CoSpa/verbinetUT Cut 190 119 294 12. CoSpa/verbinetUT Cut 190 119 337 12. CoSpa/verbinetUT Cut 190 119 399 12. CoSpa/verbinetUT Cut 190 119 4 12. CoSpa/verbinetUT Cut 190 119 4 12. CoSpa/verbinetUT Cut 190 119 2 12. CoSpa/verbinetUT Cut 190 119 2 13. CoSpa/verbinetUT Cut 190 119 3 14. CoSpa/verbinetUT Cut 190 119 3 15. CoSpa/verbinetUT Cut 190 119 3 17. CoSpa/verbinetUT Cut 190 119 3 18. CoSpa/verbinetUT Cut 190 119 3 19. CoSpa/verbinetUT Cut 190 119 4 19. CoSpa/verbinetUT Cut 190 119 4 19. CoSpa/verbinetUT Cut 190 119 3 19. CoSpa/verbinetUT Cut 190 119 4 19. CoSpa/verbinetUT Cut 190 119 4 19. CoSpa/verbinetUT Cut 190 119 5 19. CoSpa/verbinetUT Cut 190 119 5 19. CoSpa/verbinetUT Cut 190 119 6 19. CoSpa/verbinetUT Cut 190 119 6 19. CoSpa/verbinetUT Cut 190 119 6 19. CoSpa/verbinetUT Cut 190 119 7 19. CoSpa/v		
12_Colspa/wemeLT_Cst_15(0)14 12_Colspa/wemeLT_Cst_15(0)15 12_Colspa/wemeLT_Cst_15(0)15 12_Colspa/wemeLT_Cst_15(0)15 12_Colspa/wemeLT_Cst_15(0)15 12_Colspa/wemeLT_Cst_15(0)15 12_Colspa/wemeLT_Cst_15(0)15 13_Colspa/wemeLT_Cst_15(0)15 13_Colspa/wemeLT_Cst_15(0)15 14_Colspa/wemeLT_Cst_15(0)15 15_Colspa/wemeLT_Cst_15(0)15 15_Colspa/wemeLT_Cst_15(0)15		
12_CoSpavimentU_Cot_stiQUIS 2_CoSpavimentU_Cot_stiQUIS 359 12_CoSpavimentU_Cot_stiQUIS 13_CoSpavimentU_Cot_stiQUIS 14_CoSpavimentU_Cot_stiQUIS 15_CoSpavimentU_Cot_stiQUIS 15_CoSpavimentU_Cot_stiQUIS 15_CoSpavimentU_Cot_stiQUIS 16_CoSpavimentU_Cot_stiQUIS 16_CoSpavimentU_Cot_stiQUIS 17_CoSpavimentU_Cot_stiQUIS 18_CoSpavimentU_Cot_stiQUIS		
12_Collegar/went_U_Cot_still_10	T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_COSparVennetU_Cot_st0 10	T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
12. OSBAVement U. Cnt. st 1911 4	T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_OSSpavement_U_Cnt_stq1 2 3	T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_OSSpavement_U_Cnt_stq1 2 3	T2 ColSpurVernierLUT Cnt s16[1][1]	4
12. Collaps/wement_U_Cnt_stq[1]s 1 1 1 1 1 1 1 1 1		
12, CuSport/emistUT, Cnt.; 16(1)[8] 12, CuSport/emistUT, Cnt.; 16(1)[8] 12, CuSport/emistUT, Cnt.; 16(1)[8] 13, CuSport/emistUT, Cnt.; 16(1)[8] 14, CuSport/emistUT, Cnt.; 16(1)[8] 15, CuSport/emistUT, Cnt.; 16(1)[8] 16, CuSport/emistUT, Cnt.; 16(1)[8] 17, CuSport/emistUT, Cnt.; 16(1)[8] 17, CuSport/emistUT, Cnt.; 16(1)[8] 18, CuSport/emistUT, Cnt.; 16(1)[8] 19, CuSport/emistUT, Cnt.; 16(1)[8] 10, CuSport/emistUT, Cnt.; 16(1)[8] 11, CuSport/emistUT, Cnt.; 16(1)[8] 12, CuSport/emistUT, Cnt.; 16(2)[8] 13, CuSport/emistUT, Cnt.; 16(2)[8] 14, CuSport/emistUT, Cnt.; 16(2)[8] 15, CuSport/emistUT, Cnt.; 16(2)[8] 16, CuSport/emistUT, Cnt.; 16(2)[8] 17, CuSport/emistUT, Cnt.; 16(2)[8] 18, CuSport/emistUT, Cnt.; 16(2)[8] 19, CuSport/emistUT, Cnt.; 16(2)[8] 10, CuSport/emistUT, Cnt.; 16(2)[8] 11, CuSport/emistUT, Cnt.; 16(2)[8] 11, CuSport/emistUT, Cnt.; 16(2)[8] 12, CuSport/emistUT, Cnt.; 16(2)[8] 13, CuSport/emistUT, Cnt.; 16(2)[8] 14, CuSport/emistUT, Cnt.; 16(2)[8] 15, CuSport/emistUT, Cnt.; 16(2)[8] 16, CuSport/emistUT, Cnt.; 16(2)[8] 17, CuSport/emistUT, Cnt.; 16(2)[8] 18, CuSport/emistUT, Cnt.; 16(2)[8] 19, CuSport/emistUT, Cnt.; 16(2)[8] 10, CuSport/emistUT, Cnt.; 16(2)[8] 11, CuSport/emistUT, Cnt.; 16(2)[8] 11, CuSport/emistUT, Cnt.; 16(2)[8] 12, CuSport/emistUT, Cnt.; 16(2)[8] 13, CuSport/emistUT, Cnt.; 16(2)[8] 14, CuSport/emistUT, Cnt.; 16(2)[8] 15, CuSport/emistUT, Cnt.; 16(
12_Cospur/ameLU_Crt_st0 15 4		
12. Colsport/mentUT. Cnt. s16/19/17 12. Colsport/mentUT. Cnt. s16/19/19 12. Colsport/mentUT. Cnt. s16/19/19 13. Colsport/mentUT. Cnt. s16/19/19 14. Colsport/mentUT. Cnt. s16/19/19 15. Colsport/mentUT. Cnt. s16/19/19 16. Colsport/mentUT. Cnt. s16/19/19 17. Colsport/mentUT. Cnt. s16/19/19 18. Colsport/mentUT. Cnt. s16/19/19 19. Colsport/mentUT. Cnt. s16/		
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T. CosSpurVernicutU	T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
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T2_ColSpurVernierLUT_Cnt_s16[3][6]		
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T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][6] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		9
T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][2] -288 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10 T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7 T2_ColSpurVernierLUT_Cnt_s16[3][15] 4 T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_ColSpurVernierLUT_Cnt_s16[3][15]		
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17 T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396 T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360 T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][2] -324 T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		-360
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288 T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252 T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][5] -216 T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108		
	T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
	T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
12_DualSpurVernierLUT_Cnt_s16[0][9] -72	T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72

2014-10-14, 18:16:06+0530



Namo	Input Value
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2 DualSpurVernierLUT Cnt s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
	9
T2_DualSpur/crierLUT_Cnt_s16[2][20]	
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
	3
T2_DualSpurVernierLUT_Cnt_s16[3][12]	
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][13]	

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 $k_SelectFromColumn_Cnt_lgc$ 0 k_SkipStepErrDiag_Cnt_str.Threshold 132 k_SkipStepErrDiag_Cnt_str.PStep 46 k_SkipStepErrDiag_Cnt_str.NStep 36 $k_VernCorrErrorDiag_Cnt_str.Threshold$ 27 k_VernCorrErrorDiag_Cnt_str.PStep 31 $k_VernCorrErrorDiag_Cnt_str.NStep$ 43 k_VernCorrErrorThresh_Deg_f32 59.61001611 220.0944071 $k_VernOOR angeThresh_Deg_f32$ tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 266.7729402 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 132.5881469 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 1187 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc$ $tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum$ $tgt_DigColPs_Per2_MecState_Cnt_enum$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_DigColPs_Per2_TrimComp_Cnt_lgc

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓	
DigColPs_PrevColPos_Deg_M_f32	968.872925	968.8729402 ± 0.0001220703125	✓	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	✓	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓	
NTC	0x6C	0x6C	✓	
Param	0x00	0x00	✓	
Status	0x00	0x00	✓	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.45 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	125	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175	
DigColPs_ColTrimStatic_Deg_M_f32	22	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	16067	
DigColPs_I2CHwColAngle_Deg_M_f32	272.6490288	
DigCoIPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16937	
DigColPs_I2CHwSpurAngle_Deg_M_f32	47.6	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	





Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	1733.007516
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	47.6 1492
DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigCoIPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1 0
T2_ColSpurVernierLUT_Cnt_s16[1][5] T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	288 324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3] T3_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_S16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22 2
T2_DualSpurVernierLUT_Cnt_s16[3][1]	

2014-10-14, 18:16:06+0530





DigCoirs_rei2			
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2 DualSpurVernierLUT Cnt s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2 DualSpurVernierLUT Cnt s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k SkipStepErrDiag Cnt str.Threshold	80		
k SkipStepErrDiag Cnt str.PStep	43		
k_SkipStepErrDiag_Cnt_str.NStep	7		
k VernCorrErrorDiag Cnt str.Threshold	6		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	14		
k_VernCorrErrorThresh_Deg_f32	86.69760323		
k_VernOORangeThresh_Deg_f32	1173.76136		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	272.6490288		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.17228091		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	621		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt	_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn	ıt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	970.649048	970.6490288 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	v
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7	7	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V

70.6490479

0

0

0

70.64902878 ± 0.00009

Test Step 2.46 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	165
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	26.1

 $tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value\\ tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value\\$

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56371
	296.9508778
DigColPs_I2CHwColAngle_Deg_M_f32	
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11434
DigColPs_I2CHwSpurAngle_Deg_M_f32	48.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs PrevColPos Deg M f32	267.2598278
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165
DigColPs_SpurTrimStatic_Deg_M_f32	48.7
DigColPs_TrimCompStatic_Cnt_M_u16	1528
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs VernierAngleOORange Cnt M Igc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
	32
T2_ColSpurVernierLUT_Cnt_s16[0][6]	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	[·

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T3_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
12 Duan-Opur vollilote O	•
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

2014-10-14, 18:16:06+0530



DigCoiPs_Per2			CILAI
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	57		
k_SkipStepErrDiag_Cnt_str.PStep	9		
k_SkipStepErrDiag_Cnt_str.NStep	18		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	11		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	9.823269606		
k_VernOORangeThresh_Deg_f32	664.8207433		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	296.9508778		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	182.5995052		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	189		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosV		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_e		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_	_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	990.85083	990.8508778 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DiaColPs RegI2CSpsrDataType Cpt M u08	14	4	1

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	990.85083	990.8508778 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	90.8508301	90.85087785 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.47 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	144
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DigColPs_ColTrimStatic_Deg_M_f32	30.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	4911
DigColPs_I2CHwColAngle_Deg_M_f32	75.69248641
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	45521
DigColPs_I2CHwSpurAngle_Deg_M_f32	49.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	21
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1606.298487
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144
DigColPs_SpurTrimStatic_Deg_M_f32	49.8
DigColPs_TrimCompStatic_Cnt_M_u16	1564
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T0_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

2014-10-14, 18:16:06+0530



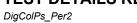
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T3_ColSpurVernierLUT_Cnt_s16[2][2]	4
T2_ColSpurVernierLUT_Cnt_s16[2][3]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
	17
T2_ColSpurVernierLUT_Cnt_s16[3][16]	
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	8 9 0
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	8 9

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
	4		
	5		
	6		
	7		
	8		
	0		
	0		
	1		
	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
	5		
	6		
	7		
	8		
	9		
	0		
	1		
	2		
	3		
	4		
	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
	9		
	10		
	22		
	2		
	4		
	6 8		
	10		
	12		
	14		
	16		
	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
	3		
	5		
2 11 1/1 1 1 1 2 2 1 1 1 1 1	7		
	9		
	11		
	13 15		
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
	19		
	21		
	1		
	27		
k_SkipStepErrDiag_Cnt_str.PStep	36		
k_SkipStepErrDiag_Cnt_str.NStep	31		
	8		
	37		
	5		
	21.03098726		
k_VernOORangeThresh_Deg_f32	132.1493682		
02 011 12 1 2 11111121 21 1 1 1 1 1	75 60248641		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	75.69248641 19.71645284		
	1		
	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_	lac	
	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_t		
	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_ttte_mot_ea_bigeon o.bigeon o_r ci2_timeonp ont ige	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
	tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL		Expected Value	Result
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL Name	tgt_Pim_DigColPsEOL	Expected Value	Result





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	✓

Т			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.48 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	124
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	186
DigColPs_ColTrimStatic_Deg_M_f32	34.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs I2CHwColAngle Cnt M u16	28758
DigColPs I2CHwColAngle Deg M f32	169.2136934
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs I2CHwSpurAngle Cnt M u16	1113
DigColPs I2CHwSpurAngle Deg M f32	50.9
DigCoIPs I2CHwTrimTransCnts Uls M u08	6
	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	
DigColPs_PrevColPos_Deg_M_f32	1698.48323
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	50.9
DigColPs_TrimCompStatic_Cnt_M_u16	1600
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





	(
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][6]	
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
	144
T2_DualSpurVernierLUT_Cnt_s16[0][15]	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216

2014-10-14, 18:16:06+0530



Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]		



Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	64.4036839		
k_VernOORangeThresh_Deg_f32	1423.580669		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	169.2136934		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	135.3572482		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3790		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	<u>f</u> 32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~

DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	494.913696	494.9136934 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-405.086304	-405.0863066 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.49 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	143
DigColPs ColParityError Cnt M Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184
DigColPs ColTrimStatic Deg M f32	38.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs I2CHwColAngle Cnt M u16	49805
DigColPs 12CHwColAngle Deg M f32	313.3494742
DigColPs 12CHwDataType Cnt M u08	3
DigColPs 12CHwSpurAngle Cnt M u16	22222
DigColPs I2CHwSpurAngle Deg M f32	52
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs 12CSensCommFlts Cnt M u08	20
DigColPs 12CSpurSensorFault Cnt M Igc	0
DigColPs PrevAngleDataAvailable Cnt M Igc	1
DigColPs PrevColPos Deg M f32	687.96434
DigColPs PrevVernierLevelNo Cnt M u08	0
DigColPs SkipStepFltDetectAcc Cnt M u16	0
DigColPs SpurParityError Cnt M Igc	0
DigColPs SpurSensorFaultAcc Cnt M u16	143
DigColPs SpurTrimStatic Deg M f32	52
	1636
DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs VernCorrDetectAcc Cnt M u16	10
	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0]	1.7
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-90
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2 ColSpurVernierLUT Cnt s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2 ColSpurVernierLUT Cnt s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
	11
T2_ColSpurVernierLUT_Cnt_s16[3][2]	
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
	10
T2_ColSpurVernierLUT_Cnt_s16[3][13]	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
	202
	216
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][6] T2_DualSpurVernierLUT_Cnt_s16[0][7]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2 DualSpurVernierLUT Cnt s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

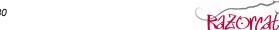
DigColPs_VernCorrDetectAcc_Cnt_M_u16
DigColPs_VernierAngleOORange_Cnt_M_lgc

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value

 $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value\\ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value$

DigColPs_Per2

2014-10-14, 18:16:06+0530



0

0

0

94.94947419 ± 0.00009

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	218		
k_SkipStepErrDiag_Cnt_str.PStep	31		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	46		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	47.04804516		
k_VernOORangeThresh_Deg_f32	914.2227411		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	313.3494742		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	162.8974475		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2322		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	994.949463	994.9494742 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

0

0

0

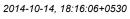
94.9494629

Test Step 2.50 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	131	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	
DigColPs_ColTrimStatic_Deg_M_f32	42.5	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	16100	
DigColPs_I2CHwColAngle_Deg_M_f32	299.0314264	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	45386	
DigColPs_I2CHwSpurAngle_Deg_M_f32	53.1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	
DigColPs_I2CSensCommFlts_Cnt_M_u08	28	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1756.602492	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	255	
DigColPs_SpurParityError_Cnt_M_lgc	0	





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	131
DigColPs_SpurTrimStatic_Deg_M_f32	53.1
DigColPs_TrimCompStatic_Cnt_M_u16	1672
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs T2_ColSpurVernierLUT_Cnt_s16[0][0]	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_GolSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2] T3_ColSpurVernierLUT_Cnt_s16[1][2]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][3]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][3]	
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_GolSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
oo.opurvomior_o1_oni_o10[o][10]	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_S10[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T3_DualSpurVernierLUT_Cnt_s16[2][10]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8

DigColPs_Per2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	161		
k_SkipStepErrDiag_Cnt_str.PStep	1		
k_SkipStepErrDiag_Cnt_str.NStep	19		
k_VernCorrErrorDiag_Cnt_str.Threshold	79		
k_VernCorrErrorDiag_Cnt_str.PStep	19		
k_VernCorrErrorDiag_Cnt_str.NStep	4	4	
k_VernCorrErrorThresh_Deg_f32	29.57760787		
k_VernOORangeThresh_Deg_f32	320.9261016	320.9261016	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	299.0314264		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	107.5765935		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3593		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	-
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	976.531433	976.5314264 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs Regl2CSnsrDataType Cnt M u08	4	4	-
		12.7	

DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	•
DigCoIPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	976.531433	976.5314264 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	161	161	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	•
Param	0x0E	0x0E	~
Status	0x01	0x01	✓

au				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.51 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	142
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	46.6





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	24812
DigColPs_I2CHwColAngle_Deg_M_f32	148.1551865
	1
DigColPs_12CHwDataType_Cnt_M_u08	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7234
DigColPs_I2CHwSpurAngle_Deg_M_f32	54.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	724.8810905
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	54.2
	1708
DigColPs_TrimCompStatic_Cnt_M_u16	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2 ColSpurVernierLUT Cnt s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
	0
T2_ColSpurVernierLUT_Cnt_s16[2][0]	
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1

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Nome	Innut Value
Name T2_ColSpurVernierLUT_Cnt_s16[3][1]	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17 -396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-390 -360
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252 -252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18] T3_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11] T3_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T3_DualSpurVernierLUT_Cnt_s16[2][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2 DualSpurVernierLUT Cnt s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][14]			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	195		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	10		
k_VernCorrErrorDiag_Cnt_str.Threshold	58		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	84.35098028		
k_VernOORangeThresh_Deg_f32	1554.614787		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	148.1551865		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	349.5774245		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_	HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_	enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs HwAVernCorrFault Cnt M lgc	1	1	
DigColPs I2CHwColAngleForTrim Deg M f32	356.646606	356.6466252 ± 0.00048828125	
DigColPs_12CHwTrimTransCnts_Uls_M_u08	1	1	
DigColPs_IzchwTillmTranscnts_Ois_M_uoo DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs PrevColPos Deg M f32	360	360 ± 0.0001220703125	
	5	5	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	1 '

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	356.646606	356.6466252 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	✓



Τ			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.52 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	134
DigColPs ColTrimStatic Deg M f32	50.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	26033
DigColPs I2CHwColAngle Deg M f32	166.9625559
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7191
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.3
DigColPs I2CHwTrimTransCnts Uls M u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	3
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs SkipStepFltDetectAcc Cnt M u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	55.3
DigColPs TrimCompStatic Cnt M u16	1744
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

2014-10-14, 18:16:06+0530



Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

2014-10-14, 18:16:06+0530



DigColPs_Per2

Digeon 6_1 et2			1-4-10-10
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6 8		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
<_SelectFromColumn_Cnt_lgc	1		
SkipStepErrDiag Cnt str.Threshold	175		
SkipStepErrDiag_Ont_str: PStep	45		
SkipStepErrDiag_Ont_str. Otep SkipStepErrDiag_Ont_str. Otep	19		
VernCorrErrorDiag Cnt str.Threshold	8		
VernCorrErrorDiag Cnt str.PStep	41		
<pre><_VernCorrErrorDiag_Cnt_str.NStep</pre>	19		
 VernCorrErrorThresh Deg f32 	31.69468141		
<pre><_vernoonEnd**rinesh_beg_id2</pre> <pre><_VernooRangeThresh_beg_fd2</pre>	1512.089929		
gt_DigColPs_Per2_MecState_Cnt_enum.value	1		
gt_Pim_DigColPsEOL.ColTrim_Deg_f32	166.9625559		
gt_Pim_DigColPsEOL.SpurTrim_Deg_f32	4.647624195		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2354		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid Cnt lgc	
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPsEOL	tgt_Pim_DigColPsEOL	g	
Name	Actual Value	Expected Value	Resu
	1	1	Rest
DigColPs_HwAVernCorrFault_Cnt_M_lgc	'	1	

490.909088

490.9090909 ± 0.00048828125

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	476.262573	476.2625559 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2 ColSpurVernierLUT Cnt s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T3_DualSpurVernierLUT_Cnt_s16[0][11]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][15]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
	360 9

2014-10-14, 18:16:06+0530



Name -	Invest Malica
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2 DualSpurVernierLUT Cnt s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2 DualSpurVernierLUT Cnt s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2 DualSpurVernierLUT Cnt s16[3][0]	22
T2 DualSpurVernierLUT Cnt s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
	10
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	223
k_SkipStepErrDiag_Cnt_str.PStep	11
k_SkipStepErrDiag_Cnt_str.NStep	45
k_VernCorrErrorDiag_Cnt_str.Threshold	41
k_VernCorrErrorDiag_Cnt_str.PStep	39
	0
k_VernCorrErrorDiag_Cnt_str.NStep	
k VernCorrErrorThresh Deg f32	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	98.42212296 1614.228407



Name	Input Value		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	125.941998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	234.6564466		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	305		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1151.14197	1151.141998 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	245.454468	245.4545455 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	✓

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.54 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	138
DigColPs_ColTrimStatic_Deg_M_f32	58.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	17718
DigColPs_I2CHwColAngle_Deg_M_f32	89.98652095
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26140
DigColPs_I2CHwSpurAngle_Deg_M_f32	57.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	686.9139401
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	57.5
DigColPs_TrimCompStatic_Cnt_M_u16	1816
DigColPs_VernCorrDetectAcc_Cnt_M_u16	16
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][13]	229 261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2 ColSpurVernierLUT Cnt s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][43]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T3_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T3_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][0] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-396 -360
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][3] T2_DualSpurVernierLUT_Cnt_s16[0][4]	-288 -252
T2_DualSpurVernierLUT_Cnt_s16[0][4] T3_DualSpurVernierLUT_Cnt_s16[0][5]	-252 -216
T2_DualSpurVernierLUT_Cnt_s16[0][5] T3_DualSpurVernierLUT_Cnt_s16[0][6]	-216 -180
T2_DualSpurVernierLUT_Cnt_s16[0][6] T2_DualSpurVernierLUT_Cnt_s16[0][7]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][7]	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108

2014-10-14, 18:16:06+0530



Name	Invest Walter
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][13]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	5
T2_DualSpurVernierLUT_Cnt_s16[1][16]	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpur/emierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
TO DualOut / caried LIT Out 10/07/103	-
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	5 7 9

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13	13	
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	168		
k_SkipStepErrDiag_Cnt_str.PStep	25		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	56		
k_VernCorrErrorDiag_Cnt_str.PStep	27		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	40.71416354		
k_VernOORangeThresh_Deg_f32	852.5587618		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	89.98652095		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	221.6592153		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2805		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1471.08655	1471.086521 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	✓

T ✓				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0

Test Step 2.55 (Repeat Count = 1)		✓.
Name	Input Value	
DigColPsInt_GetCustData()	156	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	
DigColPs_ColTrimStatic_Deg_M_f32	63	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	38087	
DigColPs_I2CHwColAngle_Deg_M_f32	291.3419048	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	36636	
DigColPs_I2CHwSpurAngle_Deg_M_f32	58.6	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	886.4049975	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	

 $tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value$

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156
DigColPs_SpurTrimStatic_Deg_M_f32	58.6
DigColPs_TrimCompStatic_Cnt_M_u16	1852
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][2] T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_coloputVcmicrLGT_cnt_s16[1][0] T2 ColSpurVcmicrLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252 -216
T2_DualSpurVernierLUT_Cnt_s16[0][5] T2_DualSpurVernierLUT_Cnt_s16[0][6]	-216 -180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2 DualSpurVernierLUT Cnt s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][6]	6
T2_DualSpurVernierLUT_Cnt_s16[1][7] T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8] T3_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
	8

2014-10-14, 18:16:06+0530



DigColPs_Per2			Razorcat
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	171		
k_SkipStepErrDiag_Cnt_str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	4		
k_VernCorrErrorDiag_Cnt_str.Threshold	61		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	80.16494608		
k_VernOORangeThresh_Deg_f32	995.0178322		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	291.3419048		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	192.5007017		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	g_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_Igc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1084.59058	1084.59059 ± 0.0004882812	.5
DigColPs I2CHwTrimTransCnts UIs M u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs PrevColPos Deg M f32	1080	1080 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✓
DigColPs Regl2CSnsrDataType Cnt M u08	1	1	~
DigColPs SkipStepFltDetectAcc Cnt M u16	0	0	✓
DigColPs VernCorrDetectAcc Cnt M u16	0	0	~
DigColPs VernierAngleOORange Cnt M lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	184.590576	184.5905902 ± 0.0009	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x6C

0x00

0x00

0x6C

0x00

0x00

Test Step 2.56 (Repeat Count = 1)	√
Name	Input Value
DigColPsInt_GetCustData()	134
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

NTC

Param

Status

2014-10-14, 18:16:06+0530



DigColPs_ColTrimStatic_Deg_M_f32 DigColPs_HwAVernCorrFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Cnt_M_u16 DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwDataType_Cnt_M_u08 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SpurParityError_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_SpurTrimCompStatic_Cnt_M_u16 DigColPs_VerniorrDetectAcc_Cnt_M_u16 DigColPs_VerniorrD	Input Value 67.1 1 0 13742 196.4963954 4 20378 59.7 0 13 0 1 1340.457155 2 21 1 1344 59.7 1888
DigColPs_HwAVernCorrFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Cnt_M_u16 DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwDataType_Cnt_M_u08 DigColPs_I2CHwSpurAngle_Cnt_M_u16 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwTrimTransCnts_UIs_M_u08 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Ont_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_SpurTrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16	1 0 13742 196.4963954 4 20378 59.7 0 13 0 1 1340.457155 2 21 1 134 59.7
igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Cnt_M_u16 igCoIPs_I2CHwCoIAngle_Cnt_M_u16 igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwDataType_Cnt_M_u08 igCoIPs_I2CHwSpurAngle_Cnt_M_u16 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CHwTrimTransCnts_UIs_M_u08 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevAngleDataAvailable_Cnt_M_lgc igCoIPs_PrevVernierLeveINo_Cnt_M_u08 igCoIPs_PrevVernierLeveINo_Cnt_M_u08 igCoIPs_SkipStepFitDetectAcc_Cnt_M_u16 igCoIPs_SpurParityError_Cnt_M_lgc igCoIPs_SpurParityError_Cnt_M_gc igCoIPs_SpurSensorFaultAcc_Cnt_M_u16 igCoIPs_SpurTrimStatic_Deg_M_f32 igCoIPs_SpurTrimStatic_Deg_M_f32 igCoIPs_TrimCompStatic_Cnt_M_u16 igCoIPs_VernCorrDetectAcc_Cnt_M_u16 igCoIPs_VernierAngleOORange_Cnt_M_u16	0 13742 196.4963954 4 20378 59.7 0 13 0 1 1340.457155 2 21 1 134 59.7
DigCoIPs_I2CHwCoIAngle_Cnt_M_u16 DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwDataType_Cnt_M_u08 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevVangleDataAvailable_Cnt_M_lgc DigCoIPs_PrevVernierLeveINo_Cnt_M_u08 DigCoIPs_PrevVernierLeveINo_Cnt_M_u08 DigCoIPs_SkipStepFitDetectAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_gc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_SpurTrimStatic_Deg_M_f32 DigCoIPs_SpurTrimStatic_Deg_M_f32 DigCoIPs_TrimCompStatic_Cnt_M_u16 DigCoIPs_VernierAngleOORange_Cnt_M_u16 DigCoIPs_VernierAngleOORange_Cnt_M_u16 DigCoIPs_VernierAngleOORange_Cnt_M_u16	13742 196.4963954 4 20378 59.7 0 13 0 1 1340.457155 2 21 1 134 59.7
DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwDataType_Cnt_M_u08 DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevAngleDataAvailable_Cnt_M_Igc DigCoIPs_PrevCoIPos_Deg_M_f32 DigCoIPs_PrevVernierLeveINo_Cnt_M_u08 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_u16 DigCoIPs_SpurTrimStatic_Deg_M_f32 DigCoIPs_TrimCompStatic_Cnt_M_u16 DigCoIPs_VernicorrDetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCoIPs_DetectAcc_Cnt_M_u16 DigCo	196.4963954 4 20378 59.7 0 13 0 1 1340.457155 2 21 1 134 59.7
bigCoIPs_I2CHwDataType_Cnt_M_u08 bigCoIPs_I2CHwSpurAngle_Cnt_M_u16 bigCoIPs_I2CHwSpurAngle_Deg_M_f32 bigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSpurSensorFault_Cnt_M_Igc bigCoIPs_PrevAngleDataAvailable_Cnt_M_Igc bigCoIPs_PrevCoIPos_Deg_M_f32 bigCoIPs_PrevVernierLeveINo_Cnt_M_u08 bigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 bigCoIPs_SpurParityError_Cnt_M_lgc bigCoIPs_SpurParityError_Cnt_M_u16 bigCoIPs_SpurTrimStatic_Deg_M_f32 bigCoIPs_SpurTrimStatic_Deg_M_f32 bigCoIPs_TrimCompStatic_Cnt_M_u16 bigCoIPs_VernicompStatic_Cnt_M_u16 bigCoIPs_VernicompStatic_Cnt_M_u16 bigCoIPs_VernicompStatic_Cnt_M_u16 bigCoIPs_VernicompStatic_Cnt_M_u16 bigCoIPs_VernicompGoORange_Cnt_M_lgc	4 20378 59.7 0 13 0 1 1340.457155 2 21 1 134 59.7
DigCoIPs_I2CHwSpurAngle_Cnt_M_u16 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevAngleDataAvailable_Cnt_M_Igc DigCoIPs_PrevCoIPos_Deg_M_f32 DigCoIPs_PrevVernierLeveINo_Cnt_M_u08 DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_u16 DigCoIPs_SpurTrimStatic_Deg_M_f32 DigCoIPs_TrimCompStatic_Cnt_M_u16 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16 DigCoIPs_VernierAngleOORange_Cnt_M_u16	20378 59.7 0 13 0 1 1 1340.457155 2 21 1 134 59.7
bigColPs_12CHwSpurAngle_Deg_M_f32 bigColPs_12CHwTrimTransCnts_UIs_M_u08 bigColPs_12CSensCommFlts_Cnt_M_u08 bigColPs_12CSensCommFlts_Cnt_M_u08 bigColPs_12CSpurSensorFault_Cnt_M_Igc bigColPs_PrevAngleDataAvailable_Cnt_M_Igc bigColPs_PrevVernierLevelNo_Cnt_M_u08 bigColPs_SkipStepFltDetectAcc_Cnt_M_u16 bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_u16 bigColPs_SpurTrimStatic_Deg_M_f32 bigColPs_TrimCompStatic_Cnt_M_u16 bigColPs_VernicompStatic_Cnt_M_u16 bigColPs_VernicompStatic_Cnt_M_u16 bigColPs_VernicompStatic_Cnt_M_u16 bigColPs_VernicompStatic_Cnt_M_u16 bigColPs_VernicompStatic_Cnt_M_u16 bigColPs_VernicompGleoColPange_Cnt_M_u16 bigColPs_bigColPs_DigColPange_Cnt_M_u16 bigColPs_bigColPange_Cnt_M_u16 bigColPs_bigColPange_Cnt_M_u16 bigColPs_bigColPange_Cnt_M_u16 bigColPs_bigColPange_Cnt_M_u16 bigColPs_bigColPange_Cnt_M_u16 bigColPs_bigColPange_DigColPange_DigColPange_DigColPange_DigColPange_DigColPange_DigColPange_DigColPange_DigColPange_DigColPange_Di	59.7 0 13 0 1 131 13440.457155 2 21 1 134 59.7
DigColPs_12CHwTrimTransCnts_UIs_M_u08 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSensCommFlts_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16	0 13 0 1 1340.457155 2 21 1 1 134 59.7
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16	13 0 1 1340.457155 2 21 1 134 59.7
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16	0 1 1340.457155 2 21 1 134 59.7
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_u16	1 1340.457155 2 21 1 134 59.7
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc	1 1340.457155 2 21 1 134 59.7
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc	1340.457155 2 21 1 134 59.7
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc	2 21 1 134 59.7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc	21 1 134 59.7
DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc	1 134 59.7
bigColPs_SpurSensorFaultAcc_Cnt_M_u16 bigColPs_SpurTrimStatic_Deg_M_f32 bigColPs_TrimCompStatic_Cnt_M_u16 bigColPs_VernCorrDetectAcc_Cnt_M_u16 bigColPs_VernCorrDetectAcc_Cnt_M_u16	134 59.7
igColPs_SpurTrimStatic_Deg_M_f32 ligColPs_TrimCompStatic_Cnt_M_u16 ligColPs_VernCorrDetectAcc_Cnt_M_u16 ligColPs_VernierAngleOORange_Cnt_M_lgc	59.7
DigColPs_TrimCompStatic_Cnt_M_u16 DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M_lgc	
bigColPs_VernCorrDetectAcc_Cnt_M_u16 bigColPs_VernierAngleOORange_Cnt_M_lgc	
ligColPs_VernierAngleOORange_Cnt_M_lgc	
	20
	1
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
² _ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2 ColSpurVernierLUT Cnt s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
	4
² 2_ColSpurVernierLUT_Cnt_s16[1][1]	
² 2_ColSpurVernierLUT_Cnt_s16[1][2]	3
² _ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2_ColSpurVernierLUT_Cnt_s16[1][7]	3
2_ColSpurVernierLUT_Cnt_s16[1][8]	2
2_ColSpurVernierLUT_Cnt_s16[1][9]	1
2_ColSpurVernierLUT_Cnt_s16[1][10]	0
2_ColSpurVernierLUT_Cnt_s16[1][11]	4
2_ColSpurVernierLUT_Cnt_s16[1][12]	3
2_ColSpurVernierLUT_Cnt_s16[1][13]	2
2_ColSpurVernierLUT_Cnt_s16[1][14]	1
2_ColSpurVernierLUT_Cnt_s16[1][15]	0
2_ColSpurVernierLUT_Cnt_s16[1][16]	4
2_ColSpurVernierLUT_Cnt_s16[2][0]	0
	8
2_ColSpurVernierLUT_Cnt_s16[2][1]	
2_ColSpurVernierLUT_Cnt_s16[2][2]	6
2_ColSpurVernierLUT_Cnt_s16[2][3]	4
2_ColSpurVernierLUT_Cnt_s16[2][4]	2
2_ColSpurVernierLUT_Cnt_s16[2][5]	0
2_ColSpurVernierLUT_Cnt_s16[2][6]	9
2_ColSpurVernierLUT_Cnt_s16[2][7]	7
2_ColSpurVernierLUT_Cnt_s16[2][8]	5
2_ColSpurVernierLUT_Cnt_s16[2][9]	3
2_ColSpurVernierLUT_Cnt_s16[2][10]	1
2_ColSpurVernierLUT_Cnt_s16[2][11]	10
2_ColSpurVernierLUT_Cnt_s16[2][12]	8
2_ColSpurVernierLUT_Cnt_s16[2][12]	6
	4
² 2_ColSpurVernierLUT_Cnt_s16[2][14]	
'2_ColSpurVernierLUT_Cnt_s16[2][15] '2_ColSpurVernierLUT_Cnt_s16[2][16]	2 10





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2 ColSpurVernierLUT Cnt s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2 DualSpurVernierLUT Cnt s16[2][10]	
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0

2014-10-14, 18:16:06+0530



Nome	Input Value		
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	72		
k_SkipStepErrDiag_Cnt_str.PStep	22		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	17		
k_VernCorrErrorDiag_Cnt_str.PStep	14		
k_VernCorrErrorDiag_Cnt_str.NStep	19		
k_VernCorrErrorThresh_Deg_f32	81.58188558		
k_VernOORangeThresh_Deg_f32	510.2277182		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	196.4963954		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	10.61436504		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1249		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cn	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt Pim DigColPsEOL		
·	Actual Value	Expected Value	Result

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	490.909088	490.9090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	489.396393	489.3963954 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x04	0x04	~
Status	0x01	0x01	v



au				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.57 (Repeat Count = 1)	🗸
Name	Input Value
DigColPsInt_GetCustData()	186
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs ColTrimStatic Deg M f32	71.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	5158
DigColPs I2CHwColAngle Deg M f32	194.3084972
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	19371
DigColPs I2CHwSpurAngle Deg M f32	60.8
DigColPs I2CHwTrimTransCnts Uls M u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1210.662194
DigColPs PrevVernierLevelNo Cnt M u08	8
DigColPs SkipStepFltDetectAcc Cnt M u16	14
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	60.8
DigColPs TrimCompStatic Cnt M u16	1924
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
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4
2
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14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
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1
2

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DigColPs_Per2

5.19 Coll 3_1 C/2		,	
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
Γ2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2 DualSpurVernierLUT Cnt s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierEUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierEUT_GNt_\$10[2][13] T2_DualSpurVernierEUT_GNt_\$16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
TZ_DualSpurVernierLUT_Cnt_\$16[2][17] TZ_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
	9		
T2_DualSpurVernierLUT_Cnt_s16[2][20]			
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	19		
T2 DualSpurVernierLUT Cnt s16[3][21]	21		
<pre>rz_buaispurvernierE01_Cnt_s1o[3][21] <_SelectFromColumn_Cnt_lgc</pre>	0		
K_SelectFromColumn_Cnt_igc K_SkipStepErrDiag_Cnt_str.Threshold	128		
_ · · · = _			
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	80		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	61.77320576		
<_VernOORangeThresh_Deg_f32	1180.024269		
gt_DigColPs_Per2_MecState_Cnt_enum.value	2		
gt_Pim_DigColPsEOL.ColTrim_Deg_f32	194.3084972		
gt_Pim_DigColPsEOL.SpurTrim_Deg_f32	258.7965072		
gt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3065		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Ci	nt_lgc	
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg		
gt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
rgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
		Formanda d Malana	Boou
Name	Actual Value	Expected value	
Name DigColPs_HwAVernCorrFault_Cnt_M_lgc	Actual Value	Expected Value	Resu





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	483.108521	483.1084972 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-409.090912	-409.0909091 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6E	0x6E	•
Param	0x00	0x00	~
Status	0x00	0x00	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.58 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	152
DigColPs ColParityError Cnt M Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	75.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58683
DigColPs_I2CHwColAngle_Deg_M_f32	226.2329707
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	52949
DigColPs_I2CHwSpurAngle_Deg_M_f32	61.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	0
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152
DigColPs_SpurTrimStatic_Deg_M_f32	61.9
DigColPs_TrimCompStatic_Cnt_M_u16	1960
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2 ColSpurVernierLUT Cnt s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
T2 ColSpurVernierLUT Cnt s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_S16[2][8] T2_ColSpurVernierLUT_Cnt_S16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-144 -108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
	-30
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





Input Value
360
9
0
1
3
4
5
6
7
8
9
0
1 2
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5
6
7
8
9
0
1
2
3
4
5
6
7
8
9 10
0
1
2
3
4
5
6
7 8
9
10
22
2
4
6
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10 12
14
16
18
20
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7
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13
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17
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21
0
240
11
37 70

2014-10-14, 18:16:06+0530



Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	36.6228292		
k_VernOORangeThresh_Deg_f32	992.7934918		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.2329707		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	113.3681837		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1804		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Namo	Actual Value	Expected Value	Pocult

tgt_ttte_mat_da_bigdoil 3.1 im_bigdoil 3EOE	tgt_i iii_bigooii scoc		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636353	163.6363636 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	150.932968	150.9329707 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363647	-736.3636364 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

Test Step 2.59 (Repeat Count = 1)	√
Name	Input Value
DigColPsInt_GetCustData()	175
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	79.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	7302
DigColPs_I2CHwColAngle_Deg_M_f32	31.81471384
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	20291
DigColPs_I2CHwSpurAngle_Deg_M_f32	63
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175
DigColPs_SpurTrimStatic_Deg_M_f32	63
DigColPs_TrimCompStatic_Cnt_M_u16	1996
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T0_ColSpurVernierLUT_Cnt_s16[2][2]	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2 ColSpurVernierLUT Cnt s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T0_ColSpurVernierLUT_Cnt_s16[3][1]	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
	-252
T2 DualSpurVernierLUT Cnt s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[0][6] -180 T2_DualSpurVernierLUT_Cnt_s16[0][7] -144 -108 T2_DualSpurVernierLUT_Cnt_s16[0][8] T2_DualSpurVernierLUT_Cnt_s16[0][9] -72 T2_DualSpurVernierLUT_Cnt_s16[0][10] -36 T2_DualSpurVernierLUT_Cnt_s16[0][11] 0 T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 180 T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2 DualSpurVernierLUT Cnt s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2 DualSpurVernierLUT Cnt s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7] 8 T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] 4 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 2 T2 DualSpurVernierLUT Cnt s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2 DualSpurVernierLUT Cnt s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2 DualSpurVernierLUT Cnt s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18

20

3

T2_DualSpurVernierLUT_Cnt_s16[3][10]
T2_DualSpurVernierLUT_Cnt_s16[3][11]
T2_DualSpurVernierLUT_Cnt_s16[3][12]

2014-10-14, 18:16:06+0530



DigColPs_Per2

Digoon 5_1 c/2			1 - 10-10
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	161		
k_SkipStepErrDiag_Cnt_str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	14		
k_VernCorrErrorDiag_Cnt_str.Threshold	63		
k_VernCorrErrorDiag_Cnt_str.PStep	40		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	59.55320692		
k_VernOORangeThresh_Deg_f32	1084.696699		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	31.81471384		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	354.2363453		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	358.528931	358.528934 ± 0.00048828125	· •
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	56	56	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

-540

0x6C

0x0C

0x01

0

0x6C

0x0C

0x01

-540 ± 0.0009

Test Step 2.60 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	0	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125	
DigColPs_ColTrimStatic_Deg_M_f32	83.5	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	42754	
DigColPs_I2CHwColAngle_Deg_M_f32	256.6914936	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	24629	
DigColPs_I2CHwSpurAngle_Deg_M_f32	64.1	

DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_l2CHwAbsPosValid_Cnt_lgc.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

NTC

Param

Status

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value





Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	16
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	688
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	3
DigColPs_SpurParityError_Cnt_M_Igc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	64.1
DigColPs_TrimCompStatic_Cnt_M_u16	2032
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs VernierAngleOORange Cnt M Igc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T3_ColSpurVernierLUT_Cnt_s16[2][10]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	2 15





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2 DualSpurVernierLUT Cnt s16[1][15]	4
T2 DualSpurVernierLUT Cnt s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[1][18]	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	
	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	4 5 6
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	4 5

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	5		
k_SkipStepErrDiag_Cnt_str.NStep	11		
k_VernCorrErrorDiag_Cnt_str.Threshold	11		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	67.07432961		
k_VernOORangeThresh_Deg_f32	836.2919484		
tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	2 256.6914936		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	105.0697877		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	921		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid Cnt Inc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt DigColPs Per2 I2CHwAbsP		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt DigColPs Per2 MecState C	-	
tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc	tgt_DigColPs_Per2_TrimComp_0	-	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	on <u>rage</u>	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	Result
DigColPs I2CHwColAngleForTrim Deg M f32			
DigColPs_I2CHwColAngleFor Him_Deg_M_52 DigColPs_I2CHwTrimTransCnts_Uls_M_u08	163.636353	163.6363636 ± 0.00048828125	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs PrevColPos Deg M f32	173.191498	173.1914936 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	3	3	
DigColPs Regl2CSnsrDataType Cnt M u08	4	4	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	
DigColPs VernierAngleOORange Cnt M lgc	1	1	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363647	-736.3636364 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	
NTC	0x6C	0x6C	
Param	0x0C	0x0C	
Status	0x01	0x01	
NTC	0x6F	0x6F	
Param	0x00	0x00	



au				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.61 (Repeat Count = 1)	v v
Name	Input Value
DigColPsInt_GetCustData()	255
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs ColSensorFaultAcc Cnt M u16	243
DigColPs_ColTrimStatic_Deg_M_f32	87.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	59004
DigColPs_I2CHwColAngle_Deg_M_f32	93.13262653
DigColPs I2CHwDataType Cnt M u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26282
DigColPs I2CHwSpurAngle Deg M f32	65.2
DigColPs I2CHwTrimTransCnts Uls M u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1600.2344
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	14
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	65.2
DigColPs TrimCompStatic Cnt M u16	2068
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt Rte Inst Sa DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
!	

2014-10-14, 18:16:06+0530



Name		
12. Objoy/went-ULT On 1911/15 12. Objoy/went-ULT On 1911/15 13. Objoy/went-ULT On 1911/15 14. Objoy/went-ULT On 1911/15 15. Objoy/went-ULT On 1911/15 16. Objoy/went-ULT On 1911/15 17. Objoy/went-ULT On 1911/15 17. Objoy/went-ULT On 1911/15 18. Objoy/went-ULT On 1911/15 19. Objoy/went-ULT On 1911/15	Name	Input Value
12_Colsaychment (_Dr., Mrt 16 13_Colsaychment (_Dr., Mrt 16 13_Colsaychment (_Dr., Mrt 16 13_Colsaychment (_Dr., Mrt 16 14_Colsaychment (_Dr., Mrt 16 15_Colsaychment (_Dr., Mrt 16 16_Colsaychment (_Dr., Mrt 16 17_Colsaychment (_Dr., Mrt 16	T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
12_Colsystement_Cols_sequine 0	T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
17_Codepartment()	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
17_Codepartment()	T2 ColSpurVernierLUT Cnt s16[2][0]	0
T2_COSQuémentUT_Cs_120[2] 6		8
T. COSSUMMENTOL CR. SPECIAL		
P_COSQAVermentU_Cn_squ[2] 2 P_COSQAVermentU_Cn_squ[2] 12 P_COSQAVermentU_Cn_squ[2] 13 P_COSQAVermentU_Cn_squ[2] 15 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 18 P_COSQAVermentU_Cn_squ[2] 18 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 11 P_COSQAVermentU_Cn_squ[2] 12 P_COSQAVermentU_Cn_squ[2] 13 P_COSQAVermentU_Cn_squ[2] 14 P_COSQAVermentU_Cn_squ[2] 15 P_COSQAVermentU_Cn_squ[2] 16 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 18 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 11 P_COSQAVermentU_Cn_squ[2] 12 P_COSQAVermentU_Cn_squ[2] 13 P_COSQAVermentU_Cn_squ[2] 14 P_COSQAVermentU_Cn_squ[2] 15 P_COSQAVermentU_Cn_squ[2] 16 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 18 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 11 P_COSQAVermentU_Cn_squ[2] 11 P_COSQAVermentU_Cn_squ[2] 12 P_COSQAVermentU_Cn_squ[2] 13 P_COSQAVermentU_Cn_squ[2] 16 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 18 P_COSQAVermentU_Cn_squ[2] 19 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 10 P_COSQAVermentU_Cn_squ[2] 11 P_COSQAVermentU_Cn_squ[2] 12 P_COSQAVermentU_Cn_squ[2] 13 P_COSQAVermentU_Cn_squ[2] 14 P_COSQAVermentU_Cn_squ[2] 15 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 17 P_COSQAVermentU_Cn_squ[2] 18 P_COSQAVermentU_Cn_squ[2] 19		
T2_Collago/wenterU_Cn_1892[5] T2_Collago/wenterU_Cn_1892[7] T2_Collago/wenterU_Cn_1892[7] T3_Collago/wenterU_Cn_1892[7] T3_Collago/wenterU_Cn_1892[7] T3_Collago/wenterU_Cn_1892[7] T3_Collago/wenterU_Cn_1892[7] T3_Collago/wenterU_Cn_1892[7] T4_Collago/wenterU_Cn_1892[7] T4_Collago/wenterU_Cn_1892[7] T5_Collago/wenterU_Cn_1892[7] T5_Collago/wenterU_Cn_1892[7] T5_Collago/wenterU_Cn_1892[7] T6_Collago/wenterU_Cn_1892[7] T7_Collago/wenterU_Cn_1892[7] T8_Collago/wenterU_Cn_1892[7] T8_Collago/wenterU_Cn_1892[7		
12_Colspar/montul_Col_strigot 7		
12. CoSSAVermentUT_CRL 1612 71 7 12. CoSSAVermentUT_CRL 1612 71 6 12. CoSSAVermentUT_CRL 1612 71 10 13. CoSSAVermentUT_CRL 1612 71 10 14. CoSSAVermentUT_CRL 1612 71 10 15. CoSSAVermentUT_CRL 1612 71 10 16. CoSSAVermentUT_CRL 1612 71 10 17. CoSSAVermentUT_CRL 1612 71 10 18. CoSSAVermentUT_CRL 1612 71 11 18. CoSSAVermentUT_CRL 1612 71 11 18. CoSSAVermentUT_CRL 1612 71 10 19. CoSSAVermentUT_CRL 1612 71		
12,005ps/wrienctU_Ot_1502[9] 5 12,005ps/wrienctU_Ot_1502[9] 3 12,005ps/wrienctU_Ot_1502[1] 10 12,005ps/wrienctU_Ot_1502[1] 10 12,005ps/wrienctU_Ot_1502[1] 0 12,005ps/wrienctU_Ot_1502[1] 0 12,005ps/wrienctU_Ot_1502[1] 0 12,005ps/wrienctU_Ot_1502[1] 0 12,005ps/wrienctU_Ot_1502[1] 0 12,005ps/wrienctU_Ot_1502[1] 10 12,005ps/wrienctU_Ot_1502[1] 10 12,005ps/wrienctU_Ot_1502[1] 10 12,005ps/wrienctU_Ot_1502[1] 11 12,005ps/wrienctU_Ot_1502[1] 11 12,005ps/wrienctU_Ot_1502[1] 11 12,005ps/wrienctU_Ot_1502[1] 11 12,005ps/wrienctU_Ot_1502[1] 11 12,005ps/wrienctU_Ot_1502[1] 11 12,005ps/wrienctU_Ot_1502[1] 12 12,005ps/wrienctU_Ot_1502[1] 13 12,005ps/wrientU_Ot_1502[1] 13 12,005ps/w		
12_CoSpar/weinetU_Cot_st@]0] 1 1 1 1 1 1 1 1 1		
12_DOSSA/VernetUT_Ort_15(2) 10_DOSSA/VernetUT_Ort_15(2) 11_DOSSA/VernetUT_Ort_15(2) 11_DOSSA/Verne		
12_CoSpa/viment UT_Cnt_stop 1 10		
12_CoSpin/Yennit U_Ort_stop 13 8 12_CoSpin/Yennit U_Ort_stop 13 0 12_CoSpin/Yennit U_Ort_stop 13 0 12_CoSpin/Yennit U_Ort_stop 13 2 12_CoSpin/Yennit U_Ort_stop 13 1 12_CoSpin/Yennit U_Ort_stop 13 2 12_CoSpin/Yennit U_Ort_stop 13 3 12_CoSpin/Yennit U_Ort_stop 13 1 13_CoSpin/Yennit U_Ort_stop 13 1 14_CoSpin/Yennit U_Ort_stop 13 1 15_CoSpin/Yennit U_Ort_stop 13 1 16_CoSpin/Yennit U_Ort_stop		
12_CoSpin/winest_U_Cnt_s100[14]		
12, CoSpa/wemsLU_Cnt_stq[1]+ 4		
12, CoSpa/WameLII_CRL, 19(2) 16 17_, CoSpa/WameLII_CRL, 19(3) 1 17_, CoSpa/WameLII_CRL, 19(3) 1 18_, CoSpa/WameLII_CRL, 19(3) 1 19_, CoSpa/WameLII_CRL, 19(3) 1 10_, CoSpa/Wam		
12_COSDAYMENT CM_1 15(5) 0 1 1 1 1 1 1 1 1 1		
12, CoSpar/venetUT, Cot. 160 31 14 12, CoSpar/venetUT, Cot. 160 31 15 16, CoSpar/venetUT, Cot. 160 31 16 17, CoSpar/venetUT, Cot. 160 31 17, CoSpar/venetUT, Cot. 160 31 18, CoSpar/venetUT, Cot. 160 31 19, CoSpar/vene	T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
12 CoSput/wentUT_Cot_st0[0] 14	T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
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12 DualSpurVermierLUT_Cnt_s16(0) 5 -216 -		
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T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
12_Dual-opin verifier.C1_Cff_510[1][13]		
	12_DuaiSpui VerilletLU1_Citt_510[1][13]	4

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Name	Input Value		
Name T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][14] T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][15] T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	222		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep k_VernCorrErrorDiag_Cnt_str.Threshold	22		
k_VernCorrErrorDiag_Cnt_str.PStep	38 1		
k_VernCorrErrorDiag_Cnt_str.NStep k VernCorrErrorThresh Deg f32	59.32419395		
k_VernOORangeThresh_Deg_f32	542.7790878		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	93.13262653		
tgt_Pim_DigColPsEOL.ConTini_Deg_132 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	131.6931788		
tgt_Pim_DigColPsEOL.Spui Hill_Deg_I32 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3932		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	Inc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	, 	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	- Codait
DigColPs_HwAveInConFault_Cnt_Ivi_lgc		0 + 0 00048828425	

0

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

0 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	5.53263092	5.532626534 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	155
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	124
DigColPs_ColTrimStatic_Deg_M_f32	91.7
DigColPs HwAVernCorrFault Cnt M lgc	1
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	15177
DigColPs_I2CHwColAngle_Deg_M_f32	20.78231114
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs I2CHwSpurAngle Cnt M u16	55143
DigColPs_I2CHwSpurAngle_Deg_M_f32	66.3
DigColPs I2CHwTrimTransCnts UIs M u08	6
DigColPs I2CSensCommFlts Cnt M u08	8
DigColPs I2CSpurSensorFault Cnt M Igc	1
DigColPs PrevAngleDataAvailable Cnt M Igc	0
DigColPs PrevColPos Deg M f32	596.9864027
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs SkipStepFltDetectAcc Cnt M u16	2
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs SpurSensorFaultAcc Cnt M u16	155
DigColPs SpurTrimStatic Deg M f32	66.3
DigColPs_TrimCompStatic_Cnt_M_u16	2104
DigColPs VernCorrDetectAcc Cnt M u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2 ColSpurVernierLUT Cnt s16[0][2]	-99
T2 ColSpurVernierLUT Cnt s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2 ColSpurVernierLUT Cnt s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2 ColSpurVernierLUT Cnt s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2 ColSpurVernierLUT Cnt s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17] T3_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2 4
T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10] T3_DualSpurVernierLUT_Cst_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	1 3
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cst_s16[3][21]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21] k_SelectFromColumn_Cnt_lgc	21 0
k_SkipStepErrDiag_Cnt_str.Threshold	19
k_SkipStepErrDiag_Cnt_str.PStep	22
k_SkipStepErrDiag_Cnt_str.NStep	49
k_VernCorrErrorDiag_Cnt_str.Threshold	91
k_VernCorrErrorDiag_Cnt_str.PStep	48
k_VernCorrErrorDiag_Cnt_str.NStep	19
k_VernCorrErrorThresh_Deg_f32	6.884903669
k_VernOORangeThresh_Deg_f32	605.936505
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0

2014-10-14, 18:16:06+0530



Name	Input Value
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	20.78231114
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	116.1393507
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	678
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T3_ColSpurVernierLUT_Cnt_s46[4][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_S10[1][0] T2_ColSpurVernierLUT_Cnt_S10[1][0]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T3_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s46[3][40]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
b	I .

2014-10-14, 18:16:06+0530



Name T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	Input Value -36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	-30
T2_DualSpurVernierLUT_Cnt_s16[0][12]	
	0
	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
	4
T2_DualSpurVernierLUT_Cnt_s16[1][15]	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2 DualSpurVernierLUT Cnt s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
	22
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	22
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
	11

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 $k_SelectFromColumn_Cnt_lgc$ 1 k_SkipStepErrDiag_Cnt_str.Threshold 206 k_SkipStepErrDiag_Cnt_str.PStep 31 k_SkipStepErrDiag_Cnt_str.NStep 36 $k_VernCorrErrorDiag_Cnt_str.Threshold$ 80 k_VernCorrErrorDiag_Cnt_str.PStep 0 $k_VernCorrErrorDiag_Cnt_str.NStep$ 13 k_VernCorrErrorThresh_Deg_f32 85.02186632 1061.295247 k_VernOORangeThresh_Deg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 99.35150683 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 127.7892992 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc$ tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum$ tgt_DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tat Pim DiaColPsEOL

tg_rtto_mot_od_bigoon o.r im_bigoon obob	tgt_i iii_bigeoii seee		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1445.27759	1445.277591 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	32	32	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.64 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	243	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105	
DigColPs_ColTrimStatic_Deg_M_f32	99.9	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	55455	
DigColPs_I2CHwColAngle_Deg_M_f32	346.7766712	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	47484	
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.5	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	
DigColPs_I2CSensCommFlts_Cnt_M_u08	31	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	1630.352482	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	
DigColPs_SpurParityError_Cnt_M_lgc	0	

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	243
DigColPs_SpurTrimStatic_Deg_M_f32	68.5
DigColPs_TrimCompStatic_Cnt_M_u16	2176
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	1.
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13] T3_ColSpurVernierLUT_Cnt_s16[0][14]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14] T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1.
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5 3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2 ColSpurVernierLUT Cnt s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	
	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
	13

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
12 Duanopui veitiieleu i Ott 510[3][3]	10

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2014-10-14, 18:16:06+0530



Input Value T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 16 T2_DualSpurVernierLUT_Cnt_s16[3][8] T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2 DualSpurVernierLUT Cnt s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 $k_SelectFromColumn_Cnt_lgc$ 0 k_SkipStepErrDiag_Cnt_str.Threshold 98 $k_SkipStepErrDiag_Cnt_str.PStep$ 47 k_SkipStepErrDiag_Cnt_str.NStep 39 47 $k_VernCorrErrorDiag_Cnt_str.Threshold$ k_VernCorrErrorDiag_Cnt_str.PStep 50 $k_VernCorrErrorDiag_Cnt_str.NStep$ 0 k_VernCorrErrorThresh_Deg_f32 6.903702974 k_VernOORangeThresh_Deg_f32 1481.66354 tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 346.7766712 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 85.06156057 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 2733 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc$ tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum$ tgt_DigColPs_Per2_MecState_Cnt_enum tgt Rte Inst Sa DigColPs.DigColPs Per2 TrimComp Cnt Igc tgt DigColPs Per2 TrimComp Cnt Igc

	191191-11 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	981.818176	981.8181818 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	966.876709	966.8766712 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	18	18	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18	18	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

I				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.65 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123
DigColPs_ColTrimStatic_Deg_M_f32	104

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	28915
DigColPs I2CHwColAngle Deg M f32	118.0404236
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	43172
DigColPs_I2CHwSpurAngle_Deg_M_f32	69.6
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs I2CSensCommFlts Cnt M u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs PrevAngleDataAvailable Cnt M lgc	1
DigColPs_PrevColPos_Deg_M_f32	943.3614662
	1
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	69.6
DigColPs_TrimCompStatic_Cnt_M_u16	2212
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2 ColSpurVernierLUT Cnt s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T3_ColSpurVernierLUT_Cnt_s46[4][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12] T0_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	
	8
T2 ColSpurVernierLUT Cnt s16[2][13]	
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	6 4 2
T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T3_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
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T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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DigColPs_Per2		(MA	Cital
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2 DualSpurVernierLUT Cnt s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2 DualSpurVernierLUT Cnt s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	37		
k_SkipStepErrDiag_Cnt_str.PStep	46		
k_SkipStepErrDiag_Cnt_str.NStep	27		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	33		
k VernCorrErrorDiag Cnt str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	16.13001919		
k_VernOORangeThresh_Deg_f32	708.4126034		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0404236		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	214.0159558		
tgt Pim DigColPsEOL.TrimComp Cnt u16	2465		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt DigColPs Per2 I2CHwAbsPo	osValid Cnt lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPc		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt DigColPs Per2 MecState Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_C	_	
tgt Rte Inst Sa DigColPs.Pim DigColPsEOL	tgt_Pim_DigColPsEOL	-3	
Name	Actual Value	Expected Value	Resul
DigColPs HwAVernCorrFault Cnt M Igc	1	1	rtegui
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	
DigColPs I2CHwColAngleForTrim_Deg_ivi_is2 DigColPs I2CHwTrimTransCnts Uls M u08	1	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	14.0404205	14.04042363 ± 0.0001220703125	
	2	14.04042363 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_Pagl2CSpsrDataType_Cnt_M_u08	1	1	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigCoIPs_SkipStepFltDetectAcc_Cnt_M_u16 DigCoIPs_VernCorrDetectAcc_Cnt_M_u16	1	1	
	0	0	
DigColPs_VernierAngleOORange_Cnt_M_lgc tat_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	
INC DINCON 3 I CIE IECHIWADSI OSVANU CIIL IUC.VAIUC	. 0	10	

0

0

-900

0

0

-900 ± 0.0009

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.66 (Repeat Count = 1)	
Name	Input Value
	142
DigColPo. ColPorityError Cot M. Igo	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	108.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	32244
DigColPs_I2CHwColAngle_Deg_M_f32	233.8189296
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	26632
DigColPs_I2CHwSpurAngle_Deg_M_f32	70.7
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	19
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	837.3964648
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs SpurSensorFaultAcc Cnt M u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	70.7
DigColPs_TrimCompStatic_Cnt_M_u16	2248
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs VernierAngleOORange Cnt M Igc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSputVernierLUT_Cnt_s16[0][1]	-131
	-99
T2_ColSpurVernierLUT_Cnt_s16[0][2]	
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Crit_s16[1][6] T2_ColSpurVernierLUT_Crit_s16[1][7]	
	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T3_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	1 2

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_\$16[1][15] T2_DualSpurVernierLUT_Cnt_\$16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2 DualSpurVernierLUT Cnt s16[1][19]	8		
T2 DualSpurVernierLUT Cnt s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	188		
k_SkipStepErrDiag_Cnt_str.PStep	45		
k_SkipStepErrDiag_Cnt_str.NStep	32		
k_VernCorrErrorDiag_Cnt_str.Threshold	36		
k_VernCorrErrorDiag_Cnt_str.PStep	9		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	32.58559203		
k_VernOORangeThresh_Deg_f32	1033.041085		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	233.8189296		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	253.5267325		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3274		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_t	32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		1
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	V

490.909088

2

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

DigColPs_I2CHwTrimTransCnts_Uls_M_u08

490.9090909 ± 0.00048828125

2





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	485.718933	485.7189296 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6	6	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	3	3	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3	3	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-414.281067	-414.2810704 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Name	Input Value	
DigColPsInt GetCustData()	105	
DigColPs ColParityError Cnt M Igc	1	
DigColPs ColSensorFaultAcc Cnt M u16	127	
DigColPs ColTrimStatic Deg M f32	112.2	
DigColPs HwAVernCorrFault Cnt M lgc	1	
DigColPs I2CColSensorFault Cnt M Igc	0	
DigColPs I2CHwColAngle Cnt M u16	50430	
DigColPs I2CHwColAngle Deg M f32	131.2116221	
DigColPs I2CHwDataType Cnt M u08	1	
DigColPs I2CHwSpurAngle Cnt M u16	62280	
DigColPs I2CHwSpurAngle Deg M f32	71.8	
DigColPs I2CHwSpurArigie_Deg_w_i32	4	
	9	
DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_I2CSpurSensorFault_Crit_ivi_igc DigColPs_PrevAngleDataAvailable_Crit_M_lgc	1	
DigColPs PrevColPos Deg M f32	339.5431169	
DigColPs PrevVernierLevelNo Cnt M u08	339.3431109	
DigColPs_PrevvernierLeveino_Crit_M_u06 DigColPs_SkipStepFltDetectAcc_Crit_M_u16	14	
	1	
DigColPs_SpurParityError_Cnt_M_lgc	105	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	71.8	
DigColPs_SpurTrimStatic_Deg_M_f32	2284	
DigColPs_TrimCompStatic_Cnt_M_u16	5	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	
DigColPs_VernierAngleOORange_Cnt_M_lgc		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	
² _ColSpurVernierLUT_Cnt_s16[0][1]	-131	
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
² _ColSpurVernierLUT_Cnt_s16[0][3]	-66	
² _ColSpurVernierLUT_Cnt_s16[0][4]	-33	
² _ColSpurVernierLUT_Cnt_s16[0][5]	0	
2_ColSpurVernierLUT_Cnt_s16[0][6]	32	
⁷ 2_ColSpurVernierLUT_Cnt_s16[0][7]	65	
2_ColSpurVernierLUT_Cnt_s16[0][8]	98	
² _ColSpurVernierLUT_Cnt_s16[0][9]	130	
² _ColSpurVernierLUT_Cnt_s16[0][10]	163	
C2_ColSpurVernierLUT_Cnt_s16[0][11]	196	
² _ColSpurVernierLUT_Cnt_s16[0][12]	229	
C2_ColSpurVernierLUT_Cnt_s16[0][13]	261	
2_ColSpurVernierLUT_Cnt_s16[0][14]	294	
Γ2_ColSpurVernierLUT_Cnt_s16[0][15]	327	
2_ColSpurVernierLUT_Cnt_s16[0][16]	359	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T3_DualSpurVernierLUT_Cnt_s16[0][10]	-72 36
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1 2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	3
T2_DualSpurVernierLUT_Cnt_s16[1][14] T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2 DualSpurVernierLUT Cnt s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_S16[2][0] T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T3_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15] T3_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	182
k_SkipStepErrDiag_Cnt_str.PStep	8
k_SkipStepErrDiag_Cnt_str.NStep	30
k_VernCorrErrorDiag_Cnt_str.Threshold	90
k_VernCorrErrorDiag_Cnt_str.PStep	7
k_VernCorrErrorDiag_Cnt_str.NStep	50

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value k_VernCorrErrorThresh_Deg_f32 44.56530905 k_VernOORangeThresh_Deg_f32 835.2161256 tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 131.2116221 96.8100152 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt Rte Inst Sa DigColPs.Pim DigColPsEOL tgt Pim DigColPsEOL

tgt_Rte_irist_3a_bigCtiFs.Fitti_bigCtiFsEOL	tgt_Filli_bigColFSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	152.268173	152.2681749 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	34	34	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12	12	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

T .			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.68 (Repeat Count = 1)	▼
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	116.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	54715
DigColPs_I2CHwColAngle_Deg_M_f32	133.5364133
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42308
DigColPs_I2CHwSpurAngle_Deg_M_f32	72.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	27
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1016.035717
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	72.9
DigColPs_TrimCompStatic_Cnt_M_u16	2320
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0

2014-10-14, 18:16:06+0530



32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	44		
k_SkipStepErrDiag_Cnt_str.NStep	34		
k_VernCorrErrorDiag_Cnt_str.Threshold	96		
k_VernCorrErrorDiag_Cnt_str.PStep	41		
k_VernCorrErrorDiag_Cnt_str.NStep	33		
k_VernCorrErrorThresh_Deg_f32	43.33685136		
k_VernOORangeThresh_Deg_f32	1120.447047		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	133.5364133		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	207.7008287		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1458		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1457.23645	1457.236413 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	✓
Status	0x01	0x01	~

T →			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.69 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	149
DigColPs_ColTrimStatic_Deg_M_f32	120.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	25072
DigColPs_I2CHwColAngle_Deg_M_f32	117.9909339
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	33822
DigColPs_I2CHwSpurAngle_Deg_M_f32	74
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	424.6977491





<u> </u>	
Name	Input Value
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	74
DigColPs_TrimCompStatic_Cnt_M_u16	2356
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
12_00.0pd.101	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	6
T2_ColSpurVernierLUT_Cnt_s16[3][8] T2_ColSpurVernierLUT_Cnt_s16[3][9]	6

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2 ColSpurVernierLUT Cnt s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2 DualSpurVernierLUT Cnt s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][7]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
	2
T2_DualSpurVernierLUT_Cnt_s16[3][1]	4

DigColPs_Per2

2014-10-14, 18:16:06+0530



897.5909339 ± 0.0009

0

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	105		
k_SkipStepErrDiag_Cnt_str.PStep	34		
k_SkipStepErrDiag_Cnt_str.NStep	24		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	86.64014435		
k_VernOORangeThresh_Deg_f32	232.6736557		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	117.9909339		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	208.2439033		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	35		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Ci	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	~
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1797.59094	1797.590934 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6	6	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

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Test Step 2.70 (Repeat Count = 1)	·
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	124.5
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	12814
DigColPs_I2CHwColAngle_Deg_M_f32	77.52818984
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	14635 75.1
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	923.4796569
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	127 75.1
DigColPs_SpurTrimStatic_Deg_M_f32 DigColPs_TrimCompStatic_Cnt_M_u16	2392
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66 22
T2_ColSpurVernierLUT_Cnt_s16[0][4] T2_ColSpurVernierLUT_Cnt_s16[0][5]	-33 0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5] T2_ColSpurVernierLUT_Cnt_s16[1][6]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2 ColSpurVernierLUT Cnt s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1.
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16]	0 4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	7 5
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	5 3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
TO 0-10-1-1/1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1	
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	1 14





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288 324
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2 DualSpurVernierLUT Cnt s16[1][4]	3
T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18] T3_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19] T2_DualSpurVernierLUT_Cnt_s16[1][20]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
	0
T2_DualSpurVernierLUT_Cnt_s16[2][10]	

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2 DualSpurVernierLUT Cnt s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2 DualSpurVernierLUT Cnt s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2 DualSpurVernierLUT Cnt s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2 DualSpurVernierLUT Cnt s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2 DualSpurVernierLUT Cnt s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	148		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k SkipStepErrDiag Cnt str.NStep	3		
k_VernCorrErrorDiag_Cnt_str.Threshold	100		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	6.626505613		
k VernOORangeThresh Deg f32	759.6732113		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt Pim DigColPsEOL.ColTrim Deg f32	77.52818984		
tgt_Pim_DigColPsEOL.Confini_Deg_i32	357.6556342		
tgt Pim DigColPsEOL.Spui Tim Deg_is2	3516		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos\	Valid Cnt Inc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
		-	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_ tgt_DigColPs_Per2_TrimComp_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc		Ligo	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	Even ate d Value	- In-
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.000488281	25

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	313.028198	313.0281898 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-586.971802	-586.9718102 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	•
Param	0x04	0x04	~
Status	0×01	0v01	4



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.71 (Repeat Count = 1)	√
Name	Input Value
DigColPsInt GetCustData()	186
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	126
DigColPs ColTrimStatic Deg M f32	128.6
DigColPs HwAVernCorrFault Cnt M lgc	0
DigColPs I2CColSensorFault Cnt M Igc	
DigColPs_I2CHwColAngle_Cnt_M_u16	21375
DigColPs_I2CHwColAngle_Deg_M_f32	76.6514684
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	38191
DigColPs_I2CHwSpurAngle_Deg_M_f32	76.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1339.267418
DigColPs_PrevVernierLevelNo_Cnt_M_u08	8
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	76.2
DigColPs_TrimCompStatic_Cnt_M_u16	2428
DigColPs_VernCorrDetectAcc_Cnt_M_u16	5
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65





Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

2014-10-14, 18:16:06+0530





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	118
k_SkipStepErrDiag_Cnt_str.PStep	15
k_SkipStepErrDiag_Cnt_str.NStep	42
k_VernCorrErrorDiag_Cnt_str.Threshold	46
k_VernCorrErrorDiag_Cnt_str.PStep	50
k_VernCorrErrorDiag_Cnt_str.NStep	4
k_VernCorrErrorThresh_Deg_f32	90.72870111
k_VernOORangeThresh_Deg_f32	378.3238977
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	76.6514684
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	336.2350776
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL
Name	Actual Value Expected Value Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1 1
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	372.711304 372.7113284 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	360	360 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-540	-540 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.72 (Repeat Count = 1)	·
Name	Input Value
DigColPsInt GetCustData()	149
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	127
DigColPs_ColTrimStatic_Deg_M_f32	132.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs I2CHwColAngle Cnt M u16	27081
DigColPs I2CHwColAngle Deg M f32	152.7639936
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs I2CHwSpurAngle Cnt M u16	49055
DigColPs I2CHwSpurAngle Deg M f32	77.3
DigColPs I2CHwTrimTransCnts Uls M u08	2
DigColPs I2CSensCommFlts Cnt M u08	9
	1
DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevAngleDataAvailable_Cnt_M_Igc	0
	96.19118387
DigColPs_PrevColPos_Deg_M_f32	11
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149 77.3
DigColPs_SpurTrimStatic_Deg_M_f32	
DigColPs_TrimCompStatic_Cnt_M_u16	2464
DigColPs_VernCorrDetectAcc_Cnt_M_u16	14
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T0_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T0_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T3_DualSpurVernierLUT_Cnt_s16[0][16]	144 180
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14] T0_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2 DualSpurVernierLUT Cnt s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2 DualSpurVernierLUT Cnt s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2 DualSpurVernierLUT Cnt s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18] T3_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21 0
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	10
k_SkipStepErrDiag_Cnt_str.Fistep	27
k_SkipStepErrDiag_Cnt_str.NStep	36
k_VernCorrErrorDiag_Cnt_str.Threshold	3
k_VernCorrErrorDiag_Cnt_str.PStep	33
n_vaniounenoiDidy_ont_su.rotep	JJ





Name	Input Value		
k_VernCorrErrorDiag_Cnt_str.NStep	13		
k_VernCorrErrorThresh_Deg_f32	24.98827672		
k_VernOORangeThresh_Deg_f32	1644.361279		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	152.7639936		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.24033874		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1344		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	t_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

igt_Rte_inst_5a_bigColPs.Plin_bigColPsEOL	tgt_Pim_bigColPseOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1472.72717	1472.727273 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1460.06396	1460.063994 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	572.727173	572.7272727 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

Test Step 2.73 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	136.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	10005
DigColPs_I2CHwColAngle_Deg_M_f32	222.9168355
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29915
DigColPs_I2CHwSpurAngle_Deg_M_f32	78.4
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	998.4962399
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	78.4
DigColPs_TrimCompStatic_Cnt_M_u16	2500
DigColPs_VernCorrDetectAcc_Cnt_M_u16	17
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	327 359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2 0
T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2 ColSpurVernierLUT Cnt s16[2][7]	7
T2_ColSpurVernierEUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2 ColSpurVernierLUT Cnt s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12] T3_ColSpurVernierLUT_Cnt_s16[3][13]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

2014-10-14, 18:16:06+0530



Name	Innut Value
Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2 DualSpurVernierLUT Cnt s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2 DualSpurVernierLUT Cnt s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2 DualSpurVernierLUT Cnt s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
	14
T2_DualSpurVernierLUT_Cnt_s16[3][7] T3_DualSpurVernierLUT_Cnt_s16[3][8]	
T2_DualSpurVernierLUT_Cnt_s16[3][8] T3_DualSpurVernierLUT_Cnt_s16[3][8]	16 18
T2_DualSpurVernierLUT_Cnt_s16[3][9]	1.10
T2 DualSpurVernierI IT Ont e46(2)(40)	
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][11]	20
T2_DualSpurVernierLUT_Cnt_s16[3][10] T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	

2014-10-14, 18:16:06+0530





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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	21		
k_VernCorrErrorDiag_Cnt_str.Threshold	95		
k_VernCorrErrorDiag_Cnt_str.PStep	18		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	6.261063576		
k_VernOORangeThresh_Deg_f32	1626.468312		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	222.9168355		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	292.4312814		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1014		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	ıt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	806.116821	806.1168355 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

-81.8182373

0

0

0

-81.81818182 ± 0.00009

Test Step 2.74 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	241
DigColPs_ColTrimStatic_Deg_M_f32	140.9
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	40621
DigColPs_I2CHwColAngle_Deg_M_f32	87.17455715
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	59269
DigColPs_I2CHwSpurAngle_Deg_M_f32	79.5
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0

DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_l2CHwAbsPosValid_Cnt_lgc.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	806.5395069
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	79.5
DigColPs_TrimCompStatic_Cnt_M_u16	2536
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66 -33
T2_ColSpurVernierLUT_Cnt_s16[0][4] T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2 ColSpurVernierLUT Cnt s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16]	2 10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T3_ColSpurVernierLUT_Cnt_s16[3][0]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
	9 6
T2_ColSpurVernierLUT_Cnt_s16[3][8]	

2014-10-14, 18:16:06+0530



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Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
	5
T2_DualSpurVernierLUT_Cnt_s16[2][16]	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	9 10
	9

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DiaColPs Per2

DigColPs_Per2		MAZI	SICAL
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	193		
k_SkipStepErrDiag_Cnt_str.PStep	35		
k_SkipStepErrDiag_Cnt_str.NStep	18		
k_VernCorrErrorDiag_Cnt_str.Threshold	3		
k VernCorrErrorDiag Cnt str.PStep	17		
k VernCorrErrorDiag Cnt str.NStep	0		
k VernCorrErrorThresh Deg f32	79.03816199		
k VernOORangeThresh Deg f32	1714.927183		
tgt DigColPs Per2 MecState Cnt enum.value	0		
tgt Pim DigColPsEOL.ColTrim Deg f32	87.17455715		
tgt Pim DigColPsEOL.SpurTrim Deg f32	277.9257751		
tgt Pim DigColPsEOL.TrimComp Cnt u16	4278		
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc	tgt DigColPs Per2 I2CHwAbsPos	Valid Cnt Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cni		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	L_igo	
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M lgc	0	0	- 100 all
DigColPs I2CHwColAngleForTrim Deg M f32	654.54541	654.5454545 ± 0.00048828125	•
DigColPs I2CHwTrimTransCnts Uls M u08	3	3	
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	-
DigColPs PrevColPos Deg M f32	666.274536	666.2745571 ± 0.0001220703125	
DigCoIPs PrevVernierLevelNo Cnt M u08	7	7	-
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4	4	
DigColPs VernCorrDetectAcc Cnt M u16	1	1	
DigColPs_VernierAngleOORange_Cnt_M_tgc	0	0	
bigoon a_vernierAngieoorkange_ont_ivi_igc		0	

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_

-233.725464

0

0

0

-233.7254429 ± 0.0009

Test Step 2.75 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	196
DigColPs_ColTrimStatic_Deg_M_f32	145

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

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Digicity Digity	Name -	InA.Value
BigGolder Dictional Content Biggolder Dictional Dictional Content Biggolder Dictional Dictional Content Biggolder Dictional Dictional Dictional Content Biggolder Dictional Dictio	Name	Input Value
Dipolane, John Acadenge, Deng M. D2 247 0001497 Dipolane, John Acadenge, Deng M. D2 247 0001497 Dipolane, John Acadenge, Deng M. D2 15 15 15 15 15 15 15 15 15 15 15 15 15 1		
Digitable Coloning		
DigOCH_DICHMONTING_COT_MUSE DigO		
DigOoPs 20th-Nigou Angle Dep M D22 80.6		
Digitary 2014-sign-white Day of S2		
Digitable Digi		
DigOrder ECRISHIC STEMP T.M. Mile DigOrder ECRISHIC TOR Mile DigOrder Previous patient Available, Colf. Mile DigOrder Previous patient Available DigOrder Previous patient DigOrde		
DigCoRing Proving Dear M. 1922 DigCoRing Proving Dear M. 1923 DigCoRing Proving Dear M. 1923 DigCoRing Proving Dear M. 1924 DigCoRing Proving Dear M. 1924 DigCoRing	DigColPs_I2CHwTrimTransCnts_Uls_M_u08	
DigCoRig PrevCoRigin Dec M. 12	DigColPs_I2CSensCommFlts_Cnt_M_u08	14
Digitable Digi	DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DipOCINE SUSSIPHICHECANCE OF M 195 DIPOCINE SUSSI	DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
Digitable SkipStepEritible Cont. Muris Digitable Digitable SkipStepEritible Digitable	DigColPs_PrevColPos_Deg_M_f32	928.9719008
DigGGPS_SupPaminEmc Cml_M_SC M_SC DigGGPS_SupPaminEmc MalbCc_DM_MITD DigGGPS_SupPaminStable_De_MITS2 80.6 DigGGPS_Marchant MalbCc_DM_MITD DigGGPS_Marchant MalbCc_DM_MITD DigGGPS_Marchant MalbCc_DM_MITD DigGGPS_Marchant MalbCc_DM_MITD DigGGPS_Marchant MalbCc_DM_MITD DigGGPS_Marchant MITD Cml_stepDigT 1.15	DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
Disposition Southernoof Buildook Chill Mul 16 127	DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigOSP_SearTemOSHE_Dep_M_152 DigOSP_SearTemOSHE_COM_M_166 1 DigOSP_SearTemOSHE_COM_M_166 1 DigOSP_SearTemOSHE_COM_M_166 1 Na_Inst_Sa_DigOSP_SearTeMOSHE_COM_M_166 1 St_OSSEARTEMOSHE_COM_M_166 1 St_OSSEARTEMOS	DigColPs_SpurParityError_Cnt_M_Igc	0
DigicalPa_ImmCompState_Cnt_M_U15 DigicalPa_ImmCompState_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigicalPa_ImmCompContextArc_Cnt_M_U15 DigitalPa_ImmCompContextArc_Cnt_M_U15 Digi	DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
Digical Part Vernican Proceedings Cent Mu 18	DigColPs_SpurTrimStatic_Deg_M_f32	80.6
DigCoRP_VernComPelectOc_Cnt_M_Urls	DigColPs TrimCompStatic Cnt M u16	1
DigCoUPs VermierAngle-ODTange Crit Mulgo 1		17
Rit. mis. St. DigCoPs		
12. Colsput/emetUT_Cot_s160[0] 163 1		
12, ColspurvementUT, Cnt., 15(0)[1]		
12, CoSpuVernierLUT, Cnt. 16(0) 3 -66 12, CoSpuVernierLUT, Cnt. 16(0) 3 -66 12, CoSpuVernierLUT, Cnt. 16(0) 4 -33 12, CoSpuVernierLUT, Cnt. 16(0) 4 -33 12, CoSpuVernierLUT, Cnt. 16(0) 6 -32 12, CoSpuVernierLUT, Cnt. 16(0) 6 -32 12, CoSpuVernierLUT, Cnt. 16(0) 6 -36 12, CoSpuVernierLUT, Cnt. 16(0) 6 -37 12, CoSpuVernierLUT, Cnt. 16(1) 6 -37 12, CoSpuVernierLUT, Cnt. 16(1) 6 -47 12, CoSpuVernierLUT, Cnt. 16(1) 6 -47 12, CoSpuVernierLUT, Cnt. 16(1) 6 -47 13, CoSpuVernierLUT, Cnt. 16(1) 6 -47 14, CoSpuVernierLUT, Cnt. 16(1) 6 -47 15, CoSpuVernierLUT, Cnt. 16(1) 6 -47 17, CoSpuVernierLUT, Cnt. 16(1) 6 -47 18, CoSpuVernierLUT, Cnt. 16(1) 6 -47 19, CoSpuVernierLUT, Cnt. 16(1) 6 -47 10, CoSpuVernierLUT, Cnt. 16(1) 6 -47 11, CoSpuVernierLUT, Cnt. 16(1) 6 -47 12, CoSpuVernierLUT, Cnt. 16(1) 6 -47 13, CoSpuVernierLUT, Cnt. 16(1) 6 -47 14, CoSpuVernierLUT, Cnt. 16(1) 6 -47 15, CoSpuVernierLUT, Cnt. 16(1) 6 -47 17, CoSpuVernierLUT, Cnt. 16(1) 6 -47 18, CoSpuVernierLUT, Cnt. 16(1) 6 -47 19, CoSpuVernierLUT, Cnt. 16(1) 6 -47 10, CoSpuVernierLUT, Cnt. 16(1) 6 -47 11, CoSpuVernierLUT, Cnt. 16(1) 6 -47 12, CoSpuVernierLUT, Cnt. 16(1) 6 -47 12, CoSpuVernierLUT, Cnt. 16(1) 6 -47 13, CoSpuVernierLUT,		
TZ_CoSpurVermieLUT_Cnt_s16[0][3] -66 TZ_CoSpurVermieLUT_Cnt_s16[0][4] -33 TZ_CoSpurVermieLUT_Cnt_s16[0][5] -0 TZ_CoSpurVermieLUT_Cnt_s16[0][5] -0 TZ_CoSpurVermieLUT_Cnt_s16[0][7] -65 TZ_CoSpurVermieLUT_Cnt_s16[0][7] -65 TZ_CoSpurVermieLUT_Cnt_s16[0][7] -65 TZ_CoSpurVermieLUT_Cnt_s16[0][7] -65 TZ_CoSpurVermieLUT_Cnt_s16[0][7] -100 TZ_C		
17. ColSputVerment.UT Cnt.s16[0] -5 18. ColSputVerment.UT Cnt.s16[0] -5 19. ColSputVerment.UT Cnt.s16[0] -5 19. ColSputVerment.UT Cnt.s16[0] -5 19. ColSputVerment.UT Cnt.s16[0] -5 19. ColSputVerment.UT Cnt.s16[0] -1 19. ColSputVerme		
12. ColSpurVement.UT_Cnt_s16[0][5]		
12. ColSpurVemiet.UT_Cnt_ste(pil) 32 32 32 32 32 32 32 3		
12 ColSput/Veniet.UT_Cnt_s16(0) 7 65 12 ColSput/Veniet.UT_Cnt_s16(0) 8 98 13 ColSput/Veniet.UT_Cnt_s16(0) 9 130 15 ColSput/Veniet.UT_Cnt_s16(0) 10 163 15 ColSput/Veniet.UT_Cnt_s16(0) 11 166 16 ColSput/Veniet.UT_Cnt_s16(0) 12 229 17 ColSput/Veniet.UT_Cnt_s16(0) 13 261 17 ColSput/Veniet.UT_Cnt_s16(0) 14 294 17 ColSput/Veniet.UT_Cnt_s16(0) 15 327 17 ColSput/Veniet.UT_Cnt_s16(0) 15 327 18 ColSput/Veniet.UT_Cnt_s16(0) 16 399 19 ColSput/Veniet.UT_Cnt_s16(0) 16 399 19 ColSput/Veniet.UT_Cnt_s16(1) 0 0 10 ColSput/Veniet.UT_Cnt_s16(1) 1 4 10 ColSput/Veniet.UT_Cnt_s16(1) 1 4 11 ColSput/Veniet.UT_Cnt_s16(1) 1 4 12 ColSput/Veniet.UT_Cnt_s16(1) 1 4 12 ColSput/Veniet.UT_Cnt_s16(1) 1 1 12 ColSput/Veniet.UT_Cnt_s16(1) 1 1 13 ColSput/Veniet.UT_Cnt_s16(1) 1 1 14 ColSput/Veniet.UT_Cnt_s16(1) 1 1 15 ColSput/Veniet.UT_Cnt_s16(1) 1 1 17 ColSput/Veniet.UT_Cnt_s16(1) 1 1 18 ColSput/Veniet.UT_Cnt_s16(1) 1 1 19 ColSput/Veniet.UT_Cnt_s16(1) 1 1 10 ColSput/Veniet.UT_Cnt_s16(1) 1 1 10 ColSput/Veniet.UT_Cnt_s16(1) 1 1 11 ColSput/Veniet.UT_Cnt_s16(1) 1 1 12 ColSput/Veniet.UT_Cnt_s16(1) 1 1 13 ColSput/Veniet.UT_Cnt_s16(1) 1 1 14 ColSput/Veniet.UT_Cnt_s16(1) 1 1 17 ColSput/Veniet.UT_Cnt_s16(1) 1 1 18 ColSput/Veniet.UT_Cnt_s16(1) 1 1 19 ColSput/Veniet.UT_Cnt_s16(1) 1 1 10 ColSput/Veniet.UT_Cnt_s16(1) 1 1 11 ColSput/Veniet.UT_Cnt_s16(1) 1 1 12 ColSput/Veniet.UT_Cnt_s16(1) 1 1 13 ColSput/Veniet.UT_Cnt_s16(1) 1 1 14 ColSput/Veniet.UT_Cnt_s16(1) 1 1 17 ColSput/Veniet.UT_Cnt_s16(1) 1 1 18 ColSput/Veniet.UT_Cnt_s16(1) 1 1 19 ColSput/Veniet.UT_Cnt_s16(1) 1 1 10 ColSput/Veniet.UT_Cnt_s16(1) 1 1 11 ColSput/Veniet.UT_Cnt_s16(1) 1 1 12 ColSput/Veniet.UT_Cnt_s16(1) 1 1 13 ColSput/Veniet.UT_Cnt_s16(1) 1 1 14 ColSput/Veniet.UT_Cnt_s16(1) 1 1 15 ColSput/Veniet.UT_Cnt_s16(1) 1 1		
12. ColSpurVerniet.UT_Cnt_s16(0)(8) 98 17. ColSpurVerniet.UT_Cnt_s16(0)(9) 130 1		
12 ColSpurVemieLUT_Cnt_st6[0] 10 163 172 ColSpurVemieLUT_Cnt_st6[0] 11 186 1		
12 ColSpurVemiet.UT_Cnt_st6[0][10] 183		
T2_ColSpurVemierLUT_Cnt_s16[0]11]		
12 ColSpurVernierLUT_Cnt_s16[0][12] 229 12 ColSpurVernierLUT_Cnt_s16[0][13] 261 12 ColSpurVernierLUT_Cnt_s16[0][14] 294 12 ColSpurVernierLUT_Cnt_s16[0][15] 327 12 ColSpurVernierLUT_Cnt_s16[0][16] 359 12 ColSpurVernierLUT_Cnt_s16[1][0] 0 12 ColSpurVernierLUT_Cnt_s16[1][1] 4 12 ColSpurVernierLUT_Cnt_s16[1][1] 4 12 ColSpurVernierLUT_Cnt_s16[1][1] 4 12 ColSpurVernierLUT_Cnt_s16[1][1] 5 13 ColSpurVernierLUT_Cnt_s16[1][1] 7 14 ColSpurVernierLUT_Cnt_s16[1][1] 1 15 ColSpurVernierLUT_Cnt_s16[1][1] 1 17 ColSpurVernierLUT_Cnt_s16[1][1] 4 18 ColSpurVernierLUT_Cnt_s16[1][1] 5 19 ColSpurVernierLUT_Cnt_s16[1][1] 6 19 ColSpurVernierLUT_Cnt_s16[1][1] 7 10 ColSpurVernierLUT_Cnt_s16[1][1] 1 11 ColSpurVernierLUT_Cnt_s16[1][1] 1 12 ColSpurVernierLUT_Cnt_s16[1][1] 1 13 ColSpurVernierLUT_Cnt_s16[1][1] 1 14 ColSpurVernierLUT_Cnt_s16[1][1] 1 15 ColSpurVernierLUT_Cnt_s16[1][1] 1 17 ColSpurVernierLUT_Cnt_s16[1][1] 1 18 ColSpurVernierLUT_Cnt_s16[1][1] 1 19 ColSpurVernierLUT_Cnt_s16[1][1] 1 10 ColSpurVernierLUT_Cnt_s16[1][1] 1 11 ColSpurVernierLUT_Cnt_s16[1][1] 1 12 ColSpurVernierLUT_Cnt_s16[2][1] 1 13 ColSpurVernierLUT_Cnt_s16[2][1] 1 14 ColSpurVernierLUT_Cnt_s16[2][1] 1 15 ColSpurVernierLUT_Cnt_s16[2][1] 1 17 ColSpurVernierLUT_Cnt_s16[2][1] 1 18 ColSpurVernierLUT_Cnt_s16[2][1] 1 19 ColSpurVernierLUT_Cnt_s16[2][1] 1 10 ColSpurVernierLUT_Cnt_s16[2][1] 1 11 ColSpurVernierLUT_Cnt_s16[2][1] 1 12 ColSpurVernierLUT_Cnt_s16[2][1] 1 17 ColSpurVernierLUT_Cnt_s16[2][1] 1 18 ColSpurVernierLUT_Cnt_s16[2][1] 1 19 ColSpurVernierLUT_Cnt_s16[2][1] 1 10 ColSpurVernierLUT_Cnt_s16[2][1] 1 10 ColSpurVernierLUT_Cnt_s16[2][1] 1 10 ColSp		
T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[0][1] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][7] 3 T2_ColSpurVernierLUT_Cnt_s16[1][7] 3 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][9] 1 T2_ColSpurVernierLUT_Cnt_s16[1][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][1] 1 T2_ColSpu	T2_ColSpurVernierLUT_Cnt_s16[0][11]	
T2_ColSpurVemierLUT_Cnt_st6[0][14]	T2_ColSpurVernierLUT_Cnt_s16[0][12]	
T2_ColSpurVernierLUT_Cnt_st6[0][15] 327 T2_ColSpurVernierLUT_Cnt_st6[0][16] 339 T2_ColSpurVernierLUT_Cnt_st6[1][1] 4 T2_ColSpurVernierLUT_Cnt_st6[1][1] 4 T2_ColSpurVernierLUT_Cnt_st6[1][2] 3 T2_ColSpurVernierLUT_Cnt_st6[1][3] 2 T2_ColSpurVernierLUT_Cnt_st6[1][8] 1 T2_ColSpurVernierLUT_Cnt_st6[1][8] 1 T2_ColSpurVernierLUT_Cnt_st6[1][8] 0 T2_ColSpurVernierLUT_Cnt_st6[1][8] 0 T2_ColSpurVernierLUT_Cnt_st6[1][8] 1 T2_ColSpurVernierLUT_Cnt_st6[1][8] 2 T2_ColSpurVernierLUT_Cnt_st6[1][8] 2 T2_ColSpurVernierLUT_Cnt_st6[1][9] 1 T2_ColSpurVernierLUT_Cnt_st6[1][9] 1 T2_ColSpurVernierLUT_Cnt_st6[1][10] 0 T2_ColSpurVernierLUT_Cnt_st6[1][10] 1 T2_ColSpurVernierLUT_Cnt_st6[1][11] 4 T2_ColSpurVernierLUT_Cnt_st6[1][12] 3 T2_ColSpurVernierLUT_Cnt_st6[1][12] 3 T2_ColSpurVernierLUT_Cnt_st6[1][13] 2 T2_ColSpurVernierLUT_Cnt_st6[1][14] 1 T2_ColSpurVernierLUT_Cnt_st6[1][16] 0 T2_ColSpurVernierLUT_Cnt_st6[1][16] 1 T2_ColSpurVernierLUT_Cnt_st6[1][16] 1 T2_ColSpurVernierLUT_Cnt_st6[1][16] 1 T2_ColSpurVernierLUT_Cnt_st6[1][16] 1 T2_ColSpurVernierLUT_Cnt_st6[2][1] 1 T2_ColSpurVernierLUT_Cnt_st6[2	T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4 T2_ColSpurVernierLUT_Cnt_s16[1][8] 2 T2_ColSpurVernierLUT_Cnt_s16[1][8] 1 T2_ColSpurVernierLUT_Cnt_s16[1][9] 1 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 5 T2_ColSpurVernierLUT_Cnt_s16[1][1] 6 T2_ColSpurVernierLUT_Cnt_s16[1][1] 6 T2_ColSpurVernierLUT_Cnt_s16[1][1] 7 T2_ColSpurVernierLUT_Cnt_s16[1][1] 7 T2_ColSpurVernierLUT_Cnt_s16[1][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][6] 1 T2_ColSpurVernierLUT	T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
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T2_ColSpurVernierLUT_Cnt_s16[1][1]	T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVemierLUT_Cnt_s16[1][2] 12_ColSpurVemierLUT_Cnt_s16[1][3] 2_ColSpurVemierLUT_Cnt_s16[1][4] 12_ColSpurVemierLUT_Cnt_s16[1][5] 0 12_ColSpurVemierLUT_Cnt_s16[1][6] 4_ColSpurVemierLUT_Cnt_s16[1][7] 12_ColSpurVemierLUT_Cnt_s16[1][7] 12_ColSpurVemierLUT_Cnt_s16[1][7] 12_ColSpurVemierLUT_Cnt_s16[1][8] 12_ColSpurVemierLUT_Cnt_s16[1][9] 12_ColSpurVemierLUT_Cnt_s16[1][10] 12_ColSpurVemierLUT_Cnt_s16[1][10] 13_ColSpurVemierLUT_Cnt_s16[1][11] 4_ColSpurVemierLUT_Cnt_s16[1][12] 13_ColSpurVemierLUT_Cnt_s16[1][12] 14_ColSpurVemierLUT_Cnt_s16[1][13] 15_ColSpurVemierLUT_Cnt_s16[1][14] 16_ColSpurVemierLUT_Cnt_s16[1][16] 17_ColSpurVemierLUT_Cnt_s16[1][16] 18_ColSpurVemierLUT_Cnt_s16[1][16] 19_ColSpurVemierLUT_Cnt_s16[1][16] 10_ColSpurVemierLUT_Cnt_s16[2][2] 10_ColSpurVemierLUT_Cnt_s16[2][3] 11_ColSpurVemierLUT_Cnt_s16[2][3] 12_ColSpurVemierLUT_Cnt_s16[2][6] 13_ColSpurVemierLUT_Cnt_s16[2][6] 14_ColSpurVemierLUT_Cnt_s16[2][6] 15_ColSpurVemierLUT_Cnt_s16[2][6] 16_ColSpurVemierLUT_Cnt_s16[2][6] 17_ColSpurVemierLUT_Cnt_s16[2][6] 18_ColSpurVemierLUT_Cnt_s16[2][6] 19_ColSpurVemierLUT_Cnt_s16[2][6] 10_ColSpurVemierLUT_Cnt_s16[2][6] 11_ColSpurVemierLUT_Cnt_s16[2][6] 12_ColSpurVemierLUT_Cnt_s16[2][6] 13_ColSpurVemierLUT_Cnt_s16[2][6] 14_ColSpurVemierLUT_Cnt_s16[2][6] 15_ColSpurVemierLUT_Cnt_s16[2][6] 16_ColSpurVemierLUT_Cnt_s16[2][6] 17_ColSpurVemierLUT_Cnt_s16[2][6] 18_ColSpurVemierLUT_Cnt_s16[2][6] 18_ColSpurVemierLUT_Cnt_s16[2][6] 18_Co	T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
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T2_ColSpurVernierLUT_Cnt_s16[1][4]	T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
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T2_ColSpurVernierLUT_Cnt_s16[2][14] 4		
T2_ColSpurVernierLUT_Cnt_s16[2][15] 2		
T2_ColSpurVernierLUT_Cnt_s16[2][16] 10		
T2_ColSpurVernierLUT_Cnt_s16[3][0] 1		





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17, CoSpur/wentUT, Cott, \$103115	T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_CosSpurVement UT_Cnt_s160[110] 388 12_DusSpurVement UT_Cnt_s160[11] 380 12_DusSpurVement UT_Cnt_s160[12] 384 12_DusSpurVement UT_Cnt_s160[13] 384 12_DusSpurVement UT_Cnt_s160[13] 384 12_DusSpurVement UT_Cnt_s160[14] 382 12_DusSpurVement UT_Cnt_s160[16] 388 12_DusSpurVement UT_Cnt_s160[16] 380 12_DusSpurVement UT_Cnt_s160[16] 380 12_DusSpurVement UT_Cnt_s160[17] 380 12_DusSpurVement UT_Cnt_s160[18] 380 12_DusSpurVement UT_Cnt_s160[18	T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
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12_DasSgru/FemerU_T_Crt_s160[12]	T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
12_DasSgru/FemerU_T_Crt_s160[12]	T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2, DuaSpurVermiet.UT_Cnt_s160[15] 288		-360
T. DualSparVermicLUT_Cnt_s180[H] 252	T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_Dus SpurVernieLUT_Cnt_s16[0][5] 216 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -144 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -144 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -148 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -148 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -72 T2_Dus SpurVernieLUT_Cnt_s16[0][7] -36 T2_Dus SpurVern		-288
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12, DualSparVermetLUT_Cnt_s16(0)(8) .189		
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12. DualSpur/vernierLUT_Cnt_s16()[11] 0 0 0 0 0 0 0 0 0		
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172 DuaiSpurVernierLUT_Cnt_sticip[14] 108 172 DuaiSpurVernierLUT_Cnt_sticip[16] 144 172 DuaiSpurVernierLUT_Cnt_sticip[17] 126 172 DuaiSpurVernierLUT_Cnt_sticip[17] 126 172 DuaiSpurVernierLUT_Cnt_sticip[17] 126 172 DuaiSpurVernierLUT_Cnt_sticip[18] 125 172 DuaiSpurVernierLUT_Cnt_sticip[18] 126 173 DuaiSpurVernierLUT_Cnt_sticip[18] 127 174 DuaiSpurVernierLUT_Cnt_sticip[18] 127 175 DuaiSpurVernierLUT_Cnt_sticip[18] 137 175 DuaiSpurVernierLUT_Cnt_sticip[18] 147 175 DuaiSpurVernierLUT_Cnt_sticip[18] 14		
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T2_DualSpurVemiert.UT_Cnt_s16[1][8] 7 T2_DualSpurVemiert.UT_Cnt_s16[1][9] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][10] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][11] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][12] 1 T2_DualSpurVemiert.UT_Cnt_s16[1][13] 2 T2_DualSpurVemiert.UT_Cnt_s16[1][14] 3 T2_DualSpurVemiert.UT_Cnt_s16[1][15] 4 T2_DualSpurVemiert.UT_Cnt_s16[1][16] 5 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][17] 6 T2_DualSpurVemiert.UT_Cnt_s16[1][19] 8 T2_DualSpurVemiert.UT_Cnt_s16[1][20] 9 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 0 T2_DualSpurVemiert.UT_Cnt_s16[1][21] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][1] 1 T2_DualSpurVemiert.UT_Cnt_s16[2][2] 2 T2_DualSpurVemiert.UT_Cnt_s16[2][3] 3 T2_DualSpurVemiert.UT_Cnt_s16[2][4] 4 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 5 T2_DualSpurVemiert.UT_Cnt_s16[2][6] 6 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemiert.UT_Cnt_s16[2][7] 7 T2_DualSpurVemier.UT_Cnt_s16[2][7] 7	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
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T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 2 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVerniert_UT_Cnt_s16[1][10] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][11] 0 T2_DualSpurVerniert_UT_Cnt_s16[1][12] 1 T2_DualSpurVerniert_UT_Cnt_s16[1][13] 2 T2_DualSpurVerniert_UT_Cnt_s16[1][14] 3 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 4 T2_DualSpurVerniert_UT_Cnt_s16[1][16] 5 T2_DualSpurVerniert_UT_Cnt_s16[1][17] 6 T2_DualSpurVerniert_UT_Cnt_s16[1][18] 7 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][19] 8 T2_DualSpurVerniert_UT_Cnt_s16[1][20] 9 T2_DualSpurVerniert_UT_Cnt_s16[1][21] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 0 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 1 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 4 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 5 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 6 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 7 T2_DualSpurVerniert_UT_Cnt_s16[2][0] 8	T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][15] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		9
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6		
T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][0] 2 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 2 T2_DualSpurVernierLUT_Cnt_s16[2][1] 3 T2_DualSpurVernierLUT_Cnt_s16[2][1] 4 T2_DualSpurVernierLUT_Cnt_s16[2][1] 5 T2_DualSpurVernierLUT_Cnt_s16[2][1] 7 T2_DualSpurVernierLUT_Cnt_s16[2][1] 7 T2_DualSpurVernierLUT_Cnt_s16[2][1] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][16] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
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T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][20] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8		
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T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	T2_DualSpurVernierLUT_Cnt_s16[2][5]	
T2_DualSpurVernierLUT_Cnt_s16[2][8] 8	T2_DualSpurVernierLUT_Cnt_s16[2][6]	
	T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] 9	T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] 10	T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11] 0	T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] 1	T2_DualSpurVernierLUT_Cnt_s16[2][12]	1

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	71		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	43		
k_VernCorrErrorDiag_Cnt_str.Threshold	18		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	16		
k_VernCorrErrorThresh_Deg_f32	4.670489788		
k_VernOORangeThresh_Deg_f32	1238.898165		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	247.6901497		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	147.1778288		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAl	osPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAl		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimCom		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	,	
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M Igc	1	A POOLOG TUILO	1 tooui

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1442.4646	1442.464623 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓

DigColPs_PrevColPos__

DigColPs_PrevColPos__



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~





Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
	4		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][4]			
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	23		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	15		
k_VernCorrErrorThresh_Deg_f32	14.63263726		
k_VernOORangeThresh_Deg_f32	215.8799315		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	212.9646001		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	227.4025638		
tgt_Pim_DigColPsEOL.7pimComp_Cnt_u16	1		
		Nalid Cpt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_C	nt_igc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	<u> </u>

1079.22607

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

1079.226107 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	23	23	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	~
Param	0x0E	0x0E	~
Status	0x01	0x01	✓

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	•
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.77 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	241
DigColPs ColParityError Cnt M Igc	1
DigColPs ColSensorFaultAcc Cnt M u16	124
DigColPs_ColTrimStatic_Deg_M_f32	153.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs I2CHwColAngle Cnt M u16	45384
DigColPs I2CHwColAngle Deg M f32	217.6150646
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs I2CHwSpurAngle Cnt M u16	58335
DigColPs I2CHwSpurAngle Deg M f32	82.8
DigColPs I2CHwTrimTransCnts Uls M u08	0
DigColPs I2CSensCommFlts Cnt M u08	0
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs PrevColPos Deg M f32	1165.499187
DigColPs PrevVernierLevelNo Cnt M u08	7
DigColPs SkipStepFltDetectAcc Cnt M u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs SpurSensorFaultAcc Cnt M u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	82.8
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs VernierAngleOORange Cnt M Igc	0
Rte Inst Sa DigColPs	tgt Rte Inst Sa DigColPs
	-163
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][2] T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2 ColSpurVernierLUT Cnt s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2 ColSpurVernierLUT Cnt s16[0][9]	130
T2 ColSpurVernierLUT Cnt s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][12]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2 ColSpurVernierLUT Cnt s16[0][15]	327
12_00/0pul veriller201_011[_910[0][10]	VEI

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DigColPs_Per2		Tabolat
Name	Input Value	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359	
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4	
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3	
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2	
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4	
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3	
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2	
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4	
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3	
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0	
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8	
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2	
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9	
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5	
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3	
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8	
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6	
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4	
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2	
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11	
T2_ColSpurVernierLUT_Cnt_s16[3][3]		





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2] T0_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3] T2_DualSpurVernierLUT_Cnt_s16[1][4]	2 3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	1 2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	7 8
T2 DualSpurVernierLUT Cnt s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10 12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpur/ornierLUT_Cnt_s16[3][14] T3_DualSpur/ornierLUT_Cst_s16[3][15]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	9
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	162
k_SkipStepErrDiag_Cnt_str.PStep	20
k_SkipStepErrDiag_Cnt_str.NStep	48
k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep	100 38

DigColPs_Per2



Name	Input Value
k_VernCorrErrorDiag_Cnt_str.NStep	3
k_VernCorrErrorThresh_Deg_f32	67.91880226
k_VernOORangeThresh_Deg_f32	1176.43799
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	217.6150646
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	178.2231709
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL

tgt_Rte_inst_sa_bigColPs.Pim_bigColPsEOL	tgt_Pilli_DigColPSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1429.35303	1429.353104 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	~
DigColPs_PrevColPos_Deg_M_f32	1440	1440 ± 0.0001220703125	-
DigColPs_PrevVernierLevelNo_Cnt_M_u08	14	14	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	42	42	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	540	540 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.78 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	196
DigColPs ColParityError Cnt M Igc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	205
DigColPs_ColTrimStatic_Deg_M_f32	157.3
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	33060
DigColPs_I2CHwColAngle_Deg_M_f32	28.42972344
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18440
DigColPs_I2CHwSpurAngle_Deg_M_f32	83.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	594.4117691
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	83.9
DigColPs_TrimCompStatic_Cnt_M_u16	2680
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33





	(1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2 ColSpurVernierLUT Cnt s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
Γ2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierEUT_Cnt_s16[2][10]	1
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
Γ2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
F2_ColSpurVernierLUT_Cnt_s16[3][15]	4
C2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
rz_bualspurvernierL01_cnt_s16[0][4] F2_bualspurvernierLUT_cnt_s16[0][5]	-216
	-216 -180
T2_DualSpurVernierLUT_Cnt_s16[0][6] T2_DualSpurVernierLUT_Cnt_s16[0][7]	
	-144
	400
T2_DualSpurVernierLUT_Cht_s16[0][8] T2_DualSpurVernierLUT_Cht_s16[0][8] T2_DualSpurVernierLUT_Cht_s16[0][9]	-108 -72

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2 DualSpurVernierLUT Cnt s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1.
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2 DualSpurVernierLUT Cnt s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11

2014-10-14, 18:16:06+0530



DigColPs_Per2

Param

Status

DigCoil 3_1 et2			
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	50		
k_SkipStepErrDiag_Cnt_str.PStep	17		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	57		
k_VernCorrErrorDiag_Cnt_str.PStep	31		
k_VernCorrErrorDiag_Cnt_str.NStep	1		
k_VernCorrErrorThresh_Deg_f32	27.292485		
k_VernOORangeThresh_Deg_f32	686.6912438		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	28.42972344		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	179.9644135		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1458		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1309.09082	1309.090909 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1311.12976	1311.129723 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7	7	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	411.129761	411.1297234 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
_			

Τ			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x0C

0x01

0x0C

0x01

Test Step 2.79 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	128
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	210
DigColPs_ColTrimStatic_Deg_M_f32	161.4
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	11998
DigColPs_I2CHwColAngle_Deg_M_f32	16.12509024
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	24120
DigColPs_I2CHwSpurAngle_Deg_M_f32	285
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	4
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_PrevColPos_Deg_M_f32	1148.961804
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	5
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128
DigColPs_SpurTrimStatic_Deg_M_f32	285
DigColPs_TrimCompStatic_Cnt_M_u16	2716
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2 ColSpurVernierLUT Cnt s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2 ColSpurVernierLUT Cnt s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10] T3_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
	6 3 16





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T2_DualSpurVernierLUT_Cnt_s16[0][10]	-72 -36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	288 324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	5 6
T2_DualSpurVernierLUT_Cnt_s16[1][7]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3] T3_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_S16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22 2
T2_DualSpurVernierLUT_Cnt_s16[3][1]	

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DigCoirs_rei2			CILCIO
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2 DualSpurVernierLUT Cnt s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k SkipStepErrDiag Cnt str.Threshold	10		
k SkipStepErrDiag Cnt str.PStep	32		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k VernCorrErrorDiag Cnt str.Threshold	52		
k_VernCorrErrorDiag_Cnt_str.PStep	20		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k VernCorrErrorThresh Deg f32	85.22490358		
k_VernOORangeThresh_Deg_f32	1677.836695		
tgt DigColPs Per2 MecState Cnt enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	16.12509024		
tgt Pim DigColPsEOL.SpurTrim Deg f32	48.06899381		
tgt Pim DigColPsEOL.TrimComp Cnt u16	4397		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt DigColPs Per2 I2CHwAbsPo	sValid Cnt Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPc	us_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	
DigColPs I2CHwColAngleForTrim Deg M f32	1309.09082	1309.090909 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	
DigColPs PrevColPos Deg M f32	1294.7251	1294.72509 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	13	13	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	
DigColPs VernCorrDetectAcc Cnt M u16	2	2	
DigColPs VernierAngleOORange Cnt M Igc	0	0	
3		•	

Т				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V

409.09082

0

0

0

409.0909091 ± 0.0009

Test Step 2.80 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	220
DigColPs_ColTrimStatic_Deg_M_f32	165.5

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value





Name	Input Value
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_12CHwColAngle_Cnt_M_u16	12814
DigColPs_I2CHwColAngle_Deg_M_f32	117.9909339
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	14635
	86.1
DigColPs_I2CHwSpurAngle_Deg_M_f32	3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	
DigColPs_I2CSensCommFlts_Cnt_M_u08	10
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	86.1
DigColPs_TrimCompStatic_Cnt_M_u16	2752
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
	0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1]	
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	1.
	6
	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5] T3_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T0_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	144
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	4
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2 DualSpurVernierLUT Cnt s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][18] T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][19]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
12 Duan-Opur volino (201 Ont 310 2 1 1	•
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2 DualSpurVernierLUT Cnt s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2 DualSpurVernierLUT Cnt s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2 DualSpurVernierLUT Cnt s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	117.9909339		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	208.2439033		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt DigColPs Per2 I2CHwAbsPos		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt DigColPs Per2 TrimComp Cr		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	Nesul
DigCoIPs_HwAvernCorrFault_Cnt_M_igc DigCoIPs_I2CHwColAngleForTrim_Deg_M_f32			
	1089.93457	1089.934589 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc			•
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	
LUIDI DIPE PARIZI SPELIATATIVA CINT M LUIX			

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1089.93457	1089.934589 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1080	1080 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11	11	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	180	180 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.81 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	205
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	214
DigColPs_ColTrimStatic_Deg_M_f32	169.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	21375
DigColPs_I2CHwColAngle_Deg_M_f32	77.52818984
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	38191
DigColPs_I2CHwSpurAngle_Deg_M_f32	87.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205
DigColPs_SpurTrimStatic_Deg_M_f32	87.2
DigColPs_TrimCompStatic_Cnt_M_u16	2788
DigColPs VernCorrDetectAcc Cnt M u16	10
DigColPs VernierAngleOORange Cnt M Igc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
	163
T2_ColSpurVernierLUT_Cnt_s16[0][10]	196
T2_ColSpurVernierLUT_Cnt_s16[0][11]	
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10] T0_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][12]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T0_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	77.52818984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	357.6556342		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2646	lan.	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	192	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL		
tgt_rte_mst_sa_bigcoiPs.Piiii_bigcoiPsEOL Name	Actual Value	Expected Value	Dooult
	1	1	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	004.040470	004 0404040 + 0 000 10000105	

981.818176

3

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

DigColPs_I2CHwTrimTransCnts_Uls_M_u08

981.8181818 ± 0.00048828125

3





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	987.928223	987.9281898 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	81.8181763	81.81818182 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.82 (Repeat Count = 1) Name	Innut Value
	Input Value
DigColPsInt_GetCustData()	196
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	173.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	27081
DigColPs_I2CHwColAngle_Deg_M_f32	76.6514684
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49055
DigColPs_I2CHwSpurAngle_Deg_M_f32	88.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	196
DigColPs_SpurTrimStatic_Deg_M_f32	88.3
DigColPs_TrimCompStatic_Cnt_M_u16	2824
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T3_DualSpurVernierLUT_Cnt_s16[0][10]	-72 36
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
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Input Value T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 2 T2 DualSpurVernierLUT Cnt s16[1][13] T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 2 T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2 DualSpurVernierLUT Cnt s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] 4 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 22 T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 4 T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 7 T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2 DualSpurVernierLUT Cnt s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 $k_SelectFromColumn_Cnt_lgc$ k SkipStepErrDiag Cnt str.Threshold 160 k_SkipStepErrDiag_Cnt_str.PStep 23 k_SkipStepErrDiag_Cnt_str.NStep 16 82 $k_VernCorrErrorDiag_Cnt_str.Threshold$ k_VernCorrErrorDiag_Cnt_str.PStep 43 12 $k_VernCorrErrorDiag_Cnt_str.NStep$

2014-10-14, 18:16:06+0530



Name	Input Value	
k_VernCorrErrorThresh_Deg_f32	16.35241604	
k_VernOORangeThresh_Deg_f32	1800	
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1	

2014-10-14, 18:16:06+0530



32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
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327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36

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2014-10-14, 18:16:06+0530



Input Value T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12] 36 T2_DualSpurVernierLUT_Cnt_s16[0][13] 72 T2_DualSpurVernierLUT_Cnt_s16[0][14] 108 T2_DualSpurVernierLUT_Cnt_s16[0][15] 144 T2_DualSpurVernierLUT_Cnt_s16[0][16] 180 T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 1 T2_DualSpurVernierLUT_Cnt_s16[1][2] T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 3 T2_DualSpurVernierLUT_Cnt_s16[1][4] T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1 T2_DualSpurVernierLUT_Cnt_s16[1][13] 2 T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 T2_DualSpurVernierLUT_Cnt_s16[1][15] 4 T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2 DualSpurVernierLUT Cnt s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2 DualSpurVernierLUT Cnt s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2 DualSpurVernierLUT Cnt s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] 4 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2 DualSpurVernierLUT Cnt s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 4 T2_DualSpurVernierLUT_Cnt_s16[3][2] T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 12 T2 DualSpurVernierLUT Cnt s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2 DualSpurVernierLUT Cnt s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 0 $k_SkipStepErrDiag_Cnt_str.Threshold$ 125 k_SkipStepErrDiag_Cnt_str.PStep 10 $k_SkipStepErrDiag_Cnt_str.NStep$ 38 k_VernCorrErrorDiag_Cnt_str.Threshold 64 $k_VernCorrErrorDiag_Cnt_str.PStep$ 8 k_VernCorrErrorDiag_Cnt_str.NStep 11 78.40277648 k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32 547.3349351 tgt_DigColPs_Per2_MecState_Cnt_enum.value n tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 152.7639936 90.24033874 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ 491 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc $tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL$ tgt_Pim_DigColPsEOL **Actual Value Expected Value**

DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	334.963989	334.9639936 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4	4	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-572.727295	-572.7272727 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.84 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_ColTrimStatic_Deg_M_f32	190.1	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	58760	
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972	
DigColPs_I2CHwSpurAngle_Deg_M_f32	92.7	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	
DigColPs_I2CSensCommFlts_Cnt_M_u08	24	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	421.9525396	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	
DigColPs_SpurParityError_Cnt_M_lgc	0	





Name	Input Value
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	92.7
DigColPs_TrimCompStatic_Cnt_M_u16	2968
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLOT_Crit_s10[0][10] T2_ColSpurVernierLUT_Crit_s16[1][0]	0
T2_ColSpurVernierLOT_Crit_s16[1][0] T2_ColSpurVernierLUT_Crit_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
	0
T2_ColSpurVernierLUT_Cnt_s16[1][5]	
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2 ColSpurVernierLUT Cnt s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
	1
T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4

2014-10-14, 18:16:06+0530



DigCoiPs_Per2		GEOTIAL
Name	Input Value	
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17	
Γ2_DualSpurVernierLUT_Cnt_s16[0][0]	-396	
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360	
Γ2_DualSpurVernierLUT_Cnt_s16[0][2]	-324	
Γ2 DualSpurVernierLUT Cnt s16[0][3]	-288	
Γ2_DualSpurVernierLUT_Cnt_s16[0][4]	-252	
Γ2_DualSpurVernierLUT_Cnt_s16[0][5]	-216	
Γ2_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
Γ2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
Γ2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
Γ2_DualSpurVernierLUT_Cnt_s16[0][9]	-72	
Γ2_DualSpurVernierLUT_Cnt_s16[0][10]	-36	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
	72	
Γ2_DualSpurVernierLUT_Cnt_s16[0][13]		
72_DualSpurVernierLUT_Cnt_s16[0][14]	108	
[2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
Γ2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
C2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
C2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
[2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
[2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
⁻ 2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
² _DualSpurVernierLUT_Cnt_s16[1][0]	9	
2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
² _BualSpurVernierLUT_Cnt_s16[1][6]	5	
2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
	7	
2_DualSpurVernierLUT_Cnt_s16[1][8]		
2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
² _DualSpurVernierLUT_Cnt_s16[1][10]	9	
[2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
Γ2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
Γ2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
Γ2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
Γ2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
Γ2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
Γ2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
Γ2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
Γ2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
2 DualSpurVernierLUT Cnt s16[2][0]	0	
2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
	2	
² 2_DualSpurVernierLUT_Cnt_s16[2][2]		
² 2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
² _DualSpurVernierLUT_Cnt_s16[2][5]	5	
2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
2_DualSpurVernierLUT_Cnt_s16[2][11]	0	
2_DualSpurVernierLUT_Cnt_s16[2][12]	1	
2_DualSpurVernierLUT_Cnt_s16[2][13]	2	
2_DualSpurVernierLOT_Gnt_s16[2][13]	3	
2_DualSpurVernierLUT_Cnt_s16[2][14] 2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
Γ2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
	4	
2 DualSpurVernierLUT Cnt s16[3][2]	4	
[2_DualSpurVernierLUT_Cnt_s16[3][2] [2_DualSpurVernierLUT_Cnt_s16[3][3] [2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8	

DigColPs_Per2

2014-10-14, 18:16:06+0530





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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2 DualSpurVernierLUT Cnt s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.143258		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	losValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	-
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	-
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	647.932129	647.9321395 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value		0 0		
Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call Sa DigColPs NytrDiagMar SetNTCStatus	1	Pte Call Sa DigColPs NytrDiagMar SetNTCStatus	1	-

Rte_Call_DigColPs_Per2_CP1_CheckpointReached

-252.067871

-252.0678605 ± 0.0009

Test Step 2.85 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	194.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432

Rte_Call_DigColPs_Per2_CP1_CheckpointReached

DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value





Name	Input Value
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	93.8
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	93.8
DigColPs_TrimCompStatic_Cnt_M_u16	3004
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2 ColSpurVernierLUT Cnt s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2 ColSpurVernierLUT Cnt s16[0][6]	32
T2 ColSpurVernierLUT Cnt s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
	196
T2_ColSpurVernierLUT_Cnt_s16[0][11]	229
T2_ColSpurVernierLUT_Cnt_s16[0][12]	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2 ColSpurVernierLUT Cnt s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_\$16[2][3] T2_ColSpurVernierLUT_Cnt_\$16[2][4]	2
	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2]	11

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7] T0_ColSpurVernierLUT_Cot_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8] T2_ColSpurVernierLUT_Cnt_s16[3][9]	9
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-324 -288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T0_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5] T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4] T0_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3 4

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2 DualSpurVernierLUT Cnt s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2 DualSpurVernierLUT Cnt s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	87		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	20		
k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k_VernCorrErrorDiag_Cnt_str.PStep	17		
k_VernCorrErrorDiag_Cnt_str.NStep	2		
k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
tgt_Pim_DigColPsEOL.CorTim_Deg_f32	88.88743997		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	797		
· · · · · · · · · · · · · · · · ·		alid Cat Igo	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosV		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_l	-	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_MecState_Cnt_e tgt_DigColPs_Per2_TrimComp_Cnt_		
		<u>igo</u>	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	Evenanted Value	P "
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	*
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	_
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	*
DigColPs_PrevColPos_Deg_M_f32	1160.1637	1160.163741 ± 0.0001220703125	~
DigColPs PrevVernierI evelNo Cnt M u08	12	12	

tgt_i iii_bigooii seee			
Actual Value	Expected Value	Result	
0	0	~	
1145.45447	1145.454545 ± 0.00048828125	✓	
0	0	~	
0	0	✓	
1160.1637	1160.163741 ± 0.0001220703125	~	
12	12	✓	
1	1	~	
2	2	~	
2	2	~	
0	0	~	
0	0	~	
260.163696	260.1637406 ± 0.0009	✓	
0	0	~	
	0 1145.45447 0 0 1160.1637 12 1 2 2 0 0	0 0 1145.45447 0 1145.4545± 0.00048828125 0 0 0 0 0 1160.1637 1160.163741± 0.0001220703125 12 12 12 1 2 2 2 2 2 0 0 0 0 0 0 0 0 0	

T V					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~	
GetResource	1	GetResource	1	✓	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•	
ReleaseResource	1	ReleaseResource	1	•	
ConstrainOneRev	2	ConstrainOneRev	2	~	
VernierLookup	1	VernierLookup	1	~	
DiagnosticThreshold	1	DiagnosticThreshold	1	~	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•	



Test Step 2.86 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	123
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_ColTrimStatic_Deg_M_f32 DigColPs_HwAVernCorrFault_Cnt_M_lgc	198.3 0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	21204
DigColPs_I2CHwColAngle_Deg_M_f32	226.4548138
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	263
DigColPs_I2CHwSpurAngle_Deg_M_f32	94.9
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	20
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13 9
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	123
DigColPs_SpurTrimStatic_Deg_M_f32	94.9
DigColPs_TrimCompStatic_Cnt_M_u16	3040
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32 65
T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][4]	4 2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_S16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2 ColSpurVernierLUT Cnt s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2 DualSpurVernierLUT Cnt s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2 DualSpurVernierLUT Cnt s16[0][18]	252
T2 DualSpurVernierLUT Cnt s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2 DualSpurVernierLUT Cnt s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2 DualSpurVernierLUT Cnt s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
	1
T2_DualSpurVernierLUT_Cnt_s16[2][1]	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5

2014-10-14, 18:16:06+0530

DigColPs_Per2



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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2 3		
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2 DualSpurVernierLUT Cnt s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7		
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	214		
k_SkipStepErrDiag_Cnt_str.PStep	38		
k_SkipStepErrDiag_Cnt_str.NStep	23		
k_VernCorrErrorDiag_Cnt_str.Threshold	66		
k_VernCorrErrorDiag_Cnt_str.PStep	39		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	90.55352902		
k_VernOORangeThresh_Deg_f32	803.1102527		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	226.4548138		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	143.9507322		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1 tot DigColDo Por? (2CHwAhoPon)/olid	Cot Igo	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwE		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_MecState_Cnt_enur tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_inst_Sa_DigColPs.DigColPs_Perz_TrimComp_Cnt_igc tgt_Rte_inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cnt_igc tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Popula
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	Expected Value	Result
DigColPs_HwAvernCorrFauit_Cnt_M_igc DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1450.4314	1450.431485 ± 0.00048828125	
DigColPs_I2CHwColAngierorTiffI_Deg_iv_i32 DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1450.431465 ± 0.00046626125	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	
DigColPs PrevColPos Deg M f32	1440	1440 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1440	1440 ± 0.0001220703125	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1	1	•

550.431396

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value

550.4314854 ± 0.0009



T · · · · · · · · · · · · · · · · · · ·			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.87 (Repeat Count = 1)	
Name	Input Value
	124
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144
DigColPs_ColTrimStatic_Deg_M_f32	-360
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.8614647
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	96
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	96
DigColPs_TrimCompStatic_Cnt_M_u16	3076
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
	98
T2_ColSpurVernierLUT_Cnt_s16[0][8]	130
T2_ColSpurVernierLUT_Cnt_s16[0][9]	
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
12_00/0put vernierE01_0nt_5 ro[1][14]	

2014-10-14, 18:16:06+0530



News	Insuré Value
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2 DualSpurVernierLUT Cnt s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2 DualSpurVernierLUT Cnt s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
	180
T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216 252
T2_DualSpurVernierLUT_Cnt_s16[0][18]	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
saa.spa. voilloite i_oiit_o i o[i][iii]	

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10] T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
	1		
T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11] T0_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][12]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059	. Inc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	102	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL		
tgt_rte_mst_sa_bigcoiPs.Piiii_bigcoiPsEOL Name	Actual Value	Expected Value	Dooult
	0	•	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	4000 00050	0	

1636.36353

2

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

DigColPs_I2CHwTrimTransCnts_Uls_M_u08

1636.363636 ± 0.00048828125

2





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1787.86145	1787.861465 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	887.86145	887.8614647 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Name	Input Value
DigColPsInt_GetCustData()	127
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs ColSensorFaultAcc Cnt M u16	105
DigColPs ColTrimStatic Deg M f32	360
DigColPs HwAVernCorrFault Cnt M lgc	0
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	14286
ligColPs_I2CHwColAngle_Deg_M_f32	298.7894
ligColPs_I2CHwDataType_Cnt_M_u08	2
igColPs_I2CHwSpurAngle_Cnt_M_u16	18921
bigColPs I2CHwSpurAngle Deg M f32	97.1
igColPs I2CHwTrimTransCnts UIs M u08	4
igColPs I2CSensCommFlts Cnt M u08	13
igColPs I2CSpurSensorFault Cnt M Igc	1
igColPs_revAngleDataAvailable_Cnt_M_lgc	1
bigColPs PrevColPos Deg M f32	814.3879313
igColPs_PrevColPos_beg_m_i32 igColPs_PrevVernierLevelNo_Cnt_M_u08	3
	13
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
igColPs_SpurParityError_Cnt_M_lgc	127
igColPs_SpurSensorFaultAcc_Cnt_M_u16	
bigColPs_SpurTrimStatic_Deg_M_f32	97.1
DigColPs_TrimCompStatic_Cnt_M_u16	3112
higColPs_VernCorrDetectAcc_Cnt_M_u16	12
ligColPs_VernierAngleOORange_Cnt_M_lgc	1
tte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4] T2_DualSpurVernierLUT_Cnt_s16[1][5]	3 4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15]	11
T2 DualSpurVernierLUT Cnt s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	125
k_SkipStepErrDiag_Cnt_str.PStep	10
k_SkipStepErrDiag_Cnt_str.NStep	38
k_VernCorrErrorDiag_Cnt_str.Threshold	64
k_VernCorrErrorDiag_Cnt_str.PStep	8
k_VernCorrErrorDiag_Cnt_str.NStep	11
k_VernCorrErrorThresh_Deg_f32	78.40277648
k_VernOORangeThresh_Deg_f32	547.3349351
tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0 298.7894





Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbs	sPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState	_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp	o_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	654.54541	654.5454545 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	658.789429	658.7894 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	~

Test Step 2.89 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt GetCustData()	186
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	106
DigColPs ColTrimStatic Deg M f32	180.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs I2CColSensorFault Cnt M Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	29294
DigColPs_I2CHwColAngle_Deg_M_f32	199.9994296
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	49318
DigColPs_I2CHwSpurAngle_Deg_M_f32	98.2
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1048.767936
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
DigColPs_SpurTrimStatic_Deg_M_f32	98.2
DigColPs_TrimCompStatic_Cnt_M_u16	3148
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7] T3_ColSpurVernierLUT_Cnt_s16[1][9]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	112

DigColPs_Per2



DigColFs_Fer2		
Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
T2 DualSpurVernierLUT Cnt s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2 DualSpurVernierLUT Cnt s16[1][2]	1	
T2 DualSpurVernierLUT Cnt s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1	
	2	
T2_DualSpurVernierLUT_Cnt_s16[2][13]		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22	
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2	
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6	
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8	
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10	
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12	
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14	
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16	
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18	
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20	
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3	
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5	
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	7	
	9	
T2_DualSpurVernierLUT_Cnt_s16[3][15]		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11	
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13	
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15	
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17	
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19	

DigColPs_Per2

2014-10-14, 18:16:06+0530



 Name
 Input Value

 T2_DualSpurVernierLUT_Cnt_s16[3][21]
 21





Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16] T2 ColSpurVernierLUT Cnt s16[1][0]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	9
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2 ColSpurVernierLUT Cnt s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
Γ2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][1] T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
	021





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0 36
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T0_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][14]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13] T3_DualSpurVernierLUT_Cnt_s18[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
	40
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	160		
k_SkipStepErrDiag_Cnt_str.PStep	23		
k_SkipStepErrDiag_Cnt_str.NStep	16		
k_VernCorrErrorDiag_Cnt_str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k_VernCorrErrorDiag_Cnt_str.NStep	12		
k_VernCorrErrorThresh_Deg_f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs I2CHwColAngleForTrim Deg M f32	1145 45447	1145 454545 + 0 00048828125	

0	102 2 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1145.45447	1145.454545 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1150.15149	1150.151465 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	250.151489	250.1514647 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	✓

Test Step 2.91 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	127	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	14286	
DigColPs_I2CHwColAngle_Deg_M_f32	298.7894	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	18921	
DigColPs_I2CHwSpurAngle_Deg_M_f32	97.1	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_I2CSensCommFlts_Cnt_M_u08	13
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	814.3879313 3
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	97.1
DigColPs_TrimCompStatic_Cnt_M_u16	3112
DigColPs_VernCorrDetectAcc_Cnt_M_u16	12
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163 -131
T2_ColSpurVernierLUT_Cnt_s16[0][1] T2_ColSpurVernierLUT_Cnt_s16[0][2]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5] T3_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T3_ColSpurVernierLUT_Cnt_s16[3][3]	11 8
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12





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Name	Input Value	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16	
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13	
T2 ColSpurVernierLUT Cnt s16[3][13]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4	
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17	
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396	
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360	
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324	
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288	
T2 DualSpurVernierLUT Cnt s16[0][4]	-252	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216	
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
	-72	
T2_DualSpurVernierLUT_Cnt_s16[0][9] T2 DualSpurVernierLUT Cnt s16[0][10]	-36	
T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][11]	-36	
	36	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	72	
T2_DualSpurVernierLUT_Cnt_s16[0][13]		
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
Γ2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
Γ2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2 DualSpurVernierLUT Cnt s16[2][3]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]		
\		

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2 DualSpurVernierLUT Cnt s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	125		
k SkipStepErrDiag Cnt str.PStep	10		
k SkipStepErrDiag Cnt str.NStep	38		
k VernCorrErrorDiag Cnt str.Threshold	64		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	11		
k_VernCorrErrorThresh_Deg_f32	78.40277648		
k_VernOORangeThresh_Deg_f32	547.3349351		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	298.7894		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	103.8339644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	491		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid Cnt loc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cn		
tgt Rte Inst Sa DigColPs.Pim DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	result
DigColPs I2CHwColAngleForTrim Deg M f32	654.54541	654.5454545 ± 0.0004882812	
DigColPs I2CHwTrimTransCnts Uls M u08	3	3	25
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	658.789429	658.7894 ± 0.0001220703125	5
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7	7	,
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16 DigColPs_VernierAngleOORange_Cnt_M lqc	1	1	
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0	0	- J
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value			~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-245.45459	-245.4545455 ± 0.0009	V

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

0

0

 $\underline{\mathsf{tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value}}$



Test Step 2.92 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
DigColPsInt_GetCustData()	149
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs_ColTrimStatic_Deg_M_f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	569.7636028
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	149
DigColPs_SpurTrimStatic_Deg_M_f32	-360
DigColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 9 T2_DualSpurVernierLUT_Cnt_s16[2][20] T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2 DualSpurVernierLUT Cnt s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2 DualSpurVernierLUT Cnt s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2 DualSpurVernierLUT Cnt s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k SelectFromColumn Cnt lqc 1 k_SkipStepErrDiag_Cnt_str.Threshold 35 2 k SkipStepErrDiag Cnt str.PStep k_SkipStepErrDiag_Cnt_str.NStep 28 42 k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep 16 k_VernCorrErrorDiag_Cnt_str.NStep 18 92 41026139 $k_VernCorrErrorThresh_Deg_f32$ k_VernOORangeThresh_Deg_f32 1413.552634 tgt DigColPs Per2 MecState Cnt enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 219.0753346 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 324 2081034 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL Name **Actual Value Expected Value** Result $DigColPs_HwAVernCorrFault_Cnt_M_lgc$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 163.636353 163.6363636 ± 0.00048828125 DigColPs_I2CHwTrimTransCnts_Uls_M_u08 5 5 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 0 4.37533569 4.375334609 ± 0.0001220703125 ${\tt DigColPs_PrevColPos_Deg_M_f32}$ DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs Reql2CSnsrDataType Cnt M u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs VernCorrDetectAcc Cnt M u16 1 1 DigColPs_VernierAngleOORange_Cnt_M_lgc 1 tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value 0 0 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value -895.624634 -895.6246654 ± 0.0009

0

0

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value



T .				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 2.93 (Repeat Count = 1)	la de la companya de
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs ColSensorFaultAcc Cnt M u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs I2CHwTrimTransCnts Uls M u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs TrimCompStatic Cnt M u16	3220
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8
DigColPs_VernierAngleOORange_Cnt_M_Igc	0
Rte_Inst_Sa_DigColPs	tgt Rte Inst Sa DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2





Input Value 1 0 4 0
4
0
8
6
4
2
0
9
7
5 3
1
10
8
6
4
2
10
1
14
11
8
5
2
15
12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
180
216
252
288
324
360
9
0
1 2
3
4
5
6
7
8
9
0
1
2

2014-10-14, 18:16:06+0530





	1		
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0] T3_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2 DualSpurVernierLUT Cnt s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2 DualSpurVernierLUT Cnt s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0 68 66713858		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1423	los//alid Cnt lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_MecState_C tgt_DigColPs_Per2_TrimComp_		
tgt_Rte_Inst_Sa_DigColPs.DigColPsEOL	tgt_Pim_DigColPsEOL	O.Iigo	
		Expected Value	Pagel
Name DigColDs HwAVeroCorrEquit Cnt M los	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	

1636.36353

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

1636.363636 ± 0.00048828125





Name	Actual Value	Expected Value	Result
DigColPs I2CHwTrimTransCnts UIs M u08	0	0	- resourc
DigColPs PrevAngleDataAvailable Cnt M Igc	0	0	✓
DigColPs PrevColPos Deg M f32	1649.86719	1649.867139 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	16	16	✓
DigColPs RegI2CSnsrDataType Cnt M u08	1	1	✓
DigColPs SkipStepFltDetectAcc Cnt M u16	11	11	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.94 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	126
DigColPs ColParityError Cnt M Igc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165
DigColPs ColTrimStatic Deg M f32	222.9
DigColPs HwAVernCorrFault Cnt M Igc	1
DigColPs 12CColSensorFault Cnt M Igc	1
DigColPs I2CHwColAngle Cnt M u16	0
DigColPs_12CHwColAngle_Deg_M_f32	325.6206695
DigColPs I2CHwDataType Cnt M u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592
DigColPs_I2CHwSpurAngle_Deg_M_f32	180.6
DigColPs_12CHwTrimTransCnts_Uls_M_u08	1
DigColPs I2CSensCommFlts Cnt M u08	23
DigColPs 12CSpurSensorFault Cnt M Igc	1
DigColPs PrevAngleDataAvailable Cnt M Igc	1
DigColPs PrevColPos Deg M f32	157.2728202
DigColPs PrevVernierLevelNo Cnt M u08	13
DigColPs SkipStepFltDetectAcc Cnt M u16	1
	1
DigColPs_SpurParityError_Cnt_M_lgc DigColPs SpurSensorFaultAcc Cnt M u16	126
	180.6
DigCoIPs_SpurTrimStatic_Deg_M_f32	3256
DigColPs_TrimCompStatic_Cnt_M_u16	7
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte Inst Sa DigColPs	tgt Rte Inst Sa DigColPs
	-163
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-00 -33
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	32
T2_ColSpurVernierLUT_Cnt_s16[0][6]	65
T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
	130
T2_ColSpurVernierLUT_Cnt_s16[0][9]	163
T2_ColSpurVernierLUT_Cnt_s16[0][10]	196
T2_ColSpurVernierLUT_Cnt_s16[0][11]	229
T2_ColSpurVernierLUT_Cnt_s16[0][12] T3_ColSpurVernierLUT_Cnt_s16[0][12]	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	327
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327 359
T2_ColSpurVernierLUT_Cnt_s16[0][16]	0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
12_0000pui verillerL01_01it_\$10[1][2]	J

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][9] T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5] T2_DualSpurVernierLUT_Cnt_s16[2][6]	5 6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1] T2_DualSpurVernierLUT_Cnt_s16[3][2]	2 4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2 DualSpurVernierLUT Cnt s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cnt_s16[3][21]	19 21
T2_DualSpurVernierLUT_Cnt_s16[3][21] k_SelectFromColumn_Cnt_lgc	21
k_SkipStepErrDiag_Cnt_str.Threshold	99
k_SkipStepErrDiag_Ont_str.PStep	3
k_SkipStepErrDiag_Cnt_str.NStep	13
k_VernCorrErrorDiag_Cnt_str.Threshold	74
k_VernCorrErrorDiag_Cnt_str.PStep	33
k_VernCorrErrorDiag_Cnt_str.NStep	6
k_VernCorrErrorThresh_Deg_f32	78.75594592
k_VernOORangeThresh_Deg_f32	1151.771932
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2

2014-10-14, 18:16:06+0530



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	<u>f</u> 32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	822.720703	822.7206695 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-77.2792969	-77.27933046 ± 0.00009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

au			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	✓

Test Step 2.95 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	149
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	103
DigColPs ColTrimStatic Deg M f32	214.7
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs I2CColSensorFault Cnt M Iqc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
DigColPs_I2CHwColAngle_Deg_M_f32	219.0753346
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs I2CHwSpurAngle Cnt M u16	58410
DigColPs I2CHwSpurAngle Deg M f32	0
DigColPs I2CHwTrimTransCnts Uls M u08	6
DigColPs I2CSensCommFlts Cnt M u08	23
DigColPs I2CSpurSensorFault Cnt M Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs PrevColPos Deg M f32	569.7636028
DigColPs PrevVernierLevelNo Cnt M u08	11
DigColPs SkipStepFltDetectAcc Cnt M u16	20
DigColPs SpurParityError Cnt M Igc	0
DigColPs SpurSensorFaultAcc Cnt M u16	149
DigColPs SpurTrimStatic Deg M f32	-82.29
DigColPs TrimCompStatic Cnt M u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7] T3_ColSpurVernierLUT_Cnt_s16[1][9]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
	3
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T3_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13] T3_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
	72
T2_DualSpurVernierLUT_Cnt_s16[0][13]	112

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T0_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpur/orpicsLUT_Cnt_s16[2][18]	
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	8 9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17

DigColPs_VernCorrDetectAcc_Cnt_M_u16
DigColPs_VernierAngleOORange_Cnt_M_lgc

 $tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value$

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

 $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value$

DigColPs_Per2

2014-10-14, 18:16:06+0530



1

0

-535.6246654 ± 0.0009

Name	Input Value		
T2 DualSpurVernierLUT Cnt s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	35		
k_SkipStepErrDiag_Cnt_str.Threshold			
k_SkipStepErrDiag_Cnt_str.PStep	2		
k_SkipStepErrDiag_Cnt_str.NStep	28		
k_VernCorrErrorDiag_Cnt_str.Threshold	42		
k_VernCorrErrorDiag_Cnt_str.PStep	16		
k_VernCorrErrorDiag_Cnt_str.NStep	18		
k_VernCorrErrorThresh_Deg_f32	92.41026139		
k_VernOORangeThresh_Deg_f32	1413.552634		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	364.677246	364.6772727 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	364.375336	364.3753346 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	~

T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

-535.624634

0

0

Test Step 2.96 (Repeat Count = 1)	range in the second
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	57565
DigColPs_I2CHwColAngle_Deg_M_f32	68.66713858
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	3220

2014-10-14, 18:16:06+0530



Name	
Digitable VerifickAngle Colling Collin Digitable	
12.058pur/emit U. Colt. 3100 0 131 12.058pur/emit U. Colt. 3100 0 131 12.058pur/emit U. Colt. 3100 0 132 12.058pur/emit U. Colt. 3100 0 133 12.058pur/emit U. Colt. 3100 0 133 12.058pur/emit U. Colt. 3100 0 132 12.058pur/emit U. Colt. 3100 0 132 12.058pur/emit U. Colt. 3100 0 132 12.058pur/emit U. Colt. 3100 0 130 12.058pur/emit U. Colt. 3100 10 132 12.058pur/emit U. Colt. 3100 10 132 12.058pur/emit U. Colt. 3100 13 132 12.058pur/emit U. Colt. 3100	
12, Colsput/mentUT, Cett., 5 (1911)	
12, Colsport/ment/LT Cett, s160 12 599 12, Colsport/ment/LT Cett, s160 13 68 12, Colsport/ment/LT Cett, s160 13 13 12, Colsport/ment/LT Cett, s160 13 18 18 18 18 18 18 18	
12 Colsput/emietUT Cett. \$160 15 2 Colsput/emietUT Cett. \$160 15 3	
12_CoSput/wentUT_Cnt_stip()	
17_CoSput/venietUT_Cnt_s180 5 32 2_CoSput/venietUT_Cnt_s180 5 32 2_CoSput/venietUT_Cnt_s180 5 38 3_CoSput/venietUT_Cnt_s180 5 39 3_CoSput/venietUT_Cnt_s180 5 30 3_CoSput/venietUT_Cnt_s180 5 32 3_CoSput/venietUT_Cnt_s180 5 32 3_CoSput/venietUT_Cnt_s180 5 32 3_CoSput/venietUT_Cnt_s180 5 32 3_CoSput/venietUT_Cnt_s180 5 39 3_CoSput/venietUT_Cnt_s180 5 39 3_CoSput/venietUT_Cnt_s180 5 39 3_CoSput/venietUT_Cnt_s180 5 30 3_CoSput/venieUT_Cnt_s180 5	
T. ColSput/emieLUT_Cnt_s160 15	
T2_Colspur/emicUT_Cnt_s160[7] 95	
12, CoSput/venietUT_Cnt_s160(8) 98	
12 CoSpurVement UT_Cnt_s18[0]11 198	
T. ColSput/vernierLUT_Cnt_s16(0)(1) 183 18	
12, CoSpuvVemiet.UT_Cnt_st6[0][12] 229 72, CoSpuvVemiet.UT_Cnt_st6[0][12] 229 72, CoSpuvVemiet.UT_Cnt_st6[0][14] 294 72, CoSpuvVemiet.UT_Cnt_st6[0][16] 237 72, CoSpuvVemiet.UT_Cnt_st6[0][16] 327 72, CoSpuvVemiet.UT_Cnt_st6[0][16] 327 72, CoSpuvVemiet.UT_Cnt_st6[0][16] 327 72, CoSpuvVemiet.UT_Cnt_st6[0][16] 327 72, CoSpuvVemiet.UT_Cnt_st6[0][17] 0 72, CoSpuvVemiet.UT_Cnt_st6[0][17] 4 72, CoSpuvVemiet.UT_Cnt_st6[0][18] 2 72, CoSpuvVemiet.UT_Cnt_st6[0][18] 2 72, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 73, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 74, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 75, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 76, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 77, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 78, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 79, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 70, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 71, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 72, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 73, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 74, CoSpuvVemiet.UT_Cnt_st6[0][18] 1 75, CoSpuvVemiet.UT	
T. Colsput/emiert.U. F. Colspit(1) 29	
17_Colsput/emiet.UT_Cnt_s16[0]13 204 17_Colsput/emiet.UT_Cnt_s16[0]16 327 17_Colsput/emiet.UT_Cnt_s16[0]16 328 327 17_Colsput/emiet.UT_Cnt_s16[0]16 329 17_Colsput/emiet.UT_Cnt_s16[0]16 320 321 321 322	
T. ColspurVerniert. UT. Cnt. 516(0)114 294 T. ColspurVerniert. UT. Cnt. 516(0)16 359 T. ColspurVerniert. UT. Cnt. 516(0)16 359 T. ColspurVerniert. UT. Cnt. 516(0)16 359 T. ColspurVerniert. UT. Cnt. 516(0)11 4 T. ColspurVerniert. UT. Cnt. 516(0)11 4 T. ColspurVerniert. UT. Cnt. 516(0)11 3 T. ColspurVerniert. UT. Cnt. 516(0)13 2 T. ColspurVerniert. UT. Cnt. 516(0)13 2 T. ColspurVerniert. UT. Cnt. 516(0)16 4 T. ColspurVerniert. UT. Cnt. 516(0)19 2 T. ColspurVerniert. UT. Cnt. 516(0)19 1 T. ColspurVerniert. UT. Cnt. 516(0)19 1 T. ColspurVerniert. UT. Cnt. 516(0)19 1 T. ColspurVerniert. UT. Cnt. 516(0)119 1 T. ColspurVerniert. UT. Cnt. 516(0)119 1 T. ColspurVerniert. UT. Cnt. 516(0)119 2 T. ColspurVerniert. UT. Cnt. 516(0)119 2 T. ColspurVerniert. UT. Cnt. 516(0)119 2 T. ColspurVerniert. UT. Cnt. 516(0)119 1 T. ColspurVerniert. UT. Cnt. 516(0)1	
T2_ColSput/emiert_UT_Cnt_s16[0]16] 359	
T2_ColSpurVemerLUT_Cnt_s16[1]10	
12 ColSpurVemiet.UT_Cnt_st6[1]0 0 12 ColSpurVemiet.UT_Cnt_st6[1]1 4 1 2 ColSpurVemiet.UT_Cnt_st6[1]2 3 3 1 2 ColSpurVemiet.UT_Cnt_st6[1]3 2 1 2 ColSpurVemiet.UT_Cnt_st6[1]3 2 1 1 1 1 1 1 1 1 1	
T2_ColSpurVemerLUT_Cnt_ste(1) 10 T2_ColSpurVemerLUT_Cnt_ste(1) 20 T2_ColSpurVemerLUT_Cnt_ste(1) 30 T2_ColSpurVemerLUT_Cnt_ste(1) 40 T2_ColSpurVemerLUT_Cnt_ste(1	
T2_ColSpurVemierLUT_Cnt_s16[1]2 3 1 2 2 2 2 2 2 2 2 2	
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T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2 ColSpurVernierI UT Cnt s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] 15	
T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSput/craint_LT_Cnt_s16[3][8] 9 T3_ColSput/craint_LT_Cnt_s16[3][8] 9	
T2_ColSput/vernierLUT_Cnt_s16[3][9] 6 T3_ColSput/vernierLUT_Cnt_s16[3][10] 3	
T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16 T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13 T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7	
T2_ColSpurVernierLUT_Cnt_s16[3][15] 4	
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17	
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396	
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360	

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2 DualSpurVernierLUT Cnt s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
	-72
T2_DualSpurVernierLUT_Cnt_s16[0][9]	
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
	7
T2_DualSpurVernierLUT_Cnt_s16[1][8]	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2 DualSpurVernierLUT Cnt s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2 DualSpurVernierLUT Cnt s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][8]	
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
	4
T2_DualSpurVernierLUT_Cnt_s16[3][2]	
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
	12
12 DuaispurvernierLOT Citt STotsilot	
T2_DualSpurVernierLUT_Cnt_s16[3][6] T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][7] T2_DualSpurVernierLUT_Cnt_s16[3][8]	14 16





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1423		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHw/	AbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHw/	AbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecSta	tgt_DigColPs_Per2_MecState_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimCo	mp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs HwAVernCorrFault Cnt M lgc	1	1	

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1649.86719	1649.867139 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	-
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	736.363525	736.3636364 ± 0.0009	✓
tgt DigColPs Per2 TrimComp Cnt Igc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.97 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
DigColPsInt_GetCustData()	127	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175	
DigColPs_ColTrimStatic_Deg_M_f32	227	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129	
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.1	





Name	Input Value
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9 7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	297.1
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359 0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T3_ColSpurVernierLUT_Cnt_s16[2][40]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	-
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6]	15





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16] T2_DualSpurVernierLUT_Cnt_s16[0][0]	17 -396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2 DualSpurVernierLUT Cnt s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T0_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21] T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8

DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value T2_DualSpurVernierLUT_Cnt_s16[2][20] T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2 DualSpurVernierLUT Cnt s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2 DualSpurVernierLUT Cnt s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 19 T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 0 $k_SkipStepErrDiag_Cnt_str.Threshold$ 70 k_SkipStepErrDiag_Cnt_str.PStep 47 k_SkipStepErrDiag_Cnt_str.NStep 44 $k_VernCorrErrorDiag_Cnt_str.Threshold$ 88 $k_VernCorrErrorDiag_Cnt_str.PStep$ 0 k VernCorrErrorDiag Cnt str.NStep 38 78 63725519 k_VernCorrErrorThresh_Deg_f32 k VernOORangeThresh Deg f32 1720.30508 tgt_DigColPs_Per2_MecState_Cnt_enum.value 115.010748 tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 0.980068922 tgt Pim DigColPsEOL.SpurTrim Deg f32 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc$ tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tot DigColPs Per2 MecState Cnt enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc $tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL$ tgt_Pim_DigColPsEOL **Actual Value Expected Value** Result DigColPs_HwAVernCorrFault_Cnt_M_lgc DigColPs_I2CHwColAngleForTrim_Deg_M_f32 981.818176 981.8181818 ± 0.00048828125 $DigColPs_I2CHwTrimTransCnts_Uls_M_u08$ DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0 DigColPs_PrevColPos_Deg_M_f32 968 010742 968.010748 ± 0.0001220703125 DigColPs_PrevVernierLevelNo_Cnt_M_u08 10 $DigColPs_Reql2CSnsrDataType_Cnt_M_u08$ 4 4 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 0 0 DigColPs VernCorrDetectAcc Cnt M u16 0 0 DigColPs_VernierAngleOORange_Cnt_M_lgc 1

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	✓
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	✓

0

81.81818182 ± 0.00009

0

0

81.8181763

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value



Test Step 2.98 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	124
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_ColTrimStatic_Deg_M_f32	231.1
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16 DigColPs_I2CHwColAngle_Deg_M_f32	25526 216.7759984
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs I2CHwSpurAngle Cnt M u16	674
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.2
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	840.5093411
DigColPs_PrevVernierLevelNo_Cnt_M_u08	6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	8
DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1 124
DigColPs_SpurTrimStatic_Deg_M_f32	298.2
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	18
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32 65
T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	1 0
T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2 DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 9 T2_DualSpurVernierLUT_Cnt_s16[2][20] T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 22 T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2 DualSpurVernierLUT Cnt s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2 DualSpurVernierLUT Cnt s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2 DualSpurVernierLUT Cnt s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 1 k_SkipStepErrDiag_Cnt_str.Threshold 74 k_SkipStepErrDiag_Cnt_str.PStep 2 k_SkipStepErrDiag_Cnt_str.NStep 33 99 k_VernCorrErrorDiag_Cnt_str.Threshold k_VernCorrErrorDiag_Cnt_str.PStep 38 k_VernCorrErrorDiag_Cnt_str.NStep 48 37198949 $k_VernCorrErrorThresh_Deg_f32$ k_VernOORangeThresh_Deg_f32 269.5857018 tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 216.7759984 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 90 56395859 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 2243 tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL Name **Actual Value Expected Value** Result $DigColPs_HwAVernCorrFault_Cnt_M_lgc$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 1636.36353 1636.363636 ± 0.00048828125 DigColPs_I2CHwTrimTransCnts_Uls_M_u08 2 2 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 1785.67603 1785.675998 ± 0.0001220703125 ${\tt DigColPs_PrevColPos_Deg_M_f32}$ DigColPs_PrevVernierLevelNo_Cnt_M_u08 17 17 DigColPs_Reql2CSnsrDataType_Cnt_M_u08 1 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs VernCorrDetectAcc Cnt M u16 1 1

0

0

0

885.676025

0

0

0

885.6759984 ± 0.0009

@ Ret	ort created	by TES	SSY V3.1	1.9. ren	ort template	V2.1

DigColPs_VernierAngleOORange_Cnt_M_lgc

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value



T →				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.99 (Repeat Count = 1)	
Name	Input Value
	241
DigColPsInt_GetCustData()	0
DigColPs_ColParityError_Cnt_M_lgc	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	99.3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs VernierAngleOORange Cnt M lgc	0
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
_ : :	229
T2_ColSpurVernierLUT_Cnt_s16[0][12]	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261 294
T2_ColSpurVernierLUT_Cnt_s16[0][14]	
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2 DualSpurVernierLUT Cnt s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	6 7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7		
T2_DualSpurVernierLUT_Cnt_s16[3][14]			
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	9		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579	* Inc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_132	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL		
tgt_rte_inst_sa_bigcoiPs.Piiii_bigcoiPsEOL Name	Actual Value	Expected Value	Post
	0	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc DigColPs_I2CHwColAngleForTrim_Deg_M_f32	327.272705	327.2727273 ± 0.00048828125	
= .g = c = .Eo con anglos of Hill Dog IV IVE			

327.272705

3

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

 ${\tt DigColPs_I2CHwTrimTransCnts_Uls_M_u08}$

327.2727273 ± 0.00048828125

3



TEST DETAILS REPORT DigColPs_Per2	2014-10-14, 18:16:06+0530	Razo	orcat
Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	340.411285	340.4112897 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08		4	•

DigColPs_Reql2CSnsraAva=

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2 ColSpurVernierLUT Cnt s16[1][15]	0
	4
T2_ColSpurVernierLUT_Cnt_s16[1][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
	7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
	12
T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2 DualSpurVernierLUT Cnt s16[0][7]	-144
_ :	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
	144
T2_DualSpurVernierLUT_Cnt_s16[0][15]	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20] T0_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0] T3_DualSpurVernierLUT_Cst_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2 DualSpurVernierLUT Cnt s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2] T0_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6
	8
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2 DualSpurVernierLUT Cnt s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	35
k_SkipStepErrDiag_Cnt_str.PStep	2
k_SkipStepErrDiag_Cnt_str.NStep	28
k_VernCorrErrorDiag_Cnt_str.Threshold	42
k_VernCorrErrorDiag_Cnt_str.PStep	16
k_VernCorrErrorDiag_Cnt_str.NStep	18
k_VernCorrErrorThresh_Deg_f32	92.41026139
k_VernOORangeThresh_Deg_f32	1413.552634
Ant. Direct Dr. Daro Manachata Cost annual 1	
tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1 219.0753346





Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	135.045456	135.0454545 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	145.300003	145.3 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-754.700012	-754.7 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value	0	0	~

T ·					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~	
GetResource	1	GetResource	1	~	
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~	
ReleaseResource	1	ReleaseResource	1	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
VernierLookup	1	VernierLookup	1	~	
DiagnosticThreshold	1	DiagnosticThreshold	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~	

Test Step 2.101 (Repeat Count = 1) ✓		
Name	Input Value	
DigColPsInt GetCustData()	124	
_ · · · _ · · · ·		
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	151	
DigColPs_ColTrimStatic_Deg_M_f32	218.8	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	57565	
DigColPs_I2CHwColAngle_Deg_M_f32	360	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866	
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.2	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6	
DigColPs_I2CSensCommFlts_Cnt_M_u08	22	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	321.3070593	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124	
DigColPs_SpurTrimStatic_Deg_M_f32	360	
DigColPs_TrimCompStatic_Cnt_M_u16	3220	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	8	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131	
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65	
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98	

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2 ColSpurVernierLUT Cnt s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T0_ColSpurVernierLUT_Cnt_s16[3][1]	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
	0 36 72

2014-10-14, 18:16:06+0530



T. D. Designation and LTJ. Cell. 5 (1915) 144 148 149 14	Name	Input Value
12_DasSparvemental_Cot_stoligits	Name	Input Value
T. DuaSquiverment_T. Co., 149(07) 26		
T2. DusSporkerentU. Cot. 3100179 T3. DusSporkerentU. Cot. 3100179 T4. DusSporkerentU. Cot. 3100179 T4. DusSporkerentU. Cot. 3100179 T5. DusSporkerentU. Cot. 3100179		
T2_Dustport/emed_TU_Crt_s100[19] 288 -T2_Dustport/emed_TU_Crt_s100[29] 294 -T2_Dustport/emed_TU_Crt_s100[29] 294 -T2_Dustport/emed_TU_Crt_s100[29] 300 -T2_Dustport/emed_TU_Crt_s100[29] 9 -T2_Dustport/emed_TU_Crt_s100[29] 9 -T2_Dustport/emed_TU_Crt_s100[29] 1 -T2_Dustport/emed_TU_Crt_s100[29] 1 -T2_Dustport/emed_TU_Crt_s100[29] 1 -T2_Dustport/emed_TU_Crt_s100[29] 1 -T2_Dustport/emed_TU_Crt_s100[29] 1 -T2_Dustport/emed_TU_Crt_s100[29] 1 -T2_Dustport/emed_TU_Crt_s100[29] 5 -T2_Dustport/emed_TU_Crt_s100[29] 5 -T2_Dustport/emed_TU_Crt_s100[29] 7 -T2_Dustport/emed_TU_Crt_s100[29] 7 -T2_Dustport/emed_TU_Crt_s100[29] 7 -T2_Dustport/emed_TU_Crt_s100[29] 9 -T2_Dustport/emed_TU_Crt_s10		
T2_Dustport/emicUT_OL_st 90(91)		
T2_DusSpurVermed.U_Dus_14(0)[23] T2_DusSpurVermed.U_Dus_14(0)[23] T2_DusSpurVermed.U_Dus_14(0)[3] T2		
T2_DusSprivement_U_Cnt_stqUp		
12_Dustpar/ment U_Cnt_sto[1]		
T2. DuslSpurVerneUT. Cnt.; 16(1)[2] 17. DuslSpurVerneUT. Cnt.; 16(1)[2] 17. DuslSpurVerneUT. Cnt.; 16(1)[3] 17. DuslSpurVerneUT. Cnt.; 16(1)[4] 18. DuslSpurVerneUT. Cnt.; 16(1)[4] 19. DuslSpurVerneUT. Cnt.; 16(1)[6] 19. DuslSpurVerneUT. Cnt.; 16(T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DusSparvementU_Cor_16(9) 3 2 72_DusSparvementU_Cor_16(9) 3 2 72_DusSparvementU_Cor_16(9) 3 3 72_DusSparvementU_Cor_16(9) 4 3 72_DusSparvementU_Cor_16(9) 6 72_DusSparvementU_Cor_16(9) 6 72_DusSparvementU_Cor_16(9) 7 7 7 7 7 7 7 7 7 7	T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DasSgov/Period UT_Ord_19(1)	T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
TP_DasSpawYene(UT_CM_16(1)): TP_Das	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DusSpurVermeUT_Cnt_st0[19]	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T. Q. DuaSgout/vernetU. Cm. 1 sticl 191 5 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 7 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 7 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 7 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 9 7 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 9 7 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 9 7 7 2. DuaSgout/vernetU. Cm. 1 sticl 191 9 7 7 7 7 7 7 7 7 7	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
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T2_DusSprivement_UT_Cett_\$16(19)	T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
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12_Dust Description 15 15 15 15 15 15 15 1	T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
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T2_DualSpurVerniert.UT_Cnt_s16[2][18] 7 T2_DualSpurVerniert.UT_Cnt_s16[2][19] 8 T2_DualSpurVerniert.UT_Cnt_s16[2][21] 9 T2_DualSpurVerniert.UT_Cnt_s16[2][21] 10 T2_DualSpurVerniert.UT_Cnt_s16[3][0] 22 T2_DualSpurVerniert.UT_Cnt_s16[3][1] 2 T2_DualSpurVerniert.UT_Cnt_s16[3][2] 4 T2_DualSpurVerniert.UT_Cnt_s16[3][2] 4 T2_DualSpurVerniert.UT_Cnt_s16[3][3] 6 T2_DualSpurVerniert.UT_Cnt_s16[3][4] 8 T2_DualSpurVerniert.UT_Cnt_s16[3][5] 10 T2_DualSpurVerniert.UT_Cnt_s16[3][6] 12 T2_DualSpurVerniert.UT_Cnt_s16[3][7] 14 T2_DualSpurVerniert.UT_Cnt_s16[3][7] 14 T2_DualSpurVerniert.UT_Cnt_s16[3][8] 16 T2_DualSpurVerniert.UT_Cnt_s16[3][9] 18 T2_DualSpurVerniert.UT_Cnt_s16[3][10] 20 T2_DualSpurVerniert.UT_Cnt_s16[3][10] 20 T2_DualSpurVerniert.UT_Cnt_s16[3][11] 1 T2_DualSpurVerniert.UT_Cnt_s16[3][11] 1 T2_DualSpurVerniert.UT_Cnt_s16[3][12] 3 T2_DualSpurVerniert.UT_Cnt_s16[3][14] 7 T2_DualSpurVerniert.UT_Cnt_s16[3][14] 7 T2_DualSpurVerniert.UT_Cnt_s16[3][15] 9 T2_DualSpurVerniert.UT_Cnt_s16[3][16] 11		
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T2_DualSpurVernierLUT_Cnt_s16[2][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11		
T2_DualSpurVernierLUT_Cnt_s16[3][0] 22 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2_DualSpurVernierLUT_Cnt_s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2_DualSpurVernierLUT_Cnt_s16[3][17] 13		
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T2_DualSpurVernierLUT_Cnt_s16[3][17] 13	T2_DualSpurVernierLUT_Cnt_s16[3][15]	
	T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
TO Discloud/amical LIT Oct -44001401	T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
12_DualSpurvernierLU1_Ght_s16[3][18]	T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] 17	T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] 19	T2_DualSpurVernierLUT_Cnt_s16[3][20]	19



DigColPs_Per2	0 14, 10.10.00		Razorcat
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k_SkipStepErrDiag_Cnt_str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	7		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHw	AbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHw	AbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecSta	ate_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimCo	pmp_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1358.22327	1358.223274 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	•
DigColPs_PrevColPos_Deg_M_f32	1371.33289	1371.332861 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	11	11	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	458.223267	458.2232736 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Test Sten 2 402 (Beneat Count = 4)		.
Test Step 2.102 (Repeat Count = 1)		×
Name	Input Value	
DigColPsInt_GetCustData()	126	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165	
DigColPs_ColTrimStatic_Deg_M_f32	222.9	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	0	
DigColPs_I2CHwColAngle_Deg_M_f32	60.482	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	11592	
DigColPs_I2CHwSpurAngle_Deg_M_f32	99.3	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	
DigColPs_I2CSensCommFlts_Cnt_M_u08	23	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	
DigColPs_PrevColPos_Deg_M_f32	157.2728202	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126	
DigColPs_SpurTrimStatic_Deg_M_f32	180.6	
DigColPs_TrimCompStatic_Cnt_M_u16	3256	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	7	

2014-10-14, 18:16:06+0530



Input Value
0
tgt_Rte_Inst_Sa_DigColPs
-163
-131
-99
-66 -33
-33
32
65
98
130
163
196
229
261
294
327
359
0
4
3
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1
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15
12
9
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16
13
10 7
7
7 4
7 4 17
7 4

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0 36
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T0_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0 1
T2_DualSpurVernierLUT_Cnt_s16[1][12] T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][14]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2 DualSpurVernierLUT Cnt s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13] T3_DualSpurVernierLUT_Cnt_s18[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
	40
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16





Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	1			
k_SkipStepErrDiag_Cnt_str.Threshold	99			
k_SkipStepErrDiag_Cnt_str.PStep	3			
k_SkipStepErrDiag_Cnt_str.NStep	13			
k_VernCorrErrorDiag_Cnt_str.Threshold	74	74		
k_VernCorrErrorDiag_Cnt_str.PStep	33	33		
k_VernCorrErrorDiag_Cnt_str.NStep	6			
k_VernCorrErrorThresh_Deg_f32	78.75594592	78.75594592		
k_VernOORangeThresh_Deg_f32	1151.771932			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	325.6206695			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	139.9007934			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1937			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos	s_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Ci	nt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Resul	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	•	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1272.13635	1272.136364 ± 0.00048828125	•	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	•	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•	
Di O ID D O ID D M MO	1077 50000	1077 500 . 0 0001000700105		

0	102 2 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1272.13635	1272.136364 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1277.58203	1277.582 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	12	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	377.582031	377.582 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	-
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	✓

Test Step 2.103 (Repeat Count = 1)		✓
Name	Input Value	
DigColPsInt_GetCustData()	127	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175	
DigColPs_ColTrimStatic_Deg_M_f32	227	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	65535	
DigColPs_I2CHwColAngle_Deg_M_f32	115.010748	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	7129	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	





Norma	Invest Value
Name	Input Value
DigColPs_I2CSensCommFlts_Cnt_M_u08	6
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1464.024646
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	127
DigColPs_SpurTrimStatic_Deg_M_f32	297.1
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2 ColSpurVernierLUT Cnt s16[2][2]	6
T2 ColSpurVernierLUT Cnt s16[2][3]	4
	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	0
T2_ColSpurVernierLUT_Cnt_s16[2][5]	
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
12_0010pui voitiioiE01_011E310[0][0]	-

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	4
T2_ColSpurVernierLUT_Cnt_s16[3][15] T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_ColspurvernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2 DualSpurVernierLUT Cnt s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21] T3_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1] T3_DualSpurVernierLUT_Cnt_s16[2][2]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
	-
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8

2014-10-14, 18:16:06+0530



DigColPs_Per2

DigCoiPs_Per2			MACILIA
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2 DualSpurVernierLUT Cnt s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2 DualSpurVernierLUT Cnt s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	70		
k_SkipStepErrDiag_Cnt_str.PStep	47		
k_SkipStepErrDiag_Cnt_str.NStep	44		
k_VernCorrErrorDiag_Cnt_str.Threshold	88		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	38		
k_VernCorrErrorThresh_Deg_f32	78.63725519		
k_VernOORangeThresh_Deg_f32	1720.30508		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt Pim DigColPsEOL.ColTrim Deg f32	115.010748		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0.980068922		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	371		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid Cnt Igc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt DigColPs Per2 I2CHwAbsf		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt DigColPs Per2 TrimComp		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	- Robuit
DigColPs I2CHwColAngleForTrim Deg M f32	1337.68176	1337.681818 ± 0.00048828125	
DigCoIPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•
DigColPs_PrevColPos_Deg_M_f32	1328.01074	1328.010748 ± 0.00012207031	125
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	· ·
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	
DigColPs SkipStepFltDetectAcc Cnt M u16	0	0	✓
DigColPs VernCorrDetectAcc Cnt M u16	0	0	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	437.681763	437.6818182 ± 0.0009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	
NTC	0x6C	0 0x6C	
Param	0x0C	0x0C	
Status	0x01	0x00	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x01

0x0C 0x01

Status

2014-10-14, 18:16:06+0530



Test Step 2.104 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	124

2014-10-14, 18:16:06+0530



Nama	Input Value
Name T2_ColSpurVernierLUT_Cnt_s16[2][12]	Input Value 8
T2_ColSpurVernierLUT_Cnt_S10[2][12] T2_ColSpurVernierLUT_Cnt_S10[2][13]	6
	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
12_Buaiopui veriici Eo 1_citt_310[2][o]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
	4 5

2014-10-14, 18:16:06+0530



DigColPs_Per2

DigColPs_Per2		MAZ	COLCAL
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18 20		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][12]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	17 19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	74		
k_SkipStepErrDiag_Cnt_str.PStep	2		
k SkipStepErrDiag Cnt str.NStep	33		
k VernCorrErrorDiag Cnt str.Threshold	99		
k VernCorrErrorDiag Cnt str.PStep	38		
k_VernCorrErrorDiag_Cnt_str.NStep	17		
k_VernCorrErrorThresh_Deg_f32	48.37198949		
k_VernOORangeThresh_Deg_f32	269.5857018		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	216.7759984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	90.56395859		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2243		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cr	_	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs HwAVernCorrFault Cnt M lgc	1	1	,
DigCoIPs I2CHwColAngleForTrim Deg M f32	355.363617	355.3636364 ± 0.00048828125	
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	345.675995	345.6759984 ± 0.0001220703125	
DigColPs PrevVernierLevelNo Cnt M u08	4	4	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	٠,
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	٠,
tgt DigColPs Per2 I2CHwAbsPos HwDeg f32.value	-554.323975	-554.3240016 ± 0.0009	

-554.323975

0

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value tgt_DigColPs_Per2_TrimComp_Cnt_Igc.value

-554.3240016 ± 0.0009

0



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.105 (Repeat Count = 1)	la de la companya de
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_12CHwSpurAngle_Cnt_M_u16	
	60.482
DigColPs_12CHwSpurAngle_Deg_M_f32	3
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	1
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2 ColSpurVernierLUT Cnt s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2 ColSpurVernierLUT Cnt s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_S16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	
	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1] T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 18:16:06+0530

DigColPs_Per2



Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4			
T2 DualSpurVernierLUT Cnt s16[1][16]	5			
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6			
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7			
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8			
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9			
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0			
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1			
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2			
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3			
T2 DualSpurVernierLUT Cnt s16[2][4]	4			
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5			
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6			
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10			
T2 DualSpurVernierLUT Cnt s16[2][11]	0			
	1			
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cst_s46[2][12]				
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2			
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3			
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4			
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5			
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6			
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22			
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4			
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6			
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8			
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2 DualSpurVernierLUT Cnt s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	0			
k_SkipStepErrDiag_Cnt_str.Threshold	46			
k SkipStepErrDiag Cnt str.PStep	49			
k SkipStepErrDiag Cnt str.NStep	17			
_ : : : = - :				
k_VernCorrErrorDiag_Cnt_str.Threshold	53			
k_VernCorrErrorDiag_Cnt_str.PStep	26			
k_VernCorrErrorDiag_Cnt_str.NStep	9			
k_VernCorrErrorThresh_Deg_f32	74.78180027			
k_VernOORangeThresh_Deg_f32	1199.291138			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	t_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
		I=		
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1782.35547	1782.355455 ± 0.00048828125	✓	
DigColPs I2CHwTrimTransCnts Uls M u08	2	2		

2

2

 ${\tt DigColPs_I2CHwTrimTransCnts_Uls_M_u08}$





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1780.41125	1780.41129 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	882.355469	882.355455 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	✓

Name	Input Value
DigColPsInt_GetCustData()	142
ligColPs_ColParityError_Cnt_M_lgc	0
igColPs ColSensorFaultAcc Cnt M u16	30
igCoIPs ColTrimStatic Deg M f32	4.6
igColPs HwAVernCorrFault Cnt M lgc	0
igColPs I2CColSensorFault Cnt M Igc	0
igColPs_I2CHwColAngle_Cnt_M_u16	58760
igColPs_I2CHwColAngle_Deg_M_f32	118.0321395
igColPs_I2CHwDataType_Cnt_M_u08	0
igColPs_I2CHwSpurAngle_Cnt_M_u16	64972
igColPs I2CHwSpurAngle Deg M f32	5.8
igColPs I2CHwTrimTransCnts Uls M u08	4
igColPs I2CSensCommFlts Cnt M u08	24
igColPs I2CSpurSensorFault Cnt M Igc	1
igColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
igColPs PrevColPos Deg M f32	421.9525396
igColPs_PrevVernierLevelNo_Cnt_M_u08	16
igColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
igColPs_SpurParityError_Cnt_M_lgc	0
igColPs_SpurSensorFaultAcc_Cnt_M_u16	142
igColPs SpurTrimStatic Deg M f32	5.8
igColPs TrimCompStatic Cnt M u16	124
igColPs_VernCorrDetectAcc_Cnt_M_u16	4
igColPs_VernierAngleOORange_Cnt_M_lgc	1
te_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2 ColSpurVernierLUT Cnt s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2 ColSpurVernierLUT Cnt s16[1][3]	2

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	4
T2_ColSpurVernierLUT_Cnt_S16[1][12] T2_ColSpurVernierLUT_Cnt_S16[1][13]	3 2
T2 ColSpurVernierLUT Cnt s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2 ColSpurVernierLUT Cnt s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T0_ColOpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	8 6
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	16 13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1 2





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2 3
T2_DualSpurVernierLUT_Cnt_s16[1][14] T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T3_DualSpurVernierLUT_Cnt_s16[2][10]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	167 27
k_SkipStepErrDiag_Cnt_str.PStep	33
k_SkipStepErrDiag_Cnt_str.NStep k_VernCorrErrorDiag_Cnt_str.Threshold	97
k_VernCorrErrorDiag_Cnt_str. Trieshold k_VernCorrErrorDiag_Cnt_str.PStep	13
k_VernCorrErrorDiag_Cnt_str.PStep	3
K_YSTIOOTIETIOIDIAG_OTIC_Str.Motep	82.93280101
k VernCorrErrorThresh Deg f32	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32	
k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum.value	1028.14



Name	Input Value		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006	55.30846006	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwD	eg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enun	า	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x0C	0x0C	~
Status	0x01	0x01	~

T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.107 (Repeat Count = 1)	√
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs_I2CHwColAngle_Deg_M_f32	274.3637406
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0





32 65 98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
98 130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
130 163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
163 196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
196 229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
229 261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1
261 294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
294 327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
327 359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
359 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2 1 0 4 3 2
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-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-12 -36

2014-10-14, 18:16:06+0530



Name	Input Value
T2 DualSpurVernierLUT Cnt s16[0][11]	0
T2 DualSpurVernierLUT Cnt s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
	108
T2_DualSpurVernierLUT_Cnt_s16[0][14]	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2 DualSpurVernierLUT Cnt s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2 DualSpurVernierLUT Cnt s16[2][2]	2
T2 DualSpurVernierLUT Cnt s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
.T2DDBBBBpdeveiniddUUTDBhts\$6BHV2e(V2.1	5
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2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 1 $k_SkipStepErrDiag_Cnt_str.Threshold$ 87 k_SkipStepErrDiag_Cnt_str.PStep 0 $k_SkipStepErrDiag_Cnt_str.NStep$ 20 k_VernCorrErrorDiag_Cnt_str.Threshold 33 $k_VernCorrErrorDiag_Cnt_str.PStep$ 17 k_VernCorrErrorDiag_Cnt_str.NStep 73.6750493 k_VernCorrErrorThresh_Deg_f32 k_VernOORangeThresh_Deg_f32 824.57 tgt_DigColPs_Per2_MecState_Cnt_enum.value n tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 274.3637406 88.88743997 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ 797 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_TrimComp_Cnt_lgc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL **Actual Value Expected Value** ${\tt DigColPs_HwAVernCorrFault_Cnt_M_lgc}$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 981.818176 981.8181818 ± 0.00048828125

DigColPs_I2CHwTrimTransCnts_UIs_M_u08	4	4	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
	·		
T			✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.108 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
DigColPsInt_GetCustData()	241
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195
DigColPs_ColTrimStatic_Deg_M_f32	235.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	56399
DigColPs_I2CHwColAngle_Deg_M_f32	215.6112897
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	60.482
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	2
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	812.7722371
DigColPs_PrevVernierLevelNo_Cnt_M_u08	4
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	12
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	241





Name	Input Value
DigColPs_SpurTrimStatic_Deg_M_f32	99.3
DigColPs_TrimCompStatic_Cnt_M_u16	2240
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10] T3_ColSpurVernierLUT_Cnt_s16[0][11]	163 196
T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	1 0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11] T0_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	4 2
	10
T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17

2014-10-14, 18:16:06+0530



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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2 DualSpurVernierLUT Cnt s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
	-144
T2_DualSpurVernierLUT_Cnt_s16[0][7]	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2 DualSpurVernierLUT Cnt s16[0][18]	252
T2 DualSpurVernierLUT Cnt s16[0][19]	288
	324
T2_DualSpurVernierLUT_Cnt_s16[0][20]	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	1
T2_DualSpurVernierLUT_Cnt_s16[1][12]	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[2][3]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2 DualSpurVernierLUT Cnt s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
	8
T2_DualSpurVernierLUT_Cnt_s16[3][4]	0
T2_DualSpurVernierLUT_Cnt_s16[3][4] T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10 12





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	9		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	215.6112897		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	58.78464067		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAb	sPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState	_Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimCom	p_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	-

0	0 0 0		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1782.35547	1782.355455 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	•
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	1780.41125	1780.41129 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16	16	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	882.355469	882.3554545 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	•
<u> </u>		1.	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.109 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
DigColPsInt_GetCustData()	142	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_ColTrimStatic_Deg_M_f32	4.6	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	58760	
DigColPs_I2CHwColAngle_Deg_M_f32	118.0321395	
DigColPs_I2CHwDataType_Cnt_M_u08	0	

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	5.8
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16 6
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	5.8
DigColPs_TrimCompStatic_Cnt_M_u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3] T3_ColSpurVernierLUT_Cnt_s16[0][4]	-66 22
T2_ColSpurVernierLUT_Cnt_s16[0][4] T2_ColSpurVernierLUT_Cnt_s16[0][5]	-33 0
T2_ColSpurVernierLUT_Cnt_s16[0][5] T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0] T3_ColSpurVernierLUT_Cnt_s16[1][1]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11] T0_ColOpurVernierLUT_Cot_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][13]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T3_ColSpurVernierLUT_Cnt_s16[2][9]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	5 3
T2_ColSpurVernierLU1_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T0_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3] T3_ColSpurVernierLUT_Cnt_s16[3][4]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5 2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3 16
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180 -144
T2_DualSpurVernierLUT_Cnt_s16[0][7] T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2 DualSpurVernierLUT Cnt s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T3_DualSpurVernierLUT_Cnt_s16[1][11]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	0 1
T2 DualSpurVernierLUT Cnt s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][3] T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][4]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
TO D. 10 . W	
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	5 6

2014-10-14, 18:16:06+0530





		•	
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2 DualSpurVernierLUT Cnt s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2 DualSpurVernierLUT Cnt s16[3][20]	19		
T2 DualSpurVernierLUT Cnt s16[3][21]	21		
k SelectFromColumn Cnt Igc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	3		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.14		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsI	PosValid Cnt lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsI		
tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum	tgt_DigColPs_Per2_MecState_0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	_oni_igo	
		Fyrna eta d Value	D
Name	Actual Value	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	818.181763	818.1818182 ± 0.00048828125	
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	833.432129	833.4321395 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	1	1	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5678711	-66.56786052 ± 0.00009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	
NTC	0x6C	0x6C	
Param	0x0C	0x0C	
Status	0×01	0v01	

0x01

Status

0x01



T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 2.110 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetCustData()	105
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50
DigColPs_ColTrimStatic_Deg_M_f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs I2CHwColAngle Deg M f32	274.3637406
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.9
DigColPs 12CHwTrimTransCnts Uls M u08	3
	18
DigColPs_I2CSensCommFlts_Cnt_M_u08	1
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs_TrimCompStatic_Cnt_M_u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2

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Input Value 1 0 4 0
4
0
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12
9
6
3
16
13
10
7
17
-396
-360
-324
-288
-252
-216
-180
-144
-108
-72
-36
0
36
72
108
144
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216
252
288
324
360
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T2_DualSpurVernierLUT_Cnt_s16[1][14] 3 12_DualSpurVernierLUT_Cnt_s16[1][16] 4 12_DualSpurVernierLUT_Cnt_s16[1][16] 5 5 7 2_DualSpurVernierLUT_Cnt_s16[1][17] 6 6 7 2_DualSpurVernierLUT_Cnt_s16[1][17] 8 7 7 2_DualSpurVernierLUT_Cnt_s16[1][19] 8 7 7 2_DualSpurVernierLUT_Cnt_s16[1][19] 9 7 2_DualSpurVernierLUT_Cnt_s16[1][20] 9 7 2_DualSpurVernierLUT_Cnt_s16[1][21] 0 0 7 2_DualSpurVernierLUT_Cnt_s16[2][0] 0 0 7 2_DualSpurVernierLUT_Cnt_s16[2][0] 0 0 7 2_DualSpurVernierLUT_Cnt_s16[2][1] 1 1 7 2_DualSpurVernierLUT_Cnt_s16[2][1] 2 2 2 2 2 2 2 2 2	out Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]			
T2_DualSpurVernierLUT_Cnt_s16[1][16] 5 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][1] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][19] 9 T2_DualSpurVernierLUT_Cnt_s16[2][1] 10 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 4 T2_DualSpurVernierLUT_Cnt_s16[2][1] 6 T2_DualSpurVer			
T2_DualSpurVernierLUT_Cnt_s16[1][17] 6 T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][19] 9 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[3][1] 10 T2_DualSpurVernierLUT_Cnt_s16[3			
T2_DualSpurVernierLUT_Cnt_s16[1][18] 7 T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][21] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 9 T2_DualSpurVernierLUT_Cnt_s16[2][1] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][7] 9 T2_DualSpurVernierLUT_Cnt_s16[2][7] 9 T2_DualSpurVernierLUT_Cnt_s16[2][7] 10 T2_DualSpurVernierLUT_Cnt_s16[2][7] 10 T2_DualSpurVernierLUT_Cnt_s16[2][7] 10 T2_DualSpurVernierLUT_Cnt_s16[2][7] 11 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][7] 11 T2_DualSpurVernierLUT_Cnt_s16[3]			
T2_DualSpurVernierLUT_Cnt_s16[1][19] 8 T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][3] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][8] 9 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] 4 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 7 T2_DualSpurVernierLUT_Cnt_s16[2][16] 8 T2_DualSpurVernierLUT_Cnt_s16[2][16] 8 T2_DualSpurVernierLUT_Cnt_s16[2][16] 8 T2_DualSpurVernierLUT_Cnt_s16[2][16] 8 T2_DualSpurVernierLUT_Cnt_s16[3][1] 10 T2_DualSpurVernierLUT_Cnt_s16[
T2_DualSpurVernierLUT_Cnt_s16[1][20] 9 T2_DualSpurVernierLUT_Cnt_s16[1][21] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][5] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][19] 8 T2_DualSpurVernierLUT_Cnt_s16[2][19] 9 T2_DualSpurVernierLUT_Cnt_s16[2][19] 9 T2_DualSpurVernierLUT_Cnt_s16[2][19] 9 T2_DualSpurVernierLUT_Cnt_s16[3][1] 10 T2_DualSpurVernierLUT_Cnt_s16			
T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][0] 0 T2_DualSpurVernierLUT_Cnt_s16[2][1] 1 T2_DualSpurVernierLUT_Cnt_s16[2][2] 2 T2_DualSpurVernierLUT_Cnt_s16[2][3] 3 T2_DualSpurVernierLUT_Cnt_s16[2][4] 4 T2_DualSpurVernierLUT_Cnt_s16[2][6] 5 T2_DualSpurVernierLUT_Cnt_s16[2][6] 6 T2_DualSpurVernierLUT_Cnt_s16[2][7] 7 T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][17] 7 T2_DualSpurVernierLUT_Cnt_s16[2][17] 8 T2_DualSpurVernierLUT_Cnt_s16[3][1] 1 T2			
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T2_DualSpurVernierLUT_Cnt_s16[3][20]			
T2_DualSpurVernierLUT_Cnt_s16[3][21] 21			
k_SelectFromColumn_Cnt_lgc 1			
k_SkipStepErrDiag_Cnt_str.Threshold 87			
k_SkipStepErrDiag_Cnt_str.PStep 0			
k_SkipStepErrDiag_Cnt_str.NStep 20			
	33		
	17		
_	2		
	73.6750493 824.57		
	+.07		
02 011 12 1 2 11111121 21 1 1 1 1 1	4.3637406		
	88743997		
0 0_			
	797		
	_DigColPs_Per2_I2CHwAbsPosValid_Cnt _DigColPs_Per2_I2CHwAbsPos_HwDeg_f		
	_DigColPs_Per2_I2CHwAbsPos_HwDeg_i _DigColPs_Per2_MecState_Cnt_enum	IOL	
	_DigColPs_Per2_TrimComp_Cnt_lgc		
	_Pim_DigColPsEOL		
	tual Value	Expected Value	Dooule
DigColPs_HwAVernCorrFault_Cnt_M_lgc 1	THE VALUE		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32 981.		Expected Value	Result

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	979.563721	979.5637406 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	2	2	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	79.5637207	79.56374056 ± 0.00009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

DigColPs_Per2

2014-10-14, 18:16:06+0530



Test Case 3: Path Test

2014-10-14, 18:16:06+0530

DigColPs_Per2



Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

12132.00 Cycles
5985.00 Cycles
5985.00 Cycles
5985.00 Cycles
5935.00 Cycles
5935.00 Cycles
5906.00 Cycles
5921.00 Cycles
2806.00 Cycles
2658.00 Cycles
5773.00 Cycles
8897.00 Cycles
8897.00 Cycles
8863.00 Cycles
8856.00 Cycles
8856.00 Cycles
8856.00 Cycles
8856.00 Cycles
8854.00 Cycles
8854.00 Cycles
8854.00 Cycles
5822.00 Cycles
5822.00 Cycles
12189.00 Cycles
12185.00 Cycles
12185.00 Cycles
3039.00 Cycles
3039.00 Cycles TS3.1 TS3.2 TS3.3 TS3.4 TS3.5 TS3.6 TS3.6 TS3.7 TS3.8 TS3.10 TS3.11 TS3.11 TS3.12 TS3.13 TS3.14 TS3.15 TS3.17 TS3.18 TS3.19 TS3.20 TS3.20 TS3.22 TS3.23



VECTOR DESCRIPTION: Description

```
TS3.1 "if (Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16 == D_TRIMCOMPLETE_CNT_U16)=>FALSE if (12CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>False && (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>False && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>False && (TrimCompleteEOL_Cnt_T_lgc == TRUE)=>False if (k_SelectFromColumn_Cnt_lgc == TRUE)=>False if (k_SelectFromColumn_Cnt_lgc == TRUE)=>False if ((AbsVernLevelDiff_Cnt_T_u08 > 1U)=>True&& (AngleDataAvailable_Cnt_T_lgc == TRUE)=>False && (DigColPs_PrevAngleDataAvailable_Cnt_T_lgc == TRUE)=>False && (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE)=>False if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) == TRUE)=>False
(AngieUataAvailable_Cnt__|gc == IRUE|=>False &&
(DigCoIPS_PrevAngleDataAvailable_Cnt_M_gc == TRUE)=>False
if (DiagFailed_m([DigCoIPS_SkipStepFltDetectAcc_Cnt_M_u16 + DigCoIPS_VernCorrDetectAcc_Cnt_M_u16), k_SkipSte;
If ((AbsCoIPosDiff_Deg_T_32 > k_VernOORangeThresh_Deg_f32) &&
(AngieDataAvailable_Cnt_T_lgc == TRUE) &&
(DigCoIPS_PrevAngleDataAvailable_Cnt_M_igc == TRUE) |=>False
if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(SkipStepFltDetect_Cnt_T_lgc == TRUE) ||
(DigCoIPS_VernCorrDetectAcc_Cnt_M_u16 == 0U) && (DigCoIPS_SkipStepFltDetectAcc_Cnt_M_u16 == 0U) ||
(1)(2CcIPS_SkipStepFltDetect_Cnt_M_u16 == 0U) ||
(1)(2CcIPS_SkipStepFltDetect_
"TS3.4 "if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
(I2CColSensorFault_Cnt_T_lgc == FALSE)=>False &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
(I2CSpurSensorFault_Cnt_T_lgc == FALSE)=>False &&
(I7ImCompleteOL_Cnt_T_lgc == TRUE)=False"
TS3.5 "if ((ColParityError_Cnt_T_lgc == TRUE) ||
(SpurParityError_Cnt_T_lgc == TRUE)=>True ||
(I2CSensCommFits_Cnt_T_u08 != 0U) )=>True"
TS3.6 "if ((AbsVernLevelDiff_Cnt_T_u08 > 1U) &&
(AngleDataAvailable_Cnt_T_lgc == TRUE) =>False
if (I2CHwDataType_Cnt_T_u08 == D_ERRORREG_CNT_U08)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>True
if (ErrorDataReady_Cnt_T_lgc == TRUE)=>False
(ParityOrCommFault_Cnt_T_lgc == FALSE)=>False &&
(I2CColSensorFault_Cnt_T_lgc == TRUE)=>False
TS3.7 if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U)=>True&& (DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 == 0U))=>False
TS3.8 if ((DigColPs_VernCorrDetectAcc_Cnt_M_u16 == 0U)=>True &&
(ParityOrCommFault_Cnt_T_lgc == TRUE)=>True
TS3.9 "else if (((I2CColSensorFault_Cnt_T_lgc == TRUE)=>True)||
TS3.10 if ((MSVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32)=>True && (AngleDataAvailable_Cnt_T_lgc ==
TRUE)=>False)=>False
TS3.11 "if ((I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True &&
(I2COlSensorFault_Cnt_T_lgc == D_ANGLEDATA_CNT_U08)=>True
TS3.10 'if ((AbsVernDiagError_Deg_T_f32 > k_VernCorrErrorThresh_Deg_f32)=>True && (AngleDataAvailable_Cnt_T_lgc == TRUE)=>False)=>False
TS3.11 " if ((12CHWDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True && (12CColSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CColSensorFault_Cnt_T_lgc == FALSE)=>True && (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CColSensorFault_Cnt_T_lgc == FALSE)=>False && (12CColSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == FALSE)=>True && (12CSpurSensorFault_Cnt_T_lgc == TRUE)=>True && (12CSpur
```



```
if ((AbsColPosDiff_Deg_T_i32 > k_VernOORangeThresh_Deg_f32) &&
    (AngleDataAvailable_Cnt_T_lgc == TRUE) &&
    (DigColPs_PrevAngleDataAvailable_Cnt_M_lgc == TRUE) >> True"

TS3.17 "DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 = DiagPStep_m(DigColPs_SkipStepFitDetectAcc_Cnt_M_u16, k_SkipStepErrDiag_Cnt_str) => False
    if (DiagFailed_m((DigColPs_SkipStepFitDetectAcc_Cnt_M_u16 + DigColPs_VernCorrDetectAcc_Cnt_M_u16), k_SkipStepErrDiag_Cnt_str) == TRUE) => True
    if ((VernCorrDetect_Cnt_T_lgc == TRUE) ||
        (SkipStepFitDetect_Cnt_T_lgc == TRUE) => True||
        (SkipStepFitDetect_
```

Test Step 3.1 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	20	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	0	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	0	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	
DigColPs_SpurTrimStatic_Deg_M_f32	0	
DigColPs_TrimCompStatic_Cnt_M_u16	0	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131	
Γ2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65	
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98	
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130	
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163	
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196	
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229	
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261	
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294	
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327	
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359	
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0	
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4	
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3 2
T2_ColSpurVernierLUT_Cnt_s16[1][13] T3_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2 ColSpurVernierLUT Cnt s16[2][5]	0
T2 ColSpurVernierLUT Cnt s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4]	2
T2_ColSpurVernierLUT_Cnt_s16[3][5]	
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11] T3_DualSpurVernierLUT_Cnt_s16[0][12]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][13]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLU1_Cnt_S16[0][15] T2_DualSpurVernierLUT_Cnt_S16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	324 360

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14] T3_DualSpurVernierLUT_Cnt_s46[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14] T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15] T2_DualSpurVernierLUT_Cnt_s16[3][16]	9 11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0
k_SkipStepErrDiag_Cnt_str.Threshold	10
k_SkipStepErrDiag_Cnt_str.PStep	0
k_SkipStepErrDiag_Cnt_str.NStep	0
k_VernCorrErrorDiag_Cnt_str.Threshold	0
k_VernCorrErrorDiag_Cnt_str.PStep	0
k_VernCorrErrorDiag_Cnt_str.NStep	0
k_VernCorrErrorThresh_Deg_f32	1
	400
k_VernOORangeThresh_Deg_f32	100

2014-10-14, 18:16:06+0530



Name	Input Value		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo	s_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn	t_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_C	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓
NTC	0x6C	0x6C	✓
Param	0x00	0x00	~
Status	0x00	0x00	✓
NTC	0x6E	0x6E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	~
NTC	0x6F	0x6F	~
Param	0x00	0x00	~
Status	0x00	0x00	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.2 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	414
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	10
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	100
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1

2014-10-14, 18:16:06+0530

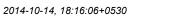


Name	
12. ColSpurVemiet.UT_Cnt_s16[0][1] 12. ColSpurVemiet.UT_Cnt_s16[0][1] 12. ColSpurVemiet.UT_Cnt_s16[0][2] 13. ColSpurVemiet.UT_Cnt_s16[0][3] 12. ColSpurVemiet.UT_Cnt_s16[0][4] 13. ColSpurVemiet.UT_Cnt_s16[0][4] 13. ColSpurVemiet.UT_Cnt_s16[0][6] 12. ColSpurVemiet.UT_Cnt_s16[0][7] 13. ColSpurVemiet.UT_Cnt_s16[0][7] 14. ColSpurVemiet.UT_Cnt_s16[0][7] 15. ColSpurVemiet.UT_Cnt_s16[0][8] 16. ColSpurVemiet.UT_Cnt_s16[0][9] 17. ColSpurVemiet.UT_Cnt_s16[0][9] 18. ColSpurVemiet.UT_Cnt_s16[0][9] 19. ColSpurVemiet.UT_Cnt_s16[0][10] 18. ColSpurVemiet.UT_Cnt_s16[0][10] 18. ColSpurVemiet.UT_Cnt_s16[0][11] 19. ColSpurVemiet.UT_Cnt_s16[0][11] 19. ColSpurVemiet.UT_Cnt_s16[0][12] 19. ColSpurVemiet.UT_Cnt_s16[0][13] 19. ColSpurVemiet.UT_Cnt_s16[0][14] 19. ColSpurVemiet.UT_Cnt_s16[0][14] 19. ColSpurVemiet.UT_Cnt_s16[0][16] 10. ColSpurVemiet.UT_Cnt_s16[0][16] 10. ColSpurVemiet.UT_Cnt_s16[0][16] 11. ColSpurVemiet.UT_Cnt_s16[0][16] 12. ColSpurVemiet.UT_Cnt_s16[0][16] 13. ColSpurVemiet.UT_Cnt_s16[0][16] 12. ColSpurVemiet.UT_Cnt_s16[1][1] 14. ColSpurVemiet.UT_Cnt_s16[1][1] 15. ColSpurVemiet.UT_Cnt_s16[1][1] 16. ColSpurVemiet.UT_Cnt_s16[1][1] 17. ColSpurVemiet.UT_Cnt_s16[1][1] 18. ColSpurVemiet.UT_Cnt_s16[1][1] 19. ColSpurVemiet.UT_Cnt_s16[1][1] 19. ColSpurVemiet.UT_Cnt_s16[1][1] 10. ColSpurVemiet.UT_Cnt_s16[1][1] 11. ColSpurVemiet.UT_Cnt_s16[1][1] 12. ColSpurVemiet.UT_Cnt_s16[1][1] 12. ColSpurVemiet.UT_Cnt_s16[1][1] 13. ColSpurVemiet.UT_Cnt_s16[1][1] 14. ColSpurVemiet.UT_Cnt_s16[1][1] 15. ColSpurVemiet.UT_Cnt_s16[1][1] 16. ColSpurVemiet.UT_Cnt_s16[1][1] 17. ColSpurVemiet.UT_Cnt_s16[1][1] 18. ColSpurVemiet.UT_Cnt_s16[1][1] 19. ColSpurVemiet.UT_Cnt_s16[1][1] 10. ColSpurVemiet.UT_Cnt_s16[1][1] 11. ColSpurVemiet.UT_Cnt_s16[1][1] 12. ColSpurVemiet.UT_Cnt_s16[1][1] 13. ColSpurVemiet.UT_Cnt_s16[1][1] 14. ColSpurVemiet.UT_Cnt_s16[1][1] 15. ColSpurVemiet.UT_Cnt_s16[1][1] 16. ColSpurVemiet.UT_Cnt_s16[1][1] 17. ColSpurVemiet.UT_Cnt_s16[1][1] 18. ColSpurVemiet.UT_Cnt_s16[1][1] 19. ColSpurVemiet.UT_Cnt_s16[1][1] 19. ColSpurVemiet.UT_Cnt_s16[1][1] 19	
12 ColSpurVemietUT_Cnt_s16[0][1]	
12 ColSpurVementUT_Cnt_st6[0][2]	
T2 ColSpurVernierLUT Cnt_s16[0][3] -86 -86 -87	
T2	
T2 ColSpurVermierLUT_Cnt_s16[0][5] 0	
T2 ColSpurVermierLUT_Cnt_s16[0][5] 0	
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T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8] T2_ColSpurVernierLUT_Cnt_s16[0][9] T2_ColSpurVernierLUT_Cnt_s16[0][19] T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][12] T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][13] T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[0][16] T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_Co	
12 ColSpurVernierLUT_Cnt_s16[0][8] 130	
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T2_ColSpurVermierLUT_Cnt_s16(0)[12] 229 T2_ColSpurVermierLUT_Cnt_s16(0)[13] 261 T2_ColSpurVermierLUT_Cnt_s16(0)[15] 327 T2_ColSpurVermierLUT_Cnt_s16(0)[15] 327 T2_ColSpurVermierLUT_Cnt_s16(0)[16] 359 T2_ColSpurVermierLUT_Cnt_s16(1)[1] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[1] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[1] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[2] 3 T2_ColSpurVermierLUT_Cnt_s16(1)[3] 2 T2_ColSpurVermierLUT_Cnt_s16(1)[3] 2 T2_ColSpurVermierLUT_Cnt_s16(1)[4] 1 T2_ColSpurVermierLUT_Cnt_s16(1)[6] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[7] 3 T2_ColSpurVermierLUT_Cnt_s16(1)[7] 3 T2_ColSpurVermierLUT_Cnt_s16(1)[7] 3 T2_ColSpurVermierLUT_Cnt_s16(1)[7] 3 T2_ColSpurVermierLUT_Cnt_s16(1)[10] 1 T2_ColSpurVermierLUT_Cnt_s16(1)[10] 0 T2_ColSpurVermierLUT_Cnt_s16(1)[10] 0 T2_ColSpurVermierLUT_Cnt_s16(1)[10] 0 T2_ColSpurVermierLUT_Cnt_s16(1)[10] 0 T2_ColSpurVermierLUT_Cnt_s16(1)[13] 2 T2_ColSpurVermierLUT_Cnt_s16(1)[13] 2 T2_ColSpurVermierLUT_Cnt_s16(1)[16] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[16] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[16] 4 T2_ColSpurVermierLUT_Cnt_s16(1)[16] 4 T2_ColSpurVermierLUT_Cnt_s16(2)[2] 6 T2_ColSpurVermierLUT_Cnt_s16(2)[2] 6 T2_ColSpurVermierLUT_Cnt_s16(2)[2] 6 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 9 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 9 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1 T2_ColSpurVermierLUT_Cnt_s16(2)[6] 1	
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T2_ColSpurVernierLUT_Cnt_s16[0][14]	
T2_ColSpurVernierLUT_Cnt_s16[0][15] 12_ColSpurVernierLUT_Cnt_s16[0][16] 12_ColSpurVernierLUT_Cnt_s16[1][1] 12_ColSpurVernierLUT_Cnt_s16[1][1] 12_ColSpurVernierLUT_Cnt_s16[1][2] 13_ColSpurVernierLUT_Cnt_s16[1][3] 12_ColSpurVernierLUT_Cnt_s16[1][4] 12_ColSpurVernierLUT_Cnt_s16[1][6] 12_ColSpurVernierLUT_Cnt_s16[1][6] 12_ColSpurVernierLUT_Cnt_s16[1][6] 12_ColSpurVernierLUT_Cnt_s16[1][7] 13_ColSpurVernierLUT_Cnt_s16[1][7] 14_ColSpurVernierLUT_Cnt_s16[1][8] 12_ColSpurVernierLUT_Cnt_s16[1][9] 12_ColSpurVernierLUT_Cnt_s16[1][9] 12_ColSpurVernierLUT_Cnt_s16[1][1] 13_ColSpurVernierLUT_Cnt_s16[1][1] 14_ColSpurVernierLUT_Cnt_s16[1][1] 15_ColSpurVernierLUT_Cnt_s16[1][1] 16_ColSpurVernierLUT_Cnt_s16[1][1] 17_ColSpurVernierLUT_Cnt_s16[1][1] 18_ColSpurVernierLUT_Cnt_s16[1][1] 19_ColSpurVernierLUT_Cnt_s16[1][1] 10_ColSpurVernierLUT_Cnt_s16[1][1] 10_ColSpurVernierLUT_Cnt_s16[1][1] 11_ColSpurVernierLUT_Cnt_s16[1][1] 12_ColSpurVernierLUT_Cnt_s16[1][1] 12_ColSpurVernierLUT_Cnt_s16[1][1] 12_ColSpurVernierLUT_Cnt_s16[2][0] 12_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16	
12_ColSpurVermierLUT_Cnt_s16[0][10]	
12_ColSpurVermierLUT_Cnt_s16[0][10]	
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7] T2_ColSpurVernierLUT_Cnt_s16[1][8] T2_ColSpurVernierLUT_Cnt_s16[1][8] T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][13] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT	
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T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 9 T2_ColSpurVernierLUT_Cnt_s16[3][6] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	
T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 9 T2_ColSpurVernierLUT_Cnt_s16[3][6] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][15] 2 T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1 T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2 ColSpurVernierLUT Cnt s16(3)(10)	
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7	
T2_ColSpurVernierLUT_Cnt_s16[3][15] 4	
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17	
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396	
T2_DualSpurVernierLUT_Cnt_s16[0][2] -324	
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288	

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		(MEC)(M)
Name	Input Value	
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216	
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72	
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2 DualSpurVernierLUT Cnt s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0] T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4	
T2_DualSpurVernierLUT_Cnt_S16[2][4] T2_DualSpurVernierLUT_Cnt_s16[2][5]	5	
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6	
T2_DualSpurVernierLUT_Cnt_s16[2][6] T2_DualSpurVernierLUT_Cnt_s16[2][7]	7	
	<i>I</i>	
T2_DualSpurVernierLUT_Cnt_s16[2][8]		





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	255		
k_SkipStepErrDiag_Cnt_str.PStep	50		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	60		
k_VernCorrErrorDiag_Cnt_str.PStep	50		
k_VernCorrErrorDiag_Cnt_str.NStep	50		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	1800		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

tgt_Rte_filst_3a_bigColFs.Filft_bigColFsEOL	IGI_FIIII_DIGCOIFSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1427.27271	1427.272727 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	5	5	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	1430	1430 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	131	131	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	60	60	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	530	530 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	~
Param	0x0D	0x0D	~
Status	0x01	0x01	✓

√ Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	12
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	212
DigColPs_ColTrimStatic_Deg_M_f32	4.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	58760
DigColPs_I2CHwColAngle_Deg_M_f32	118.032
DigColPs_I2CHwDataType_Cnt_M_u08	0





Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	64972
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.308
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	24
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	421.9525396 16
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	241
DigColPs SpurTrimStatic Deg M f32	5.8
DigColPs TrimCompStatic Cnt M u16	124
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294 327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	3527 359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2 ColSpurVernierLUT Cnt s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T3_ColSpurVernierLUT_Cnt_s16[2][9]	7 5
T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	8 5

2014-10-14, 18:16:06+0530



Name	Input Value	
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15	
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12	
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6	
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3	
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16	
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13	
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10	
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4	
T2 ColSpurVernierLUT Cnt s16[3][16]	17	
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396	
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360	
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324	
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288	
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252	
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216	
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180	
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144	
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108	
T2 DualSpurVernierLUT Cnt s16[0][9]	-72	
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36	
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72	
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108	
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144	
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180	
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216	
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252	
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324	
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360	
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0	
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1	
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3	
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4	
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5	
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6	
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7	
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8	
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9	
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2	
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3	

2014-10-14, 18:16:06+0530





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k_SkipStepErrDiag_Cnt_str.Threshold	167		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	33		
k_VernCorrErrorDiag_Cnt_str.Threshold	97		
k_VernCorrErrorDiag_Cnt_str.PStep	13		
k_VernCorrErrorDiag_Cnt_str.NStep	4		
k_VernCorrErrorThresh_Deg_f32	82.93280101		
k_VernOORangeThresh_Deg_f32	1028.143258		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	118.0321395		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55.30846006		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4351	V. F. L. O. J. L.	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cn		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Ci tgt_Pim_DigColPsEOL	nt_igc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	, ·	Function Value	Danul
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	840.685425		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32 DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	840.6854545 ± 0.00048828125 0	
DigColPs_IZCHW1rim1ransCnts_UIs_M_u08 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigCoIPs_PrevAngieDataAvailable_Cnt_ivi_igc DigCoIPs_PrevCoIPos_Deg_M_f32	833.432007	833.432 ± 0.0001220703125	
DigCoIPs_PrevVernierLevelNo Cnt M u08	9	9	
DigColPs Regl2CSnsrDataType Cnt M u08	1	1	
DigColPs SkipStepFltDetectAcc Cnt M u16	0	0	
DigCoIPs_SkipStepFitDetectAcc_Cnt_M_u16 DigCoIPs VernCorrDetectAcc Cnt M u16	0	0	
	1	1	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-66.5679932	-66.568 ± 0.00009	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	-00.506 ± 0.00009	
NTC	0 0x6C	0x6C	
1410	UXUC	UXUC	

0x04

0x01

0x04

0x01

Param

Status



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetCustData()	24
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105
DigColPs ColTrimStatic Deg M f32	14.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	24432
DigColPs I2CHwColAngle Deg M f32	274.36
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	30893
DigColPs_I2CHwSpurAngle_Deg_M_f32	88.88
DigColPs I2CHwTrimTransCnts Uls M u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1200.26039
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs SkipStepFltDetectAcc Cnt M u16	1
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	6.9
DigColPs TrimCompStatic Cnt M u16	160
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2 ColSpurVernierLUT Cnt s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2 ColSpurVernierLUT Cnt s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
0	1-

2014-10-14, 18:16:06+0530



12_Configenment Grag 19 1 1 1 1 1 1 1 1		I
T. Codeys/mental Cost 1981 10	Name	Input Value
12. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. CoSportwenset UT_Del. 1981[910] 10. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 11. CoSportwenset UT_Del. 1981[910] 12. CoSportwenset UT_Del. 1981[910] 13. CoSportwenset UT_Del. 1981[910] 14. CoSportwenset UT_Del. 1981[910] 15. CoSportwenset UT_Del. 1981[910] 16. CoSportwenset UT_Del. 1981[910] 17. CoSportwenset UT_Del. 1981[910] 18. CoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981[910] 19. DoSportwenset UT_Del. 1981	T2_ColSpurVernierLUT_Cnt_s16[1][14]	
17. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(1) 2. Costaya/ment.U. Cut. 1902(2) 3. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 4. Costaya/ment.U. Cut. 1902(3) 5. Costaya/ment.U. Cut. 1902(3) 6. Costaya/ment.U. Cut. 1902(3) 7. Costaya/ment.U. Cut. 1902(1) 7. Costaya/ment.U. Cut. 1902(T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
17_0085pvvrinstrut _Cot_st@ 0	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
17_CORSE/vernetLT_CR_15[2] 6	T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
P. Collego-Vernicut Ces. 1982 6	T2 ColSpurVernierLUT Cnt s16[2][1]	8
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PCOS_AVENNELT_CR_15[0] 2 PCOS_AVENNELT_CR_15[0] 0 PCOS_AVENNELT_CR_15[0] 0 PCOS_AVENNELT_CR_15[0] 0 PCOS_AVENNELT_CR_15[0] 7 PC		
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T2_DualSpurVerniertUT_Cnt_s16[0][3]	T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
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T2_DualSpurVernierLUT_Cnt_s16[0][4] 252 252 152 152 152 152 153 154 15		-288
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T2_DualSpurVernierLUT_Cnt_s16[0][17] 216 T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10 T2_DualSpurVernierLUT_Cnt_s16[1][10] 10		
T2_DualSpurVernierLUT_Cnt_s16[0][18] 252 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0		
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288 T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 1		
T2_DualSpurVernierLUT_Cnt_s16[0][20] 324 T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360 T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][0] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0 T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][9] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][2] 1 T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][3] 2 T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][4] 3 T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1	T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1	T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][5] 4 T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1	T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5 T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1	T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6 T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][8] 7 T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][9] 8 T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][10] 9 T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][11] 0 T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2_DualSpurVernierLUT_Cnt_s16[1][12] 1		
T2 DualSpur/erniert LT_Cnt_s16[1][13]		
	T2_DualSpurVernierLUT_Cnt_s16[1][13]	2

DigColPs_Per2





Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10 22		
T2_DualSpurVernierLUT_Cnt_s16[3][0]			
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]			
T2_DualSpurVernierLUT_Cnt_s16[3][3]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][6]			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14 16		
T2_DualSpurVernierLUT_Cnt_s16[3][8] T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_S16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_S16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_S16[3][14] T2_DualSpurVernierLUT_Cnt_S16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
	13		
T2_DualSpurVernierLUT_Cnt_s16[3][17] T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][18] T3_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][19] T3_DualSpurVernierLUT_Cnt_s16[3][20]			
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cnt_s16[3][21]	19 21		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	1		
k_SelectFromColumn_Cnt_lgc	87		
k_SkipStepErrDiag_Cnt_str.Threshold	0		
k_SkipStepErrDiag_Cnt_str.PStep	20		
k_SkipStepErrDiag_Cnt_str.NStep k_VernCorrErrorDiag_Cnt_str.Threshold	33		
k VernCorrErrorDiag Cnt str.PStep	17		
	17		
k_VernCorrErrorDiag_Cnt_str.NStep k_VernCorrErrorThresh_Deg_f32	73.6750493		
k_VernOORangeThresh_Deg_f32	73.6750493 824.5773324		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	274.3637406		
	88.88743997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	88.88743997 797		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tot_Pte_Inst_Sa_DigColPs_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Inst_		t lac	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_	192	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
	tet DisCalDa DasS Trim Comm. Out !		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	E	n -
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc		Expected Value	Result





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1339.56006	1339.56 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	439.560059	439.56 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	✓
Param	0x00	0x00	~
Status	0x00	0x00	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	62
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	124
DigColPs_ColTrimStatic_Deg_M_f32	25
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs I2CColSensorFault Cnt M Igc	0
DigColPs I2CHwColAngle Cnt M u16	21204
DigColPs I2CHwColAngle Deg M f32	226.45
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs I2CHwSpurAngle Cnt M u16	263
DigColPs I2CHwSpurAngle Deg M f32	143.95
DigColPs I2CHwTrimTransCnts Uls M u08	143.95
	20
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	· · · · · · · · · · · · · · · · · · ·
DigColPs_PrevColPos_Deg_M_f32	941.477402
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	9
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	126
DigColPs_SpurTrimStatic_Deg_M_f32	80
DigColPs_TrimCompStatic_Cnt_M_u16	196
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][3]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324





360 9
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DigColPs_Per2

2014-10-14, 18:16:06+0530



Input Value $k_VernCorrErrorDiag_Cnt_str.NStep$ 10 $k_VernCorrErrorThresh_Deg_f32$ 90.55352902 k_VernOORangeThresh_Deg_f32 803.1102527 tgt_DigColPs_Per2_MecState_Cnt_enum.value tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 226.4548138 $tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32$ 143.9507322 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 2646 $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc$ $tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 $tgt_DigColPs_Per2_MecState_Cnt_enum$ $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum$ tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc $tgt_DigColPs_Per2_TrimComp_Cnt_lgc$

tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	192.704544	192.7045455 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	201.449997	201.45 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	3	3	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-707.295471	-707.2954545 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	~

T			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.6 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	100
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	35.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	50517
DigColPs_I2CHwColAngle_Deg_M_f32	347.86
DigColPs_I2CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908
DigColPs_I2CHwSpurAngle_Deg_M_f32	210.79
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	25
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	1680.342175
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125
DigColPs_SpurTrimStatic_Deg_M_f32	9.1
DigColPs_TrimCompStatic_Cnt_M_u16	1
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163

2014-10-14, 18:16:06+0530



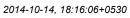
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2 ColSpurVernierLUT Cnt s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2 ColSpurVernierLUT Cnt s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
	6
T2_ColSpurVernierLUT_Cnt_s16[2][2] T0_ColSpurVernierLUT_Cnt_s16[2][2]	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2 ColSpurVernierLUT Cnt s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2 ColSpurVernierLUT Cnt s16[2][9]	3
	1
T2_ColSpurVernierLUT_Cnt_s16[2][10]	
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
	14
T2_ColSpurVernierLUT_Cnt_s16[3][1] T0_ColSpurVernierLUT_Cnt_s16[3][1]	
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
	-252
T2 DualSpurVernierLUT Cnt s16[0][4]	
T2_DualSpurVernierLUT_Cnt_s16[0][4] T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36 72
T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2 DualSpurVernierLUT Cnt s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4 5
T2_DualSpurVernierLUT_Cnt_s16[1][16] T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][17] T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15] T3_DualSpurVernierLUT_Cnt_s16[2][16]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16] T2_DualSpurVernierLUT_Cnt_s16[2][17]	5 6
T2_DualSpurVernierLUT_Cnt_s16[2][17] T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Crit_S16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3

DigColPs_Per2





2.g o s c o . 2		• "	
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2 DualSpurVernierLUT Cnt s16[3][14]	7		
T2 DualSpurVernierLUT Cnt s16[3][15]	9		
T2 DualSpurVernierLUT Cnt s16[3][16]	11		
T2 DualSpurVernierLUT Cnt s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2 DualSpurVernierLUT Cnt s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	1		
k SkipStepErrDiag Cnt str.Threshold	160		
k SkipStepErrDiag Cnt str.PStep	23		
k SkipStepErrDiag Cnt str.NStep	16		
k VernCorrErrorDiag Cnt str.Threshold	82		
k_VernCorrErrorDiag_Cnt_str.PStep	43		
k VernCorrErrorDiag Cnt str.NStep	34		
k VernCorrErrorThresh Deg f32	16.35241604		
k_VernOORangeThresh_Deg_f32	106.1935596		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt DigColPs Per2 I2CHwAbsPos HwDeg f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1400.76807	1400.768182 ± 0.00048828125	•
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1392.65991	1392.66 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	•
LI DI O ID DI O IOOU AL DI LI DI MO	100.050040	400.00 + 0.0000	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

492.659912

0x6C

0x00

0x00

492.66 ± 0.0009

0x6C

0x00

0x00

Test Step 3.7 (Repeat Count = 1)		√
Name	Input Value	
DigColPsInt_GetCustData()	138	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152	
DigColPs_ColTrimStatic_Deg_M_f32	76	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	57565	
DigColPs_I2CHwColAngle_Deg_M_f32	68.667	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	53866	
DigColPs_I2CHwSpurAngle_Deg_M_f32	190.108	

tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

NTC

Param

Status





DigColFs_Fe12	- Contraction of the contraction
Name	Input Value
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	22
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	321.3070593
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	16
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	13.5
DigColPs_TrimCompStatic_Cnt_M_u16	376
DigColPs VernCorrDetectAcc Cnt M u16	8
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
	0
T2_ColSpurVernierLUT_Cnt_s16[0][5]	
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
	5
12 ColSnurVernierLIII Cht e16131141	l o
	2
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5] T2_ColSpurVernierLUT_Cnt_s16[3][6]	2 15

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0] T3_DualSpurVernierLUT_Cst_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360 -324
T2_DualSpurVernierLUT_Cnt_s16[0][2] T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	5
T2_DualSpurVernierLUT_Cnt_s16[1][6] T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8] T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][9] T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][11]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	0

2014-10-14, 18:16:06+0530



DigColPs Per2

DigColPs_Per2		M	aultab
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2 DualSpurVernierLUT Cnt s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k SelectFromColumn Cnt lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	116		
k SkipStepErrDiag Cnt str.PStep	3		
k_SkipStepErrDiag_Cnt_str.NStep	6		
k_VernCorrErrorDiag_Cnt_str.Threshold	37		
k_VernCorrErrorDiag_Cnt_str.PStep	8		
k_VernCorrErrorDiag_Cnt_str.NStep	48		
k_VernCorrErrorThresh_Deg_f32	84.34178925		
k_VernOORangeThresh_Deg_f32	1712.165488		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	68.66713858		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	190.1087981		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3501		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPos	Valid Cnt loc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_Cn		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL	<u>Ligo</u>	
		Fynastad Value	Result
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1062.09448	1062.094545 ± 0.00048828125	
DigCoIPs_I2CHwTrimTransCnts_UIs_M_u08	3		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	*
DigColPs_PrevColPos_Deg_M_f32	1072.66699	1072.667 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	10	10	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	_
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	_

T ·				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V

162.094482

0

0

0

162.0945455 ± 0.0009

tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

 $\label{tgt_digColPs_Per2_I2CHwAbsPos_HwDeg_f32.value} $$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value $$ tgt_Dig$



September Sept	Test Step 3.8 (Repeat Count = 1)	√
Decimal part Deci		Input Value
Disposition Control Co		·
18.8	DigColPs_ColParityError_Cnt_M_lgc	
Dispose	DigColPs_ColSensorFaultAcc_Cnt_M_u16	142
DOCOME DOCOMENDED, CM JUL 100 DOCOMENDE, CM JUL 100 DOCOMENDED, CM JUL 100 DOCOMENDED, CM JUL 100 DOCOMENDE, CM JUL 100 DOCOMEND, C	DigColPs_ColTrimStatic_Deg_M_f32	
Display Disp	DigColPs_HwAVernCorrFault_Cnt_M_lgc	
Digitable Digi		
Digitary		
Digition		
Disposition		
Digitable Digi	DigColPs_I2CHwSpurAngle_Deg_M_f32	
DECORDER	DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
Digitable Digi	DigColPs_I2CSensCommFlts_Cnt_M_u08	2
Disposition	DigColPs_I2CSpurSensorFault_Cnt_M_Igc	
DipCollers Dip		·
Digitable Digi		
Dippolars Supremy France (T.M. Mgs Dippolars Supremy France (T.M. Dippolars Supremy France (T.M. Dippolars D		
Dipolone Spurimation (Page M) 22 Dipolone Spurimation (Page M) 23 Dipolone Spurimation (Page M) 23 Dipolone Spurimation (Page M) 23 Dipolone Spurimation (Page M) 24 Dipolo		
DeColors SuperfromState, Dec M. 192 DeColors SuperfromState, Color M. 191 DeColors SuperfromState, Color M. 191 DeColors SuperfromState, Color M. 191 DeColors SuperfromState, Color M. 190 DeColor M. 19		
Dipoline yeminchecknop Crd, Mu, use Dipoline yeminchape OoRange Crd, Mu (see Ris_Inst Sa_Dipoline Ris_Inst	DigColPs_SpurTrimStatic_Deg_M_f32	17.9
Digitary Amerikange Otherape, Cot. M. Joe M. International Cot.	DigColPs_TrimCompStatic_Cnt_M_u16	520
Nat. nat. S. DigoCoPe Up. No. Inst. Sa DigoCoPe Up. No. Inst. Sa DigoCoPe Up. No. Inst. Sa DigoCoPe Inst.	DigColPs_VernCorrDetectAcc_Cnt_M_u16	
12, CoSpurVemeLUT, Cnt, 1610(1) -163 -	DigColPs_VernierAngleOORange_Cnt_M_lgc	
12; CosSpurVermet.UT; Cnt.; 16(0)[12]		
12_Cospur/emet.UT_Cnt_s160[12]		
12 CoSput/venietUT_Cnt_s160[13]		
12_ColSput/venietUT_Cnt_s160[16] 33 12_ColSput/venietUT_Cnt_s160[16] 32 12_ColSput/venietUT_Cnt_s160[16] 32 12_ColSput/venietUT_Cnt_s160[16] 32 12_ColSput/venietUT_Cnt_s160[16] 98 12_ColSput/venietUT_Cnt_s160[16] 130 12_ColSput/venietUT_Cnt_s160[16] 140 12_ColSput/venietUT_Cnt_s160[17] 165 12_ColSput/venietUT_Cnt_s160[17] 165 12_ColSput/venietUT_Cnt_s160[18] 163 12_ColSput/venietUT_Cnt_s160[18] 163 12_ColSput/venietUT_Cnt_s160[18] 164 12_ColSput/venietUT_Cnt_s160		
12 ColSpurVemierLUT Cnt_s16[0][6] 32 12 13 13 13 13 13 13	T2_ColSpurVernierLUT_Cnt_s16[0][4]	
12 ColSput/emietLUT_Cnt_s16[0][7] 65 12 ColSput/emietLUT_Cnt_s16[0][8] 98 13 ColSput/emietLUT_Cnt_s16[0][8] 13 13 ColSput/emietLUT_Cnt_s16[0][1] 13 15 ColSput/emietLUT_Cnt_s16[0][1] 16 17 ColSput/emietLUT_Cnt_s16[0][1] 16 18 ColSput/emietLUT_Cnt_s16[0][1] 16 19 ColSput/emietLUT_Cnt_s16[0][1] 16 19 ColSput/emietLUT_Cnt_s16[0][1] 17 10 ColSput/emietLUT_Cnt_s16[0][1] 17 10 ColSput/emietLUT_Cnt_s16[0][1] 17 11 ColSput/emietLUT_Cnt_s16[0][1] 17 12 ColSput/emietLUT_Cnt_s16[0][1] 17 12 ColSput/emietLUT_Cnt_s16[0][1] 17 12 ColSput/emietLUT_Cnt_s16[0][1] 18 13 ColSput/emietLUT_Cnt_s16[0][1] 18 14 ColSput/emietLUT_Cnt_s16[0][1] 18 15 ColSput/emietLUT_Cnt_s16[0][1] 18 17 ColSput/emietLUT_Cnt_s16[0][1] 18 18 ColSput/emietLUT_Cnt_s16[0][1] 18 19 ColSput/emietLUT_Cnt_s16[0][1] 18 19 ColSput/emietLUT_Cnt_s16[0][1] 18 19 ColSput/emietLUT_Cnt_s16[0][1] 18 10 ColSput/emietLUT_Cnt_s16[0][1] 18 10 ColSput/emietLUT_Cnt_s16[0][1] 18 11 ColSput/emietLUT_Cnt_s16[0][1] 18 12 ColSput/emietLUT_Cnt_s16[0][1] 18 12 ColSput/emietLUT_Cnt_s16[0][1] 18 12 ColSput/emietLUT_Cnt_s16[0][1] 18 13 ColSput/emietLUT_Cnt_s16[0][1] 18 14 ColSput/emietLUT_Cnt_s16[0][1] 18 15 ColSput/emietLUT_Cnt_s16[0][1] 18 17 ColSput/emietLUT_Cnt_s16[0][1] 18 18 ColSput/emietLUT_Cnt_s16[0][1] 18 18 ColSput/emietLUT_Cnt_s16[0][1] 18 19 ColSput/emietLUT_Cnt_s16[0][1] 18 19 ColSput/emietLUT_Cnt_s16[0][1] 18 10 ColSput/emietLUT_Cnt_s16[0][1] 18 11 ColSput/emietLUT_Cnt_s16[0][1] 18 12 ColSput/emietLUT_Cnt_s16[0][1] 18 13 ColSput/emietLUT_Cnt_s16[0][1] 18 14 ColSput/emietLUT_Cnt_s16[0][1] 18 15 ColSput/emietLUT_Cnt_s16[0][1] 18 17 ColSput/emietLUT_Cnt	T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
12 ColSpurVermierLUT_Cnt_s16(0) 8 98	T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
12_ColSpurVernierLUT_Cnt_s16[0][9] 130 163	T2_ColSpurVernierLUT_Cnt_s16[0][7]	
12 ColSpuVemiert.UT Cnt, 1610[111] 186 12 ColSpuVemiert.UT Cnt, 1610[112] 229 12 ColSpuVemiert.UT Cnt, 1610[113] 261 13 ColSpuVemiert.UT Cnt, 1610[113] 261 14 ColSpuVemiert.UT Cnt, 1610[114] 294 15 ColSpuVemiert.UT Cnt, 1610[116] 327 17 ColSpuVemiert.UT Cnt, 1610[116] 359 18 ColSpuVemiert.UT Cnt, 1610[116] 359 18 ColSpuVemiert.UT Cnt, 1610[116] 369 19 ColSpuVemiert.UT Cnt, 1610[117] 4 19 ColSpuVemiert.UT Cnt, 1610[117] 4 10 ColSpuVemiert.UT Cnt, 1610[117] 4 11 ColSpuVemiert.UT Cnt, 1610[118] 2 12 ColSpuVemiert.UT Cnt, 1610[118] 2 12 ColSpuVemiert.UT Cnt, 1610[118] 4 12 ColSpuVemiert.UT Cnt, 1610[118] 5 12 ColSpuVemiert.UT Cnt, 1610[118] 5 13 ColSpuVemiert.UT Cnt, 1610[118] 6 14 ColSpuVemiert.UT Cnt, 1610[118] 7 15 ColSpuVemiert.UT Cnt, 1610[118] 7 17 ColSpuVemiert.UT Cnt, 1610[118] 7 18 ColSpuVemiert.UT Cnt, 1610[118] 7 19 ColSpuVemiert.UT Cnt, 1610[118] 7 10 ColSpuVemiert.UT Cnt, 1610[118] 7 11 ColSpuVemiert.UT Cnt, 1610[118] 7 12 ColSpuVemiert.UT Cnt, 1610[118] 7 13 ColSpuVemiert.UT Cnt, 1610[118] 7 14 ColSpuVemiert.UT Cnt, 1610[118] 7 15 ColSpuVemiert.UT Cnt, 1610[118] 7 17 ColSpuVemiert.UT Cnt, 1610[118] 7 18 ColSpuVemiert.UT Cnt, 1610[118] 7 19 ColSpuVemiert.UT Cnt, 1610[118] 7 10 ColSpuVemiert.UT Cnt, 1610[118] 7 11 ColSpuVemiert.UT Cnt, 1610[118] 7 12 Co		
172 ColSpurVermierLUT_Cnt_s16(0)[11] 196 172 ColSpurVermierLUT_Cnt_s16(0)[12] 229 172 ColSpurVermierLUT_Cnt_s16(0)[13] 261 173 ColSpurVermierLUT_Cnt_s16(0)[14] 294 174 204 294 294 175 ColSpurVermierLUT_Cnt_s16(0)[16] 397 175 ColSpurVermierLUT_Cnt_s16(0)[16] 399 175 ColSpurVermierLUT_Cnt_s16(1)[10] 0 175 ColSpurVermierLUT_Cnt_s16(1)[10] 4 175 ColSpurVermierLUT_Cnt_s16(1)[11] 4 175 ColSpurVermierLUT_Cnt_s16(1)[12] 3 175 ColSpurVermierLUT_Cnt_s16(1)[13] 2 175 ColSpurVermierLUT_Cnt_s16(1)[16] 1 175 ColSpurVermierLUT_Cnt_s16(1)[16] 1 175 ColSpurVermierLUT_Cnt_s16(1)[16] 4 175 ColSpurVermierLUT_Cnt_s16(1)[16] 4 175 ColSpurVermierLUT_Cnt_s16(1)[17] 3 175 ColSpurVermierLUT_Cnt_s16(1)[17] 3 175 ColSpurVermierLUT_Cnt_s16(1)[17] 4 175 ColSpurVermierLUT_Cnt_s16(1)[17] 5 175 ColSpurVermierLUT_Cnt_s16(1)[17] 5 175 ColSpurVermierLUT_Cnt_s16(1)[17] 5 175 ColSpurVermierLUT_Cnt_s16(1)[17] 6 175 ColSpurVermierLUT_Cnt_s16(1)[16] 6 175 ColSpurVermierLUT_Cnt_s16(1)[16] 6 175 ColSpurVermierLUT_Cnt_s16(1)[16] 6 175 ColSpurVermierLUT_Cnt_s16(1)[16] 7 175 ColSpurVermierLUT_Cnt_s16(
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12_ColSpurVernierLUT_Cnt_s16[1][0] 0	T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
12 ColSpurVernierLUT_Cnt_s16[1][1] 4 12 ColSpurVernierLUT_Cnt_s16[1][2] 3 12 ColSpurVernierLUT_Cnt_s16[1][3] 2 12 ColSpurVernierLUT_Cnt_s16[1][3] 2 12 ColSpurVernierLUT_Cnt_s16[1][6] 4 13 ColSpurVernierLUT_Cnt_s16[1][6] 4 14 ColSpurVernierLUT_Cnt_s16[1][6] 4 15 ColSpurVernierLUT_Cnt_s16[1][7] 3 16 ColSpurVernierLUT_Cnt_s16[1][8] 2 17 ColSpurVernierLUT_Cnt_s16[1][8] 2 18 ColSpurVernierLUT_Cnt_s16[1][9] 1 19 ColSpurVernierLUT_Cnt_s16[1][10] 0 10 ColSpurVernierLUT_Cnt_s16[1][11] 4 11 ColSpurVernierLUT_Cnt_s16[1][12] 3 12 ColSpurVernierLUT_Cnt_s16[1][14] 1 13 ColSpurVernierLUT_Cnt_s16[1][14] 1 14 ColSpurVernierLUT_Cnt_s16[1][16] 4 15 ColSpurVernierLUT_Cnt_s16[1][16] 4 16 ColSpurVernierLUT_Cnt_s16[1][16] 4 17 ColSpurVernierLUT_Cnt_s16[2][0] 0 18 ColSpurVernierLUT_Cnt_s16[2][1] 8 19 ColSpurVernierLUT_Cnt_s16[2][2] 6 10 ColSpurVernierLUT_Cnt_s16[2][3] 4 10 ColSpurVernierLUT_Cnt_s16[2][6] 9 11 ColSpurVernierLUT_Cnt_s16[2][6] 9 12 ColSpurVernierLUT_Cnt_s16[2][6] 9 13 ColSpurVernierLUT_Cnt_s16[2][6] 9 14 ColSpurVernierLUT_Cnt_s16[2][6] 9 15 ColSpurVernierLUT_Cnt_s16[2][6] 9 16 ColSpurVernierLUT_Cnt_s16[2][6] 9 17 ColSpurVernierLUT_Cnt_s16[2][6] 9 18 ColSpurVernierLUT_Cnt_s16[2][6] 9 19 ColSpurVernierLUT_Cnt_s16[2][6] 9 10 ColSpurVernierLUT_Cnt_s16[2][6] 9 10 ColSpu	T2_ColSpurVernierLUT_Cnt_s16[0][16]	
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T2_ColSpurVernierLUT_Cnt_s16[1][10] 0 T2_ColSpurVernierLUT_Cnt_s16[1][11] 4 T2_ColSpurVernierLUT_Cnt_s16[1][12] 3 T2_ColSpurVernierLUT_Cnt_s16[1][13] 2 T2_ColSpurVernierLUT_Cnt_s16[1][14] 1 T2_ColSpurVernierLUT_Cnt_s16[1][14] 1 T2_ColSpurVernierLUT_Cnt_s16[1][15] 0 T2_ColSpurVernierLUT_Cnt_s16[1][16] 4 T2_ColSpurVernierLUT_Cnt_s16[2][0] 0 T2_ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 1	T2_ColSpurVernierLUT_Cnt_s16[1][8]	
T2_ColSpurVernierLUT_Cnt_s16[1][11]		
T2_ColSpurVernierLUT_Cnt_s16[1][12] 3 T2_ColSpurVernierLUT_Cnt_s16[1][13] 2 T2_ColSpurVernierLUT_Cnt_s16[1][14] 1 T2_ColSpurVernierLUT_Cnt_s16[1][15] 0 T2_ColSpurVernierLUT_Cnt_s16[1][16] 4 T2_ColSpurVernierLUT_Cnt_s16[2][0] 0 T2_ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][6] 15 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 1		
T2_ColSpurVernierLUT_Cnt_s16[1][13] 2 T2_ColSpurVernierLUT_Cnt_s16[1][14] 1 T2_ColSpurVernierLUT_Cnt_s16[1][15] 0 T2_ColSpurVernierLUT_Cnt_s16[1][16] 4 T2_ColSpurVernierLUT_Cnt_s16[2][0] 0 T2_ColSpurVernierLUT_Cnt_s16[2][0] 8 T2_ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 1		
TZ_ColSpurVernierLUT_Cnt_s16[1][14] 1 TZ_ColSpurVernierLUT_Cnt_s16[1][15] 0 TZ_ColSpurVernierLUT_Cnt_s16[1][16] 4 TZ_ColSpurVernierLUT_Cnt_s16[2][0] 0 TZ_ColSpurVernierLUT_Cnt_s16[2][0] 8 TZ_ColSpurVernierLUT_Cnt_s16[2][1] 8 TZ_ColSpurVernierLUT_Cnt_s16[2][2] 6 TZ_ColSpurVernierLUT_Cnt_s16[2][3] 4 TZ_ColSpurVernierLUT_Cnt_s16[2][4] 7 TZ_ColSpurVernierLUT_Cnt_s16[2][5] 0 TZ_ColSpurVernierLUT_Cnt_s16[2][6] 7 TZ_ColSpurVernierLUT_Cnt_s16[2][7] 7 TZ_ColSpurVernierLUT_Cnt_s16[2][8] 5 TZ_ColSpurVernierLUT_Cnt_s16[2][9] 3 TZ_ColSpurVernierLUT_Cnt_s16[2][9] 3 TZ_ColSpurVernierLUT_Cnt_s16[2][9] 1		
T2_ColSpurVernierLUT_Cnt_s16[1][15] 0 T2_ColSpurVernierLUT_Cnt_s16[1][16] 4 T2_ColSpurVernierLUT_Cnt_s16[2][0] 0 T2_ColSpurVernierLUT_Cnt_s16[2][1] 8 T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 1		
T2_ColSpurVernierLUT_Cnt_s16[1][16]	T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[2][1]	T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 1	T2_ColSpurVernierLUT_Cnt_s16[2][0]	
T2_ColSpurVernierLUT_Cnt_s16[2][3]	T2_ColSpurVernierLUT_Cnt_s16[2][1]	
T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1		
T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1		
T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1		
T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1		
T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1	T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1	T2_ColSpurVernierLUT_Cnt_s16[2][8]	
	T2_ColSpurVernierLUT_Cnt_s16[2][9]	
T2_ColSpurVernierLUT_Cnt_s16[2][11] 10	T2_ColSpurVernierLUT_Cnt_s16[2][10]	
	T2_ColSpurVernierLUT_Cnt_s16[2][11]	10

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DigColPs_Per2	IMACIU
Name	Input Value
Γ2_ColSpurVernierLUT_Cnt_s16[2][12]	8
Γ2_ColSpurVernierLUT_Cnt_s16[2][13]	6
Γ2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
Γ2_ColSpurVernierLUT_Cnt_s16[2][16]	10
Γ2_ColSpurVernierLUT_Cnt_s16[3][0]	1
Γ2_ColSpurVernierLUT_Cnt_s16[3][1]	14
Γ2_ColSpurVernierLUT_Cnt_s16[3][2]	11
Γ2_ColSpurVernierLUT_Cnt_s16[3][3]	8
Γ2_ColSpurVernierLUT_Cnt_s16[3][4]	5
Γ2_ColSpurVernierLUT_Cnt_s16[3][5]	2
2_ColSpurVernierLUT_Cnt_s16[3][6]	15
2_ColSpurVernierLUT_Cnt_s16[3][7]	12
2_ColSpurVernierLUT_Cnt_s16[3][8]	9
2_ColSpurVernierLUT_Cnt_s16[3][9]	6
2_ColSpurVernierLUT_Cnt_s16[3][10]	3
2_ColSpurVernierLUT_Cnt_s16[3][11]	16
2_ColSpurVernierLUT_Cnt_s16[3][12]	13
2_ColSpurVernierLUT_Cnt_s16[3][13]	10
2_ColSpurVernierLUT_Cnt_s16[3][14]	7
2_ColSpurVernierLUT_Cnt_s16[3][15]	4
2_ColSpurVernierLUT_Cnt_s16[3][16]	17
2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
2 DualSpurVernierLUT Cnt s16[0][2]	-324
2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
2_DualSpurVernierLUT_Cnt_s16[0][11]	0
2_DualSpurVernierLUT_Cnt_s16[0][12]	36
2_DualSpurVernierLUT_Cnt_s16[0][13]	72
² _DualSpurVernierLUT_Cnt_s16[0][14]	108
[2_DualSpurVernierLUT_Cnt_s16[0][15]	144
2_DualSpurVernierLUT_Cnt_s16[0][16]	180
	216
⁷ 2_DualSpurVernierLUT_Cnt_s16[0][17]	252
72_DualSpurVernierLUT_Cnt_s16[0][18]	
⁷ 2_DualSpurVernierLUT_Cnt_s16[0][19]	288
² 2_DualSpurVernierLUT_Cnt_s16[0][20]	324
2_DualSpurVernierLUT_Cnt_s16[0][21]	360
2_DualSpurVernierLUT_Cnt_s16[1][0]	9
2_DualSpurVernierLUT_Cnt_s16[1][1]	0
2_DualSpurVernierLUT_Cnt_s16[1][2]	1
2_DualSpurVernierLUT_Cnt_s16[1][3]	2
2_DualSpurVernierLUT_Cnt_s16[1][4]	3
2_DualSpurVernierLUT_Cnt_s16[1][5]	4
2_DualSpurVernierLUT_Cnt_s16[1][6]	5
2_DualSpurVernierLUT_Cnt_s16[1][7]	6
2_DualSpurVernierLUT_Cnt_s16[1][8]	7
2_DualSpurVernierLUT_Cnt_s16[1][9]	8
2_DualSpurVernierLUT_Cnt_s16[1][10]	9
2_DualSpurVernierLUT_Cnt_s16[1][11]	0
2_DualSpurVernierLUT_Cnt_s16[1][12]	1
2_DualSpurVernierLUT_Cnt_s16[1][13]	2
2_DualSpurVernierLUT_Cnt_s16[1][14]	3
2_DualSpurVernierLUT_Cnt_s16[1][15]	4
2_DualSpurVernierLUT_Cnt_s16[1][16]	5
2_DualSpurVernierLUT_Cnt_s16[1][17]	6
2_DualSpurVernierLUT_Cnt_s16[1][18]	7
2_DualSpurVernierLUT_Cnt_s16[1][19]	8
2_DualSpurVernierLUT_Cnt_s16[1][20]	9
	0
2_DualSpurVernierLUT_Cnt_s16[2][0]	0
'2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
2_DualSpurVernierLUT_Cnt_s16[2][3]	3
	4
	4 5

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DigColPs_Per2



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Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12] T3_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13] T3_DualSpurVernierLUT_Cnt_s48[3][14]	2 3		
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2 DualSpurVernierLUT Cnt s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22		
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10] T3_DualSpurVernierLUT_Cnt_s18[3][11]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	46		
k_SkipStepErrDiag_Cnt_str.PStep	49		
k_SkipStepErrDiag_Cnt_str.NStep	17		
k_VernCorrErrorDiag_Cnt_str.Threshold	53		
k_VernCorrErrorDiag_Cnt_str.PStep	26		
k_VernCorrErrorDiag_Cnt_str.NStep	6		
k_VernCorrErrorThresh_Deg_f32	74.78180027		
k_VernOORangeThresh_Deg_f32	1199.291138		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	215 6112807		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 tgt Pim DigColPsEOL.SpurTrim Deg_f32	215.6112897 58.78464067		
tgt_Pim_DigColPsEOL.Spur1rim_Deg_132 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2579		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid Cnt Igc	
tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPos HwDeg f32	tgt_DigColPs_Per2_I2CHwAbsF		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resul
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	1 COU
DigColPs I2CHwColAngleForTrim Deg M f32	1491.31091	1491.3109 ± 0.00048828125	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	
DigColPs_PrevColPos_Deg_M_f32	1489.40503	1489.405 ± 0.0001220703125	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	18	18	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4	4	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4	4	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	591.310913	591.31091 ± 0.0009	•
tot DigCoIPs Per2 TrimComp Cnt Igc.value	0	0	

0

0

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	-
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetCustData()	214
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	241
DigColPs_ColTrimStatic_Deg_M_f32	218.8
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.48
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs PrevColPos Deg M f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	28.9
DigColPs_TrimCompStatic_Cnt_M_u16	880
DigColPs VernCorrDetectAcc Cnt M u16	3
DigColPs_VernierAngleOORange_Cnt_M_Igc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
	0
T2_ColSpurVernierLUT_Cnt_s16[0][5]	32
T2_ColSpurVernierLUT_Cnt_s16[0][6]	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65 98
T2_ColSpurVernierLUT_Cnt_s16[0][8]	
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][14] T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2 DualSpurVernierLUT Cnt s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpur/craigt UT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360 9
T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][2]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
	0
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][11] T2_DualSpurVernierLUT_Cnt_s16[1][12]	1

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4		
T2 DualSpurVernierLUT Cnt s16[2][5]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10		
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0		
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1		
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2		
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3		
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4		
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5		
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6		
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7		
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8		
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9		
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10 22		
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	2		
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4		
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6		
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8		
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10		
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12		
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17 19		
T2_DualSpurVernierLUT_Cnt_s16[3][20]			
T2_DualSpurVernierLUT_Cnt_s16[3][21] k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Ont_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	42		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2109		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPe		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPe		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc	tgt_DigColPs_Per2_TrimComp_0	Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Resu
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs 12CHwColAngleForTrim Deg M f32	1573 44543	1573 445455 + 0 00048828125	

1573.44543

5

DigColPs_I2CHwColAngleForTrim_Deg_M_f32

DigColPs_I2CHwTrimTransCnts_Uls_M_u08

1573.445455 ± 0.00048828125

5





Name	Actual Value	Expected Value	Result
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1581.19995	1581.2 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	15	15	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	673.445435	673.4454545 ± 0.0009	•
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	•
Param	0x0C	0x0C	~
Status	0x01	0x01	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.10 (Repeat Count = 1)	Innuit Value
	Input Value
DigColPsInt_GetCustData()	252
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	239.2
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	55108
DigColPs_I2CHwColAngle_Deg_M_f32	350.877
DigColPs_I2CHwDataType_Cnt_M_u08	0
DigColPs_I2CHwSpurAngle_Cnt_M_u16	51849
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	200.3508072
DigColPs_PrevVernierLevelNo_Cnt_M_u08	11
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	2
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142
DigColPs_SpurTrimStatic_Deg_M_f32	31.1
DigColPs_TrimCompStatic_Cnt_M_u16	952
DigColPs_VernCorrDetectAcc_Cnt_M_u16	4
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359



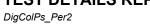


Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][12]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2] T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9] T3_DualSpurVernierLUT_Cnt_s16[0][10]	-72 36
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
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Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6 7
T2_DualSpurVernierLUT_Cnt_s16[1][8] T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10] T3_DualSpurVernierLUT_Cnt_s16[2][41]	10 0
T2_DualSpurVernierLUT_Cnt_s16[2][11] T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11] T3_DualSpurVernierLUT_Cnt_s16[3][42]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T3_DualSpurVernierLUT_Cnt_s16[3][43]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13] T2_DualSpurVernierLUT_Cnt_s16[3][14]	5 7
T2_DualSpurVernierLUT_Cnt_s16[3][14]	9
T2_DualSpurVernierLUT_Cnt_s16[3][15]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	1
k_SkipStepErrDiag_Cnt_str.Threshold	175
k_SkipStepErrDiag_Cnt_str.PStep	12
k_SkipStepErrDiag_Cnt_str.NStep	41
k_VernCorrErrorDiag_Cnt_str.Threshold	48
k_VernCorrErrorDiag_Cnt_str.PStep	12





Name	Input Value		
k_VernCorrErrorThresh_Deg_f32	78.9135704		
k_VernOORangeThresh_Deg_f32	1722.743855		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	350.8777566		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2056		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_0	:nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDe	g_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColDo HwAVorpCorrEquit Cot M Igo	0	0	

tgt_rttc_mst_da_bigdoii 3:i iii_bigdoii 3EOE	tgt_i iii_bigooii acoc		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	476.772736	476.7727273 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	471.677002	471.677 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-428.322998	-428.323 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	~

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	•
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 3.11 (Repeat Count = 1)	▼
Name	Input Value
DigColPsInt_GetCustData()	290
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4_
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2 ColSpurVernierLUT Cnt s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2 ColSpurVernierLUT Cnt s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][11] T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
	2
T2_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2 ColSpurVernierLUT Cnt s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2 DualSpurVernierLUT Cnt s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2 DualSpurVernierLUT Cnt s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10] T2_DualSpurVernierLUT_Cnt_s16[1][11]	9
T2 DualSpurVernierLUT Cnt s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2 DualSpurVernierLUT Cnt s16[2][17]	7
T2 DualSpurVernierLUT Cnt s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	
	1
T2_DualSpurVernierLUT_Cnt_s16[3][12] T2_DualSpurVernierLUT_Cnt_s16[3][13]	1 3 5

2014-10-14, 18:16:06+0530



DigColPs_Per2

DigCoil 3_1 et2			1000
Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	41		
k_SkipStepErrDiag_Cnt_str.PStep	27		
k_SkipStepErrDiag_Cnt_str.NStep	50		
k_VernCorrErrorDiag_Cnt_str.Threshold	85		
k_VernCorrErrorDiag_Cnt_str.PStep	4		
k_VernCorrErrorDiag_Cnt_str.NStep	46		
k_VernCorrErrorThresh_Deg_f32	8.884848118		
k_VernOORangeThresh_Deg_f32	1087.934204		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4242		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsF	PosValid_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF	Pos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc	
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	447.151367	447.1513636 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	460.399994	460.4 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	5	5	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	-
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~

lacksquare				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	-

-452.848633

0x6C

0x0C

0x01

0x6F

0x00

0x00

-452.8486364 ± 0.0009

0x6C

0x0C

0x01

0x6F

0x00

0x00

Test Step 3.12 (Repeat Count = 1)		√
Name	Input Value	
DigColPsInt_GetCustData()	3	
DigColPs_ColParityError_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185	
DigColPs_ColTrimStatic_Deg_M_f32	22	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	16067	
DigColPs_I2CHwColAngle_Deg_M_f32	272.64	
DigColPs_I2CHwDataType_Cnt_M_u08	1	

DigColPs_VernierAngleOORange_Cnt_M_lgc tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value

 $tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value$

NTC Param

Status

Param

Status

NTC

 $tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value$





Name	Input Value
DigColPs_I2CHwSpurAngle_Cnt_M_u16	16937
DigColPs_I2CHwSpurAngle_Deg_M_f32	19.172
DigColPs_I2CHwTrimTransCnts_UIs_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08	15
DigColPs_I2CSensComments_Cnt_M_uoo	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1733.007516
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs SkipStepFltDetectAcc Cnt M u16	13
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124
DigColPs_SpurTrimStatic_Deg_M_f32	47.6
DigColPs_TrimCompStatic_Cnt_M_u16	1492
DigColPs_VernCorrDetectAcc_Cnt_M_u16	15
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131 -99
T2_ColSpurVernierLUT_Cnt_s16[0][2] T2_ColSpurVernierLUT_Cnt_s16[0][3]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2 ColSpurVernierLUT Cnt s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	0
T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T3_ColSpurVernierLUT_Cnt_s16[2][13]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6 4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][3] T2_ColSpurVernierLUT_Cnt_s16[3][4]	5

2014-10-14, 18:16:06+0530



	I
Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2 ColSpurVernierLUT Cnt s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
	7
T2_ColSpurVernierLUT_Cnt_s16[3][14]	
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
	-108
T2_DualSpurVernierLUT_Cnt_s16[0][8]	
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2 DualSpurVernierLUT Cnt s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
	288
T2_DualSpurVernierLUT_Cnt_s16[0][19]	
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2 DualSpurVernierLUT Cnt s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[1][9]	
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1]	
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
LA THISISOHIVARDIAN III I DE CIBIANIA	
T2_DualSpurVernierLUT_Cnt_s16[2][12]	2
T2_DualSpurVernierLUT_Cnt_s16[2][13]	
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][13]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3

2014-10-14, 18:16:06+0530



Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7			
T2 DualSpurVernierLUT Cnt s16[2][19]	8			
T2 DualSpurVernierLUT Cnt s16[2][20]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10			
T2 DualSpurVernierLUT Cnt s16[3][0]	22			
T2 DualSpurVernierLUT Cnt s16[3][1]	2			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4			
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6			
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8			
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2 DualSpurVernierLUT Cnt s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2 DualSpurVernierLUT Cnt s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2 DualSpurVernierLUT Cnt s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
	13			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][18] T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]				
		19		
T2_DualSpurVernierLUT_Cnt_s16[3][21] k_SelectFromColumn_Cnt_lgc		21		
	1			
k_SkipStepErrDiag_Cnt_str.Threshold	80			
k_SkipStepErrDiag_Cnt_str.PStep	43			
k_SkipStepErrDiag_Cnt_str.NStep	7			
k_VernCorrErrorDiag_Cnt_str.Threshold	6			
k_VernCorrErrorDiag_Cnt_str.PStep	27			
k_VernCorrErrorDiag_Cnt_str.NStep	49			
k_VernCorrErrorThresh_Deg_f32	86.69760323			
k_VernOORangeThresh_Deg_f32	1173.76136			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	272.6490288			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	19.17228091			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	621			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPo			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPo			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cr	_		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_C	nt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Resu	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	•	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	968.896362	968.8963636 ± 0.00048828125		
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•	
DigColPs_PrevColPos_Deg_M_f32	970.640015	970.64 ± 0.0001220703125		
DigColPs PrevVernierLevelNo Cnt M u08	9	9		

· · - ·	1		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	968.896362	968.8963636 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	✓
DigColPs_PrevColPos_Deg_M_f32	970.640015	970.64 ± 0.0001220703125	•
DigColPs_PrevVernierLevelNo_Cnt_M_u08	9	9	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	6	6	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	70.6400146	70.64 ± 0.00009	~
tgt DigColPs Per2 TrimComp Cnt Igc.value	0	0	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	-
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	V





Test Step 3.13 (Repeat Count = 1)	V
Name	Input Value
DigColPsInt_GetCustData()	7
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigCoIPs_I2CHwColAngle_Cnt_M_u16 DigCoIPs_I2CHwColAngle_Deg_M_f32	46069 360
DigColPs_12CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	224.1625181 7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	1 tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7] T2_ColSpurVernierLUT_Cnt_s16[0][8]	65 98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	327 359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6] T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4-
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16]	0 4
T2_ColSpurVernierLUT_Cnt_s16[1][10] T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6] T3_ColSpurVernierLUT_Cnt_s16[2][7]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

DigColPs_Per2

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Input Value T2_DualSpurVernierLUT_Cnt_s16[2][7] T2_DualSpurVernierLUT_Cnt_s16[2][8] 8 T2_DualSpurVernierLUT_Cnt_s16[2][9] 9 T2_DualSpurVernierLUT_Cnt_s16[2][10] 10 T2_DualSpurVernierLUT_Cnt_s16[2][11] 0 T2_DualSpurVernierLUT_Cnt_s16[2][12] 1 T2_DualSpurVernierLUT_Cnt_s16[2][13] 2 T2_DualSpurVernierLUT_Cnt_s16[2][14] 3 T2_DualSpurVernierLUT_Cnt_s16[2][15] T2_DualSpurVernierLUT_Cnt_s16[2][16] 5 T2_DualSpurVernierLUT_Cnt_s16[2][17] 6 T2_DualSpurVernierLUT_Cnt_s16[2][18] 7 T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20] 9 T2_DualSpurVernierLUT_Cnt_s16[2][21] 10 22 T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1] 2 T2_DualSpurVernierLUT_Cnt_s16[3][2] 4 T2_DualSpurVernierLUT_Cnt_s16[3][3] 6 T2_DualSpurVernierLUT_Cnt_s16[3][4] 8 T2_DualSpurVernierLUT_Cnt_s16[3][5] 10 T2_DualSpurVernierLUT_Cnt_s16[3][6] 12 T2_DualSpurVernierLUT_Cnt_s16[3][7] 14 T2_DualSpurVernierLUT_Cnt_s16[3][8] 16 T2_DualSpurVernierLUT_Cnt_s16[3][9] 18 T2_DualSpurVernierLUT_Cnt_s16[3][10] 20 T2_DualSpurVernierLUT_Cnt_s16[3][11] 1 T2_DualSpurVernierLUT_Cnt_s16[3][12] 3 T2 DualSpurVernierLUT Cnt s16[3][13] 5 T2_DualSpurVernierLUT_Cnt_s16[3][14] 7 T2_DualSpurVernierLUT_Cnt_s16[3][15] 9 T2_DualSpurVernierLUT_Cnt_s16[3][16] 11 T2 DualSpurVernierLUT Cnt s16[3][17] 13 T2_DualSpurVernierLUT_Cnt_s16[3][18] 15 T2_DualSpurVernierLUT_Cnt_s16[3][19] 17 T2_DualSpurVernierLUT_Cnt_s16[3][20] 19 T2_DualSpurVernierLUT_Cnt_s16[3][21] 21 k_SelectFromColumn_Cnt_lgc 0 k_SkipStepErrDiag_Cnt_str.Threshold 41 27 k_SkipStepErrDiag_Cnt_str.PStep k_SkipStepErrDiag_Cnt_str.NStep 50 85 $k_VernCorrErrorDiag_Cnt_str.Threshold$ k_VernCorrErrorDiag_Cnt_str.PStep k_VernCorrErrorDiag_Cnt_str.NStep 46 8 884848118 $k_VernCorrErrorThresh_Deg_f32$ k_VernOORangeThresh_Deg_f32 100 $tgt_DigColPs_Per2_MecState_Cnt_enum.value$ 360 tgt_Pim_DigColPsEOL.ColTrim_Deg_f32 297 0333536 tgt Pim DigColPsEOL.SpurTrim Deg f32 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16 tgt Rte Inst Sa DigColPs.DigColPs Per2 I2CHwAbsPosValid Cnt Igc tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt Rte Inst Sa DigColPs.DigColPs Per2 MecState Cnt enum tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL **Actual Value Expected Value** Result Name $DigColPs_HwAVernCorrFault_Cnt_M_lgc$ DigColPs_I2CHwColAngleForTrim_Deg_M_f32 163.636185 163.6362029 ± 0.00048828125 DigColPs_I2CHwTrimTransCnts_Uls_M_u08 2 2 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 1 0 ± 0.0001220703125 ${\tt DigColPs_PrevColPos_Deg_M_f32}$ 0 DigColPs_PrevVernierLevelNo_Cnt_M_u08 1 1 DigColPs Reql2CSnsrDataType Cnt M u08 DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 41 41 DigColPs VernCorrDetectAcc Cnt M u16 10 10 DigColPs_VernierAngleOORange_Cnt_M_lgc 1 tgt DigColPs Per2 I2CHwAbsPosValid Cnt Igc.value 0 0 tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value -736.363831 -736.3637971 ± 0.0009 tgt DigColPs Per2 TrimComp Cnt Igc.value

0x6C

0x0E

0x01

0x6F

0x6C

0x0E

0x01

NTC

Param

Status





Name	Actual Value	Expected Value	Result
Param	0x00	0x00	· · · · · · · · · · · · · · · · · · ·
Status	0x00	0x00	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	•
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	54
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs I2CHwSpurAngle Deg M f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2 ColSpurVernierLUT Cnt s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][4]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15 12
T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T3_DualSpurVernierLUT_Cnt_s16[0][11]	-36 0
T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2 DualSpurVernierLUT Cnt s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2 DualSpurVernierLUT Cnt s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9





Input Value
0
1
2
3
5
6
7
8
9
0
0
1 2
3
4
5
6
7
8
9
10
0 1
2
3
4
5
6
7
8
9
10 22
2
4
6
8
10
12
14
16
18 20
1
3
5
7
9
11
13
15
17 19
21
0
41
27
50
85
4
46 9.98/19/19
8.884848118 1087.934204
0
360
297.0333536
1
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_TrimComp_Cnt_lgc





Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	3	3	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	✓
NTC	0x6F	0x6F	~
Param	0x00	0x00	~
Status	0x00	0x00	~

7				~2
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.15 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	101
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.03
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	6
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
	0
T2_ColSpurVernierLUT_Cnt_s16[1][0]	
T2_ColSpurVernierLUT_Cnt_s16[1][1] T0_0s10sus1(string LUT_Out_s10[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21]	324 360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_bualSpurVernierLUT_Cnt_s16[1][1]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2] T3_DualSpurVernierLUT_Cst_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3] T2_DualSpurVernierLUT_Cnt_s16[3][4]	6 8
T2_DualSpurVernierLUT_Cnt_s16[3][4] T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	10
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0

2014-10-14, 18:16:06+0530



DigColPs_Per2 Input Value $k_SkipStepErrDiag_Cnt_str.Threshold$ 41 k_SkipStepErrDiag_Cnt_str.PStep 27 k_SkipStepErrDiag_Cnt_str.NStep 50 $k_VernCorrErrorDiag_Cnt_str.Threshold$ 85 k_VernCorrErrorDiag_Cnt_str.PStep 46 k_VernCorrErrorDiag_Cnt_str.NStep k_VernCorrErrorThresh_Deg_f32 3 100 k_VernOORangeThresh_Deg_f32 tgt_DigColPs_Per2_MecState_Cnt_enum.value $tgt_Pim_DigColPsEOL.ColTrim_Deg_f32$ 360 tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 297.0333536 $tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16$ $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc$ tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32$ tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32 tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum tgt_DigColPs_Per2_MecState_Cnt_enum tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL tgt_Pim_DigColPsEOL

tgt_tte_mst_sa_bigcoir s.Fim_bigcoir sLOL	tgt_Filli_DigColF3LOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.634827	163.6348393 ± 0.00048828125	✓
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4	4	✓
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	41	41	~
DigCoIPs_VernCorrDetectAcc_Cnt_M_u16	10	10	✓
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.365173	-736.3651607 ± 0.0009	✓
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	✓
NTC	0x6C	0x6C	~
Param	0x0E	0x0E	✓
Status	0x01	0x01	~
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.16 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetCustData()	148
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4

2014-10-14, 18:16:06+0530



Name	
DigCoIPs_SpurSensorFaultAc_Cnt_M_u16 DigCoIPs_SpurTrinStatic_Deg_M_132 33.3 33	
DigCoIPs_SpurTrimStatic_Deg_M_f32 33.3 DigCoIPs_VermCorrDetectAcc_Cnt_M_u16 1024 DigCoIPs_VermCorrDetectAcc_Cnt_M_u16 6 DigCoIPs_VermicrAnglecOORange_Cnt_M_lgc 1 Rts_Inst_Sa_DigCoIPs tgt_Rts_Inst_Sa_DigCoIPs T2_CoISpurVermierLUT_Cnt_sf6[0][0] -163 T2_CoISpurVermierLUT_Cnt_sf6[0][2] -99 T2_CoISpurVermierLUT_Cnt_sf6[0][3] -66 T2_CoISpurVermierLUT_Cnt_sf6[0][4] -33 T2_CoISpurVermierLUT_Cnt_sf6[0][6] 32 T2_CoISpurVermierLUT_Cnt_sf6[0][6] 32 T2_CoISpurVermierLUT_Cnt_sf6[0][8] 98 T2_CoISpurVermierLUT_Cnt_sf6[0][9] 130 T2_CoISpurVermierLUT_Cnt_sf6[0][9] 130 T2_CoISpurVermierLUT_Cnt_sf6[0][10] 163 T2_CoISpurVermierLUT_Cnt_sf6[0][11] 196 T2_CoISpurVermierLUT_Cnt_sf6[0][11] 196 T2_CoISpurVermierLUT_Cnt_sf6[0][14] 294 T2_CoISpurVermierLUT_Cnt_sf6[0][16] 327 T2_CoISpurVermierLUT_Cnt_sf6[0][16] 359 T2_CoISpurVermierLUT_Cnt_sf6[0][16] 359 T2_CoISpurVermierLUT_Cnt_sf6[0][16] 359 T2_CoISpurVermierLUT_Cnt_sf6[0][16] 359 T2_CoISpurVermierLUT_Cnt_sf6[1][1] 4 T2_CoISpurVermierLUT_Cnt_sf6[1][1] 6 T2_CoISpurVermierLUT_Cnt_sf6[1][1] 6 T2_CoISpurVermierLUT_Cnt_sf6[1][1] 6 T2_CoISpurVermierLUT_Cnt_sf6[1][1] 7	
DigCoIPs_TrimCompStatic_Cnt_M_u16	
DigCoIPs_VernicorDetectAcc_Cnt_M_u16 6 DigCoIPs_VernicarAngleOORange_Cnt_M_lgc 1 Rte_Inst_Sa_DigCoIPs tgt_Rte_Inst_Sa_DigCoIPs T2_CoISpurVernierLUT_Cnt_s16[0][0] -163 T2_CoISpurVernierLUT_Cnt_s16[0][1] -131 T2_CoISpurVernierLUT_Cnt_s16[0][2] -99 T2_CoISpurVernierLUT_Cnt_s16[0][3] -66 T2_CoISpurVernierLUT_Cnt_s16[0][4] -33 T2_CoISpurVernierLUT_Cnt_s16[0][6] 32 T2_CoISpurVernierLUT_Cnt_s16[0][6] 32 T2_CoISpurVernierLUT_Cnt_s16[0][7] 65 T2_CoISpurVernierLUT_Cnt_s16[0][8] 98 T2_CoISpurVernierLUT_Cnt_s16[0][9] 130 T2_CoISpurVernierLUT_Cnt_s16[0][10] 163 T2_CoISpurVernierLUT_Cnt_s16[0][11] 196 T2_CoISpurVernierLUT_Cnt_s16[0][12] 229 T2_CoISpurVernierLUT_Cnt_s16[0][14] 294 T2_CoISpurVernierLUT_Cnt_s16[0][15] 327 T2_CoISpurVernierLUT_Cnt_s16[0][16] 359 T2_CoISpurVernierLUT_Cnt_s16[0][16] 359 T2_CoISpurVernierLUT_Cnt_s16[0][16] 372 T2_CoISpurVernierLUT_Cnt_s16[0][16] 373 T2_CoISpurVernierLUT_Cnt_s16[0][16] 373 T2_CoISpurVernierLUT_Cnt_s16[0][16] 374 T2_CoISpurVernierLUT_Cnt_s16[0][16] 375 T2_COISpurVernierLUT_Cnt_	
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T2_ColSpurVernierLUT_Cnt_s16[0][4] -33 T2_ColSpurVernierLUT_Cnt_s16[0][5] 0 T2_ColSpurVernierLUT_Cnt_s16[0][6] 32 T2_ColSpurVernierLUT_Cnt_s16[0][7] 65 T2_ColSpurVernierLUT_Cnt_s16[0][8] 98 T2_ColSpurVernierLUT_Cnt_s16[0][9] 130 T2_ColSpurVernierLUT_Cnt_s16[0][10] 163 T2_ColSpurVernierLUT_Cnt_s16[0][11] 196 T2_ColSpurVernierLUT_Cnt_s16[0][12] 229 T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 2 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][1] 1 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][6] 1 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
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T2_ColSpurVernierLUT_Cnt_s16[0][13] 261 T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[0][14] 294 T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[0][15] 327 T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[0][16] 359 T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][0] 0 T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][1] 4 T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][2] 3 T2_ColSpurVernierLUT_Cnt_s16[1][3] 2 T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][4] 1 T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][5] 0 T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][6] 4	
_ ' '	
T2_ColSpurVernierLUT_Cnt_s16[1][7] 3	
T2_ColSpurVernierLUT_Cnt_s16[1][8] 2	
T2_ColSpurVernierLUT_Cnt_s16[1][9] 1	
T2_ColSpurVernierLUT_Cnt_s16[1][10] 0	
T2_ColSpurVernierLUT_Cnt_s16[1][11] 4	
T2_ColSpurVernierLUT_Cnt_s16[1][12] 3	
T2_ColSpurVernierLUT_Cnt_s16[1][13] 2 T5_ColSpurVernierLUT_Cnt_s16[1][13]	
T2_ColSpurVernierLUT_Cnt_s16[1][14]	
T2 ColSpurVernierLUT Cnt s16[1][16] 4	
T2_ColSpurVernierLUT_Cnt_s16[2][0] 0	
T2 ColSpurVernierLUT Cnt s16[2][1] 8	
T2_ColSpurVernierLUT_Cnt_s16[2][2] 6	
T2_ColSpurVernierLUT_Cnt_s16[2][3] 4	
T2_ColSpurVernierLUT_Cnt_s16[2][4] 2	
T2_ColSpurVernierLUT_Cnt_s16[2][5] 0	
T2_ColSpurVernierLUT_Cnt_s16[2][6] 9	
T2_ColSpurVernierLUT_Cnt_s16[2][7] 7	
T2_ColSpurVernierLUT_Cnt_s16[2][8] 5	
T2_ColSpurVernierLUT_Cnt_s16[2][9] 3	
T2_ColSpurVernierLUT_Cnt_s16[2][10] 1	
T2_ColSpurVernierLUT_Cnt_s16[2][11] 10	
T2_ColSpurVernierLUT_Cnt_s16[2][12] 8	
T2_ColSpurVernierLUT_Cnt_s16[2][13] 6	
T2_ColSput/craint_LIT_Cot_cs[6]2][14] 4 T3_ColSput/craint_LIT_Cot_cs[6]2][15]	
T2_ColSpur/versied_LIT_Cot_e16(2)[15] 2 T3_ColSpur/versied_LIT_Cot_e16(2)[16] 10	
T2_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 1	
T2_ColSpurVernierLUT_Cnt_s16[3][1] 14	
T2_ColSpurVernierLUT_Cnt_s16[3][2] 11	
T2_ColSpurVernierLUT_Cnt_s16[3][3] 8	
T2_ColSpurVernierLUT_Cnt_s16[3][4] 5	
T2_ColSpurVernierLUT_Cnt_s16[3][5] 2	
T2_ColSpurVernierLUT_Cnt_s16[3][6] 15	
T2_ColSpurVernierLUT_Cnt_s16[3][7] 12	
T2_ColSpurVernierLUT_Cnt_s16[3][8] 9	
T2_ColSpurVernierLUT_Cnt_s16[3][9] 6	
T2_ColSpurVernierLUT_Cnt_s16[3][10] 3	
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7	

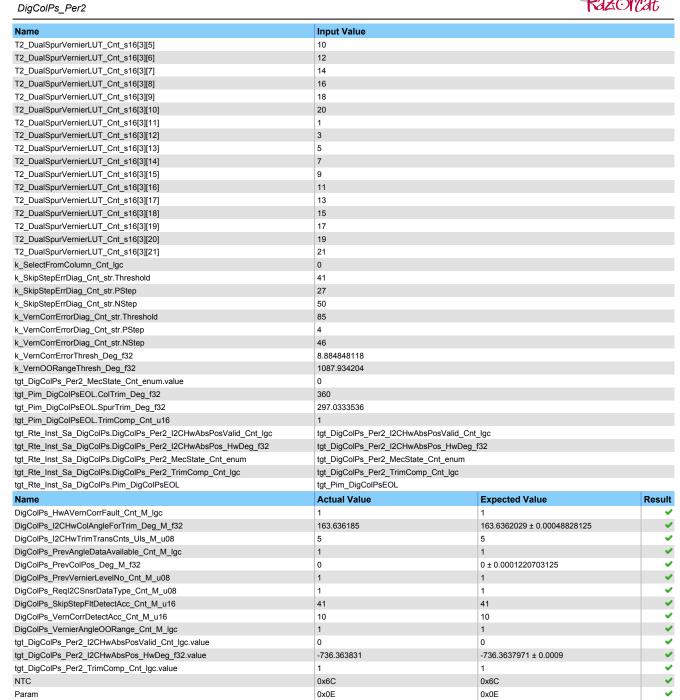
DigColPs_Per2



	Input Value 4 17 -396 -360 -324 -288 -252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 1 2 3 4
T2_DualSpurVernierLUT_Cnt_s16[3][16]	17 -396 -360 -324 -288 -252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396 -360 -324 -288 -252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360 -324 -288 -252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324 -288 -252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][3] 2_DualSpurVernierLUT_Cnt_s16[0][4] 2_DualSpurVernierLUT_Cnt_s16[0][5] 2_DualSpurVernierLUT_Cnt_s16[0][6] 2_DualSpurVernierLUT_Cnt_s16[0][7] 2_DualSpurVernierLUT_Cnt_s16[0][8] 2_DualSpurVernierLUT_Cnt_s16[0][9] 2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][19] 2_DualSpurVernierLUT_Cnt_s16[1][10]	-288 -252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
### T2_DualSpurVernierLUT_Cnt_s16[0][4] ### T2_DualSpurVernierLUT_Cnt_s16[0][5] ### T2_DualSpurVernierLUT_Cnt_s16[0][6] ### T2_DualSpurVernierLUT_Cnt_s16[0][7] ### T2_DualSpurVernierLUT_Cnt_s16[0][8] ### T2_DualSpurVernierLUT_Cnt_s16[0][9] ### T2_DualSpurVernierLUT_Cnt_s16[0][10] ### T2_DualSpurVernierLUT_Cnt_s16[0][11] ### T2_DualSpurVernierLUT_Cnt_s16[0][12] ### T2_DualSpurVernierLUT_Cnt_s16[0][13] ### T2_DualSpurVernierLUT_Cnt_s16[0][14] ### T2_DualSpurVernierLUT_Cnt_s16[0][15] ### T2_DualSpurVernierLUT_Cnt_s16[0][16] ### T2_DualSpurVernierLUT_Cnt_s16[0][17] ### T2_DualSpurVernierLUT_Cnt_s16[0][18] ### T2_DualSpurVernierLUT_Cnt_s16[0][19] ### T2_DualSpurVernierLUT_Cnt_s16[0][20] ### T2_DualSpurVernierLUT_Cnt_s16[1][0] ### T2_DualSpurVernierLUT_Cnt_s16[1][1] ### T2_DualSpurVernierLUT_	-252 -216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][5] 2_DualSpurVernierLUT_Cnt_s16[0][6] 2_DualSpurVernierLUT_Cnt_s16[0][7] 2_DualSpurVernierLUT_Cnt_s16[0][8] 2_DualSpurVernierLUT_Cnt_s16[0][9] 2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][19] 2_DualSpurVernierLUT_Cnt_s16[1][19]	-216 -180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][6] 2_DualSpurVernierLUT_Cnt_s16[0][7] 2_DualSpurVernierLUT_Cnt_s16[0][8] 2_DualSpurVernierLUT_Cnt_s16[0][9] 2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9]	-180 -144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][7] 2_DualSpurVernierLUT_Cnt_s16[0][8] 2_DualSpurVernierLUT_Cnt_s16[0][9] 2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9]	-144 -108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
T2_DualSpurVernierLUT_Cnt_s16[0][8] T2_DualSpurVernierLUT_Cnt_s16[0][9] T2_DualSpurVernierLUT_Cnt_s16[0][10] T2_DualSpurVernierLUT_Cnt_s16[0][11] T2_DualSpurVernierLUT_Cnt_s16[0][12] T2_DualSpurVernierLUT_Cnt_s16[0][13] T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16] T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][17] T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20] T2_DualSpurVernierLUT_Cnt_s16[0][21] T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][0] T2_DualSpurVernierLUT_Cnt_s16[1][1]	-108 -72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][9] 2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	-72 -36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	-36 0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][10] 2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][11] 2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	0 36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][12] 2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	36 72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][13] 2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	72 108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][14] 2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	108 144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][15] 2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	144 180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][16] 2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	180 216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][17] 2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	216 252 288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][18] 2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	252 288 324 360 9 0 1 2 3
2_DualSpurVernierLUT_Cnt_s16[0][19] 2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	288 324 360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[0][20] 2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	324 360 9 0 1 2 3
2_DualSpurVernierLUT_Cnt_s16[0][21] 2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	360 9 0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[1][0] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	9 0 1 2 3 4
"2_DualSpurVernierLUT_Cnt_s16[1][1] "2_DualSpurVernierLUT_Cnt_s16[1][2] "2_DualSpurVernierLUT_Cnt_s16[1][3] "2_DualSpurVernierLUT_Cnt_s16[1][4] "2_DualSpurVernierLUT_Cnt_s16[1][5] "2_DualSpurVernierLUT_Cnt_s16[1][6] "2_DualSpurVernierLUT_Cnt_s16[1][7] "2_DualSpurVernierLUT_Cnt_s16[1][8] "2_DualSpurVernierLUT_Cnt_s16[1][9] "2_DualSpurVernierLUT_Cnt_s16[1][9]	0 1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[1][2] 2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][9]	1 2 3 4
2_DualSpurVernierLUT_Cnt_s16[1][3] 2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][1]	2 3 4
2_DualSpurVernierLUT_Cnt_s16[1][4] 2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][10]	3 4
2_DualSpurVernierLUT_Cnt_s16[1][5] 2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][1] 2_DualSpurVernierLUT_Cnt_s16[1][10]	4
2_DualSpurVernierLUT_Cnt_s16[1][6] 2_DualSpurVernierLUT_Cnt_s16[1][7] 2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	
"2_DualSpurVernierLUT_Cnt_s16[1][7] "2_DualSpurVernierLUT_Cnt_s16[1][8] "2_DualSpurVernierLUT_Cnt_s16[1][9] "2_DualSpurVernierLUT_Cnt_s16[1][10]	5
2_DualSpurVernierLUT_Cnt_s16[1][8] 2_DualSpurVernierLUT_Cnt_s16[1][9] 2_DualSpurVernierLUT_Cnt_s16[1][10]	· ·
"2_DualSpurVernierLUT_Cnt_s16[1][9] "2_DualSpurVernierLUT_Cnt_s16[1][10]	6
2_DualSpurVernierLUT_Cnt_s16[1][10]	7
	8
2 DualSpurVernierI LIT Cot e19(4)(44)	9
⁻ 2_DualSpurVernierLUT_Cnt_s16[1][11]	0
[2_DualSpurVernierLUT_Cnt_s16[1][12]	1
[2_DualSpurVernierLUT_Cnt_s16[1][13]	2
[2_DualSpurVernierLUT_Cnt_s16[1][14]	3
	4
	5
	6
	7
	8
	9
	0
	0
_ , ,	
	1
	2
	3
	4
	5
	6
	7
	8
	9
	10
	0
	1
	2
2_DualSpurVernierLUT_Cnt_s16[2][14]	3
2_DualSpurVernierLUT_Cnt_s16[2][15]	4
2_DualSpurVernierLUT_Cnt_s16[2][16]	5
	6
	7
	8
	9
_ ,	10
	22
	2
	4
	6
	8

2014-10-14, 18:16:06+0530





Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	

0x01

0x6F

0x00

0x00

0x01

0x6F

0x00

0x00

Status

NTC

Param

Status

2014-10-14, 18:16:06+0530





Test Step 3.17 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetCustData()	195
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs_I2CHwSpurAngle_Deg_M_f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	

 ${\sf DigColPs_SkipStepFltDetectAcc_Cnt_M_u16}$

2014-10-14, 18:16:06+0530



Nama	Input Value
Name T2_ColSpurVernierLUT_Cnt_s16[2][12]	Input Value 8
T2 ColSpurVernierLUT Cnt s16[2][12]	6
	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
12_Buaiopui veriici Eo 1_citt_310[2][o]	
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
	4 5

2014-10-14, 18:16:06+0530





Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9			
	10			
T2_DualSpurVernierLUT_Cnt_s16[2][10]				
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0			
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1			
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2			
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3			
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4			
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5			
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6			
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22			
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4			
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6			
	8			
T2_DualSpurVernierLUT_Cnt_s16[3][4]				
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1			
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]		15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	0			
k_SkipStepErrDiag_Cnt_str.Threshold	41			
k_SkipStepErrDiag_Cnt_str.PStep	40			
k_SkipStepErrDiag_Cnt_str.NStep	50			
k_VernCorrErrorDiag_Cnt_str.Threshold	85			
k_VernCorrErrorDiag_Cnt_str.PStep	4			
k VernCorrErrorDiag Cnt str.NStep	46			
k_VernCorrErrorThresh_Deg_f32	8.884848118			
k_VernOORangeThresh_Deg_f32	1087.934204			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0			
tqt Pim DigColPsEOL.ColTrim Deg f32	360			
= = ==				
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsF			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsF			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_0	Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	Jount	
DigColPs_12CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125		
DigCoIPs_I2CHwCoiAnglerolTilli_Deg_M_I32 DigCoIPs I2CHwTrimTransCnts Uls M u08	0	0		
			•	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1		
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	41	41	~	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	10	10	~	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	_	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	·	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1		
		0x6C	•	
NTC	0x6C		Ž	
Param	0x06	0x06	Y	

0x01

0x6F

0x01

0x6F

Status

NTC





Name	Actual Value	Expected Value	Result
Param	0x00	0x00	~
Status	0x00	0x00	•

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	•

Test Step 3.18 (Repeat Count = 1)	
Name	Input Value
DigColPsInt GetCustData()	242
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124
DigColPs_ColTrimStatic_Deg_M_f32	259.6
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs_I2CColSensorFault_Cnt_M_Igc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	46069
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs I2CHwDataType Cnt M u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	29552
DigColPs I2CHwSpurAngle Deg M f32	297.033
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1
DigColPs_I2CSensCommFlts_Cnt_M_u08	9
DigColPs I2CSpurSensorFault Cnt M Igc	0
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32	224.1625181
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	4
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte Inst Sa DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_GolSpurVernierLUT_Cnt_s16[0][15]	327
T2 ColSpurVernierLUT Cnt s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[0][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
12_00/0pur verificize 1_0/1(_010[1][10]	

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Name	
T2_ColSpurVernierLUT_Cnt_s16[1][12] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][15] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[
T2_ColSpurVernierLUT_Cnt_s16{ st } 1	
T2_ColSpurVernierLUT_Cnt_s16[1][14] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[1][16] T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] T3_ColSpurVernierLUT_Cnt_s16[2][7] T4_ColSpurVernierLUT_Cnt_s16[2][8] T5_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][6] T3_ColSpurVernierL	
T2_ColSpurVernierLUT_Cnt_s16[1][15]	
T2_ColSpurVernierLUT_Cnt_s16[1][16]	
T2_ColSpurVernierLUT_Cnt_s16[2][0] T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][6] T2_ColSpurVernierLUT_Cnt_s16[2][7] T2_ColSpurVernierLUT_Cnt_s16[2][8] T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[2][16] T2_ColSpurVernierLUT_Cnt_s16[3][0] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][1] T2_ColSpurVernierLUT_Cnt_s16[3][2] T1_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7] T2_ColSpurVernierLUT_Cnt_s16[3][6] T2_ColSpurVernierLUT_Cnt_s16[3][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][1] 12_ColSpurVernierLUT_Cnt_s16[2][2] 6 T2_ColSpurVernierLUT_Cnt_s16[2][3] 4 T2_ColSpurVernierLUT_Cnt_s16[2][4] 2 T2_ColSpurVernierLUT_Cnt_s16[2][5] 0 T2_ColSpurVernierLUT_Cnt_s16[2][6] 9 T2_ColSpurVernierLUT_Cnt_s16[2][7] 7 T2_ColSpurVernierLUT_Cnt_s16[2][8] 5 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][9] 3 T2_ColSpurVernierLUT_Cnt_s16[2][10] 1 T2_ColSpurVernierLUT_Cnt_s16[2][11] 10 T2_ColSpurVernierLUT_Cnt_s16[2][12] 8 T2_ColSpurVernierLUT_Cnt_s16[2][13] 6 T2_ColSpurVernierLUT_Cnt_s16[2][14] 4 T2_ColSpurVernierLUT_Cnt_s16[2][15] 72_ColSpurVernierLUT_Cnt_s16[2][16] 10 T2_ColSpurVernierLUT_Cnt_s16[3][0] 11 T2_ColSpurVernierLUT_Cnt_s16[3][0] 12_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][6] 16 T2_ColSpurVernierLUT_Cnt_s16[3][6] 17 T2_ColSpurVernierLUT_Cnt_s16[3][6] 18 T2_ColSpurVernierLUT_Cnt_s16[3][6] 19 T2_ColSpurVernierLUT_Cnt_s16[3][6] 10 T2_ColSpurVernierLUT_Cnt_s16[3][6] 11 T2_ColSpurVernierLUT_Cnt_s16[3][6] 12 T2_ColSpurVernierLUT_Cnt_s16[3][6] 13	
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T2_ColSpurVernierLUT_Cnt_s16[3][0]	
T2_ColSpurVernierLUT_Cnt_s16[3][1] 14 T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][2] 11 T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][3] 8 T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][4] 5 T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][5] 2 T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][6] 15 T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][7] 12 T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][8] 9 T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][9] 6 T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][10] 3 T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][11] 16	
T2_ColSpurVernierLUT_Cnt_s16[3][12] 13	
T2_ColSpurVernierLUT_Cnt_s16[3][13] 10	
T2_ColSpurVernierLUT_Cnt_s16[3][14] 7	
T2_ColSpurVernierLUT_Cnt_s16[3][15] 4	
T2_ColSpurVernierLUT_Cnt_s16[3][16] 17	
T2_DualSpurVernierLUT_Cnt_s16[0][0] -396	
T2_DualSpurVernierLUT_Cnt_s16[0][1] -360	
T2_DualSpurVernierLUT_Cnt_s16[0][2] -324	
T2_DualSpurVernierLUT_Cnt_s16[0][3] -288	
T2_DualSpurVernierLUT_Cnt_s16[0][4] -252	
T2 DualSpurVernierLUT Cnt s16[0][5] -216	
T2_DualSpurVernierLUT_Cnt_s16[0][6] -180	
T2_DualSpurVernierLUT_Cnt_s16[0][7] -144	
T2_DualSpurVernierLUT_Cnt_s16[0][8] -108	
T2_DualSpurVernierLUT_Cnt_s16[0][9] -72	
T2_DualSpurVernierLUT_Cnt_s16[0][10] -36	
T2_DualSpurVernierLUT_Cnt_s16[0][11] 0	
T2_DualSpurVernierLUT_Cnt_s16[0][12] 36	
T2_DualSpurVernierLUT_Cnt_s16[0][13] 72	
T2_DualSpurVernierLUT_Cnt_s16[0][13]	
T2_DualSpurVernierLUT_Cnt_s16[0][16] 180	
T2_DualSpurVernierLUT_Cnt_s16[0][17] 216	
T2_DualSpurVernierLUT_Cnt_s16[0][18] 252	
T2_DualSpurVernierLUT_Cnt_s16[0][19] 288	
T2_DualSpurVernierLUT_Cnt_s16[0][20] 324	
T2_DualSpurVernierLUT_Cnt_s16[0][21] 360	
T2_DualSpurVernierLUT_Cnt_s16[1][0] 9	
T2_DualSpurVernierLUT_Cnt_s16[1][1] 0	
T2_DualSpurVernierLUT_Cnt_s16[1][2] 1	
T2_DualSpurVernierLUT_Cnt_s16[1][3] 2	
T2_DualSpurVernierLUT_Cnt_s16[1][4] 3	
T2_DualSpurVernierLUT_Cnt_s16[1][5] 4	
T2_DualSpurVernierLUT_Cnt_s16[1][6] 5	
T2_DualSpurVernierLUT_Cnt_s16[1][7] 6	
T2_DualSpurVernierLUT_Cnt_s16[1][8] 7	
T2_DualSpurVernierLUT_Cnt_s16[1][9] 8	
T2_DualSpurVernierLUT_Cnt_s16[1][10] 9	

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2 DualSpurVernierLUT Cnt s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2 DualSpurVernierLUT Cnt s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2 3
T2_DualSpurVernierLUT_Cnt_s16[2][14] T2_DualSpurVernierLUT_Cnt_s16[2][15]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][15]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][17]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19] T3_DualSpurVernierLUT_Cnt_s16[3][20]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	19 21
k SelectFromColumn Cnt lgc	0
k_Selectr-romcolumn_Cnt_gc k_SkipStepErrDiag_Cnt_str.Threshold	41
k_SkipStepErrDiag_Cnt_str.PStep	27
k_SkipStepErrDiag_Cnt_str.NStep	50
k_VernCorrErrorDiag_Cnt_str.Threshold	85
k_VernCorrErrorDiag_Cnt_str.PStep	4
k_VernCorrErrorDiag_Cnt_str.NStep	46
k_VernCorrErrorThresh_Deg_f32	3
k_VernOORangeThresh_Deg_f32	1087.934204
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	297.0333536
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum
	tot DiaColDa Dara TrimComp Cot Iga
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	163.636185	163.6362029 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	0	0	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1	✓
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevVernierLevelNo_Cnt_M_u08	1	1	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	24	24	✓
DigColPs_VernCorrDetectAcc_Cnt_M_u16	24	24	•
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-736.363831	-736.3637971 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	1	1	~
NTC	0x6C	0x6C	~
Param	0x0C	0x0C	~
Status	0x01	0x01	✓
NTC	0x6F	0x6F	~
Param	0x00	0x00	~
Status	0x00	0x00	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	2	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.19 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt GetCustData()	289
DigColPs ColParityError Cnt M Igc	0
DigColPs ColSensorFaultAcc Cnt M u16	124
DigColPs ColTrimStatic Deg M f32	259.6
DigColPs HwAVernCorrFault Cnt M Igc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	164
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	54257
DigColPs_I2CHwSpurAngle_Deg_M_f32	250.48
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	2
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1593.059906
DigColPs_PrevVernierLevelNo_Cnt_M_u08	7
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	17
DigColPs_SpurParityError_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1
DigColPs_SpurTrimStatic_Deg_M_f32	33.3
DigColPs_TrimCompStatic_Cnt_M_u16	1024
DigColPs_VernCorrDetectAcc_Cnt_M_u16	3
DigColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2 ColSpurVernierLUT Cnt s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
	9
T2_ColSpurVernierLUT_Cnt_s16[3][8]	6
T2_ColSpurVernierLUT_Cnt_s16[3][9] T3_ColSpurVernierLUT_Cnt_s16[3][40]	3
T2_ColSpurVernierLUT_Cnt_s16[3][10] T2_ColSpurVernierLUT_Cnt_s16[3][11]	3 16
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2_ColSpurVernierLUT_Cnt_s16[3][12]	
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
	108
T2_DualSpurVernierLUT_Cnt_s16[0][14]	100

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
	9
T2_DualSpurVernierLUT_Cnt_s16[1][0]	
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2 DualSpurVernierLUT Cnt s16[1][4]	3
T2 DualSpurVernierLUT Cnt s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
	9
T2_DualSpurVernierLUT_Cnt_s16[1][10]	
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[2][2]	
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2 DualSpurVernierLUT Cnt s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc	0

DigColPs_Per2



Name	Input Value		
k_SkipStepErrDiag_Cnt_str.Threshold	16		
k_SkipStepErrDiag_Cnt_str.PStep	4		
k_SkipStepErrDiag_Cnt_str.NStep	47		
k_VernCorrErrorDiag_Cnt_str.Threshold	98		
k_VernCorrErrorDiag_Cnt_str.PStep	3		
k_VernCorrErrorDiag_Cnt_str.NStep	42		
k_VernCorrErrorThresh_Deg_f32	99.41426611		
k_VernOORangeThresh_Deg_f32	359.5822154		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	250.4857173		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2109		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

tgt_tte_mst_sa_bigcoirs.rim_bigcoirscoc	tgt_Filli_DigColFSLOL	tgt_Fiii_DigCoiFsECE		
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	98.7181778	98.71818182 ± 0.00048828125	•	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	1	1	•	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	•	
DigColPs_PrevColPos_Deg_M_f32	100.399994	100.4 ± 0.0001220703125	•	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	✓	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	•	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	•	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	•	
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	✓	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	✓	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-801.281799	-801.2818182 ± 0.0009	✓	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	✓	
NTC	0x6C	0x6C	~	
Param	0x0C	0x0C	✓	
Status	0x01	0x01	~	

T .				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Test Step 3.20 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetCustData()	336	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_ColTrimStatic_Deg_M_f32	0	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	
DigColPs_I2CHwColAngle_Cnt_M_u16	0	
DigColPs_I2CHwColAngle_Deg_M_f32	0	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0	
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	0	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	0	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	
DigColPs_SpurParityError_Cnt_M_lgc	0	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	
DigColPs_SpurTrimStatic_Deg_M_f32	0	

2014-10-14, 18:16:06+0530



Name	Input Value
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99 -66
T2_ColSpurVernierLUT_Cnt_s16[0][3]	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33 0
T2_ColSpurVernierLUT_Cnt_s16[0][5]	
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65 98
T2_ColSpurVernierLUT_Cnt_s16[0][8]	130
T2_ColSpurVernierLUT_Cnt_s16[0][9]	163
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLOT_Cnt_s16[0][11] T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
	327
T2_ColSpurVernierLUT_Cnt_s16[0][15] T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[0][10] T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][2] T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][3] T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][4] T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2 ColSpurVernierLUT Cnt s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2 ColSpurVernierLUT Cnt s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
	16
12 ColSpurVernierLUT Cnt s16[3][11]	
T2_ColSpurVernierLUT_Cnt_s16[3][11] T2 ColSpurVernierLUT Cnt s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13 10
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	13 10 7
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13] T2_ColSpurVernierLUT_Cnt_s16[3][14]	10 7
T2_ColSpurVernierLUT_Cnt_s16[3][12] T2_ColSpurVernierLUT_Cnt_s16[3][13]	10

2014-10-14, 18:16:06+0530



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	

2014-10-14, 18:16:06+0530



Name	Input Value		
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16		
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18		
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3		
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5		
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7		
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9		
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11		
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13		
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15		
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17		
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19		
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21		
k_SelectFromColumn_Cnt_lgc	0		
k_SkipStepErrDiag_Cnt_str.Threshold	10		
k_SkipStepErrDiag_Cnt_str.PStep	0		
k_SkipStepErrDiag_Cnt_str.NStep	0		
k_VernCorrErrorDiag_Cnt_str.Threshold	0		
k_VernCorrErrorDiag_Cnt_str.PStep	0		
k_VernCorrErrorDiag_Cnt_str.NStep	0		
k_VernCorrErrorThresh_Deg_f32	1		
k_VernOORangeThresh_Deg_f32	100		
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt_lgc	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum	
$tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc$	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result

igi_Rie_ilisi_3a_DigColFs.Filii_DigColFsEOL	tgt_Fiii1_DigColFSEOL		
Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	•
DigCoIPs_I2CHwTrimTransCnts_Uls_M_u08	2	2	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	2	2	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	•
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~
NTC	0x6C	0x6C	~
Param	0x00	0x00	~
Status	0x00	0x00	•
NTC	0x6E	0x6E	~
Param	0x00	0x00	•
Status	0x01	0x01	~
NTC	0x6F	0x6F	•
Param	0x00	0x00	~
Status	0x00	0x00	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	•
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	•
VernierLookup	1	VernierLookup	1	•
DiagnosticThreshold	1	DiagnosticThreshold	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	•
Rte Call DigColPs Per2 CP1 CheckpointReached	1	Rte Call DigColPs Per2 CP1 CheckpointReached	1	_



Test Step 3.21 (Repeat Count = 1)	V
Name	Input Value
DigColPsInt_GetCustData()	383
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigCoIPs_I2CHwColAngle_Cnt_M_u16 DigCoIPs_I2CHwColAngle_Deg_M_f32	0
DigColPs_12CHwDataType_Cnt_M_u08	3
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CHwSpurAngle_Deg_M_f32	0
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	4
DigColPs_I2CSensCommFlts_Cnt_M_u08	0
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0
DigColPs_PrevColPos_Deg_M_f32 DigColPs_PrevVernierLevelNo_Cnt_M_u08	0
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0
DigColPs SpurParityError Cnt M Igc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0
DigColPs_SpurTrimStatic_Deg_M_f32	0
DigColPs_TrimCompStatic_Cnt_M_u16	0
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0
DigColPs_VernierAngleOORange_Cnt_M_lgc	0
Rte_Inst_Sa_DigColPs T3_ColCourt (orginal LIT_Cost_od0/07/07)	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][0] T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9] T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0] T2_ColSpurVernierLUT_Cnt_s16[1][1]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1] T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	1
T2_ColSpurVernierLUT_Cnt_s16[1][9] T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2 ColSpurVernierLUT Cnt s16[1][10]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0] T3_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][1] T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10] T2_ColSpurVernierLUT_Cnt_s16[2][11]	1 10
T2_ColSpurVernierLUT_Cnt_s16[2][11]	IU

2014-10-14, 18:16:06+0530



Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2_ColSpurVernierLUT_Cnt_s16[3][5]	5 2
T2_ColSpurVernierLUT_Crit_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2 DualSpurVernierLUT Cnt s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0 0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	
T2_DualSpurVernierLUT_Cnt_s16[2][1] T2_DualSpurVernierLUT_Cnt_s16[2][2]	1 2
T2_DualSpurVernierLUT_Cnt_s16[2][2] T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	3 4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
	6
T2_DualSpurVernierLUT_Cnt_s16[2][6]	

2014-10-14, 18:16:06+0530



Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10			
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0			
T2_DualSpurVernierLUT_Cnt_s16[2][12] T2_DualSpurVernierLUT_Cnt_s16[2][13]	2			
T2_DualSpurVernierLUT_Cnt_s16[2][13] T2_DualSpurVernierLUT_Cnt_s16[2][14]	3			
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4			
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5			
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6			
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7			
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8			
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9			
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][0] T2_DualSpurVernierLUT_Cnt_s16[3][1]	22			
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4			
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6			
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8			
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10			
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12			
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14			
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16			
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20			
T2_DualSpurVernierLUT_Cnt_s16[3][11] T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	0 10			
k_SkipStepErrDiag_Cnt_str.PStep		0		
k SkipStepErrDiag Cnt str.NStep	0			
k_VernCorrErrorDiag_Cnt_str.Threshold	0			
k_VernCorrErrorDiag_Cnt_str.PStep	0			
k_VernCorrErrorDiag_Cnt_str.NStep	0			
k_VernCorrErrorThresh_Deg_f32	1			
k_VernOORangeThresh_Deg_f32	100			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	0			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32 tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cr	nt lac		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0	~	
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	0	0 ± 0.00048828125	~	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	3	~	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	V	
DigColPs_PrevColPos_Deg_M_f32	0	0 ± 0.0001220703125	-	
DigColPs_PrevVernierLevelNo_Cnt_M_u08 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	2		
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	-	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0		
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	-900 ± 0.0009	~	
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~	
NTC	0x6C	0x6C	~	
Param	0x00	0x00	~	
Status NTC	0x00	0x00	V	
	0x6E	0x6E	✓	

2014-10-14, 18:16:06+0530



Name	Actual Value	Expected Value	Result
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x6F	0x6F	✓
Param	0x00	0x00	✓
Status	0x01	0x01	✓

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

Name	Input Value
DigColPsInt_GetCustData()	430
DigColPs_ColParityError_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103
DigColPs ColTrimStatic Deg M f32	214.7
DigCoIPs_HwAVernCorrFault_Cnt_M_lgc	0
DigColPs_I2CColSensorFault_Cnt_M_lgc	0
DigColPs_I2CHwColAngle_Cnt_M_u16	15468
higColPs_I2CHwColAngle_Deg_M_f32	219.075
DigColPs_I2CHwDataType_Cnt_M_u08	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	58410
DigColPs_I2CHwSpurAngle_Deg_M_f32	324.208
higColPs_I2CHwTrimTransCnts_Uls_M_u08	5
higColPs_I2CSensCommFlts_Cnt_M_u08	23
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1
ligColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
ligColPs_PrevColPos_Deg_M_f32	569.7636028
ligColPs_PrevVernierLevelNo_Cnt_M_u08	11
ligColPs_SkipStepFltDetectAcc_Cnt_M_u16	20
DigColPs_SpurParityError_Cnt_M_lgc	0
ligColPs_SpurSensorFaultAcc_Cnt_M_u16	149
igColPs_SpurTrimStatic_Deg_M_f32	0
igColPs_TrimCompStatic_Cnt_M_u16	3184
DigColPs_VernCorrDetectAcc_Cnt_M_u16	19
igColPs_VernierAngleOORange_Cnt_M_lgc	1
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs
2_ColSpurVernierLUT_Cnt_s16[0][0]	-163
2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
2_ColSpurVernierLUT_Cnt_s16[0][5]	0
2_ColSpurVernierLUT_Cnt_s16[0][6]	32
2_ColSpurVernierLUT_Cnt_s16[0][7]	65
2_ColSpurVernierLUT_Cnt_s16[0][8]	98
2_ColSpurVernierLUT_Cnt_s16[0][9]	130
2_ColSpurVernierLUT_Cnt_s16[0][10]	163
2_ColSpurVernierLUT_Cnt_s16[0][11]	196
2_ColSpurVernierLUT_Cnt_s16[0][12]	229
2_ColSpurVernierLUT_Cnt_s16[0][13]	261
2_ColSpurVernierLUT_Cnt_s16[0][14]	294
2_ColSpurVernierLUT_Cnt_s16[0][15]	327
2_ColSpurVernierLUT_Cnt_s16[0][16]	359
2_ColSpurVernierLUT_Cnt_s16[1][0]	0
2_ColSpurVernierLUT_Cnt_s16[1][1]	4
2_ColSpurVernierLUT_Cnt_s16[1][2]	3
2_ColSpurVernierLUT_Cnt_s16[1][3]	2
2_ColSpurVernierLUT_Cnt_s16[1][4]	1
2_ColSpurVernierLUT_Cnt_s16[1][5]	0
2_ColSpurVernierLUT_Cnt_s16[1][6]	4
2 ColSpurVernierLUT Cnt s16[1][7]	3





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2] T0_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3] T2_ColSpurVernierLUT_Cnt_s16[2][4]	4 2
T2_ColSpurVernierLUT_Cnt_s16[2][4] T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12]	8
T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14]	4
T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9] T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][10] T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2 ColSpurVernierLUT Cnt s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10] T0_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12] T3_DualSpurVernierLUT_Cnt_s46[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14] T2_DualSpurVernierLUT_Cnt_s16[0][15]	108 144
T2_DualSpurVernierLUT_Cnt_s16[0][15] T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2_DualSpurVernierLUT_Cnt_s16[1][3]	2
	3
T2_DualSpurVernierLUT_Cnt_s16[1][4]	
T2_DualSpurVernierLUT_Cnt_\$16[1][4] T2_DualSpurVernierLUT_Cnt_\$16[1][5]	4
	4 5





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8 9
T2_DualSpurVernierLUT_Cnt_s16[1][20] T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9 10
T2_DualSpurVernierLUT_Cnt_s16[2][21] T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2 DualSpurVernierLUT Cnt s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T3_DualSpurVernierLUT_Cnt_s16[3][21]	19
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
k_SelectFromColumn_Cnt_lgc k_SkipStepErrDiag_Cnt_str.Threshold	35
k_SkipStepErrDiag_Cnt_str.Trieshold k_SkipStepErrDiag_Cnt_str.PStep	2
k_SkipStepErrDiag_Cnt_str.NStep	28
k_VernCorrErrorDiag_Cnt_str.Threshold	42
k_VernCorrErrorDiag_Cnt_str.PStep	16
k_VernCorrErrorDiag_Cnt_str.NStep	31
k_VernCorrErrorThresh_Deg_f32	92.41026139
k_VernOORangeThresh_Deg_f32	1413.552634
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	219.0753346
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324.2081034
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3313
	LI B. O.B. B. O.BOH ALB. M.F.L. O.L.
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc

DigColPs_Per2

Param

Status

2014-10-14, 18:16:06+0530



Input Value $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum$ tgt_DigColPs_Per2_MecState_Cnt_enum $tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_lgc$ tgt_DigColPs_Per2_TrimComp_Cnt_lgc tgt_Pim_DigColPsEOL $tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL$ Name **Actual Value Expected Value** Result ${\tt DigColPs_HwAVernCorrFault_Cnt_M_lgc}$ 1456.45813 DigColPs_I2CHwColAngleForTrim_Deg_M_f32 1456.458182 ± 0.00048828125 $DigColPs_I2CHwTrimTransCnts_Uls_M_u08$ 4 DigColPs_PrevAngleDataAvailable_Cnt_M_lgc 0 0 1444.375 ± 0.0001220703125 DigColPs_PrevColPos_Deg_M_f32 1444.375 DigColPs_PrevVernierLevelNo_Cnt_M_u08 14 14 $DigColPs_Reql2CSnsrDataType_Cnt_M_u08$ DigColPs_SkipStepFltDetectAcc_Cnt_M_u16 0 0 DigColPs_VernCorrDetectAcc_Cnt_M_u16 n 0 DigColPs_VernierAngleOORange_Cnt_M_lgc 1 1 tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_lgc.value n n tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value 544.375 544.375 ± 0.0009 tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value 0 0 0x6C NTC 0x6C

T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	~
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

0x0C

0x01

0x0C

0x01

Test Step 3.23 (Repeat Count = 1) ✓		
Name	Input Value	
DigColPsInt_GetCustData()	64	
DigColPs_ColParityError_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124	
DigColPs_ColTrimStatic_Deg_M_f32	35.2	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	50517	
DigColPs_I2CHwColAngle_Deg_M_f32	347.86	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	27908	
DigColPs_I2CHwSpurAngle_Deg_M_f32	210.79	
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	3	
DigColPs_I2CSensCommFlts_Cnt_M_u08	25	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	
DigColPs_PrevColPos_Deg_M_f32	1680.342175	
DigColPs_PrevVernierLevelNo_Cnt_M_u08	12	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	7	
DigColPs_SpurParityError_Cnt_M_lgc	1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	
DigColPs_SpurTrimStatic_Deg_M_f32	9.1	
DigColPs_TrimCompStatic_Cnt_M_u16	1	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	13	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	-163	
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131	
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99	
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66	
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33	
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0	
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32	
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65	
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98	
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130	





Name	Input Value
T2_ColSpurVernierLUT_Cnt_s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2_ColSpurVernierLUT_Cnt_s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9 7
T2_ColSpurVernierLUT_Cnt_s16[2][7]	
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9] T2_ColSpurVernierLUT_Cnt_s16[2][10]	3
	10
T2_ColSpurVernierLUT_Cnt_s16[2][11] T3_ColSpurVernierLUT_Cnt_s16[2][11]	8
T2_ColSpurVernierLUT_Cnt_s16[2][12]	
T2_ColSpurVernierLUT_Cnt_s16[2][13] T2_ColSpurVernierLUT_Cnt_s16[2][14]	6
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][4] T2 ColSpurVernierLUT Cnt s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Crit_\$10[3][0] T2_ColSpurVernierLUT_Crit_\$10[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2 ColSpurVernierLUT Cnt s16[3][15]	4
T2 ColSpurVernierLUT Cnt s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
	1.33

2014-10-14, 18:16:06+0530





Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19] T2_DualSpurVernierLUT_Cnt_s16[0][20]	288 324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
T2_DualSpurVernierLUT_Cnt_s16[1][13]	2
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7 8
T2_DualSpurVernierLUT_Cnt_s16[1][19] T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2_DualSpurVernierLUT_Cnt_s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2_DualSpurVernierLUT_Cnt_s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
T2_DualSpurVernierLUT_Cnt_s16[2][10]	10
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6 7
T2_DualSpurVernierLUT_Cnt_s16[2][18] T2_DualSpurVernierLUT_Cnt_s16[2][19]	8
T2_DualSpurVernierLUT_Cnt_s16[2][19] T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2 DualSpurVernierLUT Cnt s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
T2_DualSpurVernierLUT_Cnt_s16[3][5]	10
T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][7]	14
T2_DualSpurVernierLUT_Cnt_s16[3][8]	16
T2_DualSpurVernierLUT_Cnt_s16[3][9]	18
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15 17
T2_DualSpurVernierLUT_Cnt_s16[3][19] T2_DualSpurVernierLUT_Cnt_s16[3][20]	17
T2_DualSpurVernierLUT_Cnt_s16[3][20] T2_DualSpurVernierLUT_Cnt_s16[3][21]	21
12_54416pti votnici 201_01it_510[0][21]	-

tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value			
k_SelectFromColumn_Cnt_lgc	1			
k_SkipStepErrDiag_Cnt_str.Threshold	160			
k_SkipStepErrDiag_Cnt_str.PStep	23			
k_SkipStepErrDiag_Cnt_str.NStep	16			
k_VernCorrErrorDiag_Cnt_str.Threshold	82			
k_VernCorrErrorDiag_Cnt_str.PStep	43			
k_VernCorrErrorDiag_Cnt_str.NStep	34			
k_VernCorrErrorThresh_Deg_f32	16.35241604			
k_VernOORangeThresh_Deg_f32	106.1935596			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	1			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	347.8614647			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	210.7976598			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	3059			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsP	osValid_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsP	os_HwDeg_f32		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_C	nt_enum		
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_	tgt_DigColPs_Per2_TrimComp_Cnt_lgc		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Resul	
DigColPs_HwAVernCorrFault_Cnt_M_lgc	0	0		
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1400.76807	1400.768182 ± 0.00048828125	•	
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	2	2		
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1	1		
DigColPs_PrevColPos_Deg_M_f32	1392.65991	1392.66 ± 0.0001220703125		
DigColPs_PrevVernierLevelNo_Cnt_M_u08	13	13		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•	
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	Ō	•	
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	•	
DigColPs_VernierAngleOORange_Cnt_M_lgc	0	0	•	
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1	1	•	
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	492.659912	492.66 ± 0.0009	~	

_				
I I				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	✓
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~

1

Test Step 3.24 (Repeat Count = 1)	<u> </u>
Name	Input Value
DigColPsInt_GetCustData()	53
DigColPs_ColParityError_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255
DigColPs_ColTrimStatic_Deg_M_f32	0
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1
DigColPs_I2CColSensorFault_Cnt_M_Igc	1
DigColPs_I2CHwColAngle_Cnt_M_u16	65535
DigColPs_I2CHwColAngle_Deg_M_f32	360
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CHwSpurAngle_Deg_M_f32	360
DigColPs_I2CHwTrimTransCnts_UIs_M_u08	6
DigColPs_I2CSensCommFlts_Cnt_M_u08	31
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	1
DigColPs_PrevColPos_Deg_M_f32	1800
DigColPs_PrevVernierLevelNo_Cnt_M_u08	16
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	21
DigColPs_SpurParityError_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255
DigColPs_SpurTrimStatic_Deg_M_f32	360
DigColPs_TrimCompStatic_Cnt_M_u16	4488
DigColPs_VernCorrDetectAcc_Cnt_M_u16	20

2014-10-14, 18:16:06+0530

DigColPs_Per2



Nome	Innut Value
Name	Input Value
DigColPs_VernierAngleOORange_Cnt_M_lgc Rte_Inst_Sa_DigColPs	
T2_ColSpurVernierLUT_Cnt_s16[0][0]	tgt_Rte_Inst_Sa_DigColPs -163
T2_ColSpurVernierLUT_Cnt_s16[0][1]	-131
T2_ColSpurVernierLUT_Cnt_s16[0][2]	-99
T2_ColSpurVernierLUT_Cnt_s16[0][3]	-66
T2_ColSpurVernierLUT_Cnt_s16[0][4]	-33
T2_ColSpurVernierLUT_Cnt_s16[0][5]	0
T2_ColSpurVernierLUT_Cnt_s16[0][6]	32
T2_ColSpurVernierLUT_Cnt_s16[0][7]	65
T2_ColSpurVernierLUT_Cnt_s16[0][8]	98
T2_ColSpurVernierLUT_Cnt_s16[0][9]	130
T2 ColSpurVernierLUT Cnt s16[0][10]	163
T2_ColSpurVernierLUT_Cnt_s16[0][11]	196
T2_ColSpurVernierLUT_Cnt_s16[0][12]	229
T2_ColSpurVernierLUT_Cnt_s16[0][13]	261
T2_ColSpurVernierLUT_Cnt_s16[0][14]	294
T2_ColSpurVernierLUT_Cnt_s16[0][15]	327
T2_ColSpurVernierLUT_Cnt_s16[0][16]	359
T2 ColSpurVernierLUT Cnt s16[1][0]	0
T2_ColSpurVernierLUT_Cnt_s16[1][1]	4
T2_ColSpurVernierLUT_Cnt_s16[1][2]	3
T2_ColSpurVernierLUT_Cnt_s16[1][3]	2
T2_ColSpurVernierLUT_Cnt_s16[1][4]	1
T2_ColSpurVernierLUT_Cnt_s16[1][5]	0
T2_ColSpurVernierLUT_Cnt_s16[1][6]	4
T2_ColSpurVernierLUT_Cnt_s16[1][7]	3
T2_ColSpurVernierLUT_Cnt_s16[1][8]	2
T2_ColSpurVernierLUT_Cnt_s16[1][9]	1
T2_ColSpurVernierLUT_Cnt_s16[1][10]	0
T2_ColSpurVernierLUT_Cnt_s16[1][11]	4
T2_ColSpurVernierLUT_Cnt_s16[1][12]	3
T2_ColSpurVernierLUT_Cnt_s16[1][13]	2
T2_ColSpurVernierLUT_Cnt_s16[1][14]	1
T2_ColSpurVernierLUT_Cnt_s16[1][15]	0
T2_ColSpurVernierLUT_Cnt_s16[1][16]	4
T2_ColSpurVernierLUT_Cnt_s16[2][0]	0
T2_ColSpurVernierLUT_Cnt_s16[2][1]	8
T2_ColSpurVernierLUT_Cnt_s16[2][2]	6
T2_ColSpurVernierLUT_Cnt_s16[2][3]	4
T2_ColSpurVernierLUT_Cnt_s16[2][4]	2
T2_ColSpurVernierLUT_Cnt_s16[2][5]	0
T2_ColSpurVernierLUT_Cnt_s16[2][6]	9
T2_ColSpurVernierLUT_Cnt_s16[2][7]	7
T2_ColSpurVernierLUT_Cnt_s16[2][8]	5
T2_ColSpurVernierLUT_Cnt_s16[2][9]	3
T2_ColSpurVernierLUT_Cnt_s16[2][10]	1
T2_ColSpurVernierLUT_Cnt_s16[2][11]	10
T2_ColSpurVernierLUT_Cnt_s16[2][12] T2_ColSpurVernierLUT_Cnt_s16[2][13]	6
	4
T2_ColSpurVernierLUT_Cnt_s16[2][14] T2_ColSpurVernierLUT_Cnt_s16[2][15]	2
T2_ColSpurVernierLUT_Cnt_s16[2][16]	10
T2_ColSpurVernierLUT_Cnt_s16[3][0]	1
T2_ColSpurVernierLUT_Cnt_s16[3][1]	14
T2_ColSpurVernierLUT_Cnt_s16[3][2]	11
T2_ColSpurVernierLUT_Cnt_s16[3][3]	8
T2_ColSpurVernierLUT_Cnt_s16[3][4]	5
T2_ColSpurVernierLUT_Cnt_s16[3][5]	2
T2_ColSpurVernierLUT_Cnt_s16[3][6]	15
T2_ColSpurVernierLUT_Cnt_s16[3][7]	12
T2_ColSpurVernierLUT_Cnt_s16[3][8]	9
T2_ColSpurVernierLUT_Cnt_s16[3][9]	6
T2_ColSpurVernierLUT_Cnt_s16[3][10]	3
T2_ColSpurVernierLUT_Cnt_s16[3][11]	16
T2_ColSpurVernierLUT_Cnt_s16[3][12]	13
T2_ColSpurVernierLUT_Cnt_s16[3][13]	10
T2_ColSpurVernierLUT_Cnt_s16[3][14]	7
T2_ColSpurVernierLUT_Cnt_s16[3][15]	4
T2_ColSpurVernierLUT_Cnt_s16[3][16]	17
T2_DualSpurVernierLUT_Cnt_s16[0][0]	-396
T2_DualSpurVernierLUT_Cnt_s16[0][1]	-360
T2_DualSpurVernierLUT_Cnt_s16[0][2]	-324

2014-10-14, 18:16:06+0530

DigColPs_Per2



Name	Input Value
T2_DualSpurVernierLUT_Cnt_s16[0][3]	-288
T2_DualSpurVernierLUT_Cnt_s16[0][4]	-252
T2_DualSpurVernierLUT_Cnt_s16[0][5]	-216
T2_DualSpurVernierLUT_Cnt_s16[0][6]	-180
T2_DualSpurVernierLUT_Cnt_s16[0][7]	-144
T2_DualSpurVernierLUT_Cnt_s16[0][8]	-108
T2_DualSpurVernierLUT_Cnt_s16[0][9]	-72
T2_DualSpurVernierLUT_Cnt_s16[0][10]	-36
T2_DualSpurVernierLUT_Cnt_s16[0][11]	0
T2_DualSpurVernierLUT_Cnt_s16[0][12]	36
T2_DualSpurVernierLUT_Cnt_s16[0][13]	72
T2_DualSpurVernierLUT_Cnt_s16[0][14]	108
T2_DualSpurVernierLUT_Cnt_s16[0][15]	144
T2_DualSpurVernierLUT_Cnt_s16[0][16]	180
T2_DualSpurVernierLUT_Cnt_s16[0][17]	216
T2_DualSpurVernierLUT_Cnt_s16[0][18]	252
T2_DualSpurVernierLUT_Cnt_s16[0][19]	288
T2_DualSpurVernierLUT_Cnt_s16[0][20]	324
T2_DualSpurVernierLUT_Cnt_s16[0][21]	360
T2_DualSpurVernierLUT_Cnt_s16[1][0]	9
T2_DualSpurVernierLUT_Cnt_s16[1][1]	0
T2_DualSpurVernierLUT_Cnt_s16[1][2]	1
T2 DualSpurVernierLUT Cnt s16[1][3]	2
T2_DualSpurVernierLUT_Cnt_s16[1][4]	3
T2_DualSpurVernierLUT_Cnt_s16[1][5]	4
T2_DualSpurVernierLUT_Cnt_s16[1][6]	5
T2_DualSpurVernierLUT_Cnt_s16[1][7]	6
T2_DualSpurVernierLUT_Cnt_s16[1][8]	7
T2_DualSpurVernierLUT_Cnt_s16[1][9]	8
T2_DualSpurVernierLUT_Cnt_s16[1][10]	9
T2_DualSpurVernierLUT_Cnt_s16[1][11]	0
T2_DualSpurVernierLUT_Cnt_s16[1][12]	1
	2
T2_DualSpurVernierLUT_Cnt_s16[1][13]	
T2_DualSpurVernierLUT_Cnt_s16[1][14]	3
T2_DualSpurVernierLUT_Cnt_s16[1][15]	4
T2_DualSpurVernierLUT_Cnt_s16[1][16]	5
T2_DualSpurVernierLUT_Cnt_s16[1][17]	6
T2_DualSpurVernierLUT_Cnt_s16[1][18]	7
T2_DualSpurVernierLUT_Cnt_s16[1][19]	8
T2_DualSpurVernierLUT_Cnt_s16[1][20]	9
T2 DualSpurVernierLUT Cnt s16[1][21]	0
T2_DualSpurVernierLUT_Cnt_s16[2][0]	0
T2_DualSpurVernierLUT_Cnt_s16[2][1]	1
T2_DualSpurVernierLUT_Cnt_s16[2][2]	2
T2_DualSpurVernierLUT_Cnt_s16[2][3]	3
T2 DualSpurVernierLUT Cnt s16[2][4]	4
T2_DualSpurVernierLUT_Cnt_s16[2][5]	5
T2_DualSpurVernierLUT_Cnt_s16[2][6]	6
T2_DualSpurVernierLUT_Cnt_s16[2][7]	7
T2_DualSpurVernierLUT_Cnt_s16[2][8]	8
T2_DualSpurVernierLUT_Cnt_s16[2][9]	9
	10
T2_DualSpurVernierLUT_Cnt_s16[2][10]	
T2_DualSpurVernierLUT_Cnt_s16[2][11]	0
T2_DualSpurVernierLUT_Cnt_s16[2][12]	1
T2_DualSpurVernierLUT_Cnt_s16[2][13]	2
T2_DualSpurVernierLUT_Cnt_s16[2][14]	3
T2_DualSpurVernierLUT_Cnt_s16[2][15]	4
T2_DualSpurVernierLUT_Cnt_s16[2][16]	5
T2_DualSpurVernierLUT_Cnt_s16[2][17]	6
T2_DualSpurVernierLUT_Cnt_s16[2][18]	7
	8
T2_DualSpurVernierLUT_Cnt_s16[2][19]	
T2_DualSpurVernierLUT_Cnt_s16[2][20]	9
T2_DualSpurVernierLUT_Cnt_s16[2][21]	10
T2_DualSpurVernierLUT_Cnt_s16[3][0]	22
T2_DualSpurVernierLUT_Cnt_s16[3][1]	2
T2_DualSpurVernierLUT_Cnt_s16[3][2]	4
T2_DualSpurVernierLUT_Cnt_s16[3][3]	6
T2_DualSpurVernierLUT_Cnt_s16[3][4]	8
	10
	1 10
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12
T2_DualSpurVernierLUT_Cnt_s16[3][5]	
T2_DualSpurVernierLUT_Cnt_s16[3][5] T2_DualSpurVernierLUT_Cnt_s16[3][6]	12

2014-10-14, 18:16:06+0530



DigColPs_Per2

Name	Input Value			
T2_DualSpurVernierLUT_Cnt_s16[3][10]	20	20		
T2_DualSpurVernierLUT_Cnt_s16[3][11]	1	1		
T2_DualSpurVernierLUT_Cnt_s16[3][12]	3			
T2_DualSpurVernierLUT_Cnt_s16[3][13]	5			
T2_DualSpurVernierLUT_Cnt_s16[3][14]	7			
T2_DualSpurVernierLUT_Cnt_s16[3][15]	9			
T2_DualSpurVernierLUT_Cnt_s16[3][16]	11			
T2_DualSpurVernierLUT_Cnt_s16[3][17]	13			
T2_DualSpurVernierLUT_Cnt_s16[3][18]	15			
T2_DualSpurVernierLUT_Cnt_s16[3][19]	17			
T2_DualSpurVernierLUT_Cnt_s16[3][20]	19			
T2_DualSpurVernierLUT_Cnt_s16[3][21]	21			
k_SelectFromColumn_Cnt_lgc	1			
k_SkipStepErrDiag_Cnt_str.Threshold	255	255		
k_SkipStepErrDiag_Cnt_str.PStep	50			
k_SkipStepErrDiag_Cnt_str.NStep	50			
k_VernCorrErrorDiag_Cnt_str.Threshold	100			
k_VernCorrErrorDiag_Cnt_str.PStep	50			
k_VernCorrErrorDiag_Cnt_str.NStep	50			
k_VernCorrErrorThresh_Deg_f32	100			
k_VernOORangeThresh_Deg_f32	1800			
tgt_DigColPs_Per2_MecState_Cnt_enum.value	2			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc	tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_I2CHwAbsPos_HwDeg_f32	tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_MecState_Cnt_enum	tgt_DigColPs_Per2_MecState_Cnt_enum			
tgt_Rte_Inst_Sa_DigColPs.DigColPs_Per2_TrimComp_Cnt_Igc	tgt_DigColPs_Per2_TrimComp_Cnt_lgc			
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL			
Name	Actual Value	Expected Value	Result	
DigColPs HwAVernCorrFault Cnt M lgc	1	1	•	

Name	Actual Value	Expected Value	Result
DigColPs_HwAVernCorrFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngleForTrim_Deg_M_f32	1636.36353	1636.363636 ± 0.00048828125	~
DigColPs_I2CHwTrimTransCnts_Uls_M_u08	5	5	~
DigColPs_PrevAngleDataAvailable_Cnt_M_lgc	0	0	~
DigColPs_PrevColPos_Deg_M_f32	1800	1800 ± 0.0001220703125	~
DigColPs_PrevVernierLevelNo_Cnt_M_u08	17	17	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SkipStepFltDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernCorrDetectAcc_Cnt_M_u16	0	0	~
DigColPs_VernierAngleOORange_Cnt_M_lgc	1	1	~
tgt_DigColPs_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	0	~
tgt_DigColPs_Per2_I2CHwAbsPos_HwDeg_f32.value	900	900 ± 0.0009	~
tgt_DigColPs_Per2_TrimComp_Cnt_lgc.value	0	0	~

T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP0_CheckpointReached	1	~
GetResource	1	GetResource	1	✓
DigColPsInt_GetCustData	1	DigColPsInt_GetCustData	1	~
ReleaseResource	1	ReleaseResource	1	~
ConstrainOneRev	2	ConstrainOneRev	2	~
VernierLookup	1	VernierLookup	1	~
DiagnosticThreshold	1	DiagnosticThreshold	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per2_CP1_CheckpointReached	1	~



Project DigColPs Module DigColPs Test Object ComputeRoughTurns

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy Work Area\DigColPs C1XX
••••	D. Joynergy_vonk_Area bigoon 3_0177
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -l\$(PROJECTROOT)\DigColPs\utp\contract -l\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -l\$(PROJECTROOT)\DigColPs\include -l\$(PROJECTROOT)\NxtrLib\include -l\$(PROJECTROOT)\StdDef\include -l\$(Compiler Install Path)\include

Commonte/Doscri	ption/Specification
Comments/Descri	puon/opecincation

Comments/Description	n/Specification
Name	Text
Module 'DigColPs'	Unit_Test_Information
	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date: 10-14-2014 Comments:



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

5.00 Cycles Longest Execution Path 3.00 Cycles Shortest Execution Path TS1.1 TS1.2

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path => if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=FALSE else if (Delta_Deg_T_f32 < -k_StepDetect_Deg_f32)=FALSE" TS1.2 "Shortest Execution Path => if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32)=TRUE"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	30		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16	3	
k_StepDetect_Deg_f32	30		
tgt_RoughTurnAccPtr_Cnt_T_s16	-3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1080	-1080 ± 0.00048828125	•
tgt_RoughTurnAccPtr_Cnt_T_s16	-3	-3	✓

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1080	1080 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3	~



Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

8.00 Cycles
3.00 Cycles
8.00 Cycles
8.00 Cycles
5.00 Cycles
5.00 Cycles
8.00 Cycles
8.00 Cycles
5.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.8 TS2.9 TS2.10 TS2.11 TS2.12 TS2.13 TS2.14 TS2.15

Description

VECTOR DESCRIPTION:

| S2.1 All Min | TS2.2 All Max | TS2.3 Delta_Deg_T_f32=Min | TS2.4 Delta_Deg_T_f32=Max | TS2.5 Delta_Deg_T_f32=Zero | TS2.6 Delta_Deg_T_f32=Pos | TS2.7 Delta_Deg_T_f32=Neg | TS2.8 k_StepDetect_Deg_f32=Min | S2.9 k_StepDete IS2.8 k_StepDetect_Deg_f32=Min TS2.9 k_StepDetect_Deg_f32=Max TS2.10 k_StepDetect_Deg_f32=Pos TS2.11 RoughTurnAccPtr_Cnt_T_s08=Min TS2.12 RoughTurnAccPtr_Cnt_T_s08=Max TS2.13 RoughTurnAccPtr_Cnt_T_s08=Zero TS2.14 RoughTurnAccPtr_Cnt_T_s08=Pos TS2.15 RoughTurnAccPtr_Cnt_T_s08=Neg

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	20		
tgt_RoughTurnAccPtr_Cnt_T_s16	-5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1440	-1440 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-4	-4	✓

Test Step 2.2 (Repeat Count = 1)			✓	
Name	Input Value			
Delta_Deg_T_f32	360			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	5	5		
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	1440	1440 ± 0.00048828125	~	
tgt RoughTurnAccPtr Cnt T s16	4	4	✓	

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	24.5		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	•

Test Step 2.4 (Repeat Count = 1)	✓
Name	Input Value
Delta_Deg_T_f32	360
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16
k_StepDetect_Deg_f32	28



Name	Input Value		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	0	0 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	0	0	✓

Test Step 2.5 (Repeat Count = 1)			✓	
Name	Input Value			
Delta_Deg_T_f32	0			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	6		
k_StepDetect_Deg_f32	44.4			
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2		
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	720	720 ± 0.00048828125	~	
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	~	

Test Step 2.6 (Repeat Count = 1)			✓	
Name	Input Value			
Delta_Deg_T_f32	180.5			
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	16		
k_StepDetect_Deg_f32	68	68		
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3		
Name	Actual Value	Expected Value	Result	
ComputeRoughTurns()	720	720 ± 0.00048828125	✓	
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	✓	

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-150.6		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	90.2		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1800	1800 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	5	5	✓

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	50		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	20		
tgt_RoughTurnAccPtr_Cnt_T_s16	3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-30.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	16	
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓



Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	0		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	16	
k_StepDetect_Deg_f32	100.8		
tgt_RoughTurnAccPtr_Cnt_T_s16	2		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	720	720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	2	2	✓

Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	154.2		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	s16	
k_StepDetect_Deg_f32	155		
tgt_RoughTurnAccPtr_Cnt_T_s16	-5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1800	-1800 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-5	-5	✓

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	40		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	6	
k_StepDetect_Deg_f32	25.4		
tgt_RoughTurnAccPtr_Cnt_T_s16	5		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1440	1440 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	4	4	✓

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-300		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s1	6	
k_StepDetect_Deg_f32	300		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	0	0 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	0	0	✓

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	10.5		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	s16	
k_StepDetect_Deg_f32	150.1		
tgt_RoughTurnAccPtr_Cnt_T_s16	1		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt RoughTurnAccPtr Cnt T s16	1	1	✓

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	150		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	200		
tgt_RoughTurnAccPtr_Cnt_T_s16	-2		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-720	-720 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-2	-2	•



Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 3.00 Cycles TS3.2 8.00 Cycles TS3.3 5.00 Cycles

VECTOR DESCRIPTION: Description

 $\begin{tabular}{ll} TS3.1 & if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32) = TRUE \\ TS3.2 & "if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32) = FALSE \\ else & if (Delta_Deg_T_f32 < -k_StepDetect_Deg_f32) = TRUE" \\ TS3.3 & "if (Delta_Deg_T_f32 > k_StepDetect_Deg_f32) = FALSE \\ else & if (Delta_Deg_T_f32 < -k_StepDetect_Deg_f32) = FALSE" \\ \end{tabular}$

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	340		
tgt_RoughTurnAccPtr_Cnt_T_s16	4		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	1080	1080 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	3	3	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	-360		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s16		
k_StepDetect_Deg_f32	24		
tgt_RoughTurnAccPtr_Cnt_T_s16	0		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	360	360 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	1	1	✓

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
Delta_Deg_T_f32	30		
RoughTurnAccPtr_Cnt_T_s16	tgt_RoughTurnAccPtr_Cnt_T_s	16	
k_StepDetect_Deg_f32	30		
tgt_RoughTurnAccPtr_Cnt_T_s16	-3		
Name	Actual Value	Expected Value	Result
ComputeRoughTurns()	-1080	-1080 ± 0.00048828125	~
tgt_RoughTurnAccPtr_Cnt_T_s16	-3	-3	✓

DigColPs_SCom_CustSetTrim

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ject
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t Object

Instrumentation: Test Object Only

Statement (C0) Coverage

Branch (C1) Coverage

MCC Coverage

MC/DC Coverage

Statistics

Total Testcases	
Successful	✓
Failed	
Not Executed	

Module Properties

Project Root Directory	
Configuration File	
Target Environment	
Kind of Test	
Linker Options	
Source File(s)	
File	
Compiler Option	s
File	
Compiler Option	s

ne	ription/Specification Text		

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2014-10-14, 18:18:40+0530

DigColPs_SCom_CustSetTrim



Attributes	
Name	Value
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>

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Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 1027.00 Cycles TS1.2 1499.00 Cycles

Description

Test Step 1.1 (Repeat Count = 1)			~
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			•
			~
			Y
			- V

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DigColPs_SCom_CustSetTrim

Test Step 1.2 (Repeat Count = 1)			V
Name	Input Value		
		I=	I
Name	Actual Value	Expected Value	Result
			Y
			Y.
			-
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			-
			1

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2014-10-14, 18:18:40+0530



Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

1356.00 Cycles
1026.00 Cycles
1028.00 Cycles
1027.00 Cycles
1037.00 Cycles
1037.00 Cycles
1037.00 Cycles
1027.00 Cycles
1027.00 Cycles
1027.00 Cycles
1356.00 Cycles
1492.00 Cycles
1492.00 Cycles
1027.00 Cycles TS2.1 TS2.2 TS2.3 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.10 TS2.11 TS2.11 TS2.12 TS2.13 TS2.14 TS2.15 TS2.16 TS2.17 TS2.18 TS2.19 TS2.17

Description

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
Name	Actual Value	Expected value	resuit ✓
			-
			✓
			✓
			~
			✓

DigColPs_SCom_CustSetTrim



T (0) 00/P (0 (1)			
Test Step 2.2 (Repeat Count = 1)			~
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
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			~
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			~
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			•
Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
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			~
			\rightarrow \right
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			~
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			-
	<u> </u>		~
			_
Test Step 2.4 (Repeat Count = 1)			~
1 Cot Otop 2.4 (Nopout Count 1)			
Name	Input Value		
Name	Input Value		
Name	Input Value		

DigColPs_SCom_CustSetTrim



Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			•
			*
			~
			•
			\rightarrow \right
			•
Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
	100000		~
			•
			✓
			~
			V
			✓
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		I .	
Test Step 2.6 (Repeat Count = 1)	Innut Value		✓
Name	Input Value		

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DigColPs_SCom_CustSetTrim

Name	Input Value		
Name	Actual Value	Expected Value	Result
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Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
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			~
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DigColPs_SCom_CustSetTrim



T (0) 00/D (0) (1)			
Test Step 2.8 (Repeat Count = 1)			~
Name	Input Value		
Name	Actual Value	Expected Value	Result
		·	~
			~
			y
			~
			~
			~
			✓
			~
			*
			~
			~
Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
Name	Actual Value	Expected Value	~
Name	Actual Value	Expected Value	✓
Name	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
Name	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
Name	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
Name	Actual Value	Expected Value	✓
Name	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
Name	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
	Actual Value	Expected Value	* * * * * * * * * * * * * * * * * * *
Test Step 2.10 (Repeat Count = 1)		Expected Value	* * * * * * * * * * * * * * * * * * *
	Actual Value Input Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.10 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.10 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >

DigColPs_SCom_CustSetTrim



	Input Value		
Mana-	Actual Value	From a stand Walter	D 14
Name	Actual value	Expected Value	Result
			~
			~
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			✓
			*
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			\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
			-
	I	I	
Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
News	Ashral Value	Francisco de Matrico	Decult
Name	Actual Value	Expected Value	Result
Name	Actual Value	Expected Value	~
Name	Actual Value	Expected Value	\ \ \
Name	Actual Value	Expected Value	\ \ \
Name	Actual Value	Expected Value	> > > >
Name	Actual Value	Expected Value	*****
Name	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Name	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Name	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Name	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Name	Actual Value	Expected Value	*****
Name	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Name	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
	Actual Value Input Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 2.12 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >

DigColPs_SCom_CustSetTrim



Name	Input Value		
Name	Actual Value	Expected Value	Result
	7101001110100		
			~
			✓
			~
			~
			~
			~
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			~

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
			_
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			~
			~
			~

DigColPs_SCom_CustSetTrim

2014-10-14, 18:18:40+0530



Test Step 2.14 (Repeat Count = 1)

DigColPs_SCom_CustSetTrim



Name	Input Value		
Name	Actual Value	Expected Value	Result
			•
			y
			✓
			*
			y
			y
			✓
			~
Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			~
			V
			•
			* * * * * * * * * * * * * * * * * * *
			~
			y
Test Step 2.18 (Repeat Count = 1) Name	Input Value		✓
Numb	input value		

2014-10-14, 18:18:40+0530



DigColPs_SCom_CustSetTrim

Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			~
			~
			~
			•
			•
			~
			✓
			~
			~
			~
	·		

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			~
			~
			~

DigColPs_SCom_CustSetTrim

2014-10-14, 18:18:40+0530



Test Step 2.20 (Repeat Count = 1)

2014-10-14, 18:18:40+0530



DigColPs_SCom_CustSetTrim

Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 1027.00 Cycles
TS3.2 1031.00 Cycles
TS3.3 1356.00 Cycles
TS3.4 1492.00 Cycles
TS3.5 1500.00 Cycles
TS3.6 1032.00 Cycles
TS3.7 1036.00 Cycles
TS3.8 1047.00 Cycles

Description

Name Actual Value Expected Value Result	Test Step 3.1 (Repeat Count = 1)			✓
	Name	Input Value		
	Name	Actual Value	Expected Value	Result
				✓
				~
				~
				✓
· · · · · · · · · · · · · · · · · · ·				✓
				~
				~

DigColPs_SCom_CustSetTrim



T (0) 00/P (0) (1)			
Test Step 3.2 (Repeat Count = 1)			~
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			~
			y
			~
			~
			y
			✓
			✓
			*
			~
			~
Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
Name	Actual Value	Expected Value	~
Name	Actual Value	Expected Value	y
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	>
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	y
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	*
	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.4 (Repeat Count = 1)		Expected Value	*
	Actual Value Input Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.4 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.4 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > > >

DigColPs_SCom_CustSetTrim



Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			*
			-
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			~
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			~
			- V
			· · · · · · · · · · · · · · · · · · ·
Test Step 3.5 (Repeat Count = 1)			✓
Name	Input Value		
Namo	Actual Value	Evnocted Value	Posult
Name	Actual Value	Expected Value	Result
Name	Actual Value	Expected Value	~
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	*
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
Name	Actual Value	Expected Value	· · · · · · · · · · · · · · · · · · ·
	Actual Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	· · · · · · · · · · · · · · · · · · ·
	Actual Value Input Value	Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >
Test Step 3.6 (Repeat Count = 1)		Expected Value	> > > > > > > > > > > > > > > > > > >

2014-10-14, 18:18:40+0530



DigColPs_SCom_CustSetTrim

Name	Input Value		
Name	Actual Value	Expected Value	Result
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Test Step 3.7 (Repeat Count = 1)			✓
Name	Input Value		
			1
Name	Actual Value	Expected Value	Result
			~
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			~
			~ ~ ~
			~
			_

2014-10-14, 18:18:40+0530



DigColPs_SCom_CustSetTrim

Test Step 3.8 (Repeat Count = 1)			✓
Name	Input Value		
		1=	1
Name	Actual Value	Expected Value	Result
			Y
			V
			· ·

2014-10-14, 18:07:04+0530





Project	DigColPs
Module	DigColPs
Test Object	DigColPs_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date: 10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested. Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code. Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2014-10-14, 18:07:04+0530

DigColPs_Init1



Attributes			
Name	Value		
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj		
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src		
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd		
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl		
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2		
Time Unit	Cycles		
Timer Enabled	false		
Timer Prescale	0		
Timer Resolution	1		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP		



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 1098.00 Cycles Shortest Execution Path TS1.2 821.00 Cycles Longest Execution Path

Description

VECTOR DESCRIPTION:

TS1.1 "Shortest Execution Path =>

(Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32)=FALSE

 $(Redundant_Format_1_m(*(uint32*)\&Rte_Pim_DigColPsEOL().>ColTrim_Deg_f32) != Rte_Pim_DigColPsEOL().>R_ColTrim_Cnt_u32) = FALSE_Pim_DigColPsEOL().$

|| CREdundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32) != Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)=FALSE ||((uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16) != Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16)=TRUE |

Test Step 1.1 (Repeat Count = 1)		✓
Name	Input Value	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
k_ColAngSenseLPFFc_Hz_f32	2	
k_SpurAngSenseLPFFc_Hz_f32	9	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	200	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3166175231	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220	

2014-10-14, 18:07:04+0530



DigColPs_Init1

Name	Actual Value	Expected Value	Result
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180	180 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951	3167485951	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200	200 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231	3166175231	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100	100	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435	65435	~

Т				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1068.00 Cycles 1070.00 Cycles 1071.00 Cycles 1071.00 Cycles 824.00 Cycles 1072.00 Cycles TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 1072.00 Cycles 1072.00 Cycles 825.00 Cycles 1071.00 Cycles 1073.00 Cycles 1074.00 Cycles 1072.00 Cycles TS2.7 TS2.9 TS2.10 TS2.11 TS2.12 825.00 Cycles 1072.00 Cycles 1070.00 Cycles 1071.00 Cycles 824.00 Cycles TS2.13 TS2.14 TS2.14 TS2.15 TS2.16 TS2.17 TS2.18 TS2.19 TS2.20 TS2.21 1072.00 Cycles 1072.00 Cycles 1072.00 Cycles 825.00 Cycles 1072.00 Cycles 1072.00 Cycles 824.00 Cycles 1071.00 Cycles 824.00 Cycles TS2.22 TS2.23 TS2.24 TS2.25 TS2.26 824.00 Cycles 1071.00 Cycles 1071.00 Cycles 824.00 Cycles 1071.00 Cycles 1073.00 Cycles 1073.00 Cycles 1073.00 Cycles TS2.27 TS2.28 TS2.29 TS2.30 TS2.31 TS2.32 826.00 Cvcles TS2.33 TS2.34 TS2.35 TS2.36 1073.00 Cycles 826.00 Cycles 1073.00 Cycles

Description

VECTOR DESCRIPTION:

TS2 1All Min TS2.2All Max

TS2.3k_ColAngSenseLPFFc_Hz_f32=Min

TS2.4k_ColAngSenseLPFFc_Hz_f32=Max TS2.5k_ColAngSenseLPFFc_Hz_f32=pos

TS2.5k_ColAngSenseLPFFc_Hz_f32=pos
TS2.6DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Min
TS2.7DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Max
TS2.8DigColPs_ColAngleLPFKSV_Cnt_M_str.K=Pos
TS2.9k_SpurAngSenseLPFFc_Hz_f32=Min
TS2.10k_SpurAngSenseLPFFc_Hz_f32=Max
TS2.11k_SpurAngSenseLPFFc_Hz_f32=Pos
TS2.12DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Min
TS2.13DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Max
TS2.14DigColPs_SpurAngleLPFKSV_Cnt_M_str.K=Pos
TS2.15Rte_Pim_DigColPsEOL.COlTrim_Deg_f32=Min
TS2.16Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Min

TS2.16Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Max TS2.17Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Pos TS2.18Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Neg TS2.19Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Zero

TS2.20Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Min TS2.20Rte_Pim_DigColPsEOL.SpurTrim_Deg_132=Max TS2.22Rte_Pim_DigColPsEOL.SpurTrim_Deg_132=Pos

TS2.23Rte Pim_DigColPsEOL.SpurTrim_Deg_f32=Neg TS2.24Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Zero TS2.25Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Min

TS2.26Rte Pim_DigColPsEOL.TrimComp_Cnt_u16=Max TS2.27Rte_Pim_DigColPsEOL.TrimComp_Cnt_u16=Pos TS2.28Rte_Pim_DigColPsEOL.R_ColTrim_Cnt_u32=Min

TS2.29Rte Pim_DigColPsEOL.R_ColTrim_Cnt_u32=Max TS2.30Rte_Pim_DigColPsEOL.R_ColTrim_Cnt_u32=Pos TS2.31Rte_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32=Min

TS2.32Rte Pim_DigColPsEOL.R_SpurTrim_Cnt_u32=Min TS2.33Rte Pim_DigColPsEOL.R_SpurTrim_Cnt_u32=Nos TS2.33Rte Pim_DigColPsEOL.R_TrimCom_Cnt_u16=Min TS2.35Rte Pim_DigColPsEOL.R_TrimCom_Cnt_u16=Max

TS2.36Rte_Pim_DigColPsEOL.R_TrimCom_Cnt_u16=Pos

Test Step 2.1 (Repeat Count = 1)		<u> </u>
Name	Input Value	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
k_ColAngSenseLPFFc_Hz_f32	1	
k_SpurAngSenseLPFFc_Hz_f32	1	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	
tat Pim DiaColPsEOL.R TrimCom Cnt u16	0	

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5

2014-10-14, 18:07:04+0530



Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	•

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.2 (Repeat Count = 1)			V
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	65535		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	-
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	-
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	·
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		

Test Step 2.3 (Repeat Count = 1)		✓
Name	Input Value	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.012	
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs	
k_ColAngSenseLPFFc_Hz_f32	1	
k_SpurAngSenseLPFFc_Hz_f32	8.5	
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	1	
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1	
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	12	
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2456	
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	124	
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	525	

DigColPs_Init1

2014-10-14, 18:07:04+0530



Name	Input Value		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.101307333	0.101307321 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	_

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.015		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	16		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	5		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3210739711		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	36.2		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3186570034		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	224		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65311		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	-
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	-
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.182138205	0.18213822 ± 0.00048828125	-
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	-
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	5	5 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3210739711	3210739711	-
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	36.2000008	36.2 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3186570034	3186570034	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	224	224	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65311	65311	

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus	1	_





Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.018		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	30.5		
k_SpurAngSenseLPFFc_Hz_f32	24		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	9.9		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	1999		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	60		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4672		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	324		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.318374097	0.318374099 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.260360897	0.260360885 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

T .						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-		

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.021		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	6		
k_SpurAngSenseLPFFc_Hz_f32	32.9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	13		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2998		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	5780		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	424		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0726258755	0.07262589 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.338624597	0.338624547 ± 0.00048828125	
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	•

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		



Test Sten 2.7 (Beneat Count = 4)			
Test Step 2.7 (Repeat Count = 1)			_
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	12		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	17.4		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3997		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	108		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	6888		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	524		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.139977276	0.139977259 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	-
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.027		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	18.4		
k_SpurAngSenseLPFFc_Hz_f32	48		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	21		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3193438207		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.1		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3170625125		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64911		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.206436098	0.206436105 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.452934027	0.45293398 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	21	21 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3193438207	3193438207	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	132.100006	132.1 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3170625125	3170625125	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	624	624	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64911	64911	~

Τ				
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.9 (Repeat Count = 1)			4
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.01		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.03		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	24		
k_SpurAngSenseLPFFc_Hz_f32	1		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	25		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	5995		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	156		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	9104		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	724		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.260360897	0.260360885 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-		

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.033		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	30		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	29.2		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6994		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	10212		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	824		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.314077795	0.314077834 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Τ						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•		



Toot Ston 2.44 (Bonoot Count = 4)			
Test Step 2.11 (Repeat Count = 1)			<u> </u>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.03		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.036		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	36		
k_SpurAngSenseLPFFc_Hz_f32	30		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	33		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7993		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	204.4		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	11320		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	924		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1325		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.363893569	0.363893542 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.314077795	0.314077834 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.04		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	42.9		
k_SpurAngSenseLPFFc_Hz_f32	5		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	37		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	8992		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	228		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	12428		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1024		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.416725516	0.416725463 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•		



Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.05		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	48		
k_SpurAngSenseLPFFc_Hz_f32	10.5		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	41.1		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3185285529		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	252		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3162767359		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1124		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64411		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.452934027	0.45293398 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.123612463	0.123612462 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	41.0999985	41.1 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3185285529	3185285529	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	252	252 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3162767359	3162767359	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1124	1124	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64411	64411	

T .						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.06		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	54		
k_SpurAngSenseLPFFc_Hz_f32	15		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	45		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	10990		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	276		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	14644		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1224		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.492665172	0.492665137 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.171795845	0.171795819 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Τ						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		



T+ 04 0.45 (D+ 0+ -4)			
Test Step 2.15 (Repeat Count = 1)			~
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.07		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.014		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	20.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	11989		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	300.5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	15752		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1324		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.230012119	0.230012123 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	-
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-	

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1.1		
k_SpurAngSenseLPFFc_Hz_f32	25		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	12988		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16860		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1424		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0137279034	0.013727909 ± 0.00048828125	✓
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		



Test Step 2.17 (Repeat Count = 1)			.
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180.25		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	-
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.07		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.014		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	60		
k_SpurAngSenseLPFFc_Hz_f32	20.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	-74.29		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	11989		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	300.5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	15752		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1324		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1725		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.230012119	0.230012123 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•		



Toot Ston 2.40 (Bonoot Count = 4)			
Test Step 2.19 (Repeat Count = 1)			<u> </u>
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.024		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1.1		
k_SpurAngSenseLPFFc_Hz_f32	25		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	12988		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	324		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16860		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1424		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	1825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0137279034	0.013727909 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-	

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.044		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	5		
k_SpurAngSenseLPFFc_Hz_f32	35.3		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14986		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-180		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	19076		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.358273387	0.358273374 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	



Test Step 2.21 (Repeat Count = 1)			J.
Name	Input Value		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.11		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.054		
Rte Inst Sa DigColPs	tgt Rte Inst Sa DigColPs		
k ColAngSenseLPFFc Hz f32	7.7		
k SpurAngSenseLPFFc Hz f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	44		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	15985		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	360		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	20184		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1724		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0922271013	0.092227111 ± 0.00048828125	•
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	✓
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.064		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	9		
k_SpurAngSenseLPFFc_Hz_f32	45		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	64.6		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	16984		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	180.25		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	21292		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1824		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2225		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.431916416	0.431916394 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Т					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	✓	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	



Test Step 2.23 (Repeat Count = 1)			4
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.044		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	5		
k_SpurAngSenseLPFFc_Hz_f32	35.3		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	24		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	14986		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	-74.29		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	19076		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1624		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2025		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0608986616	0.060898633 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.358273387	0.358273374 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-	

Test Step 2.24 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.11		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.054		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	7.7		
k_SpurAngSenseLPFFc_Hz_f32	40		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	44		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	15985		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	20184		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1724		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0922271013	0.092227111 ± 0.00048828125	•
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395077467	0.395077437 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	•
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	



Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.13		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.074		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	11		
k_SpurAngSenseLPFFc_Hz_f32	50.8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3176660991		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	5		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3210739711		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.129101694	0.129101705 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.471848249	0.471848248 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	84	84 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3176660991	3176660991	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	5	5 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3210739711	3210739711	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	0	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.26 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.14		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.084		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	13		
k_SpurAngSenseLPFFc_Hz_f32	55		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	104		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	18982		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	15.1		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	23508		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	65535		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2425		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.15071702	0.150717003 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.499000609	0.499000604 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

Τ					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	



Test Step 2.27 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.15		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.094		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	15.1		
k_SpurAngSenseLPFFc_Hz_f32	60		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	124		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171418111		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	25		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3191341055		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	63291		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.172835946	0.172835917 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.529510796	0.529510782 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	124	124 ± 0.00048828125	•
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171418111	3171418111	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	25	25 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3191341055	3191341055	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	2244	2244	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	63291	63291	✓

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.28 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.16		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.104		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	17		
k_SpurAngSenseLPFFc_Hz_f32	2.2		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	144.4		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	35		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	25724		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2625		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.19235146	0.192351468 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0272673368	0.027267362 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	



Test Step 2.29 (Percet Count = 1)			4
Test Step 2.29 (Repeat Count = 1)			
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.17		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.114		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	19		
k_SpurAngSenseLPFFc_Hz_f32	4		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	45.8		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3184053452		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	500		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65035		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.21239692	0.212396936 ± 0.00048828125	•
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0490230918	0.049023077 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	45.7999992	45.8 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3184053452	3184053452	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	500	500	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65035	65035	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.30 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.18		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.124		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	21		
k_SpurAngSenseLPFFc_Hz_f32	6		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	184		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	2451658		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	55		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	27940		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	550		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2825		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.231944919	0.231944884 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0726258755	0.07262589 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	•

T						
Actual Function	Count	Expected Function	Count	Result		
DigColPsInt_Init	1	DigColPsInt_Init	1	~		
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•		



Test Step 2.31 (Repeat Count = 1)			J.
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.19		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.134		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	23.3		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	204		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	65		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	0		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	600		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	2925		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.253826022	0.25382598 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	-	

Test Step 2.32 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.144		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	25		
k_SpurAngSenseLPFFc_Hz_f32	10		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	224		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3164602367		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	650		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64885		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.269597292	0.269597309 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.118088603	0.118088622 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	224	224 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3164602367	3164602367	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	650	650	•
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	64885	64885	•

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	





Test Step 2.33 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.21		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.154		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	27		
k_SpurAngSenseLPFFc_Hz_f32	12.4		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	244.7		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	85		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	2145623		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	700		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	3125		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.287725627	0.28772557 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.144289374	0.144289358 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓

T					
Actual Function	Count	Expected Function	Count	Result	
DigColPsInt_Init	1	DigColPsInt_Init	1	~	
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.34 (Repeat Count = 1)			✓
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.22		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.164		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	29.8		
k_SpurAngSenseLPFFc_Hz_f32	14		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	264		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	7444		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	95		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	1		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	750		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	0		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312351763	0.312351755 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.161322653	0.161322631 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	✓
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•





Test Step 2.35 (Repeat Count = 1)			4
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.23		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.174		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	31		
k_SpurAngSenseLPFFc_Hz_f32	16		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	284		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3161587711		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3173908479		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.322643459	0.322643454 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.182138205	0.18213822 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	284	284 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3161587711	3161587711	✓
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	105	105 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3173908479	3173908479	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	0	0	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65535	65535	~

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.36 (Repeat Count = 1)			✓			
Name	Input Value					
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24	0.24				
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.184					
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs					
k_ColAngSenseLPFFc_Hz_f32	33					
k_SpurAngSenseLPFFc_Hz_f32	18					
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	304					
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	9444					
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	115					
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	25846					
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	850					
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	254					
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL					
Name	Actual Value	Expected Value	Result			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.339455187	0.339455134 ± 0.00048828125	~			
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.202437222	0.202437176 ± 0.00048828125	•			
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓			
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	✓			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	•			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	•			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	•			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓			

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•



Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

821.00 Cycles 823.00 Cycles 1084.00 Cycles 1071.00 Cycles TS3.1 TS3.2 TS3.3 TS3.4

Description

VECTOR DESCRIPTION:

TS3.1 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()-

| SS.1 ((Redundant_Format_1_m(\u00fcdlPsEOL()->ColTrim_beg_[32]) != Rte_Pim_bigColPsEoL()->R_ColTrim_Cnt_u32) ||
| (Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->SpurTrim_beg_[32]) != Rte_Pim_bigColPsEoL()->R_SpurTrim_Cnt_u32) ||
| ((uint16)Redundant_Format_1_m(Rte_Pim_bigColPsEoL()->TrimComp_Cnt_u16) != Rte_Pim_bigColPsEoL()->R_TrimCom_Cnt_u16))F"
| TS3.2 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->ColTrim_beg_[32]) != Rte_Pim_bigColPsEoL()| TS3.2 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "(Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "(Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "(Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "(Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_bigColPsEoL()| TS3.2 "(Redundant_Format_1_m(*(uint32*)&Rte_Pim_bigColPsEoL()->Rte_Pim_big

T\$3.2 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_ColTrim_Cnt_u32)|| (Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)|| ((uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16)!= Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16))F"
T\$3.3 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)|| (Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)|| ((uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16)!= Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16))F"
T\$3.4 "((Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->ColTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)|| (Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)|| (Redundant_Format_1_m(*(uint32*)&Rte_Pim_DigColPsEOL()->SpurTrim_Deg_f32)!= Rte_Pim_DigColPsEOL()->R_SpurTrim_Cnt_u32)|| ((uint16)Redundant_Format_1_m(Rte_Pim_DigColPsEOL()->TrimComp_Cnt_u16)!= Rte_Pim_DigColPsEOL()->R_TrimCom_Cnt_u16))F"

Test Step 3.1 (Repeat Count = 1)			V
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	1		
k_SpurAngSenseLPFFc_Hz_f32	8		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0124877691	0.012487743 ± 0.00048828125	•
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0956429243	0.095642891 ± 0.00048828125	•
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	180	180 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3167485951	3167485951	•
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	200	200 ± 0.00048828125	✓
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3166175231	3166175231	✓
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	100	100	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	65435	65435	

Т				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 3.2 (Repeat Count = 1)			<i>•</i>
			Ť
Name	Input Value		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs		
k_ColAngSenseLPFFc_Hz_f32	2		
k_SpurAngSenseLPFFc_Hz_f32	9		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047		
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0248195529	0.024819543 ± 0.00048828125	~
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.106936276	0.106936271 ± 0.00048828125	~
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	220	220 ± 0.00048828125	~
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	3164864511	3164864511	~
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			✓			
Name	Input Value					
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3	0.3				
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7					
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs					
k_ColAngSenseLPFFc_Hz_f32	3					
k_SpurAngSenseLPFFc_Hz_f32	10					
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	120					
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	3171942399					
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	150					
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4265000					
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	550					
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	655					
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL					
Name	Actual Value	Expected Value	Result			
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0369973183	0.036997347 ± 0.00048828125	~			
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.118088603	0.118088622 ± 0.00048828125	~			
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	✓			
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	•			
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓			
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	✓			
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~			
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~			
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~			
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	~			

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~

2014-10-14, 18:07:04+0530





Test Step 3.4 (Repeat Count = 1)			✓		
Name	Input Value				
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4	0.4			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8				
Rte_Inst_Sa_DigColPs	tgt_Rte_Inst_Sa_DigColPs				
k_ColAngSenseLPFFc_Hz_f32	4				
k_SpurAngSenseLPFFc_Hz_f32	11				
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	260				
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	6548212				
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	340				
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	16598742				
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	1321				
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	43625				
tgt_Rte_Inst_Sa_DigColPs.Pim_DigColPsEOL	tgt_Pim_DigColPsEOL				
Name	Actual Value	Expected Value	Result		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.0490230918	0.049023077 ± 0.00048828125	~		
DigColPs_ColTrimStatic_Deg_M_f32	0	0 ± 0.0048828125	~		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.129101694	0.129101705 ± 0.00048828125	~		
DigColPs_SpurTrimStatic_Deg_M_f32	0	0 ± 0.00048828125	~		
DigColPs_TrimCompStatic_Cnt_M_u16	4488	4488	~		
tgt_Pim_DigColPsEOL.ColTrim_Deg_f32	0	0 ± 0.00048828125	✓		
tgt_Pim_DigColPsEOL.R_ColTrim_Cnt_u32	4294967295	4294967295	~		
tgt_Pim_DigColPsEOL.SpurTrim_Deg_f32	0	0 ± 0.00048828125	~		
tgt_Pim_DigColPsEOL.R_SpurTrim_Cnt_u32	4294967295	4294967295	~		
tgt_Pim_DigColPsEOL.TrimComp_Cnt_u16	4488	4488	~		
tgt_Pim_DigColPsEOL.R_TrimCom_Cnt_u16	61047	61047	✓		

T				✓
Actual Function	Count	Expected Function	Count	Result
DigColPsInt_Init	1	DigColPsInt_Init	1	~
Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	Rte_Call_Sa_DigColPs_EOLDigColPosCals_WriteBlock	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓



		–			_
DigColPs_	_SCor	n_NxtS	SetTr	im	

Project	
Module	
Test Object	

Instrumentation: Test Object Only

Statement (C0) Coverage	
Branch (C1) Coverage	
MCC Coverage	
MC/DC Coverage	

Statistics

Total Testcases	
Successful	✓
Failed	
Not Executed	

Module Properties

Project Root Directory
Configuration File
Target Environment
Kind of Test
Linker Options
Source File(s)
File
Compiler Options
File
Compiler Options

Comments/Description	n/Specification
Name	Text

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9

DigColPs_SCom_NxtSetTrim



Attributes				
Name	Value			
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>			
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>			
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>			
Time Unit	Cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP</pre>			

DigCoIPs_SCom_NxtSetTrim

1651	Case '	I. Mer	1105	1651

Specification

Test Step 1.2 (Repeat Count = 1)

Name

Name

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 765.00 Cycles TS1.2 1429.00 Cycles

Description

Test Step 1.1 (Repeat Count = 1)					~
Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					✓
					~
					~
T					✓
Actual Function	Count	Expected Function		Count	Result
					~
					~
					~

Input Value

Actual Value

Expected Value

Result

2014-10-14, 18:21:16+0530



DigColPs_SCom_NxtSetTrim

Name	Actual Value	Expected Value	Result
			✓
			✓

T				V
Actual Function	Count	Expected Function	Count	Result
				-
				-
				-
				V

Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

812.00 Cycles 765.00 Cycles 812.00 Cycles 812.00 Cycles 765.00 Cycles 812.00 Cycles 812.00 Cycles 765.00 Cycles 765.00 Cycles 767.00 Cycles 767.00 Cycles 765.00 Cycles 767.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.11 TS2.12 TS2.12 TS2.15 TS2.15 TS2.15 TS2.16 TS2.16 TS2.17 TS2.18

Description

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			✓
			✓

DigColPs_SCom_NxtSetTrim

Actual Function

2014-10-14, 18:21:16+0530



Count Result

Name		Actual Value	Expected Value		Result
					~
					~
Т					V
Actual Function	Count	Expected Function		Count	Result
					~

Test Step 2.2 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
Name		Actual value	Expected value		Nesuit
					~
					~
					✓
					~
T					V
Actual Function	Count	Expected Function		Count	
	Count			Count	√ V
					~
					_

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
			✓
			~
			_
			~

Count Expected Function



DiaColPs 3	SCom	NxtSetTrim
2.g 0 0 0_		

Name

Test Step 2.4 (Repeat Count = 1)				V
Name		Input Value		
		•		
Name		Actual Value	Expected Value	Result
				~
				\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
				~
				~
Т				✓
Actual Function	Count	Expected Function	Cour	t Result
Actual Full Cloth	Count	Expected Function	Cour	v Kesuit
				~
				•
Test Step 2.5 (Repeat Count = 1)				✓
Name		Input Value		
Name		Actual Value	Expected Value	Result
				~
				~
				~
T				V
Actual Function	Count	Expected Function	Cour	t Result
				~
				~
				~
Took Ston 2 C (Donost Count - 4)				
Test Step 2.6 (Repeat Count = 1)				✓

Input Value

DigColPs_SCom_NxtSetTrim



Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					~
					~
					V
Т					V
Actual Function	Count	Expected Function		Count	Result
					~
					-
Test Step 2.7 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					V
					~
					-
		1	1		

T				✓
Actual Function	Count	Expected Function	Count	Result
				~
				•
				~
				~

DigColPs_SCom_NxtSetTrim



Test Step 2.8 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
Name		Actual value	Expected value		rtesuit ✓
					J
					\ \ \
					~
		<u>I</u>			
T					
					✓
Actual Function	Count	Expected Function		Count	Result
					~
					•
					~
Test Step 2.9 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					V
					✓
					~
-					
T					V
Actual Function	Count	Expected Function		Count	Result
					~
					~
					~
Test Step 2.10 (Repeat Count = 1)					✓
Name		Input Value			

DigColPs_SCom_NxtSetTrim



Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					•
					~
					~
			I		
T					✓
	Count	Expected Function		Count	Result
Actual Full-citoti	Count	Expected Function		Oount	\(\sigma\)
					~
					~
		'			
Test Step 2.11 (Repeat Count = 1)					V
Name		Input Value			
Tullio		mpat value			
Name		Actual Value	Expected Value		Result
Name		Actual Value	Expected Value		Result
Name		Actual Value	Expected Value		
Name		Actual Value	Expected Value		~ ~
Name		Actual Value	Expected Value		· · ·
Name		Actual Value	Expected Value		~ ~
		Actual Value	Expected Value		\rightarrow \right
T					\rightarrow \right
T	Count	Actual Value Expected Function		Count	v v v
T	Count			Count	v v v
T	Count			Count	Result
T	Count			Count	v v v
T	Count			Count	v v v
T	Count			Count	v v v
T Actual Function	Count			Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v
T Actual Function	Count			Count	Result
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	Result
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.12 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.12 (Repeat Count = 1)	Count	Expected Function		Count	Result
Test Step 2.12 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.12 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result

DigColPs_SCom_NxtSetTrim



Name		Actual Value	Expected Value		Result
					~
Τ					V
Actual Function	Count	Expected Function		Count	Result
					~
					~
					~

Test Step 2.13 (Repeat Count = 1)				✓
Name		Input Value		
Name		Actual Value	Expected Value	Result
Name		Actual value	Expected value	Kesuit
				V
				~
				~
				~
Τ				✓
Actual Function	Count	Expected Function	Coun	Result
				~
				~

Test Step 2.14 (Repeat Count = 1	1)			✓
Name	<u>'</u>	Input Value		
Name		Actual Value	Expected Value	Result
		7 Column Fallan		✓ ✓
				~
				✓
				~
T				~

DigColPs_SCom_NxtSetTrim



Test Step 2.15 (Repeat Count = 1)					V
Name		Input Value			
			1=		
Name		Actual Value	Expected Value		Result
					•
					\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
					~
		I			
T					✓
Actual Function	Count	Expected Function	Co	ount	Result
					✓
					Ž
		-	· · · · · · · · · · · · · · · · · · ·		
- 101 010 (D. 10 11)					
Test Step 2.16 (Repeat Count = 1) Name		Input Value			✓
Name		mput value			
Name		Actual Value	Expected Value		Result
					·
					J
					~
					~
Т					✓
Actual Function	Count	Expected Function	Co	ount	Result
					~
					~
					~
Test Step 2.17 (Repeat Count = 1)					✓
Name		Input Value			

DigColPs_SCom_NxtSetTrim



Name		Input Value			
Name		Actual Value	Expected Value		Result
··········		The state of the s			~
					~
					-
					~
					~
T					V
Actual Function	Count	Expected Function		Count	Result
	- Count				~
					~
					~
T (0) 0 (0 (D - 10 (D)(D - 10 (D - 10 (D - 10 (D)(D - 10 (D)(D - 10 (D)(D - 10 (D					
Test Step 2.18 (Repeat Count = 1)					✓
Name		Input Value			
Namo		Actual Value	Expected Value		Docult
Name		Actual Value	Expected Value		Result
Name		Actual Value	Expected Value		~
Name		Actual Value	Expected Value		*
Name		Actual Value	Expected Value		~ ~
Name		Actual Value	Expected Value		· · ·
Name		Actual Value	Expected Value		~ ~
		Actual Value	Expected Value		· · · · · · · · · · · · · · · · · · ·
Т					\rightarrow \right
Т	Count	Actual Value Expected Function		Count	v v v
Т	Count			Count	v v v
	Count			Count	Result
Т	Count			Count	v v v
Т	Count			Count	v v v
Т	Count			Count	v v v
T Actual Function	Count			Count	v v v v v v v v v v v v v v v v v v v
T Actual Function	Count			Count	v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function	Count			Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	v v v v v v v v v v v v v v v v v v v
Test Step 2.19 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
T Actual Function Test Step 2.19 (Repeat Count = 1)	Count	Expected Function		Count	Result
Test Step 2.19 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.19 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.19 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result

DigColPs_SCom_NxtSetTrim



Name		Actual Value	Expected Value		Result
					~
Т					V
Actual Function	Count	Expected Function		Count	Result
					~
					✓

Test Step 2.20 (Repeat Count = 1)					V
Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					✓
					~
					•
_					
Т					✓
Actual Function	Count	Expected Function	Cou	unt	Result
					~
					✓

				✓
	Input Value			
	Actual Value	Expected Value		Result
	7 TOTALL TUILU	ZAPOOLOU TULUO		
				/
				•
				\(\frac{1}{2}\)
				7
				7
				7
				· ·
Count	Expected Function		Count	7
		Input Value Actual Value		

DigCoIPs_SCom_NxtSetTrim

2014-10-14, 18:21:16+0530



Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 812.00 Cycles
TS3.2 1338.00 Cycles
TS3.3 1381.00 Cycles
TS3.4 781.00 Cycles
TS3.5 778.00 Cycles
TS3.6 772.00 Cycles
TS3.7 769.00 Cycles
TS3.7 769.00 Cycles

Description

Test Step 3.1 (Repeat Count = 1)				✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
Name		Actual Value	Expected Value		ixesuit
					•
					•
					•
T					J
Actual Function	Count	Expected Function		Count	Result
		,		223110	~

DigColPs_SCom_NxtSetTrim

Test Step 3.2 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
			•		~
					~
					~
					~
Т					✓
Actual Function	Count	Expected Function		Count	Result
					~
					~
					~
Test Step 3.3 (Repeat Count = 1)					✓
Name		Input Value			
Name		A - 4 1 V - 1	From a stand Walking		D16
Name		Actual Value	Expected Value		Result
					~
					✓
					-
Т					~
Actual Function	Count	Expected Function		Count	Result
					→
					•
					~
Test Step 3.4 (Repeat Count = 1)					V
Name		Input Value			

DigColPs_SCom_NxtSetTrim

2014-10-14, 18:21:16+0530



Name		Input Value		
Name		Actual Value	Francisco Volum	Result
Name		Actual value	Expected Value	Result
				•
				J
T	_		_	V
Actual Function	Count	Expected Function	Coun	t Result
				•
				•
Test Step 3.5 (Repeat Count = 1)				✓
Name		Input Value		
			- -	l
Name		Actual Value	Expected Value	Result
				-
				•
				•
Τ				V
Actual Function	Count	Expected Function	Coun	t Result
				•
Test Step 3.6 (Repeat Count = 1)		Inmut Value		✓
Name		Input Value		
		I and the second		
Name		Actual Value	Expected Value	Result

DigColPs_SCom_NxtSetTrim

2014-10-14, 18:21:16+0530



Name	Actual Value	Expected Value	Result
			~
			~
			~
			~

Τ			V	
Actual Function	Count	Expected Function	Count	Result
				~
				✓
				_

Test Step 3.7 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					•
					~
					Ž
T					V
	Count	Expected Function		Count	
Actual Fullction	Count	Expected Function		Count	Result
					_

Test Step 3.8 (Repeat Count = 1)			✓
Name	Input Value		
Name	Actual Value	Expected Value	Result
			~
			✓
			~
			~

DigColPs_SCom_NxtSetTrim

2014-10-14, 18:21:16+0530



Τ ·				
Actual Function	Count	Expected Function	Count	Result
				~
				~
				~

2014-10-14, 18:11:16+0530





 Project
 DigColPs

 Module
 DigColPs

 Test Object
 DigColPs_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -Dinline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs\utp\contr

ame	Text
odule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):3994 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested. Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code. Note 3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 4: In ""DigColPs_Init1()" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mag

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2014-10-14, 18:11:16+0530



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg</pre>
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

2014-10-14, 18:11:16+0530





Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cvcles:

TS1.1 4792.00 Cycles Longest Execution Path TS1.2 4457.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

```
TS1.1 "Longest Execution Path =>
 if (I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>False
if (I2CSensCommFits_Cnt_T_u08 != 0U)=>False
 If (I2CSensCommFits_Cnt_T_u08 != 0U)=>False
if (I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if (I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>False
if (I0IgColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>True
if (ICOlSensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if (ICOlSensorFault_Cnt_T_lgc == TRUE) ||
if ((DigColPs_SpurSensorDiagFailed_Cnt_M_Igc == TRUE) || (SpurParityOrCommErr_Cnt_T_Igc == TRUE))=>False
if ((ColSensorFault_Cnt_T_Igc == TRUE) ||
(SpurSensorFault_Cnt_T_Igc == TRUE) ||
(SpurSensorFault_Cnt_T_Igc == TRUE) ||
(SpurParityErrorEvt_Cnt_T_Igc == TRUE) ||
(SpurParityErrorEvt_Cnt_T_Igc == TRUE) >>False
if (I2CHwDataType_Cnt_T_u08 == D_ANGLEDATA_CNT_U08)=>True
if (SensorsSampleOK_Cnt_T_Igc == TRUE) >> True
if (DigColPs_ColLPFinitDone_Cnt_M_Igc == FALSE)=> True
if (DigColPs_SpurLPFInitDone_Cnt_M_Igc == FALSE)=> True
if ((ColParityOrCommErr_Cnt_T_Igc == TRUE) || (SpurParityOrCommErr_Cnt_T_Igc == TRUE))=> False
if ((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=> True"
TS1.2 "Shortest Execution_Path =>
 If ((DIgCoIP's_CoIsensorFaultAcc_Cnt_M_u16 == 00) && (DIgCoIP's_St
TS1.2 "Shortest Execution Path =>
if (I2CHwDataType_Cnt_T_u08!= D_ANGLEDATA_CNT_U08)=>False
if (I2CHwColAngle_Cnt_T_u16 & 0x40000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x40000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000!)!= 00!)=>False
if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000!)!= 00!)=>False
 if ((I2CHwSpurAngle_Cnt_T_u16 & 0x80000) != 0U)=>False
if ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>False
if ((ColSensorFault_Cnt_T_lgc == TRUE) ||
(SpurSensorFault_Cnt_T_lgc == TRUE) ||
(ColParityErrorEvt_Cnt_T_lgc == TRUE) => True
if (SensorSampleOK_Cnt_T_lgc == TRUE) => False
if ((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) => False
if ((ColParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) => False
if ((DigColPs_ColSensorFaultAcc_Cnt_M_u16 == 0U) && (DigColPs_SpurSensorFaultAcc_Cnt_M_u16 == 0U))=> False"
```

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	250		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	512		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	122		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	920		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	210.6		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓

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DigColPs_Per1

2014-10-14, 18:11:16+0530



Expected Value Actual Value DigColPs_ColParityErrorAcc_Cnt_M_u16 240 240 DigColPs_ColParityError_Cnt_M_lgc 0 0 DigColPs_ColRoughTurns_Cnt_M_s16 3 3 DigColPs_ColSensorDiagFailed_Cnt_M_lgc 1 DigColPs_ColSensorFaultAcc_Cnt_M_u16 0 0 DigColPs_I2CColSensorFault_Cnt_M_lgc 1 0.087890625 0.087890625 ± 0.0001220703125 DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 0.087890625 0.087890625 ± 0.0001220703125 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc 0 0 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 0.087890625 0.087890625 ± 0.0001220703125 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 0.087890625 0.087890625 ± 0.0001220703125 DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 1080.08789 1080.087891 ± 0.00048828125 ${\sf DigColPs_SpurLPFInitDone_Cnt_M_lgc}$ DigColPs_SpurParityErrorAcc_Cnt_M_u16 502 502 ${\sf DigColPs_SpurParityError_Cnt_M_lgc}$ 0 0 DigColPs_SpurRoughTurns_Cnt_M_s16 3 3 ${\tt DigColPs_SpurSensorDiagFailed_Cnt_M_lgc}$ 0 0 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 0 0 109 $Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$ 109 Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 0 0 Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

Τ				✓.
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

0

0

Test Step 1.2 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetData()	7
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-160
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.324
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	256
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156
DigColPs_I2CHwColAngle_Cnt_M_u16	0
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0
DigColPs_I2CSensCommFlts_Cnt_M_u08	12
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1561
DigColPs_PrevI2CHwColAngle_Deg_M_f32	190
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3500
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	225
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	406
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.344
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	254
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168
k_SenseDetErrDiag_Cnt_str.Threshold	80
k_SenseDetErrDiag_Cnt_str.PStep	38
k_SenseDetErrDiag_Cnt_str.NStep	13

2014-10-14, 18:11:16+0530



Name	Input Value		
k_SenseParityErrDiag_Cnt_str.Threshold	710		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-413.628082	-413.6280859 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		✓

DigColPs_Per1

2014-10-14, 18:11:16+0530



Test Case 2: Boundary Test

2014-10-14, 18:11:16+0530



Specification

DigColPs_Per1

Performance Metrics: (With "None" instrumentation and WithPS Environment) CPU Cycles: 5109.00 Cycles 5174.00 Cycles 4830.00 Cycles 5043.00 Cycles TS2 1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 5043.00 Cycles 5026.00 Cycles 5010.00 Cycles 5004.00 Cvcles TS2.8 4994.00 Cycles 4997.00 Cycles 5003.00 Cycles TS2.9 TS2.10 TS2.11 5016.00 Cycles 4890.00 Cycles 5026.00 Cycles 5036.00 Cycles TS2.12 TS2.13 TS2.14 TS2.15 5016.00 Cycles TS2.16 TS2.17 TS2.18 4994.00 Cycles 4881.00 Cycles 4982.00 Cycles TS2.19 4982.00 Cycles 5001.00 Cycles 5015.00 Cycles 5028.00 Cycles 5028.00 Cycles 5015.00 Cycles 5015.00 Cycles 5016.00 Cycles 5016.00 Cycles 5009.00 Cycles 5014.00 Cycles 5014.00 Cycles 5042.00 Cycles 5042.00 Cycles TS2.20 TS2.21 TS2.22 TS2.23 TS2.24 TS2.25 TS2.26 TS2.27 TS2.28 TS2.29 TS2.30 TS2.31 TS2.32 TS2.33 TS2.34 TS2.35 5042.00 Cycles 5010.00 Cycles 5044.00 Cycles 5039.00 Cycles 5018.00 Cycles 5018.00 Cycles 5027.00 Cycles 4951.00 Cycles 5002.00 Cycles 4986.00 Cycles 4983.00 Cycles 4975.00 Cycles TS2.36 TS2.37 TS2.38 TS2.39 TS2.40 TS2.41 TS2.42 4975.00 Cycles 4885.00 Cycles 5004.00 Cycles 5015.00 Cycles 5015.00 Cycles 5030.00 Cycles 5030.00 Cycles 4980.00 Cycles 5028.00 Cycles TS2.43 TS2.44 TS2.45 TS2.46 TS2.46 TS2.47 TS2.48 TS2.49 TS2.50 TS2.51 5028.00 Cycles 4898.00 Cycles 5021.00 Cycles 5018.00 Cycles 5014.00 Cycles 5034.00 Cycles 4976.00 Cycles 5018.00 Cycles TS2.52 TS2.53 TS2.54 TS2.55 TS2.56 TS2.57 TS2.58 5018.00 Cycles 4977.00 Cycles 5021.00 Cycles 5042.00 Cycles 5082.00 Cycles 5012.00 Cycles 5086.00 Cycles 5096.00 Cycles 5037.00 Cycles 4987.00 Cycles 5040.00 Cycles TS2.59 TS2.60 TS2.61 TS2.62 TS2.62 TS2.63 TS2.64 TS2.65 TS2.66 TS2.67 4987.00 Cycles 5040.00 Cycles 5009.00 Cycles 5014.00 Cycles 4999.00 Cycles 4988.00 Cycles 5041.00 Cycles TS2.68 TS2.69 TS2.70 TS2.71 TS2.72 TS2.73 TS2.74 TS2.75 4988.00 Cycles 5043.00 Cycles 4988.00 Cycles 4991.00 Cycles 4996.00 Cycles TS2.76 TS2.77 TS2.78 4992.00 Cycles 4930.00 Cycles 4746.00 Cycles 4729.00 Cycles TS2.79 TS2.80 TS2.81 TS2.82 4729.00 Cycles 4729.00 Cycles 4987.00 Cycles 5015.00 Cycles 5014.00 Cycles 4999.00 Cycles 4988.00 Cycles 5040.00 Cycles TS2.83 TS2.84 TS2.85 TS2.86 TS2.87 TS2.88 TS2.89 4988.00 Cycles 5014.00 Cycles 4988.00 Cycles 5028.00 Cycles 5014.00 Cycles TS2.90 TS2.91 TS2.92 TS2.93 TS2.94 TS2.95 TS2.96 TS2.97 4988.00 Cycles 5009.00 Cycles 4988.00 Cycles TS2.98 4988.00 Cycles 4988.00 Cycles 4973.00 Cycles 5028.00 Cycles 4988.00 Cycles 4746.00 Cycles 4726.00 Cycles 4750.00 Cycles 4773.00 Cycles TS2.99 TS2.100 TS2.101

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TS2.102 TS2 103 TS2.103 TS2.104 TS2.105





Description VECTOR DESCRIPTION:

TS2.1 All Min TS2.2 All Max TS2.3 DigColPs_I2CHwColAngle_Cnt_M_u16=Min TS2.4 DigColPs_I2CHwColAngle_Cnt_M_u16=Max TS2.5 DigColPs_I2CHwColAngle_Cnt_M_u16=Pos TS2.6 DigColPs_I2CHwSpurAngle_Cnt_M_u16=Min TS2.7 DigColPs_I2CHwSpurAngle_Cnt_M_u16=Max TS2.8 DigColPs_I2CHwSpurAngle_Cnt_M_u16=Pos TS2.9 DigColPs_I2CHwDataType_Cnt_M_u08=Min TS2.10 DigColPs_I2CHwDataType_Cnt_M_u08=Max TS2.11 DigColPs_I2CHwDataType_Cnt_M_u08=Pos DigColPs_I2CSensCommFlts_Cnt_M_u08=Min DigColPs_I2CSensCommFlts_Cnt_M_u08=Max DigColPs_I2CSensCommFlts_Cnt_M_u08=Pos TS2.13 TS2.14 TS2.15 DigColPs_ColSensorDiagFailed_Cnt_M_lgc=Min DigCoIPs_ColSensorDiagFailed_Cnt_M_igc=Min DigCoIPs_ColSensorDiagFailed_Cnt_M_igc=Max k_SenseDetErrDiag_Cnt_str.Threshold=Min k_SenseDetErrDiag_Cnt_str.Threshold=Max k_SenseDetErrDiag_Cnt_str.Threshold=Pos k_SenseDetErrDiag_Cnt_str.Pstep=Min k_SenseDetErrDiag_Cnt_str.Pstep=Max k_SenseDetErrDiag_Cnt_str.Pstep=Pos k_SenseDetErrDiag_Cnt_str.Pstep=Min TS2.16 TS2.17 TS2.18 TS2.19 TS2 20 TS2.21 TS2.22 k_SenseDetErrDiag_Cnt_str.Nstep=Min
k_SenseDetErrDiag_Cnt_str.Nstep=Mix
k_SenseDetErrDiag_Cnt_str.Nstep=Max
k_SenseDetErrDiag_Cnt_str.Nstep=Pos
DigColPs_ColParityErrorAcc_Cnt_M_u16=Min
DigColPs_ColParityErrorAcc_Cnt_M_u16=Max TS2 23 TS2.24 TS2.25 TS2 26 TS2.27 DigCoIPs_ColParityErrorAcc_Cnt_M_u16=Pos
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16=Min
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16=Max
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16=Pos TS2.28 TS2.29 TS2.30 TS2.31 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc=Min DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc=Max DigCoIPs_SpurParityErrorAcc_Cnt_M_u16=Min TS2.32 TS2.33 TS2.34 DigColPs_SpurParityErrorAcc_Cnt_M_u16=Max DigColPs_SpurParityErrorAcc_Cnt_M_u16=Pos DigColPs_PrevI2CHwColAngle_Cnt_M_u16=Min TS2.35 TS2.36 TS2.37 DigCoIPs PrevI2CHwCoIAngle Cnt M u16=Max DigCoIPs PrevI2CHwCoIAngle Cnt M u16=Pos DigCoIPs CoISensorFaultAcc Cnt M u16=Min TS2.38 TS2 39 TS2.40 DigColPs_ColSensorFaultAcc_Cnt_M_u16=Max DigColPs_ColSensorFaultAcc_Cnt_M_u16=Pos DigColPs_ColRoughTurns_Cnt_M_s08=Min TS2.41 TS2 42 TS2.43 TS2.44 DigColPs_ColRoughTurns_Cnt_M_s08=Max DigColPs_ColRoughTurns_Cnt_M_s08=Zero DigColPs_ColRoughTurns_Cnt_M_s08=Pos TS2.45 TS2.46 DigCoIPS_CoIROughTurns_Cnt_M_s08=Pos DigCoIPs_CoIRoughTurns_Cnt_M_s08=Neg DigCoIPs_SpurRoughTurns_Cnt_M_s08=Min DigCoIPs_SpurRoughTurns_Cnt_M_s08=Zero TS2.48 TS2.49 DigCoIPs_SpurRoughTurns_Cnt_M_s08=Pos
DigCoIPs_SpurRoughTurns_Cnt_M_s08=Neg
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.K=Min
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.K=Max
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.K=Pos
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV=Min TS2.51 TS2.52 TS2.53 TS2.54 TS2 55 TS2.56 DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV=Min DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV=Max DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV=Pos DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV=Zero DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV=Neg DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.K=Min DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.K=Pos DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Min DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Min DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Max DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Pos DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Zero DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Zero DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Zero DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV=Reg Rte_Pim_DigCoIPsEOL.CoITrim_Deg_f32=Min Rte_Pim_DigCoIPsEOL.CoITrim_Deg_f32=Max TS2.57 TS2 58 TS2.59 TS2.60 TS2 61 TS2.62 TS2.63 TS2.64 TS2.65 TS2.66 TS2.67 TS2.68 TS2.69 TS2.70 Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Max Rte_Pim_DigColPsEOL.ColTrim_Deg_f32=Pos Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Min TS2 71 TS2.72 TS2.73 Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Max Rte_Pim_DigColPsEOL.SpurTrim_Deg_f32=Pos DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16=Min TS2 74 TS2.75 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16=Max DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16=Pos k_SenseParityErrDiag_Cnt_str.Threshold=Min TS2.76 TS2 77 TS2.78 k_SenseParityErrDiag_Cnt_str.Threshold=Max TS2.79 k_SenseParityErrDiag_Cnt_str.Threshold=Pos k_StepDetect_Deg_f32=Min k_StepDetect_Deg_f32=Max TS2.80 TS2.81 TS2.82 k_StepDetect_Deg_f32=Pos DigColPs_PrevI2CHwColAngle_Deg_M_f32=Min DigColPs_PrevI2CHwColAngle_Deg_M_f32=Max TS2.83 TS2.84 TS2.85 DigColPs_PrevI2CHwColAngle_Deg_M_f32=Pos DigColPs_PrevI2CHwSpurAngle_Deg_M_f32=Min DigColPs_PrevI2CHwSpurAngle_Deg_M_f32=Max TS2.86 TS2.87 TS2.88 DigCoIPs PrevI2CHwSpurAngle Deg M f32=Pos DigCoIPs ReqI2CSnsrDataType_Cnt_M_u08=Min DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08=Max TS2.89 TS2 90 TS2 91 TS2.92 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08=Pos k_SenseParityErrDiag_Cnt_str.Pstep=Min k_SenseParityErrDiag_Cnt_str.Pstep=Max TS2 93

2014-10-14, 18:11:16+0530





TS2.95 k_SenseParityErrDiag_Cnt_str.Pstep=Pos
TS2.96 k_SenseParityErrDiag_Cnt_str.Nstep=Min
TS2.97 k_SenseParityErrDiag_Cnt_str.Nstep=Max
TS2.98 k_SenseParityErrDiag_Cnt_str.Nstep=Pos
TS2.90 DigColPsInt_GetData=Min
TS2.101 DigColPsInt_GetData=Max
TS2.101 DigColPsInt_GetData=Pos
TS2.102 DigColPs_ColLPFInitDone_Cnt_M_lgc=Min
TS2.103 DigColPs_ColLPFInitDone_Cnt_M_lgc=Max
TS2.104 DigColPs_SpurLPFInitDone_Cnt_M_lgc=Min
TS2.105 DigColPs_SpurLPFInitDone_Cnt_M_lgc=Min

TS2.104 DigColPs_SpurLPFInitDone_Cnt_M_lgc	=Max		
Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	0		
DigColPs_ColRoughTurns_Cnt_M_s16	-5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	1		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	0		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960	-3960 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0	0	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓



T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enable12CInterrupt	1	Enablel2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.2 (Repeat Count = 1) Name	Input Value		
	16		
DigColPsInt_GetData()			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000		
DigColPs_ColRoughTurns_Cnt_M_s16	5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255		
DigColPs_I2CHwColAngle_Cnt_M_u16	65535		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08	31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4320		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000		
DigColPs_SpurRoughTurns_Cnt_M_s16	11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	255		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	50		
k_SenseParityErrDiag_Cnt_str.Threshold	1000		
k_SenseParityErrDiag_Cnt_str.PStep	50		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	340		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2159.91211	2159.912109 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000	1000	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	205	205	
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	
	1	1	
DigColPs I2CSpurSensorFault Cnt M Igc		4095	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	4095 359.912109	359.9121094 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095 359.912109 4095	359.9121094 ± 0.0001220703125 4095	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4095 359.912109 4095 359.912109	359.9121094 ± 0.0001220703125 4095 359.9121094 ± 0.0001220703125	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4095 359.912109 4095 359.912109 4	359.9121094 ± 0.0001220703125 4095 359.9121094 ± 0.0001220703125 4	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	4095 359.912109 4095 359.912109 4 4319.91211	$359.9121094 \pm 0.0001220703125$ 4095 $359.9121094 \pm 0.0001220703125$ 4 $4319.912109 \pm 0.00048828125$	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	4095 359.912109 4095 359.912109 4	359.9121094 ± 0.0001220703125 4095 359.9121094 ± 0.0001220703125 4	

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205	205	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	999		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	124		
DigColPs_I2CSensCommFlts_Cnt_M_u08	1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30		
k_SenseDetErrDiag_Cnt_str.Threshold	2		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	2		
k_SenseParityErrDiag_Cnt_str.Threshold	15		
k_SenseParityErrDiag_Cnt_str.PStep	1		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	22.3		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-612.747253	-612.7472656 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	15	15	•
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	122	122	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	107.252747	107.2527344 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	105.221069	105.2210938 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5	5	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.439453125	0.439453125 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12	12	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.0546875	1.0546875 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1694.77893	-1694.778906 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	15	15	•
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	28	28	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T ·					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓	

Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.16		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	153		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128		
DigColPs_I2CHwColAngle_Cnt_M_u16	65535		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	524		
DigColPs_I2CSensCommFlts_Cnt_M_u08	2		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	15		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.08		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	126		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50		
k_SenseDetErrDiag_Cnt_str.Threshold	4		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	3		
k_SenseParityErrDiag_Cnt_str.Threshold	10		
k_SenseParityErrDiag_Cnt_str.PStep	3		
k_SenseParityErrDiag_Cnt_str.NStep	2		
k_StepDetect_Deg_f32	24		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-566.189087	-566.1890625 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	10	10	
DigColPs ColParityError Cnt M Igc	0	0	

-4

1

125

-4 1

125

DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	153.810913	153.8109375 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	212.807007	212.8070313 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	15	15	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	1.31835938	1.318359375 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1587.19299	-1587.192969 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	10	10	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	47	47	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Γ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.5 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	168		
DigColPs_I2CHwColAngle_Cnt_M_u16	2048		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	924		
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	3		
DigCoIPs PrevI2CHwCoIAngle Cnt M u16	1		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.6		
	16		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16			
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.14		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	142		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	101		
k_SenseDetErrDiag_Cnt_str.Threshold	6		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	4		
k_SenseParityErrDiag_Cnt_str.Threshold	20		
k_SenseParityErrDiag_Cnt_str.PStep	5		
k_SenseParityErrDiag_Cnt_str.NStep	3		
k_StepDetect_Deg_f32	26		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-455.982422	-455.9824219 ± 0.00048828125	1.000
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
	20	20	
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	
DigColPs_ColParityError_Cnt_M_lgc			
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	164	164	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	264.017578	264.0175781 ± 0.0001220703125	•
	050 000000	358.996875 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	358.996826		
· - · · · · - · - · · · · · · · · · · ·	358.996826	3	•
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc		3	•
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	3		•
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	3	1	•
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	3 1 1	1	
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3 1 1 0.087890625	1 1 0.087890625 ± 0.0001220703125	
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3 1 1 0.087890625 16	1 1 0.087890625 ± 0.0001220703125 16	
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3 1 1 0.087890625 16 1.40625	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	3 1 1 0.087890625 16 1.40625 3	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	3 1 1 0.087890625 16 1.40625 3 -1441.00317	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	3 1 1 0.087890625 16 1.40625 3 -1441.00317 1	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Det_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	3 1 1 0.087890625 16 1.40625 3 -1441.00317 1 20	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Det_M_u16 DigColPs_PrevI2CHwSpurAngle_Det_M_g32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	3 1 1 0.087890625 16 1.40625 3 -1441.00317 1 20 0	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	3 1 1 0.087890625 16 1.40625 3 -1441.00317 1 20 0	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	3 1 1 0.087890625 16 1.40625 3 -1441.00317 1 20 0 -3	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3 0 97	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	3 1 1 0.087890625 16 1.40625 3 -1441.00317 1 20 0	1 1 0.087890625 ± 0.0001220703125 16 1.40625 ± 0.0001220703125 3 -1441.003125 ± 0.00048828125 1 20 0 -3	



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146		
DigColPs_I2CHwColAngle_Cnt_M_u16	124		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	25		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	35		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	20		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	12		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	8		
k_SenseDetErrDiag_Cnt_str.PStep	20		
k_SenseDetErrDiag_Cnt_str.NStep	5		
k_SenseParityErrDiag_Cnt_str.Threshold	30		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	4		
k_StepDetect_Deg_f32	28.5		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-237.87265	-237.8726563 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	30	30	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141	141	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	122.12735	122.1273438 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	176.351563	176.3515625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	25	25	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	2.19726563	2.197265625 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	20	20	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.7578125	1.7578125 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1263.64844	-1263.648438 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	30	30	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	139	139	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
ComputeRoughTurns	2	ComputeRoughTurns	2	•	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•	

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.28		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	123		
DigColPs_I2CHwColAngle_Cnt_M_u16	628		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08	5		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	15.5		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	856		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	105		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	40		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	30		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-71.1386719	-71.13867188 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	40	40	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	117	117	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	288.861328	288.8613281 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	24.9484863	24.9484375 ± 0.0001220703125	~
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35	35	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.07617188	3.076171875 ± 0.0001220703125	✓

DigColPs_Per1

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24	24	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.109375	2.109375 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1055.05151	-1055.051563 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	40	40	✓
DigColPs_SpurParityError_Cnt_M_lgc	1	1	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	10	10	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.8 (Repeat Count = 1)	Innut Value		
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.32		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	245		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146		
DigColPs_I2CHwColAngle_Cnt_M_u16	1132		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2124		
DigColPs_I2CSensCommFlts_Cnt_M_u08	6		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	45		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	55		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	28		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	18		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.32		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	146		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106		
k_SenseDetErrDiag_Cnt_str.Threshold	12		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	7		
k_SenseParityErrDiag_Cnt_str.Threshold	50		
k_SenseParityErrDiag_Cnt_str.PStep	11		
k_SenseParityErrDiag_Cnt_str.NStep	6		
k_StepDetect_Deg_f32	32		
Name	Actual Value	Expected Value	Resul
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	116.465622	116.465625 ± 0.00048828125	-
DigColPs ColLPFInitDone Cnt M Igc	1	1	•
DigColPs ColParityErrorAcc Cnt M u16	50	50	
B: O ID O ID 'I F O I M I			

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DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	116.465622	116.465625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	264.787476	264.7875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	45	45	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.95507813	3.955078125 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	28	28	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.4609375	2.4609375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-815.212524	-815.2125 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	50	50	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	99	99	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~



Test Step 2.9 (Repeat Count = 1) Name	Input Value		
	Input Value		
DigColPsInt_GetData()			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.36		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158		
DigColPs_I2CHwColAngle_Cnt_M_u16	1636		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2524		
DigColPs_I2CSensCommFlts_Cnt_M_u08	7		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	55		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	65		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	32		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	21		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.38		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	756		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	103		
k_SenseDetErrDiag_Cnt_str.Threshold	14		
k_SenseDetErrDiag_Cnt_str.PStep	35		
k_SenseDetErrDiag_Cnt_str.NStep	8		
k_SenseParityErrDiag_Cnt_str.Threshold	60		
k_SenseParityErrDiag_Cnt_str.PStep	13		
k_SenseParityErrDiag_Cnt_str.NStep	7		
k_StepDetect_Deg_f32	34		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	324.940247	324.9402344 ± 0.00048828125	
	324.340241		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
		0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	0		•
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc	0	60	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	0 60 0	60 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0 60 0 2	60 0 2	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0 60 0 2 0	60 0 2 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc	0 60 0 2 0 150	60 0 2 0 150	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32	0 60 0 2 0 150	60 0 2 0 150 1 324.9402344 ± 0.0001220703125	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32	0 60 0 2 0 150 1 324.940247 175.868713	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSburSensorFault_Cnt_M_u08	0 60 0 2 0 150 1 324.940247 175.868713 7	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc	0 60 0 2 0 150 1 324.940247 175.868713 7	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_lgc	0 60 0 2 0 150 1 324.940247 175.868713 7	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_DFKSV_Cnt_M_str.SV_Uls_f32	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CCHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_u16 DigCoIPs_Prev12CHwColAngle_Deg_M_f32 DigCoIPs_Prev12CHwColAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0	
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHsvSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHsvSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHsvSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_Prev12CHwColAngle_Cnt_M_u16 DigCoIPs_Prev12CHwColAngle_Cnt_M_u16 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CCHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0 1 0 95	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1 0 95	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CCbessorFault_Cnt_M_lgc DigColPs_12CCbessorFault_Cnt_M_lgc DigColPs_12CCbessorCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigColPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0 1 0 95	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1 0 95 109	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 60 0 2 0 150 1 324.940247 175.868713 7 1 55 4.83398438 32 2.8125 2 -544.131287 1 60 0 1 0 95	60 0 2 0 150 1 324.9402344 ± 0.0001220703125 175.86875 ± 0.0001220703125 7 1 55 4.833984375 ± 0.0001220703125 32 2.8125 ± 0.0001220703125 2 -544.13125 ± 0.00048828125 1 60 0 1 0 95	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	263		
	2		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs I2CHwColAngle Cnt M u16	2140		
0 = 0 = = =	4		
DigColPs_I2CHwDataType_Cnt_M_u08			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	2924		
DigColPs_I2CSensCommFlts_Cnt_M_u08	8		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	65		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75.8		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	24		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.44		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	964		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151		
k_SenseDetErrDiag_Cnt_str.Threshold	16		
k_SenseDetErrDiag_Cnt_str.PStep	40		
k_SenseDetErrDiag_Cnt_str.NStep	9		
k_SenseParityErrDiag_Cnt_str.Threshold	70		
k_SenseParityErrDiag_Cnt_str.PStep	15		
k_SenseParityErrDiag_Cnt_str.NStep	8		
k_StepDetect_Deg_f32	36.4		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	554.285156	554.2851563 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	70	70	٠,
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	٠,
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	177	177	
DigColPs I2CColSensorFault Cnt M Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	194.285156	194.2851563 ± 0.0001220703125	
DigColPs I2CHwSpurAngle Deg M f32	118.1922	118.1921875 ± 0.0001220703125	
DigColPs 12CSensCommFlts Cnt M u08	8	8	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs PrevI2CHwColAngle Deg M f32	65 5.71289063	65 5.712890625 ± 0.0001220703125	
· - · - · - · - · - · - · - · - · - · · - ·	36	5.712890625 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16		1.1	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.1640625	3.1640625 ± 0.0001220703125	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-241.8078	-241.8078125 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	70	70	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	, , , , , , , , , , , , , , , , , , ,

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142	142	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.44		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	254		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152		
DigColPs_I2CHwColAngle_Cnt_M_u16	2644		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	3324		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	75		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	85		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	40		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	27.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	746		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	18		
k_SenseDetErrDiag_Cnt_str.PStep	45		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	80		
k_SenseParityErrDiag_Cnt_str.PStep	17		
k_SenseParityErrDiag_Cnt_str.NStep	9		
k_StepDetect_Deg_f32	38		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	804.500366	804.5003906 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	80	80	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142	142	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	84.5003662	84.50039063 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	91.7578125	91.7578125 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	75	75	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	6.59179688	6.591796875 ± 0.0001220703125	~

DigColPs_Per1

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	40	40	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.515625	3.515625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	91.7578125	91.7578125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	80	80	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155	155	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	10		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.48		
DigColPs ColLPFInitDone Cnt M lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	214		
DigColPs ColRoughTurns Cnt M s16	4		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
DigColPs I2CHwColAngle Cnt M u16	3148		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	3724		
DigColPs I2CSensCommFlts Cnt M u08	0		
DigColPs PrevI2CHwColAngle Cnt M u16	85		
DigColPs PrevI2CHwColAngle Deg M f32	95		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	44		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	30		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.56		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	175		
k_SenseDetErrDiag_Cnt_str.Threshold	20		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	11		
k_SenseParityErrDiag_Cnt_str.Threshold	90		
k_SenseParityErrDiag_Cnt_str.PStep	19		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	40		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1075.58594	1075.585938 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	90	90	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	•
DisColDe ColCensorDiscFoiled Cat M Iss	4	4	

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164

DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	355.585938	355.5859375 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	96.5656738	96.565625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	85	85	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	7.47070313	7.470703125 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	44	44	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3.8671875	3.8671875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	456.565674	456.565625 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	90	90	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	164	164	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.13 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.52		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	3652		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	2		
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	95		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	105		
DigColPs_PrevI2CHwSpurAngle_Deg_wi_32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	48		
	33		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	-700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.62		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1 523		
DigColPs_SpurParityErrorAcc_Cnt_M_u16			
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	185		
k_SenseDetErrDiag_Cnt_str.Threshold	22		
k_SenseDetErrDiag_Cnt_str.PStep	2		
k_SenseDetErrDiag_Cnt_str.NStep	12		
k_SenseParityErrDiag_Cnt_str.Threshold	100		
k_SenseParityErrDiag_Cnt_str.PStep	21		
k_SenseParityErrDiag_Cnt_str.NStep	11		
k_StepDetect_Deg_f32	42		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1180.3418	1180.341797 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	100	100	
DiaColDo ColDorityError Cot M Igo		0	
DIGCOILS_COLLAUITALITALINITIGC	0	U	
	5	5	
DigColPs_ColRoughTurns_Cnt_M_s16			
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	5	5	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	5	5	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	5 0 174	5 0 174	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	5 0 174 1	5 0 174 1	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32	5 0 174 1 100.341797	5 0 174 1 100.3417969 ± 0.0001220703125	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08	5 0 174 1 100.341797 269.415649	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc	5 0 174 1 100.341797 269.415649	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	5 0 174 1 100.341797 269.415649 11	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prevl2CHwColAngle_Cnt_M_u16 DigColPs_Prevl2CHwColAngle_Cnt_M_u16	5 0 174 1 100.341797 269.415649 11 1	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95 8.349609375 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prevl2CHwColAngle_Cnt_M_u16 DigColPs_Prevl2CHwColAngle_Deg_M_f32 DigColPs_Prevl2CHwColAngle_Deg_M_f32 DigColPs_Prevl2CHwSpurAngle_Deg_M_f32 DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigColPs_Prevl2CHwSpurAngle_Deg_M_f32	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95 8.349609375 ± 0.0001220703125 48	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95 8.349609375 ± 0.0001220703125 48 4.21875 ± 0.0001220703125	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95 8.349609375 ± 0.0001220703125 48 4.21875 ± 0.0001220703125	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensorFault_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649	$\begin{array}{c} 5 \\ 0 \\ 174 \\ 1 \\ 100.3417969 \pm 0.0001220703125 \\ 269.415625 \pm 0.0001220703125 \\ 11 \\ 1 \\ 95 \\ 8.349609375 \pm 0.0001220703125 \\ 48 \\ 4.21875 \pm 0.0001220703125 \\ 1 \\ 629.415625 \pm 0.00048828125 \\ \end{array}$	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649 1 100	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95 8.349609375 ± 0.0001220703125 48 4.21875 ± 0.0001220703125 1 629.415625 ± 0.00048828125 1 100	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649 1 100 0	$\begin{array}{c} 5 \\ 0 \\ 174 \\ 1 \\ 100.3417969 \pm 0.0001220703125 \\ 269.415625 \pm 0.0001220703125 \\ 11 \\ 1 \\ 95 \\ 8.349609375 \pm 0.0001220703125 \\ 48 \\ 4.21875 \pm 0.0001220703125 \\ 1 \\ 629.415625 \pm 0.00048828125 \\ 1 \\ 100 \\ 0 \\ \end{array}$	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_gc	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649 1 100 0 4	$\begin{array}{c} 5 \\ 0 \\ 174 \\ 1 \\ 100.3417969 \pm 0.0001220703125 \\ 269.415625 \pm 0.0001220703125 \\ 11 \\ 1 \\ 95 \\ 8.349609375 \pm 0.0001220703125 \\ 48 \\ 4.21875 \pm 0.0001220703125 \\ 1 \\ 629.415625 \pm 0.00048828125 \\ 1 \\ 100 \\ 0 \\ 4 \end{array}$	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649 1 100 0 4	$\begin{array}{c} 5 \\ 0 \\ 174 \\ 1 \\ 100.3417969 \pm 0.0001220703125 \\ 269.415625 \pm 0.0001220703125 \\ 11 \\ 1 \\ 95 \\ 8.349609375 \pm 0.0001220703125 \\ 48 \\ 4.21875 \pm 0.0001220703125 \\ 1 \\ 629.415625 \pm 0.00048828125 \\ 1 \\ 100 \\ 0 \\ 4 \\ 0 \end{array}$	
DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DiaCoIPs_NathDiagNatr_SetNTCStatus(NTC_Cnt_T_enum)	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649 1 100 0 4 0	5 0 174 1 100.3417969 ± 0.0001220703125 269.415625 ± 0.0001220703125 11 1 95 8.349609375 ± 0.0001220703125 48 4.21875 ± 0.0001220703125 1 629.415625 ± 0.00048828125 1 100 0 4 0 173	
DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	5 0 174 1 100.341797 269.415649 11 1 95 8.34960938 48 4.21875 1 629.415649 1 100 0 4	$\begin{array}{c} 5 \\ 0 \\ 174 \\ 1 \\ 100.3417969 \pm 0.0001220703125 \\ 269.415625 \pm 0.0001220703125 \\ 11 \\ 1 \\ 95 \\ 8.349609375 \pm 0.0001220703125 \\ 48 \\ 4.21875 \pm 0.0001220703125 \\ 1 \\ 629.415625 \pm 0.00048828125 \\ 1 \\ 100 \\ 0 \\ 4 \\ 0 \end{array}$	

Test Step 2.14 (Repeat Count = 1)



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.56		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	865		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142		
DigColPs_I2CHwColAngle_Cnt_M_u16	88		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	22		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	105		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	115		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	52		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	36		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	195		
k_SenseDetErrDiag_Cnt_str.Threshold	24		
k_SenseDetErrDiag_Cnt_str.PStep	4		
k_SenseDetErrDiag_Cnt_str.NStep	13		
k_SenseParityErrDiag_Cnt_str.Threshold	110		
k SenseParityErrDiag Cnt str.PStep	23		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	44.2		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-335.632019	-335.6320313 ± 0.00048828125	/
DigColPs_ColLPFInitDone_Cnt_M_igc	1	1	V
DigColPs_ColParityErrorAcc_Cnt_M_u16	110	110	
DigColPs_ColParityError_Cnt_M_lgc	0	0	_
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	_
DigColPs_ColSensorFaultAcc_Cnt_M_u16	129	129	
DigColPs_I2CColSensorFault_Cnt_M_gc	1	1	V
DigColPs_I2CHwColAngle_Deg_M_f32	24.367981	24.36796875 ± 0.0001220703125	
DigColPs I2CHwSpurAngle Deg M f32	271.907837	271.9078125 ± 0.0001220703125	V
DigColPs I2CSensCommFlts Cnt M u08	12	12	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	_
DigColPs PrevI2CHwColAngle Cnt M u16	105	105	
DigColPs PrevI2CHwColAngle Deg M f32	9.22851563	9.228515625 ± 0.0001220703125	-
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	52	52	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.5703125	4.5703125 ± 0.0001220703125	•
DigColPs Reql2CSnsrDataType Cnt M u08	2	4.5703125 ± 0.0001220703125	
DigColPs_ReqizCSristDataType_Cnt_M_utoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1168.09216	-1168.092188 ± 0.00048828125	~
DigColPs SpurLPFInitDone Cnt M Igc	0	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	110	110	•
	110	110	
DigColPs SpurParityError Cnt M lgc	0	0	✓

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DigCoIPs_P	er1

Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	182	182	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.15 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetData()	13	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	568	
DigColPs_ColRoughTurns_Cnt_M_s16	-4	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	
DigColPs_I2CHwColAngle_Cnt_M_u16	244	
DigColPs_I2CHwDataType_Cnt_M_u08	3	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42	
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	39	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.74	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	235	
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	
DigColPs SpurSensorFaultAcc Cnt M u16	142	
k_SenseDetErrDiag_Cnt_str.Threshold	26	
k_SenseDetErrDiag_Cnt_str.PStep	6	
k SenseDetErrDiag Cnt str.NStep	14	
k_SenseParityErrDiag_Cnt_str.Threshold	120	
k_SenseParityErrDiag_Cnt_str.PStep	25	
k_SenseParityErrDiag_Cnt_str.NStep	13	
k StepDetect Deg f32	46	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-361.935547	-361.9355469 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	120	120	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	172	172	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	358.064453	358.0644531 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	248.042236	248.0421875 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115	115	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.1074219	10.10742188 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56	56	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	4.921875	4.921875 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1191.95776	-1191.957813 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120	120	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128	128	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.16 (Repeat Count = 1) Name	Input Value		
DigColPsInt GetData()	14		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.64		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	965		
DigColPs ColRoughTurns Cnt M s16	-3		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	400		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	62		
DigColPs_I2CSensCommFlts_Cnt_M_u08	11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	125		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	135		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	60		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	42		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152		
k_SenseDetErrDiag_Cnt_str.Threshold	28		
k_SenseDetErrDiag_Cnt_str.PStep	8		
k_SenseDetErrDiag_Cnt_str.NStep	15		
k_SenseParityErrDiag_Cnt_str.Threshold	130		
k_SenseParityErrDiag_Cnt_str.PStep	27		
k_SenseParityErrDiag_Cnt_str.NStep	14		
k_StepDetect_Deg_f32	48		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-165.768738	-165.76875 ± 0.00048828125	-
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	•
DI O ID O ID II F			

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169

-2

169

DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	194.231262	194.23125 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	140.21875	140.21875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	14	14	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	125	125	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10.9863281	10.98632813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	60	60	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	5.2734375	5.2734375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-939.78125	-939.78125 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130	130	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	137	137	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	14	14	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.17 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	456		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	556		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	82		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	135		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	145		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	64		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	45.5		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.86		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	142		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	163		
<_SenseDetErrDiag_Cnt_str.Threshold	1		
_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	140		
k_SenseParityErrDiag_Cnt_str.PStep	29		
k_SenseParityErrDiag_Cnt_str.NStep	15		
<_StepDetect_Deg_f32	50.5		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	51.2683716	51.26835937 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	140	140	٠,
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs ColRoughTurns Cnt M s16	-1	-1	
	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	170	0 170	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	170 1	0 170 1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	170 1 51.2683716	0 170 1 51.26835937 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	170 1 51.2683716 63.6374512	0 170 1 51.26835937 ± 0.0001220703125 63.6375 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08	170 1 51.2683716 63.6374512 15	0 170 1 51.26835937 ± 0.0001220703125 63.6375 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	170 1 51.2683716 63.6374512 15	0 170 1 51.26835937 ± 0.0001220703125 63.6375 ± 0.0001220703125 15	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	170 1 51.2683716 63.6374512 15 1	0 170 1 51.26835937 ± 0.0001220703125 63.6375 ± 0.0001220703125 15 1 1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	170 1 51.2683716 63.6374512 15 1 135 11.8652344	0 170 1 51.26835937 ± 0.0001220703125 63.6375 ± 0.0001220703125 15 1 1 135 11.86523438 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64	0 170 1 51.26835937 ± 0.0001220703125 63.6375 ± 0.0001220703125 15 1 135 11.86523438 ± 0.0001220703125 64	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625	0 170 1 51.26835937 \pm 0.0001220703125 63.6375 \pm 0.0001220703125 15 1 135 11.86523438 \pm 0.0001220703125 64 5.625 \pm 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0	0 170 1 51.26835937 \pm 0.0001220703125 63.6375 \pm 0.0001220703125 15 1 135 11.86523438 \pm 0.0001220703125 64 5.625 \pm 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549	0 170 1 51.26835937 \pm 0.0001220703125 63.6375 \pm 0.0001220703125 15 1 135 11.86523438 \pm 0.0001220703125 64 5.625 \pm 0.0001220703125 0 -656.3625 \pm 0.00048828125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549	0 170 1 51.26835937 \pm 0.0001220703125 63.6375 \pm 0.0001220703125 15 1 135 11.86523438 \pm 0.0001220703125 64 5.625 \pm 0.0001220703125 0 -656.3625 \pm 0.00048828125 1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549 1 140	0 170 1 51.26835937 \pm 0.0001220703125 63.6375 \pm 0.0001220703125 15 1 135 11.86523438 \pm 0.0001220703125 64 5.625 \pm 0.0001220703125 0 -656.3625 \pm 0.00048828125 1 140	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549 1 140 0	$\begin{array}{c} 0 \\ 170 \\ 1 \\ 51.26835937 \pm 0.0001220703125 \\ 63.6375 \pm 0.0001220703125 \\ 15 \\ 1 \\ 135 \\ 11.86523438 \pm 0.0001220703125 \\ 64 \\ 5.625 \pm 0.0001220703125 \\ 0 \\ -656.3625 \pm 0.00048828125 \\ 1 \\ 140 \\ 0 \end{array}$	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_st6	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549 1 140 0 -2	$\begin{array}{c} 0 \\ 170 \\ 1 \\ 51.26835937 \pm 0.0001220703125 \\ 63.6375 \pm 0.0001220703125 \\ 15 \\ 1 \\ 135 \\ 11.86523438 \pm 0.0001220703125 \\ 64 \\ 5.625 \pm 0.0001220703125 \\ 0 \\ -656.3625 \pm 0.00048828125 \\ 1 \\ 140 \\ 0 \\ -2 \end{array}$	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Req12CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549 1 140 0 -2	$\begin{array}{c} 0 \\ 170 \\ 1 \\ 51.26835937 \pm 0.0001220703125 \\ 63.6375 \pm 0.0001220703125 \\ 15 \\ 1 \\ 135 \\ 11.86523438 \pm 0.0001220703125 \\ 64 \\ 5.625 \pm 0.0001220703125 \\ 0 \\ -656.3625 \pm 0.00048828125 \\ 1 \\ 140 \\ 0 \\ -2 \\ 1 \end{array}$	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurLPFintDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549 1 140 0 -2 1 147	$\begin{array}{c} 0 \\ 170 \\ 1 \\ 51.26835937 \pm 0.0001220703125 \\ 63.6375 \pm 0.0001220703125 \\ 15 \\ 1 \\ 135 \\ 11.86523438 \pm 0.0001220703125 \\ 64 \\ 5.625 \pm 0.0001220703125 \\ 0 \\ -656.3625 \pm 0.00048828125 \\ 1 \\ 140 \\ 0 \\ -2 \\ 1 \\ 147 \end{array}$	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	170 1 51.2683716 63.6374512 15 1 135 11.8652344 64 5.625 0 -656.362549 1 140 0 -2	$\begin{array}{c} 0 \\ 170 \\ 1 \\ 51.26835937 \pm 0.0001220703125 \\ 63.6375 \pm 0.0001220703125 \\ 15 \\ 1 \\ 135 \\ 11.86523438 \pm 0.0001220703125 \\ 64 \\ 5.625 \pm 0.0001220703125 \\ 0 \\ -656.3625 \pm 0.00048828125 \\ 1 \\ 140 \\ 0 \\ -2 \\ 1 \end{array}$	



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.18 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.72		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156		
DigColPs_I2CHwColAngle_Cnt_M_u16	712		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	102		
DigColPs_I2CSensCommFlts_Cnt_M_u08	13		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	68		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	48		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142		
k_SenseDetErrDiag_Cnt_str.Threshold	255		
k_SenseDetErrDiag_Cnt_str.PStep	12		
k_SenseDetErrDiag_Cnt_str.NStep	17		
k_SenseParityErrDiag_Cnt_str.Threshold	150		
k_SenseParityErrDiag_Cnt_str.PStep	31		
k_SenseParityErrDiag_Cnt_str.NStep	16		
k_StepDetect_Deg_f32	52		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	289.175781	289.1757813 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	150	150	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	139	139	v
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	289.175781	289.1757813 ± 0.0001220703125	*
DigColPs_I2CHwSpurAngle_Deg_M_f32	18.2984314	18.2984375 ± 0.0001220703125	V
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	V
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	V
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	145	145	Y
DigColPs_PrevI2CHwColAngle_Deg_M_f32	12.7441406	12.74414063 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	68	68	*
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	5.9765625	5.9765625 ± 0.0001220703125	V
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	V
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-341.701569	-341.7015625 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	150	150	~
DigColPs_SpurParityError_Cnt_M_lgc	1	1	

2014-10-14, 18:11:16+0530





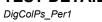
Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.76		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	526		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	868		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	122		
DigColPs_I2CSensCommFlts_Cnt_M_u08	14		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	155		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	165		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	72		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	51		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.125		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	50		
k_SenseDetErrDiag_Cnt_str.PStep	14		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	160		
k_SenseParityErrDiag_Cnt_str.PStep	33		
k_SenseParityErrDiag_Cnt_str.NStep	17		
k_StepDetect_Deg_f32	54		
Name	Actual Value	Expected Value	Result

K_0tep56test_569_162	0-7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	537.600037	537.6 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	509	509	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	177.600037	177.6 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	272.5	272.5 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-87.5	-87.5 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	608	608	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	286		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135		
DigColPs_I2CHwColAngle_Cnt_M_u16	1024		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	142		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	54		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.135		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	170		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	18		
k_StepDetect_Deg_f32	56		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	827.601563	827.6015625 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
			

170

0

1

116

2

170

2

1

116

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	107.601563	107.6015625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	49.5017586	49.50175781 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165	165	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	14.5019531	14.50195313 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76	76	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	49.5017586	49.50175781 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	170	170	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106	106	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.21 (Repeat Count = 1) Name	Input Value		
	2		
DigColPsInt_GetData()			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.84		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	245		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	1180		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	162		
DigColPs_I2CSensCommFlts_Cnt_M_u08	16		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	175		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	185		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	80		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	57		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.145		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	532		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
<_SenseDetErrDiag_Cnt_str.Threshold	20		
<_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	20		
k_SenseParityErrDiag_Cnt_str.Threshold	180		
k_SenseParityErrDiag_Cnt_str.PStep	37		
k_SenseParityErrDiag_Cnt_str.NStep	19		
<_StepDetect_Deg_f32	58		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1128.11987	1128.119922 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	180	180	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs ColRoughTurns Cnt M s16	3	3	
DigColPs ColSensorDiagFailed Cnt M lgc	0	0	
DigColPs ColSensorFaultAcc Cnt M u16	10	10	
DigColPs I2CColSensorFault Cnt M Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	48.119873	48.11992188 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	190.919525	190.9195313 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	
DigColPs PrevI2CHwColAngle Cnt M u16	175	175	
DigColPs PrevI2CHwColAngle Deg M f32	15.3808594	15.38085938 ± 0.0001220703125	
· - · - · - · - · - · - · · · · · · · ·	80	80	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	7.03125		
	4	7.03125 ± 0.0001220703125	
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08			
DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	190.919525	190.9195313 ± 0.00048828125	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	190.919525 1	1	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	190.919525 1 180	1 180	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	190.919525 1 180 0	1 180 0	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	190.919525 1 180 0	1 180 0 2	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	190.919525 1 180 0 2	1 180 0 2 1	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_lgc	190.919525 1 180 0 2 1 145	1 180 0 2 1 145	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	190.919525 1 180 0 2	1 180 0 2 1	



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1400		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.88		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	863		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	50		
DigColPs I2CHwColAngle Cnt M u16	1336		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	182		
DigColPs I2CSensCommFlts Cnt M u08	17		
	185		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195		
DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	84		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	60		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.155		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	30		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	21		
k_SenseParityErrDiag_Cnt_str.Threshold	190		
k_SenseParityErrDiag_Cnt_str.PStep	39		
k_SenseParityErrDiag_Cnt_str.NStep	20		
k_StepDetect_Deg_f32	60.8		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1449.50854	1449.508594 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	190	190	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	29	29	
DigColPs I2CColSensorFault Cnt M Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	9.50854492	9.50859375 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	337.544342	337.5443359 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	185	185	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	16.2597656	16.25976563 ± 0.0001220703125	~
DigColPs PrevI2CHwSpurAngle Cnt M u16	84	84	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.3828125	7.3828125 ± 0.0001220703125	~
DigColPs Regl2CSnsrDataType Cnt M u08	0	0	
DigColPs SpurAngleLPFKSV Cnt M str.SV Uls f32	337.544342	337.5443359 ± 0.00048828125	·
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	190	190	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	
Digoon a_opuir antychoi_ont_ivi_igo	U	U	

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123	123	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name Input Value DigColPsInt_GetData() 4 DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32 1500	
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32 1500	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32 0.92	
DigColPs_ColLPFInitDone_Cnt_M_lgc 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 865	
DigColPs_ColRoughTurns_Cnt_M_s16 4	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc 0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 101	
DigColPs_I2CHwColAngle_Cnt_M_u16 1492	
DigColPs_I2CHwDataType_Cnt_M_u08	
DigColPs_I2CHwSpurAngle_Cnt_M_u16 202	
DigColPs_I2CSensCommFlts_Cnt_M_u08 18	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 195	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 205	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 88	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 63	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 1	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 300	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32 0.165	
DigColPs_SpurLPFInitDone_Cnt_M_lgc 1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16 351	
DigColPs_SpurRoughTurns_Cnt_M_s16 4	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc 1	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16 124	
k_SenseDetErrDiag_Cnt_str.Threshold 40	
k_SenseDetErrDiag_Cnt_str.PStep 3	
k_SenseDetErrDiag_Cnt_str.NStep 0	
k_SenseParityErrDiag_Cnt_str.Threshold 200	
k_SenseParityErrDiag_Cnt_str.PStep 41	
k_SenseParityErrDiag_Cnt_str.NStep 21	
k_StepDetect_Deg_f32 62	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1791.76758	1791.767578 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	200	200	•
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101	101	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	351.767578	351.7675781 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	129.37616	129.3761719 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195	195	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	17.1386719	17.13867188 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88	88	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.734375	7.734375 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	489.37616	489.3761719 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	200	200	~
DigColPs_SpurParityError_Cnt_M_lgc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124	124	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	-
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	456		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144		
DigColPs_I2CHwColAngle_Cnt_M_u16	1648		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	222		
DigColPs_I2CSensCommFlts_Cnt_M_u08	19		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	205		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	215		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	92		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	66.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.175		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	143		
k_SenseDetErrDiag_Cnt_str.Threshold	50		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	50		
k_SenseParityErrDiag_Cnt_str.Threshold	210		
k_SenseParityErrDiag_Cnt_str.PStep	43		
k_SenseParityErrDiag_Cnt_str.NStep	22		
k_StepDetect_Deg_f32	64		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1621.80176	1621.801758 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•

210

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1

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DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	181.801758	181.8017578 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	223.415039	223.4150391 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	205	205	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.0175781	18.01757813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	92	92	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.0859375	8.0859375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	583.415039	583.4150391 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	210	210	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	93	93	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.25 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	235		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	105		
DigColPs_I2CHwColAngle_Cnt_M_u16	1804		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	242		
DigColPs_I2CSensCommFlts_Cnt_M_u08	20		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	225		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	69		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.185		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	321		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	131		
k_SenseDetErrDiag_Cnt_str.Threshold	60		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	220		
k_SenseParityErrDiag_Cnt_str.PStep	45		
k_SenseParityErrDiag_Cnt_str.NStep	23		
k_StepDetect_Deg_f32	66.6		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1147.7793	1147.779297 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	220	220	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs ColSensorFaultAcc Cnt M u16	80	80	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	67.7792969	67.77929688 ± 0.0001220703125	•
DigCoIPs I2CHwSpurAngle Deg M f32	142.660919	142.6609375 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	٠,
DigColPs I2CSpurSensorFault Cnt M lgc	1	1	
DigColPs PrevI2CHwColAngle Cnt M u16	215	215	٠,
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.8964844	18.89648438 ± 0.0001220703125	
DigColPs PrevI2CHwSpurAngle Cnt M u16	96	96	٠,
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.4375	8.4375 ± 0.0001220703125	
DigColPs RegI2CSnsrDataType Cnt M u08	3	3	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	142.660919	142.6609375 ± 0.00048828125	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220	220	
DigColPs SpurParityError Cnt M Igc	0	0	
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	
zigoon o_opantoughtumo_onc_m_oro	1	1	
DigColPs SpurSensorDiagFailed Cnt M Igc		'	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc		106	
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	106	106	
		106 109 6	



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Test Step 2.26 (Repeat Count = 1)			✓
DigColPsint_Cettbala()	Name	Input Value		
DigCoIPs ColAngleLPFKSV Cnt M str X Us 52	DigColPsInt GetData()			
DigCoPs_ ColAngleLPFKSV_Cnt_M_str K_Uis_532 0.3	_ "	-1700		
DigCoIPs CalLPFinitDone Cnt_M Lig				
DigCoIPs_ColParityErrorAcc_Cnt_M_s16				
DigColPs_ColRoughTurns_Cnt_M_s16				
DigCoIPs_CoISensorDiagFailed_Cnt_M_igc				
DigColPs, ColSensorFaultAcc_Cnt_M_u16				
DigCoIPS_12CHwColAngle_Cnt_M_u16				
DigCoIPs_I2CHwSpurAngle_Cnt_M_u16				
DigCoIPs ZCHwSpur/Angle_Cnt_M_u16 262 DigCoIPs_PrevIZCHwColAngle_Cnt_M_u16 225 DigCoIPs_PrevIZCHwColAngle_Deg_M_132 235 DigCoIPs_PrevIZCHwSpur/Angle_Cnt_M_u16 100 DigCoIPs_PrevIZCHwSpur/Angle_Cnt_M_u16 100 DigCoIPs_PrevIZCHwSpur/Angle_Deg_M_132 72 DigCoIPs_PrevIZCHwSpur/Angle_Deg_M_132 72 DigCoIPs_PrevIZCHwSpur/Angle_Deg_M_132 72 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_str.SV_UIs_132 600 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_str.SV_UIs_132 600 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_str.SV_UIs_132 0.195 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 314 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 314 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 314 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 314 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 4 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 4 DigCoIPs_Spur/Angle_IPFKSV_Cnt_M_16 100 Langle Location Langle Location				
DigCoIPs_I2CSensCommFits_Cnt_M_u16	· · · · · · · · · · · · · · · · · · ·			
DigCoIPs_Previ2CHwColAngle_Cnt_M_u16 225 DigCoIPs_Previ2CHwSopurAngle_Deg_M_f32 235 DigCoIPs_Previ2CHwSpurAngle_Deg_M_f32 72 DigCoIPs_Reqi2CSnsrDataType_Cnt_M_u08 4 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 600 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.K_UIs_f32 0.195 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.K_UIs_f32 0.195 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.K_UIs_f32 0 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 314 DigCoIPs_SpurBoughTurns_Cnt_M_str.B -4 DigCoIPs_SpurBoughTurns_Cnt_M_str.B -4 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 100 k_SenseDetErrDiag_Cnt_str.Threshold 70 k_SenseDetErrDiag_Cnt_str.Str.Step 12 k_SenseParityErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.Threshold 24 k_SenseParityErrDiag_Cnt_str.Threshold 24 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.Threshold 24 k_SenseParityE				
DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16				
DigCoIPs_PrevIZCHwSpurAngle_Deg_M_f32				
DigCoIPs				
DigCoIPs_Reqi2CSnsrDataType_Cnt_M_u08				
DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.X_UIs_f32 0.195 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.X_UIs_f32 0.195 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 0.195 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 314 DigCoIPs_SpurRoughTurns_Cnt_M_s16 4 DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 0.0 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 1.00 R_senseDetErrDiag_Cnt_str.Threshold 70 R_senseDetErrDiag_Cnt_str.Threshold 2.0 R_senseDetErrDiag_Cnt_str.Threshold 2.0 R_senseParityErrDiag_Cnt_str.Threshold 2.0 DigCoIPs_CollAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.06				
DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32 0.195 DigCoIPs_SpurLPrInitDone_Cnt_M_lgc 0 DigCoIPs_SpurPaintyErrorAcc_Cnt_M_u16 314 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc 0 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc 0 DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 100 k_SenseDetErrDiag_Cnt_str.Threshold 70 k_SenseDetErrDiag_Cnt_str.PStep 12 k_SenseDetErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.NIstep 230 k_SenseParityErrDiag_Cnt_str.NIstep 47 k_SenseParityErrDiag_Cnt_str.NIstep 24 k_SenseParityErrDiag_Cnt_str.NIstep 24 k_SenseParityErrDiag_Cnt_str.NIstep 46 k_SenseParityErrDiag_Cnt_str.NIstep 24 k_SenseParityErrDiag_Cnt_str.NIstep 24 k_SenseParityErrDiag_Cnt_str.NIstep 47 k_SenseParityErrDiag_Cnt_str.NIstep 48 Name Actual Value Expected Value Result DigCoIPs_ColAnglePFKSV_Cnt_M_str.SV_Uls_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16				
DigCoIPs_SpurLPFInitDone_Cnt_M_Igc 0 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 314 DigCoIPs_SpurRoughTurns_Cnt_M_s16 -4 DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc 0 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 100 k_SenseDetErrDiag_Cnt_str.Threshold 70 k_SenseDetErrDiag_Cnt_str.PStep 12 k_SenseDetErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.NStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_SenseParityErrDiag_Cnt_str.Nstep 25 k_SensePari				
DigColPs_SpurParityErrorAcc_Cnt_M_u16				
DigColPs_SpurRoughTurns_Cnt_M_s16				
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc 0				
DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 100 k_SenseDetErrDiag_Cnt_str.Threshold 70 k_SenseDetErrDiag_Cnt_str.PStep 12 k_SenseDetErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.PStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColIPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColRoughTurns_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_SenseDetErrDiag_Cnt_str.Threshold 70 k_SenseDetErrDiag_Cnt_str.PStep 12 k_SenseDetErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.PStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColIPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_SenseDetErrDiag_Cnt_str.PStep 12 k_SenseDetErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.PStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColIPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_SenseDetErrDiag_Cnt_str.NStep 2 k_SenseParityErrDiag_Cnt_str.NIstep 47 k_SenseParityErrDiag_Cnt_str.NStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColIPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_SenseParityErrDiag_Cnt_str.Threshold 230 k_SenseParityErrDiag_Cnt_str.PStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColIPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_SenseParityErrDiag_Cnt_str.PStep 47 k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColLPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_SenseParityErrDiag_Cnt_str.NStep 24 k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColLPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
k_StepDetect_Deg_f32 68 Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_CoILPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
Name Actual Value Expected Value Result DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 ✓ DigCoIPs_ColLPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓				
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 -1508.06738 -1508.067383 ± 0.00048828125 DigCoIPs_ColLPFInitDone_Cnt_M_lgc 1 1 DigCoIPs_ColParityErrorAcc_Cnt_M_u16 47 47 DigCoIPs_ColParityError_Cnt_M_lgc 0 0 DigCoIPs_ColRoughTurns_Cnt_M_s16 -3 -3 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1	k_StepDetect_Deg_f32	68		
DigCoIPs_CoILPFInitDone_Cnt_M_lgc 1 1 ✓ DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 47 47 ✓ DigCoIPs_CoIParityError_Cnt_M_lgc 0 0 ✓ DigCoIPs_CoIRoughTurns_Cnt_M_s16 -3 -3 ✓ DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓	Name	Actual Value	Expected Value	Result
DigColPs_ColParityErrorAcc_Cnt_M_u16 47 47 ✓ DigColPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigColPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigColPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓	DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1508.06738	-1508.067383 ± 0.00048828125	~
DigColPs_ColParityError_Cnt_M_lgc 0 0 ✓ DigColPs_ColRoughTurns_Cnt_M_s16 -3 -3 ✓ DigColPs_ColSensorDiagFailed_Cnt_M_lgc 1 1 ✓	DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColRoughTurns_Cnt_M_s16 -3 -3 -3 DigColPs_ColSensorDiagFailed_Cnt_M_lgc 1 1	DigColPs_ColParityErrorAcc_Cnt_M_u16	47	47	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc 1 1	DigColPs_ColParityError_Cnt_M_lgc	0	0	~
3.4. 12.4.4.4.4. 13.4.12.12.2.2.3.	DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
	DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 104 104 ✓	DigColPs_ColSensorFaultAcc_Cnt_M_u16	104	104	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc 1 1 ✓	DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigCoIPs_I2CHwColAngle_Deg_M_f32 291.932617 291.9326172 ± 0.0001220703125 ✓	DigColPs_I2CHwColAngle_Deg_M_f32	291.932617	291.9326172 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32 203.913879 203.9138672 ± 0.0001220703125 ✓	DigColPs_I2CHwSpurAngle_Deg_M_f32	203.913879	203.9138672 ± 0.0001220703125	✓
$ DigColPs_I2CSensCommFlts_Cnt_M_u08 $	DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc 1 1 ✓	DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 225 225 ✓	DigColPs_PrevI2CHwColAngle_Cnt_M_u16	225	225	~
DigColPs_Prevl2CHwColAngle_Deg_M_f32 19.7753906 19.7753906 ± 0.0001220703125 ✓	DigColPs_PrevI2CHwColAngle_Deg_M_f32	19.7753906	19.77539063 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 100 100 ✓	DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100	100	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 8.7890625 8.7890625 ± 0.0001220703125 ✓	DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.7890625	8.7890625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 4 ✓	DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 203.913879 203.9138672 ± 0.00048828125 ✓	DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	203.913879	203.9138672 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc 0 0 ✓	DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16 230 230 ✓	DigColPs_SpurParityErrorAcc_Cnt_M_u16	230	230	✓
DigColPs_SpurParityError_Cnt_M_lgc 0	DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	98	98	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.27 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103		
DigColPs_I2CHwColAngle_Cnt_M_u16	2116		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	282		
DigColPs_I2CSensCommFlts_Cnt_M_u08	22		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	235		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	245		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	104		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	75		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.205		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	568		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	182		
k_SenseDetErrDiag_Cnt_str.Threshold	80		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	3		
k_SenseParityErrDiag_Cnt_str.Threshold	240		
k_SenseParityErrDiag_Cnt_str.PStep	49		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	70		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1239.73828	-1239.738281 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100	100	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	200.261719	200.2617188 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	336.973846	336.9738281 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	235	235	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	20.6542969	20.65429688 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	104	104	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.140625	9.140625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336.973846	336.9738281 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240	240	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	179	179	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T ·				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	9		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	500		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	151		
DigColPs I2CHwColAngle Cnt M u16	2272		
DigColPs I2CHwDataType Cnt M u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	302		
DigColPs I2CSensCommFlts Cnt M u08	23		
DigColPs PrevI2CHwColAngle Cnt M u16	245		
DigColPs PrevI2CHwColAngle Deg M f32	255		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	108		
DigColPs PrevI2CHwSpurAngle Deg M f32	78		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.215		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	425		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	150		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	18		
k_SenseDetErrDiag_Cnt_str.NStep	4		
k_SenseParityErrDiag_Cnt_str.Threshold	250		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	26		
k_StepDetect_Deg_f32	72.2		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-919.233398	-919.2333984 ± 0.00048828125	
DigColPs ColLPFInitDone Cnt M Igc	1	1	

250

1

-1

1

147

250

1

-1

1

147

DigColPs_ColParityErrorAcc_Cnt_M_u16

DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	160.766602	160.7666016 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	115.240814	115.2408203 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	245	245	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	21.5332031	21.53320313 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	108	108	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.4921875	9.4921875 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	475.240814	475.2408203 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	250	250	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146	146	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.29 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	253		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165		
DigColPs_I2CHwColAngle_Cnt_M_u16	2428		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
0igColPs_I2CHwSpurAngle_Cnt_M_u16	322		
ligColPs_I2CSensCommFlts_Cnt_M_u08	24		
igColPs_PrevI2CHwColAngle_Cnt_M_u16	255		
igColPs_PrevI2CHwColAngle_Deg_M_f32	265		
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112		
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	81		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
ligColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.225		
ligColPs_SpurLPFInitDone_Cnt_M_lgc	1		
igColPs_SpurParityErrorAcc_Cnt_M_u16	965		
ligColPs_SpurRoughTurns_Cnt_M_s16	-1		
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
igColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
_SenseDetErrDiag_Cnt_str.Threshold	100		
_SenseDetErrDiag_Cnt_str.PStep	21		
_SenseDetErrDiag_Cnt_str.NStep	5		
_SenseParityErrDiag_Cnt_str.Threshold	260		
_SenseParityErrDiag_Cnt_str.PStep	2		
_SenseParityErrDiag_Cnt_str.NStep	27		
_StepDetect_Deg_f32	74		
lame	Actual Value	Expected Value	Res
higColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-546.552673	-546.5527344 ± 0.00048828125	
igColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
igColPs_ColParityErrorAcc_Cnt_M_u16	255	255	
igColPs_ColParityError_Cnt_M_lgc	0	0	
igColPs_ColRoughTurns_Cnt_M_s16	0	0	
igColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
igColPs ColSensorFaultAcc Cnt M u16	160	160	
igColPs_I2CColSensorFault_Cnt_M_lgc	0	0	
igColPs_I2CHwColAngle_Deg_M_f32	173.447327	173.4472656 ± 0.0001220703125	
igColPs I2CHwSpurAngle Deg M f32	258.714844	258.7148438 ± 0.0001220703125	
igcoir s_izor iwopurArigie_beg_ivi_ioz		10	
igColDo 12CConoCommElto Cot M u09			
	10		
ligColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16	1 255	1 255	
igColPs_I2CSpurSensorFault_Cnt_M_Igc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32	1 255 22.4121094	1 255 22.41210938 ± 0.0001220703125	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1 255 22.4121094 112	1 255 22.41210938 ± 0.0001220703125 112	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	1 255 22.4121094 112 9.84375	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	1 255 22.4121094 112 9.84375 2	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	1 255 22.4121094 112 9.84375 2 618.714844	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16	1 255 22.4121094 112 9.84375 2 618.714844 1	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260	
igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc igCoIPs_SpurParityErrorAcc_Cnt_M_u16 igCoIPs_SpurParityErrorCnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844 1 260	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0	
igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityErrorCnt_M_lgc igColPs_SpurParityErrorCnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0	
bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Deg_M_f32 bigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 bigColPs_PrevI2CHwSpurAngle_Deg_M_f32 bigColPs_ReqI2CSnsrDataType_Cnt_M_u08 bigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 bigColPs_SpurLPFInitDone_Cnt_M_lgc bigColPs_SpurParityErrorAcc_Cnt_M_u16 bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_lgc bigColPs_SpurParityError_Cnt_M_lgc	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0 -1 1	
DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0 -1	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0 -1 1 1 0	
DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorCnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigCoIPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Sa_DigCoIPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 255 22.4121094 112 9.84375 2 618.714844 1 260 0	1 255 22.41210938 ± 0.0001220703125 112 9.84375 ± 0.0001220703125 2 618.7148438 ± 0.00048828125 1 260 0 -1 1	



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.30 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.7		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	126		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
	2584		
DigColPs_I2CHwColAngle_Cnt_M_u16	3		
DigCoIPs_I2CHwDataType_Cnt_M_u08			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	25		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	110		
k_SenseDetErrDiag_Cnt_str.PStep	24		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	270		
k_SenseParityErrDiag_Cnt_str.PStep	4		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	76		
Name	Actual Value	Expected Value	Resul
		· ·	
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-121.696289	-121.6962891 ± 0.00048828125	•
DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_ColLPFInitDone_Cnt_M_lgc	-121.696289 1	-121.6962891 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16	1 130	1 130	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc	1 130 0	1 130 0	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	1 130 0 1	1 130 0 1	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1 130 0 1	1 130 0 1 1	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	1 130 0 1 1 169	1 130 0 1 1 1	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	1 130 0 1 1 169	1 130 0 1 1 169	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32	1 130 0 1 1 169 1 238.303711	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	1 130 0 1 1 169 1 238.303711 47.395874	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CChwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08	1 130 0 1 1 169 1 238.303711 47.395874	1 130 0 1 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CChwColAngle_Deg_M_f32 DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CSensCommFlts_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc	1 130 0 1 1 169 1 238.303711 47.395874 11	1 130 0 1 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFlts_Cnt_M_u08 DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1 130 0 1 1 1 169 1 238.303711 47.395874 11 1	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	1 130 0 1 1 1 169 1 238.303711 47.395874 11 1 265 23.2910156	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1 265 23.29101563 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFits_Cnt_M_u08 DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1 130 0 1 1 169 1 238.303711 47.395874 11 1 265 23.2910156	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1 265 23.29101563 ± 0.0001220703125 116	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1 130 0 1 1 1 169 1 238.303711 47.395874 11 1 265 23.2910156 116 10.1953125	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1 265 23.29101563 ± 0.0001220703125 116 10.1953125 ± 0.0001220703125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1 130 0 1 1 1 169 1 238.303711 47.395874 11 1 265 23.2910156 116 10.1953125 3	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1 265 23.29101563 ± 0.0001220703125 116 10.1953125 ± 0.0001220703125 3	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Req12CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	1 130 0 1 1 1 169 1 238.303711 47.395874 11 1 265 23.2910156 116 10.1953125 3 767.395874	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1 265 23.29101563 ± 0.0001220703125 116 10.1953125 ± 0.0001220703125 3 767.3958984 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Cnt_M_u16 DigColPs_Prev12CHwSpurAngle_Cnt_M_u16 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Req12CSnsrDataType_Cnt_M_u08	1 130 0 1 1 1 169 1 238.303711 47.395874 11 1 265 23.2910156 116 10.1953125 3	1 130 0 1 1 169 1 238.3037109 ± 0.0001220703125 47.39589844 ± 0.0001220703125 11 1 265 23.29101563 ± 0.0001220703125 116 10.1953125 ± 0.0001220703125 3	

Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

DigColPs_Per1

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1200
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	142
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	185
DigColPs_I2CHwColAngle_Cnt_M_u16	2740
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs_I2CHwSpurAngle_Cnt_M_u16	362
DigColPs_I2CSensCommFlts_Cnt_M_u08	26
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	275
DigColPs_PrevI2CHwColAngle_Deg_M_f32	285
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	120
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	87.7
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.245
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	523
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	155
k_SenseDetErrDiag_Cnt_str.Threshold	120
k_SenseDetErrDiag_Cnt_str.PStep	27
k_SenseDetErrDiag_Cnt_str.NStep	7
k_SenseParityErrDiag_Cnt_str.Threshold	280
k_SenseParityErrDiag_Cnt_str.PStep	6
k_SenseParityErrDiag_Cnt_str.NStep	29
k_StepDetect_Deg_f32	78

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	355.335938	355.3359375 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	148	148	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	178	178	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	355.335938	355.3359375 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	201.283997	201.2839844 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	275	275	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	24.1699219	24.16992188 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	120	120	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.546875	10.546875 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	921.283997	921.2839844 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	280	280	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	148	148	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	13		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.9		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	195		
DigColPs_I2CHwColAngle_Cnt_M_u16	2896		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	27		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	285		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	295		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	124		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	90		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1200		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.255		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	654		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	124		
k_SenseDetErrDiag_Cnt_str.Threshold	130		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	8		
k_SenseParityErrDiag_Cnt_str.Threshold	290		
k_SenseParityErrDiag_Cnt_str.PStep	8		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	80		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	884.543945	884.5439453 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	290	290	٠,
DigColPs ColParityError Cnt M Igc	0	0	

3

1

187

3

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187

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	164.543945	164.5439453 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.379150391	0.379101562 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	13	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	285	285	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.0488281	25.04882813 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	124	124	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.8984375	10.8984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.37915	1080.379102 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	290	290	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	116	116	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.33 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	14		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	856		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142		
DigColPs_I2CHwColAngle_Cnt_M_u16	3052		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	402		
DigColPs_I2CSensCommFlts_Cnt_M_u08	28		
DigColPs PrevI2CHwColAngle Cnt M u16	295		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	305		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	128		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	93		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs SpurAngleLPFKSV Cnt M str.SV Uls f32	1300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.265		
	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	258 3		
DigColPs_SpurRoughTurns_Cnt_M_s16			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	128		
k_SenseDetErrDiag_Cnt_str.Threshold	140		
k_SenseDetErrDiag_Cnt_str.PStep	33		
k_SenseDetErrDiag_Cnt_str.NStep	9		
k_SenseParityErrDiag_Cnt_str.Threshold	300		
k_SenseParityErrDiag_Cnt_str.PStep	10		
k_SenseParityErrDiag_Cnt_str.NStep	31		
k_StepDetect_Deg_f32	82		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1465.92773	1465.927734 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	300	300	•
DigColPs_ColParityError_Cnt_M_lgc	1	1	•
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	· •
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	133	133	١ ,
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	25.9277344	25.92773438 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	164.681274	164.68125 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	14	14	•
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	•
DigColPs PrevI2CHwColAngle Cnt M u16	295	295	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.9277344	25.92773438 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	128	128	٠,
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.25	11.25 ± 0.0001220703125	•
DigColPs Reql2CSnsrDataType Cnt M u08	1	1	٠,
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1244.68127	1244.68125 ± 0.00048828125	•
	1	1	
DIGCOIPS SpurLPFInitDone Cnt M Igc	268	268	
DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc		1	
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	1	1 3	
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	1 3	3	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1 3 1	3	
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1 3 1 119	3 1 119	
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1 3 1	3	•



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.34 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	15		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.224		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	146		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	3208		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	422		
DigColPs_I2CSensCommFlts_Cnt_M_u08	29		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	305		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	315		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	132		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	96		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1400		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.275		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	150		
k_SenseDetErrDiag_Cnt_str.PStep	36		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	310		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	32		
k_StepDetect_Deg_f32	84		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-289.195313	-289.1953125 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	158	158	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	70.8046875	70.8046875 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	73.1904297	73.19042969 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	15	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	305	305	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	26.8066406	26.80664063 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	132	132	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.6015625	11.6015625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1513.19043	1513.19043 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	12	12	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

2014-10-14, 18:11:16+0530





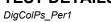
Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.35 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.226		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	756		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	163		
DigColPs_I2CHwColAngle_Cnt_M_u16	3364		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	442		
DigColPs_I2CSensCommFlts_Cnt_M_u08	30		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	315		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	325		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	136		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	99.1		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.285		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	160		
k_SenseDetErrDiag_Cnt_str.PStep	39		
k_SenseDetErrDiag_Cnt_str.NStep	11		
k_SenseParityErrDiag_Cnt_str.Threshold	320		
k_SenseParityErrDiag_Cnt_str.PStep	14		
k_SenseParityErrDiag_Cnt_str.NStep	33		
k_StepDetect_Deg_f32	86		
Name	Actual Value	Expected Value	Result

K_OtopBeteot_Beg_102	00		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-206.143066	-206.1430664 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	320	320	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	5	5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152	152	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	153.856934	153.8569336 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	148.906616	148.9066406 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	315	315	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	27.6855469	27.68554688 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	136	136	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	11.953125	11.953125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1588.90662	1588.906641 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	320	320	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.228		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	964		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	3520		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	462		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	325		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	335		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	140		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	102		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.295		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	501		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	170		
k_SenseDetErrDiag_Cnt_str.PStep	42		
k_SenseDetErrDiag_Cnt_str.NStep	12		
k_SenseParityErrDiag_Cnt_str.Threshold	330		
k_SenseParityErrDiag_Cnt_str.PStep	16		
k_SenseParityErrDiag_Cnt_str.NStep	34		
k_StepDetect_Deg_f32	88.5		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-786.640015	-786.64 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	,

930

0

-3

1

0

930

-3

1

0

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	293.359985	293.36 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	89.4000244	89.4 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	809.400024	809.4 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	467	467	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

l Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.37 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.23		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	746		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	158		
DigColPs_I2CHwColAngle_Cnt_M_u16	3676		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	482		
DigColPs_I2CSensCommFlts_Cnt_M_u08	1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	345		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	144		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	105		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.305		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	235		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs SpurSensorFaultAcc Cnt M u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	180		
k SenseDetErrDiag Cnt str.PStep	45		
k_SenseDetErrDiag_Ont_str.NStep	13		
k_SenseParityErrDiag_Ont_str.Threshold	340		
	18		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	90		
k_StepDetect_Deg_f32		From a start Walter	D
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-710.400024	-710.4 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	340	340	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	145	145	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	9.59997559	9.6 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	135.960144	135.9601563 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	- ·
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	144	144	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	12.65625	12.65625 ± 0.0001220703125	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	855.960144	855.9601563 ± 0.00048828125	•
gopan ingioti	1	1	•
		253	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	253		
DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16	253 0	0	
DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	0	1	
DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	0 -3	-3	
DigCoIPs_Spurt.PFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 -3 0	-3 0	
DigCoIPs_Spurt.PFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 -3 0 133	-3 0 133	
DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 -3 0	-3 0	•



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.38 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.232		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	865		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	125		
DigColPs_I2CHwColAngle_Cnt_M_u16	3832		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	2		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	355		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	148		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	108		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.315		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	145		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	158		
k_SenseDetErrDiag_Cnt_str.Threshold	190		
k_SenseDetErrDiag_Cnt_str.PStep	48		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	350		
k_SenseParityErrDiag_Cnt_str.PStep	20		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	92		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-551.060364	-551.0603906 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_Igc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	350	350	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	111	111	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	168.939636	168.9396094 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	137.982468	137.9824609 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	148	148	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	13.0078125	13.0078125 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-222.017532	-222.0175391 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	165	165	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144	144	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.39 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.234		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165		
DigColPs_I2CHwColAngle_Cnt_M_u16	3988		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	522		
DigColPs_I2CSensCommFlts_Cnt_M_u08	3		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2047		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	152		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	111		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	10		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.325		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	568		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	200		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	15		
k_SenseParityErrDiag_Cnt_str.Threshold	360		
k_SenseParityErrDiag_Cnt_str.PStep	22		
k_SenseParityErrDiag_Cnt_str.NStep	37		
k_StepDetect_Deg_f32	94		
Name	Actual Value	Expected Value	Result

K_0tcpb6tcst_beg_to2	0.		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-517.020569	-517.0205664 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	360	360	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150	150	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	202.979431	202.9794336 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	254.091797	254.0917969 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2047	2047	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	179.912109	179.9121094 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	152	152	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	13.359375	13.359375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-105.908195	-105.9082031 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	360	360	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	171	171	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	45.985199	45.98519531 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	13.2999992	13.3 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	7	7	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.615234375	0.615234375 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	13.2999992	13.3 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	370	370	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	136	136	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	V

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Name	Input Value	
DigColPsInt_GetData()	5	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-200	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.238	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs ColParityErrorAcc Cnt M u16	235	
DigColPs_ColRoughTurns_Cnt_M_s16	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255	
DigColPs_I2CHwColAngle_Cnt_M_u16	8	
DigColPs I2CHwDataType Cnt M u08	4	
DigColPs I2CHwSpurAngle Cnt M u16	562	
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	49	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	10	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	117	
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	30	
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.345	
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	
DigColPs_SpurP=rintDorie_Crit_ivi_igc DigColPs_SpurParityErrorAcc_Cnt_M_u16	354	
DigColPs SpurRoughTurns Cnt M s16	0	
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_spurSensorFaultAcc Cnt M u16	175	
k_SenseDetErrDiag_Cnt_str.Threshold	220	
k SenseDetErrDiag Cnt_str.PStep	2	
	17	
k_SenseDetErrDiag_Cnt_str.NStep	380	
k_SenseParityErrDiag_Cnt_str.Threshold	26	
k_SenseParityErrDiag_Cnt_str.PStep	39	
k_SenseParityErrDiag_Cnt_str.NStep	98	
k_StepDetect_Deg_f32		
Name	Actual Value Expected Val	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32		0.00048828125
DidColPs Coll Peiniflone Cnt M. Idc		
	0 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	261 261	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc	261 0 261 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	261 261 0 0 0 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16	261 261 0 0 0 0 0 0 0 0 238 238	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc	261 261 0 0 0 0 0 0 0 0 0 0 0 238 238 0 0 0	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_12CCoISensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFlts_Cnt_M_u08 DigCoIPs_12CSensCommFlts_Cnt_M_u08 DigCoIPs_Prevl2CHwColAngle_Deg_M_f32 DigCoIPs_Prevl2CHwColAngle_Deg_M_f32 DigCoIPs_Prevl2CHwColAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwColAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_Def_M_f32 DigCoIPs_SpurAngle_DefKSV_Cnt_M_u08 DigCoIPs_SpurAngle_DefKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_ColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_Prevl2CHwColAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwColAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigCoIPs_Prevl2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_DFKSV_Cnt_M_u68 DigCoIPs_SpurAngle_DFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngle_DFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	261 261 261 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHsSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHsSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc	261	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CCHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_Prev12CHwColAngle_Cnt_M_u16 DigCoIPs_Prev12CHwColAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	261	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_CoILPFInitDone_Cnt_M_Igc DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	261 261 0 0 0 0 0 0 238 238 0 0 208.624985 208.6249805 ± 19.6196785 19.61967773 ± 5 5 1 1 49 49 4.30664063 4.306640625 ± 4095 359.912109 359.912109 359.9121094 ± 4 4 19.6196785 19.61967773 ± 1 1 380 380 0 0 -1 -1 0 0 158 158	0.0001220703125 0.0001220703125 0.0001220703125
DigCoIPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CCHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_Prev12CHwColAngle_Cnt_M_u16 DigCoIPs_Prev12CHwColAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_Prev12CHsypurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	261	0.0001220703125 0.0001220703125 0.0001220703125



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.42 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.24		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156		
DigColPs_I2CHwColAngle_Cnt_M_u16	15		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	582		
DigColPs_I2CSensCommFlts_Cnt_M_u08	6		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	91		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	120		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	40		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.355		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	536		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_Igc	1		
DigColPs SpurSensorFaultAcc Cnt M u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	230		
k_SenseDetErrDiag_Cnt_str.PStep	3		
k SenseDetErrDiag Cnt str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	390		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	40		
k_StepDetect_Deg_f32	100		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	12.3195267	12.31953125 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	284	284	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	_
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	138	138	_
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	12.3195267	12.31953125 ± 0.0001220703125	_
DigColPs_I2CHwSpurAngle_Deg_M_f32	217.468796	217.4687988 ± 0.0001220703125	•
DigColPs I2CSensCommFlts Cnt M u08	6	6	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	91	91	_
DigColPs_PrevI2CHwColAngle_Deg_M_f32	7.99804688	7.998046875 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047	2047	-
DigColPs PrevI2CHwSpurAngle Deg M f32	179.912109	179.9121094 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	217.468796	217.4687988 ± 0.00048828125	V
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	-
DigColPs_SpurParityErrorAcc_Cnt_M_u16	390	390	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	
5.goo o_opan anyenor_ont_m_igo		· ·	

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.43 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.242		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	142		
DigColPs_ColRoughTurns_Cnt_M_s16	-5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131		
DigColPs_I2CHwColAngle_Cnt_M_u16	22		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	602		
DigColPs_I2CSensCommFlts_Cnt_M_u08	7		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	20		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	123		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	50		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.365		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	142		
k_SenseDetErrDiag_Cnt_str.Threshold	240		
k_SenseDetErrDiag_Cnt_str.PStep	4		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	400		
k_SenseParityErrDiag_Cnt_str.PStep	30		
k_SenseParityErrDiag_Cnt_str.NStep	41		
k_StepDetect_Deg_f32	102		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-432.771149	-432.7711523 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	172	172	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	112	112	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	287.228851	287.2288477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.757995605	0.758007813 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	133	133	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	11.6894531	11.68945313 ± 0.0001220703125	~

DigColPs_Per1

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	100	100	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.7890625	8.7890625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-359.242004	-359.2419922 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_Igc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	400	400	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123	123	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.244		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	253		
DigColPs_ColRoughTurns_Cnt_M_s16	5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	100		
DigColPs_I2CHwColAngle_Cnt_M_u16	29		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	622		
DigColPs_I2CSensCommFlts_Cnt_M_u08	8		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	175		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	25.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	126		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	60		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.375		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	586		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	250		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	20		
k_SenseParityErrDiag_Cnt_str.Threshold	410		
k_SenseParityErrDiag_Cnt_str.PStep	32		
k_SenseParityErrDiag_Cnt_str.NStep	42		
k_StepDetect_Deg_f32	104		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	518.552979	518.5529297 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	285	285	•
D: 0 ID 0 ID 1 E 0 I M I			

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DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	158.552979	158.5529297 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.091797	359.0917969 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	175	175	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	15.3808594	15.38085938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	200	200	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	17.578125	17.578125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	719.091797	719.0917969 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	410	410	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	166	166	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.45 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.4		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	625		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	182		
DigColPs I2CHwColAngle Cnt M u16	2140		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs I2CHwSpurAngle Cnt M u16	2924		
DigColPs_I2CSensCommFlts_Cnt_M_u08	8		
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	65		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	36		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	24.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1000		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.44		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
bigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs SpurRoughTurns Cnt M s16	0		
· - · · ·	1		
ligColPs_SpurSensorDiagFailed_Cnt_M_lgc ligColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
	16		
_SenseDetErrDiag_Cnt_str.Threshold			
_SenseDetErrDiag_Cnt_str.PStep	40		
_SenseDetErrDiag_Cnt_str.NStep	9		
SenseParityErrDiag_Cnt_str.Threshold	70		
x_SenseParityErrDiag_Cnt_str.PStep	15		
x_SenseParityErrDiag_Cnt_str.NStep	8		
_StepDetect_Deg_f32	36.3		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	266.285156	266.2851563 ± 0.00048828125	
ligColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
ligColPs_ColParityErrorAcc_Cnt_M_u16	70	70	
igColPs_ColParityError_Cnt_M_lgc	0	0	
ligColPs_ColRoughTurns_Cnt_M_s16	1	1	
igColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
	1 173	1 173	
bigColPs_ColSensorFaultAcc_Cnt_M_u16			
ligColPs_ColSensorFaultAcc_Cnt_M_u16 ligColPs_I2CColSensorFault_Cnt_M_lgc	173	173	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32	173 1	173 1	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32	173 1 266.285156	173 1 266.2851563 ± 0.0001220703125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08	173 1 266.285156 161.392212	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	173 1 266.285156 161.392212 8	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8	
igColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFlts_Cnt_M_u08 bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_Previ2CHwColAngle_Cnt_M_u16	173 1 266.285156 161.392212 8	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1	
igColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFlts_Cnt_M_u08 bigColPs_I2CSpurSensorFault_Cnt_M_lgc bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Deg_M_f32	173 1 266.285156 161.392212 8 1 65	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	173 1 266.285156 161.392212 8 1 65 5.71289063	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Cnt_M_u16 igColPs_PrevI2CHwSpurAngle_Deg_M_f32	173 1 266.285156 161.392212 8 1 65 5.71289063 36	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Det_M_u16 igColPs_PrevI2CHwSpurAngle_Det_M_u16 igColPs_PrevI2CHwSpurAngle_Det_M_u16 igColPs_PrevI2CHwSpurAngle_Det_M_u16 igColPs_PrevI2CHwSpurAngle_Det_M_u16	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125	
igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1	173 1 $266.2851563 \pm 0.0001220703125$ $161.3921875 \pm 0.0001220703125$ 8 1 65 $5.712890625 \pm 0.0001220703125$ 36 $3.1640625 \pm 0.0001220703125$ 3 $-558.6078125 \pm 0.00048828125$ 1	
igCoIPs_CoISensorFaultAcc_Cnt_M_u16 digCoIPs_I2CCoISensorFault_Cnt_M_lgc digCoIPs_I2CHwCoIAngle_Deg_M_f32 digCoIPs_I2CHwSpurAngle_Deg_M_f32 digCoIPs_I2CSensCommFlts_Cnt_M_u08 digCoIPs_I2CSpurSensorFault_Cnt_M_lgc digCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 digCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 digCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 digCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 digCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 digCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 digCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 digCoIPs_SpurLPFInitDone_Cnt_M_lgc digCoIPs_SpurParityErrorAcc_Cnt_M_u16 digCoIPs_SpurParityError_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1 70	173 1 $266.2851563 \pm 0.0001220703125$ $161.3921875 \pm 0.0001220703125$ 8 1 65 $5.712890625 \pm 0.0001220703125$ 36 $3.1640625 \pm 0.0001220703125$ 3 $-558.6078125 \pm 0.00048828125$ 1 70	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1 70 0	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125 1 70 0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3558.607788 1 70 0 0 1	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125 1 70 0 0 1	
DigCoIPs_CoISensorDiagFailed_Cnt_M_Igc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CCOISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurPrinitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_Igc DigCoIPs_SpurRoughTurns_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_u16 DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 DigCoIPs_D	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3 -558.607788 1 70 0 0 1 175	173 1 $266.2851563 \pm 0.0001220703125$ $161.3921875 \pm 0.0001220703125$ 8 1 65 $5.712890625 \pm 0.0001220703125$ 3 $3.1640625 \pm 0.0001220703125$ 3 $-558.6078125 \pm 0.00048828125$ 1 70 0 0 1 175	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	173 1 266.285156 161.392212 8 1 65 5.71289063 36 3.1640625 3558.607788 1 70 0 0 1	173 1 266.2851563 ± 0.0001220703125 161.3921875 ± 0.0001220703125 8 1 65 5.712890625 ± 0.0001220703125 36 3.1640625 ± 0.0001220703125 3 -558.6078125 ± 0.00048828125 1 70 0 0 1	



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.46 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt GetData()	1		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.8		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	150		
DigColPs_I2CHwColAngle_Cnt_M_u16	1024		
DigColPs I2CHwDataType Cnt M u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	142		
DigColPs I2CSensCommFlts Cnt M u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	54		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.135		
	0.135		
DigColPs_SpurLPFInitDone_Cnt_M_lgc			
DigColPs_SpurParityErrorAcc_Cnt_M_u16	563		
DigColPs_SpurSonorPiceFeiled_Cat_M_Isa	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	10		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	19		
k_SenseParityErrDiag_Cnt_str.Threshold	170		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	18		
k_StepDetect_Deg_f32	56	1	1
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1403.60156	1403.601563 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	131	131	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	323.601563	323.6015625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	195.301773	195.3017578 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	165	165	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	14.5019531	14.50195313 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	76	76	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	195.301773	195.3017578 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	170	170	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~

DigColPs_Per1

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	167	167	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Pto Call Sa DigColPe NytrDigaMar SetNTCStatus(Status Cnt T enum)	1	1	_

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.47 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	200		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.246		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	532		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	141		
DigColPs_I2CHwColAngle_Cnt_M_u16	36		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	642		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	217		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	30		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	129		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	70		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.385		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	286		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156		
k_SenseDetErrDiag_Cnt_str.Threshold	12		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	21		
k_SenseParityErrDiag_Cnt_str.Threshold	420		
k_SenseParityErrDiag_Cnt_str.PStep	34		
k_SenseParityErrDiag_Cnt_str.NStep	43		
k_StepDetect_Deg_f32	106		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-110.188232	-110.1882227 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	420	420	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	249.811768	249.8117773 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.801392	218.8013672 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	217	217	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	19.0722656	19.07226563 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	300	300	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	26.3671875	26.3671875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-501.198608	-501.1986328 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	320	320	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

T →						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•		
DiagnosticThreshold	2	DiagnosticThreshold	2	•		
OddParityFault	2	OddParityFault	2	•		
DiagnosticThreshold	2	DiagnosticThreshold	2	•		
ComputeRoughTurns	2	ComputeRoughTurns	2	•		
ConstrainOneRev	2	ConstrainOneRev	2	•		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•		

Test Step 2.48 (Repeat Count = 1)			Ų
Name	Input Value		
DigColPsInt GetData()	10		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	300		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.248		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	652		
DigColPs ColRoughTurns Cnt M s16	-1		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	142		
DigColPs_I2CHwColAngle_Cnt_M_u16	43		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	662		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	259		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	35.2		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	132		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	80		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.395		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	134		
k_SenseDetErrDiag_Cnt_str.Threshold	14		
k_SenseDetErrDiag_Cnt_str.PStep	7		
k_SenseDetErrDiag_Cnt_str.NStep	22		
k_SenseParityErrDiag_Cnt_str.Threshold	430		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	44		
k_StepDetect_Deg_f32	108		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	141.965393	141.9653906 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	430	430	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	

-1

1

120

-1

1

120

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	141.965393	141.9653906 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	298.08667	298.0867188 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	259	259	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	22.7636719	22.76367188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	400	400	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	35.15625	35.15625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1501.91333	-1501.913281 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	289	289	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	112	112	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.49 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	400		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.25		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	351		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	50		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	682		
DigColPs_I2CSensCommFlts_Cnt_M_u08	11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	301		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	40		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	500		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	135		
DigColPs_Previ2CnwSpurArigie_Deg_in_i32 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_ReqizeShsiDataType_Cht_M_uoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	90		
	0.405		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	236		
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurRoughTurns_Cnt_M_s16	11		
	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	16		
k_SenseDetErrDiag_Cnt_str.PStep	8		
k_SenseDetErrDiag_Cnt_str.NStep	23		
k_SenseParityErrDiag_Cnt_str.Threshold	440		
k_SenseParityErrDiag_Cnt_str.PStep	38		
k_SenseParityErrDiag_Cnt_str.NStep	45		
k_StepDetect_Deg_f32	110		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	36.6137695	36.61376953 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_Igc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	389	389	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
0	0		
	0	0	•
DigColPs_I2CColSensorFault_Cnt_M_Igc		0 36.61376953 ± 0.0001220703125	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32	0		•
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	0 36.6137695	36.61376953 ± 0.0001220703125	•
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08	0 36.6137695 235.147827	36.61376953 ± 0.0001220703125 235.1478516 ± 0.0001220703125	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	0 36.6137695 235.147827 11	36.61376953 ± 0.0001220703125 235.1478516 ± 0.0001220703125 11	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc	0 36.6137695 235.147827 11 0	36.61376953 ± 0.0001220703125 235.1478516 ± 0.0001220703125 11 0	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 36.6137695 235.147827 11 0 301	36.61376953 ± 0.0001220703125 235.1478516 ± 0.0001220703125 11 0 301	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 36.6137695 235.147827 11 0 301 26.4550781	36.61376953 ± 0.0001220703125 235.1478516 ± 0.0001220703125 11 0 301 26.45507813 ± 0.0001220703125	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0 36.6137695 235.147827 11 0 301 26.4550781 500	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$ 1	
DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$ 1 274	
DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$ 1 274 0	
DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0 11	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$ 1 274 0 11 0	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_Igc DigColPs_SpurRoughTurns_Cnt_M_Igc DigColPs_SpurSensorDiagFailed_Cnt_M_Igc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0 11	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$ 1 274 0 11 0 0	
DigColPs_I2CColSensorFault_Cnt_M_Igc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_Igc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_Igc DigColPs_SpurRoughTurns_Cnt_M_Igc DigColPs_SpurRoughTurns_Cnt_M_Igc	0 36.6137695 235.147827 11 0 301 26.4550781 500 43.9453125 0 1675.14783 1 274 0 11	$36.61376953 \pm 0.0001220703125$ $235.1478516 \pm 0.0001220703125$ 11 0 301 $26.45507813 \pm 0.0001220703125$ 500 $43.9453125 \pm 0.0001220703125$ 0 $1675.147852 \pm 0.00048828125$ 1 274 0 11 0	



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable/2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.50 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	13		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	700		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.6		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	124		
DigColPs_I2CHwColAngle_Cnt_M_u16	244		
DigColPs I2CHwDataType Cnt M u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	42		
DigColPs 12CSensCommFlts Cnt M u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	115		
	125		
DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	56		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	39		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.74		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	452		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	30		
k_SenseDetErrDiag_Cnt_str.Threshold	18		
k_SenseDetErrDiag_Cnt_str.PStep	6		
k_SenseDetErrDiag_Cnt_str.NStep	14		
k_SenseParityErrDiag_Cnt_str.Threshold	120		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	13		
k_StepDetect_Deg_f32	46.5		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	70.0644531	70.06445313 ± 0.00048828125	-
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	120	120	_
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc			
DIUCUIES CUIGEISULDIAUFAIIEU CIII IVI IUC	0	0	✓
	110	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	110	110	-
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	110	110 1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	110 1 70.0644531	110 1 70.06445313 ± 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	110 1 70.0644531 233.642181	110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08	110 1 70.0644531 233.642181 13	110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	110 1 70.0644531 233.642181 13	110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	110 1 70.0644531 233.642181 13 1	110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	110 1 70.0644531 233.642181 13 1 115 10.1074219	110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	110 1 70.0644531 233.642181 13 1 115 10.1074219 56	110 1 70.06445313 ± 0.0001220703125 233.6421875 ± 0.0001220703125 13 1 115 10.10742188 ± 0.0001220703125 56	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875	110 1 70.06445313 \pm 0.0001220703125 233.6421875 \pm 0.0001220703125 13 1 115 10.10742188 \pm 0.0001220703125 56 4.921875 \pm 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875 3	110 1 70.06445313 \pm 0.0001220703125 233.6421875 \pm 0.0001220703125 13 1 115 10.10742188 \pm 0.0001220703125 56 4.921875 \pm 0.0001220703125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_I2CSpurSensorFault_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875 3 -126.357819	110 1 70.06445313 \pm 0.0001220703125 233.6421875 \pm 0.0001220703125 13 1 115 10.10742188 \pm 0.0001220703125 56 4.921875 \pm 0.0001220703125 3 -126.3578125 \pm 0.00048828125	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	110 1 70.0644531 233.642181 13 1 115 10.1074219 56 4.921875 3	110 1 70.06445313 \pm 0.0001220703125 233.6421875 \pm 0.0001220703125 13 1 115 10.10742188 \pm 0.0001220703125 56 4.921875 \pm 0.0001220703125	

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	13	13	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.51 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	321		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	128		
DigColPs_I2CHwColAngle_Cnt_M_u16	1804		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	242		
DigColPs_I2CSensCommFlts_Cnt_M_u08	20		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	225		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	69.2		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.185		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	635		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	50		
k_SenseDetErrDiag_Cnt_str.Threshold	20		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	220		
k_SenseParityErrDiag_Cnt_str.PStep	45		
k_SenseParityErrDiag_Cnt_str.NStep	23		
k_StepDetect_Deg_f32	66		
Name	Actual Value	Expected Value	Result

K_Otopbeted_beg_toz	00		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1363.7793	1363.779297 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	220	220	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	103	103	✓
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	283.779297	283.7792969 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	248.860962	248.8609375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	215	215	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	18.8964844	18.89648438 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	96	96	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	8.4375	8.4375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	608.860962	608.8609375 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220	220	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	25	25	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.52 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	314		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	57		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	702		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	343		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	138		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	100		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.415		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	241		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	22		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	450		
k_SenseParityErrDiag_Cnt_str.PStep	40		
k_SenseParityErrDiag_Cnt_str.NStep	46		
k_StepDetect_Deg_f32	112		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	381.596924	381.5969141 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	-

354

1

0

1

0

354

0

1

0

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	21.5969238	21.59691406 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	352.184784	352.1847656 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	343	343	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	30.1464844	30.14648438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	600	600	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	52.734375	52.734375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-367.815216	-367.8152344 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	281	281	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~



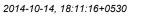
Test Step 2.53 (Repeat Count = 1)		
Name	Input Value	
DigColPsInt_GetData()	13	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	568	
DigColPs_ColRoughTurns_Cnt_M_s16	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	146	
DigColPs_I2CHwColAngle_Cnt_M_u16	64	
DigColPs_I2CHwDataType_Cnt_M_u08	2	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	722	
DigColPs_I2CSensCommFlts_Cnt_M_u08	13	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	385	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	50	
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	700	
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	141	
nigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	
ligColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	110	
ligColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.425	
ligColPs_SpurLPFInitDone_Cnt_M_lgc	1	
higColPs_SpurParityErrorAcc_Cnt_M_u16	523	
ligColPs_SpurRoughTurns_Cnt_M_s16	1	
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
igColPs_SpurSensorFaultAcc_Cnt_M_u16	144	
_SenseDetErrDiag_Cnt_str.Threshold	24	
_SenseDetErrDiag_Cnt_str.PStep	10	
_SenseDetErrDiag_Cnt_str.NStep	25	
_SenseParityErrDiag_Cnt_str.Threshold	460	
_SenseParityErrDiag_Cnt_str.PStep	42	
_SenseParityErrDiag_Cnt_str.NStep	47	
_StepDetect_Deg_f32	114	
Name	Actual Value Expected Value	Res
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800 ± 0.00048828125	
ligColPs_ColLPFInitDone_Cnt_M_lgc	0 0	
igColPs_ColParityErrorAcc_Cnt_M_u16	460 460	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc	460 0 460 0	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16	460 460 0 0 1 1	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc	460 460 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16	460 460 0 0 1 1 0 0 0 121 121	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc	460 460 0 0 1 1 0 0 0 121 121 1 1 1	
bigColPs_ColParityErrorAcc_Cnt_M_u16 bigColPs_ColParityError_Cnt_M_lgc bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32	460 460 0 0 0 1 1 0 0 0 121 1 121 1 1 0 0 0 0	
bigColPs_ColParityErrorAcc_Cnt_M_u16 bigColPs_ColParityError_Cnt_M_lgc bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32	460 460 0 0 0 1 1 1 0 0 0 121 1 121 1 1 1 1 0 0 0 0	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08	460 460 0 0 0 1 1 1 0 0 0 121 121 121 121 1 1 1	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	
igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc	460 460 0 0 0 0 1 1 1 1 0 0 0 121 121 121 121	
bigColPs_ColParityErrorAcc_Cnt_M_u16 bigColPs_ColParityError_Cnt_M_lgc bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_12CColSensorFault_Cnt_M_lgc bigColPs_12CHwColAngle_Deg_M_f32 bigColPs_12CHwSpurAngle_Deg_M_f32 bigColPs_12CSensCommFlts_Cnt_M_u08 bigColPs_12CSpurSensorFault_Cnt_M_lgc bigColPs_12CSpurSensorFault_Cnt_M_lgc bigColPs_12CSpurSensorFault_Cnt_M_u16 bigColPs_Previ2CHwColAngle_Cnt_M_u16 bigColPs_Previ2CHwColAngle_Deg_M_f32	460 460 0 0 0 0 1 1 1 1 0 0 0 1 1 1 1 1 1 1 1	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_12CColSensorFault_Cnt_M_lgc igColPs_12CHwColAngle_Deg_M_f32 igColPs_12CHwSpurAngle_Deg_M_f32 igColPs_12CSensCommFlts_Cnt_M_u08 igColPs_12CSpurSensorFault_Cnt_M_lgc igColPs_12CSpurSensorFault_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32	460 460 0 0 0 0 1 1 1 1 0 0 0 1 1 1 1 1 1 1 1	
igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	
igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	460 460 460 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
igCoIPs_CoIParityErrorAcc_Cnt_M_u16 igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoIParityError_Cnt_M_lgc igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	460 460 460 0 0 0 1 1 1 0 0 0 1 1 1 0 0 0 1 1 1 1	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	
bigCoIPs_CoIParityErrorAcc_Cnt_M_u16 bigCoIPs_CoIParityError_Cnt_M_lgc bigCoIPs_CoIRoughTurns_Cnt_M_s16 bigCoIPs_CoISensorDiagFailed_Cnt_M_lgc bigCoIPs_CoISensorFaultAcc_Cnt_M_u16 bigCoIPs_I2CCoISensorFault_Cnt_M_lgc bigCoIPs_I2CHwCoIAngle_Deg_M_f32 bigCoIPs_I2CHwSpurAngle_Deg_M_f32 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSpurSensorFault_Cnt_M_lgc bigCoIPs_I2CSpurSensorFault_Cnt_M_u16 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_SpurAngle_PFKSV_Cnt_M_u08 bigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 bigCoIPs_SpurAprityErrorAcc_Cnt_M_u16	460 460 460 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
bigCoIPs_CoIParityErrorAcc_Cnt_M_u16 bigCoIPs_CoIParityError_Cnt_M_lgc bigCoIPs_CoIRoughTurns_Cnt_M_s16 bigCoIPs_CoISensorDiagFailed_Cnt_M_lgc bigCoIPs_CoISensorFaultAcc_Cnt_M_u16 bigCoIPs_I2CCoISensorFault_Cnt_M_lgc bigCoIPs_I2CCHwCoIAngle_Deg_M_f32 bigCoIPs_I2CHwCoIAngle_Deg_M_f32 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSpurSensorFault_Cnt_M_u16 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.SV_UIS_f32 bigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.SV_UIS_f32 bigCoIPs_SpurAngleIPFKSV_Cnt_M_lgc bigCoIPs_SpurParityError_Cnt_M_lgc bigCoIPs_SpurParityError_Cnt_M_lgc	460 460 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
bigCoIPs_CoIParityErrorAcc_Cnt_M_u16 bigCoIPs_CoIParityError_Cnt_M_lgc bigCoIPs_CoIRoughTurns_Cnt_M_s16 bigCoIPs_CoISensorDiagFailed_Cnt_M_lgc bigCoIPs_CoISensorFaultAcc_Cnt_M_u16 bigCoIPs_I2CCoISensorFault_Cnt_M_lgc bigCoIPs_I2CCHwCoIAngle_Deg_M_f32 bigCoIPs_I2CHwCoIAngle_Deg_M_f32 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSensCommFlts_Cnt_M_u08 bigCoIPs_I2CSpurSensorFault_Cnt_M_u16 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 bigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 bigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 bigCoIPs_SpurParityError_Cnt_M_lgc bigCoIPs_SpurParityError_Cnt_M_lgc bigCoIPs_SpurRoughTurns_Cnt_M_s16	460	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CCHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	460	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_u6 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u16 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	460	
DigCoIPs_CoIParityErrorAcc_Cnt_M_u16 DigCoIPs_CoIParityError_Cnt_M_lgc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CCHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	460	

Test Step 2.54 (Repeat Count = 1)



T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

rest stop 2.04 (repeat sount 1)			
Name	Input Value		
DigColPsInt GetData()	14		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	425		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	71		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	742		
DigColPs_I2CSensCommFlts_Cnt_M_u08	14		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	427		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	55		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	144		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	120		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.435		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	365		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	26		
k_SenseDetErrDiag_Cnt_str.PStep	11		
k_SenseDetErrDiag_Cnt_str.NStep	26		
k_SenseParityErrDiag_Cnt_str.Threshold	470		
k_SenseParityErrDiag_Cnt_str.PStep	44		
k_SenseParityErrDiag_Cnt_str.NStep	48		
k_StepDetect_Deg_f32	116		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	757.529297	757.5292969 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	469	469	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs I2CColSensorFault Cnt M Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	37.5292969	37.52929688 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	51.5859375	51.5859375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	14	14	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	427	427	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	37.5292969	37.52929688 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	800	800	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	70.3125	70.3125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	411.585938	411.5859375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	409	409	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Stan 2 FF (Beneat Count = 4)	
Test Step 2.55 (Repeat Count = 1) Name	Input Value
DigColPsInt_GetData()	15
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	900
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.258
DigColPs ColLPFInitDone Cnt M Igc	0
DigColPs ColParityErrorAcc Cnt M u16	965
DigColPs ColRoughTurns Cnt M s16	3
DigColPs ColSensorDiagFailed Cnt M lgc	0
DigColPs ColSensorFaultAcc Cnt M u16	146
DigColPs I2CHwColAngle Cnt M u16	78
DigColPs_I2CHwDataType_Cnt_M_u08	4
DigColPs I2CHwSpurAngle Cnt M u16	762
DigColPs 12CSensCommFlts Cnt M u08	15
DigColPs PrevI2CHwColAngle Cnt M u16	469
DigColPs PrevI2CHwColAngle Deg M f32	60
DigColPs PrevI2CHwSpurAngle Cnt M u16	900
DigColPs PrevI2CHwSpurAngle Deg M f32	147
DigColPs Reql2CSnsrDataType Cnt M u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	130
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.445
DigColPs SpurLPFInitDone Cnt M Igc	1
DigColPs SpurParityErrorAcc Cnt M u16	256
DigColPs SpurRoughTurns Cnt M s16	3
DigColPs SpurSensorDiagFailed Cnt M lgc	0
DigColPs SpurSensorFaultAcc Cnt M u16	106
k_SenseDetErrDiag_Cnt_str.Threshold	28
k SenseDetErrDiag Cnt str.PStep	12
k SenseDetErrDiag Cnt str.NStep	27
k_SenseParityErrDiag_Cnt_str.Threshold	480
k SenseParityErrDiag Cnt str.PStep	46
k_SenseParityErrDiag_Cnt_str.NStep	49
k_StepDetect_Deg_f32	118

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	900	900	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	79.1015625	79.1015625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	587.950195	587.9501953 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	302	302	✓
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	79	79	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	15	15	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	85		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	782		
DigColPs_I2CSensCommFlts_Cnt_M_u08	16		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	511		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	65		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	150		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	140		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.455		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	365		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	30		
k_SenseDetErrDiag_Cnt_str.PStep	13		
k_SenseDetErrDiag_Cnt_str.NStep	28		
k_SenseParityErrDiag_Cnt_str.Threshold	490		
k_SenseParityErrDiag_Cnt_str.PStep	48		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	120		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-945.922913	-945.9228516 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	

460

0

4

1

0

460

4

1

0

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	134.077087	134.0771484 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	51.4902344	51.49023438 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	511	511	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	44.9121094	44.91210938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1000	1000	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	87.890625	87.890625 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	771.490234	771.4902344 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	413	413	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~





Test Step 2.57 (Repeat Count = 1) Name	Input Value		
DigColPsInt GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.262		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs ColParityErrorAcc Cnt M u16	523		
· - ·	4		
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	92		
DigCoIPs I2CHwDataType Cnt M u08	1		
DigColPs I2CHwSpurAngle Cnt M u16	802		
	17		
DigColPs_I2CSensCommFlts_Cnt_M_u08			
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	553 70		
DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1100		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	153		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	150		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.465		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	251		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	151		
k_SenseDetErrDiag_Cnt_str.Threshold	32		
k_SenseDetErrDiag_Cnt_str.PStep	14		
k_SenseDetErrDiag_Cnt_str.NStep	29		
k_SenseParityErrDiag_Cnt_str.Threshold	500		
k_SenseParityErrDiag_Cnt_str.PStep	1		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	122		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1984.09412	1984.094121 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	500	500	- I
DigColPs_ColParityError_Cnt_M_lgc	1	1	٠ ا
DigColPs_ColRoughTurns_Cnt_M_s16	<u> </u>	4	
	4	7	٠ ا
	0	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc			
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	0 157	0 157	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	0 157 1	0 157 1	•
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32	0 157 1 184.094116	0 157 1 184.0941211 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08	0 157 1 184.094116 74.8060303	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc	0 157 1 184.094116 74.8060303	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	0 157 1 184.094116 74.8060303 0	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	0 157 1 184.094116 74.8060303 0 0 553	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	0 157 1 184.094116 74.8060303 0 0 553 48.6035156	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875	0 157 1 184.0941211 \pm 0.0001220703125 74.80605469 \pm 0.0001220703125 0 0 553 48.60351563 \pm 0.0001220703125 1100 96.6796875 \pm 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875	0 157 1 184.0941211 \pm 0.0001220703125 74.80605469 \pm 0.0001220703125 0 0 553 48.60351563 \pm 0.0001220703125 1100 96.6796875 \pm 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603	0 157 1 184.0941211 \pm 0.0001220703125 74.80605469 \pm 0.0001220703125 0 0 553 48.60351563 \pm 0.0001220703125 1100 96.6796875 \pm 0.0001220703125 1 794.8060547 \pm 0.00048828125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1	0 157 1 184.0941211 \pm 0.0001220703125 74.80605469 \pm 0.0001220703125 0 0 553 48.60351563 \pm 0.0001220703125 1100 96.6796875 \pm 0.0001220703125 1 794.8060547 \pm 0.00048828125 1 252	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252	0 157 1 184.0941211 \pm 0.0001220703125 74.80605469 \pm 0.0001220703125 0 0 553 48.60351563 \pm 0.0001220703125 1100 96.6796875 \pm 0.0001220703125 1 794.8060547 \pm 0.00048828125 1 252	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252	0 157 1 184.0941211 \pm 0.0001220703125 74.80605469 \pm 0.0001220703125 0 0 553 48.60351563 \pm 0.0001220703125 1100 96.6796875 \pm 0.0001220703125 1 794.8060547 \pm 0.00048828125 1 252 1 4	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngle_IPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252 1 4	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125 1100 96.6796875 ± 0.0001220703125 1 794.8060547 ± 0.00048828125 1 252 1 4 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252 1 4 0	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125 1100 96.6796875 ± 0.0001220703125 1 794.8060547 ± 0.00048828125 1 252 1 4 0 122	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 157 1 184.094116 74.8060303 0 0 553 48.6035156 1100 96.6796875 1 794.80603 1 252 1 4	0 157 1 184.0941211 ± 0.0001220703125 74.80605469 ± 0.0001220703125 0 0 553 48.60351563 ± 0.0001220703125 1100 96.6796875 ± 0.0001220703125 1 794.8060547 ± 0.00048828125 1 252 1 4 0	



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.58 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	654		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	152		
DigColPs_I2CHwColAngle_Cnt_M_u16	99		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	822		
DigColPs_I2CSensCommFlts_Cnt_M_u08	18		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	595		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	75.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	156		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	160		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.475		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	362		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	34		
k_SenseDetErrDiag_Cnt_str.PStep	15		
k_SenseDetErrDiag_Cnt_str.NStep	30		
k_SenseParityErrDiag_Cnt_str.Threshold	510		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	2		
k_StepDetect_Deg_f32	124		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900	900 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	510	510	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs ColSensorFaultAcc Cnt M u16	122	122	
DigColPs I2CColSensorFault Cnt M Igc	1	1	~
DigColPs I2CHwColAngle Deg M f32	180	180 ± 0.0001220703125	~
DigColPs I2CHwSpurAngle Deg M f32	170.097656	170.0976563 ± 0.0001220703125	~
DigColPs I2CSensCommFlts Cnt M u08	1	1	~
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	595	595	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	52.2949219	52.29492188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1200	1200	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	105.46875	105.46875 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-549.902344	-549.9023438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	364	364	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.59 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	126		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
DigColPs_I2CHwColAngle_Cnt_M_u16	2584		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	25		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	110		
k_SenseDetErrDiag_Cnt_str.PStep	24		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	270		
k_SenseParityErrDiag_Cnt_str.PStep	4		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	76		
Nama	Actual Value	Expected Value	Pocult

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	268.303711	268.3037109 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	268.303711	268.3037109 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	2.39589834	2.395898438 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	116	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2.39589834	2.395898438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	•

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	119.838867	119.8388672 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	337.208191	337.2082031 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	3	3	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	679	679	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	59.6777344	59.67773438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1400	1400	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	123.046875	123.046875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-382.791809	-382.7917969 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	530	530	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	153	153	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.61 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.2		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs ColSensorFaultAcc Cnt M u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	120		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	882		
DigColPs_I2CSensCommFlts_Cnt_M_u08	21		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	721		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	90		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1500		
DigColPs PrevI2CHwSpurAngle Deg M f32	165		
DigColPs_Previ2CHwSpurArigie_Deg_M_i32 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_ReqizeShsiDataType_Cht_w_uoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_Igc DigColPs_SpurParityErrorAcc_Cnt_M_u16	125		
	-2		
DigColPs_SpurRoughTurns_Cnt_M_s16			
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	40		
k_SenseDetErrDiag_Cnt_str.PStep	18		
k_SenseDetErrDiag_Cnt_str.NStep	33		
k_SenseParityErrDiag_Cnt_str.Threshold	540		
k_SenseParityErrDiag_Cnt_str.PStep	5		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	130.9		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	588.673828	588.6738281 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	417	417	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	•
	-2 0	-2 0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	-2 0	0	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	-2 0 0	0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	-2 0 0 0	0 0 0	
DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32	-2 0 0 0 228.673828	0 0 0 228.6738281 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08	-2 0 0 0 0 228.673828	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_l2CColSensorFault_Cnt_M_lgc DigColPs_l2CHwColAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CSensCommFlts_Cnt_M_u08 DigColPs_l2CSpurSensorFault_Cnt_M_lgc	-2 0 0 0 228.673828 0 4	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	-2 0 0 0 228.673828 0 4	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	-2 0 0 0 228.673828 0 4 0 721	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	-2 0 0 0 228.673828 0 4 0 721 63.3691406	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721 63.36914063 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721 63.36914063 ± 0.0001220703125 1500	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938	0 0 0 228.6738281 ± 0.0001220703125 0 ± 0.0001220703125 4 0 721 63.36914063 ± 0.0001220703125 1500 131.8359375 ± 0.0001220703125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0	0 0 0 0 0 228.6738281 \pm 0.0001220703125 0 \pm 0.0001220703125 4 0 0 721 63.36914063 \pm 0.0001220703125 1500 131.8359375 \pm 0.0001220703125 0	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800	0 0 0 0 0 228.6738281 \pm 0.0001220703125 0 \pm 0.0001220703125 4 0 0 721 63.36914063 \pm 0.0001220703125 1500 131.8359375 \pm 0.0001220703125 0 -1800 \pm 0.00048828125	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800	0 0 0 0 0 228.6738281 \pm 0.0001220703125 0 \pm 0.0001220703125 4 0 0 721 63.36914063 \pm 0.0001220703125 1500 131.8359375 \pm 0.0001220703125 0 -1800 \pm 0.00048828125 1	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130	$ \begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ \end{array} $	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0	$ \begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ \end{array} $	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0 -2	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ 0 \end{array}$	
DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_12CCoISensorFault_Cnt_M_lgc DigCoIPs_12CCHoCoIAngle_Deg_M_f32 DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSensCommFits_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_s16 DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DiaCoIPs_NxtrDiagMarc_SetNTCStatus(NTC_Cnt_T_enum)	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0 -2 0	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ 0 \\ 0 \end{array}$	
DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	-2 0 0 0 228.673828 0 4 0 721 63.3691406 1500 131.835938 0 -1800 1 130 0 -2	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 228.6738281 \pm 0.0001220703125 \\ 0 \pm 0.0001220703125 \\ 4 \\ 0 \\ 721 \\ 63.36914063 \pm 0.0001220703125 \\ 1500 \\ 131.8359375 \pm 0.0001220703125 \\ 0 \\ -1800 \pm 0.00048828125 \\ 1 \\ 130 \\ 0 \\ -2 \\ 0 \end{array}$	

Test Step 2.62 (Repeat Count = 1)

Name



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Input Value

Name	iliput value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1000		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.25		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	127		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	902		
DigColPs_I2CSensCommFlts_Cnt_M_u08	22		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	763		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	95		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	168.5		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	42		
k_SenseDetErrDiag_Cnt_str.PStep	19		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	550		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	6		
k_StepDetect_Deg_f32	132		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	676.765137	676.7651367 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	262	262	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	316.765137	316.7651367 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	140.625	140.625 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	763	763	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	67.0605469	67.06054688 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1600	1600	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	140.625	140.625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-219.375	-219.375 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	259	259	~

0

DigColPs_SpurParityError_Cnt_M_lgc

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.63 (Repeat Count = 1)			•
Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1100		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.3		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	235		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	134		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	922		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	805		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	100.9		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	171		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	152		
k_SenseDetErrDiag_Cnt_str.Threshold	44		
k_SenseDetErrDiag_Cnt_str.PStep	20		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	560		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	7		
k_StepDetect_Deg_f32	134		
Name	Actual Value	Expected Value	Result
DisColDe ColAnglel DEI/CV/ Cot M etc CV/ Ille 100	704 225506	701 2255050 + 0 00040020425	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	791.225586	791.2255859 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	242	242	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	149	149	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	71.2255859	71.22558594 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	164.707031	164.7070313 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	805	805	~
DigColPs PrevI2CHwColAngle Deg M f32	70.7519531	70.75195313 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1700	1700	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	149.414063	149.4140625 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	524.707031	524.7070313 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	560	560	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	117	117	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.64 (Repeat Count = 1)	Innut Walus		
Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1400		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.6		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	253		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	165		
DigColPs_I2CHwColAngle_Cnt_M_u16	2428		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	322		
DigColPs_I2CSensCommFlts_Cnt_M_u08	24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	255		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	265		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	81		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3960		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.225		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	100		
k_SenseDetErrDiag_Cnt_str.PStep	21		
k_SenseDetErrDiag_Cnt_str.NStep	5		
k_SenseParityErrDiag_Cnt_str.Threshold	260		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	27		
k_StepDetect_Deg_f32	74		
Name	Actual Value	Expected Value	Resul
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-546.552673	-546.5527344 ± 0.00048828125	
DigColPs ColLPFInitDone Cnt M lgc	0	0	
DigOol Do Col Dorito France Cost M 444	055	055	

255

0

0

0

160

255

0

0

160

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	173.447327	173.4472656 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	92.2148438	92.21484375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	255	255	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	22.4121094	22.41210938 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	112	112	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	9.84375	9.84375 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-3147.78516	-3147.785156 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	260	260	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~





Name	Input Value	
DigColPsInt_GetData()	8	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.4	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	568	
DigColPs_ColRoughTurns_Cnt_M_s16	2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	
DigColPs_I2CHwColAngle_Cnt_M_u16	148	
DigColPs_I2CHwDataType_Cnt_M_u08	4	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	962	
DigColPs_I2CSensCommFlts_Cnt_M_u08	25	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	889	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	110	
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1900	
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	177	
ligColPs_ReqI2CSnsrDataType_Cnt_M_u08	4	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4320	
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.545	
ligColPs_SpurLPFInitDone_Cnt_M_lgc	1	
igColPs_SpurParityErrorAcc_Cnt_M_u16	241	
igColPs_SpurRoughTurns_Cnt_M_s16	2	
ligColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	
igColPs_SpurSensorFaultAcc_Cnt_M_u16	0	
_SenseDetErrDiag_Cnt_str.Threshold	48	
_SenseDetErrDiag_Cnt_str.PStep	22	
_SenseDetErrDiag_Cnt_str.NStep	37	
_SenseParityErrDiag_Cnt_str.Threshold	580	
_SenseParityErrDiag_Cnt_str.PStep	9	
_SenseParityErrDiag_Cnt_str.NStep	9	
_ochocr anytholog_on_strivotep	· ·	
	138	
_StepDetect_Deg_f32		Res
_StepDetect_Deg_f32 lame	138	Res
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125	Res
_StepDetect_Deg_f32 lame ligColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligColPs_ColLPFInitDone_Cnt_M_lgc	138 Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16	138 Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc	138 Actual Value 1099.25391 0 577 0 Expected Value 1099.253906 ± 0.00048828125 0 0 577 0 0	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16	138 Actual Value 1099.25391 0 577 0 2 Expected Value 1099.253906 ± 0.00048828125 0 0 577 2 2	Res
_StepDetect_Deg_f32 lame ligColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligColPs_ColLPFInitDone_Cnt_M_lgc ligColPs_ColParityErrorAcc_Cnt_M_u16 ligColPs_ColParityError_Cnt_M_lgc ligColPs_ColRoughTurns_Cnt_M_s16 ligColPs_ColSensorDiagFailed_Cnt_M_lgc	138 Actual Value 1099.25391 0 577 0 2 0 2 0 0 1099.253906 ± 0.00048828125 0 0 0 0 0 0 0 0 0 0 0 0 0	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_lgc	138 Actual Value 1099.25391 0 577 0 0 2 0 0 0 2 0 0 0 0 0 0	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CColSensorFault_Cnt_M_lgc	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125	Resi
_StepDetect_Deg_f32 lame DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CCHwColAngle_Deg_M_f32	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125	Resi
_StepDetect_Deg_f32 lame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_12CColSensorFault_Cnt_M_lgc igColPs_12CCHyColAngle_Deg_M_f32 igColPs_12CSensCommFlts_Cnt_M_u08	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8	Res
LigcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligColPs_ColLPFInitDone_Cnt_M_lgc ligColPs_ColParityErrorAcc_Cnt_M_u16 ligColPs_ColParityError_Cnt_M_lgc ligColPs_ColRoughTurns_Cnt_M_s16 ligColPs_ColSensorDiagFailed_Cnt_M_lgc ligColPs_ColSensorFaultAcc_Cnt_M_u16 ligColPs_12CColSensorFault_Cnt_M_lgc ligColPs_12CHwColAngle_Deg_M_f32 ligColPs_12CHwSpurAngle_Deg_M_f32 ligColPs_12CSensCommFlts_Cnt_M_u08 ligColPs_12CSpurSensorFault_Cnt_M_lgc	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0	Res
LigcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_12CColSensorFault_Cnt_M_lgc igColPs_12CHwColAngle_Deg_M_f32 igColPs_12CHwSpurAngle_Deg_M_f32 igColPs_12CSensCommFlts_Cnt_M_u08 igColPs_12CSpurSensorFault_Cnt_M_lgc igColPs_12CSpurSensorFault_Cnt_M_lgc	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889	Res
Listep Detect_Deg_f32 Iame IigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 IigColPs_ColLPFInitDone_Cnt_M_lgc IigColPs_ColParityErrorAcc_Cnt_M_u16 IigColPs_ColParityError_Cnt_M_lgc IigColPs_ColParityError_Cnt_M_st6 IigColPs_ColSensorDiagFailed_Cnt_M_lgc IigColPs_ColSensorFaultAcc_Cnt_M_u16 IigColPs_I2CColSensorFault_Cnt_M_lgc IigColPs_I2CHwColAngle_Deg_M_f32 IigColPs_I2CHwSpurAngle_Deg_M_f32 IigColPs_I2CSensCommFlts_Cnt_M_u08 IigColPs_I2CSensCommFlts_Cnt_M_lgc IigColPs_I2CSensCommFlts_Cnt_M_lgc IigColPs_I2CSensCommFlts_Cnt_M_u08 IigColPs_I2CSensCommFlts_Cnt_M_lgc IigColPs_I2CSensCommFlts_Cnt_M_lgc IigColPs_I2CSensCommFlts_Cnt_M_lgc IigColPs_I2CSensColAngle_Deg_M_f32	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125	Res
stepDetect_Deg_f32 ame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_I2CSpurSensorFault_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900	Res
stepDetect_Deg_f32ame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_l2CColSensorFaultAcc_Cnt_M_u16 igColPs_l2CColSensorFault_Cnt_M_lgc igColPs_l2CHwColAngle_Deg_M_f32 igColPs_l2CHwSpurAngle_Deg_M_f32 igColPs_l2CSensCommFlts_Cnt_M_u08 igColPs_l2CSpurSensorFault_Cnt_M_lgc igColPs_l2CSpurSensorFault_Cnt_M_lgc igColPs_Prevl2CHwColAngle_Cnt_M_u16 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900 166.992188 166.9921875 ± 0.0001220703125	Res
ame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColAngleLPFKSV_Cnt_M_gc igColPs_ColPrititDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_I2CSpurSensorFault_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32	138 Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900 166.992188 166.9921875 ± 0.0001220703125 4 4	Res
stepDetect_Deg_f32 ame igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColParityError_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_l2CColSensorFaultAcc_Cnt_M_u16 igColPs_l2CCHwColAngle_Deg_M_f32 igColPs_l2CHwColAngle_Deg_M_f32 igColPs_l2CSensorFault_Cnt_M_lgc igColPs_l2CSensCommFlts_Cnt_M_u08 igColPs_l2CSensCommFlts_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_ReqI2CSnsrDataType_Cnt_M_u08 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900 166.992188 166.9921875 ± 0.0001220703125 4 2449.010744 2449.010742 ± 0.00048828125	Res
StepDetect_Deg_f32 Iame IgColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 IgColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 IgColPs_ColLPFInitDone_Cnt_M_lgc IgColPs_ColParityErrorAcc_Cnt_M_u16 IgColPs_ColParityError_Cnt_M_lgc IgColPs_ColSensorDiagFailed_Cnt_M_lgc IgColPs_ColSensorDiagFailed_Cnt_M_lgc IgColPs_I2CColSensorFault_Cnt_M_lgc IgColPs_I2CHwColAngle_Deg_M_f32 IgColPs_I2CHwSpurAngle_Deg_M_f32 IgColPs_I2CSensCommFlts_Cnt_M_u08 IgColPs_I2CSensCommFlts_Cnt_M_u08 IgColPs_I2CSensCommFlts_Cnt_M_u16 IgColPs_PrevI2CHwColAngle_Deg_M_f32 IgColPs_PrevI2CHwColAngle_Deg_M_f32 IgColPs_PrevI2CHwColAngle_Deg_M_f32 IgColPs_PrevI2CHwSpurAngle_Ont_M_u16 IgColPs_PrevI2CHwSpurAngle_Deg_M_f32 IgColPs_PrevI2CHwSpurAngle_Deg_M_f32 IgColPs_PrevI2CHwSpurAngle_Deg_M_f32 IgColPs_ReqI2CSnsrDataType_Cnt_M_u08 IgColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 IgColPs_SpurLPFInitDone_Cnt_M_lgc	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900 166.992188 166.9921875 ± 0.0001220703125 4 2449.01074 2449.010742 ± 0.00048828125 1 1	Res
StepDetect_Deg_f32 Imme igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_ColLPFInitDone_Cnt_M_lgc igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_l2CColSensorFault_Cnt_M_lgc igColPs_l2CHwColAngle_Deg_M_f32 igColPs_l2CHwSpurAngle_Deg_M_f32 igColPs_l2CSensCommFits_Cnt_M_u08 igColPs_l2CSpurSensorFault_Cnt_M_lgc igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwColAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Cnt_M_u16 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_Prevl2CHwSpurAngle_Deg_M_f32 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurAprityErrorAcc_Cnt_M_u16	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900 166.992188 166.9921875 ± 0.0001220703125 4 4 2449.01074 2449.010742 ± 0.00048828125 1 1 250 250	Res
LigcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligcolPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligColPs_ColLPFInitDone_Cnt_M_lgc ligColPs_ColParityError_Cnt_M_gc ligColPs_ColParityError_Cnt_M_gc ligColPs_ColRoughTurns_Cnt_M_s16 ligColPs_ColSensorDiagFailed_Cnt_M_lgc ligColPs_ColSensorFaultAcc_Cnt_M_u16 ligColPs_I2CColSensorFault_Cnt_M_lgc ligColPs_I2CCHwColAngle_Deg_M_f32 ligColPs_I2CHwColAngle_Deg_M_f32 ligColPs_I2CSensCommFlts_Cnt_M_u08 ligColPs_I2CSpurSensorFault_Cnt_M_lgc ligColPs_PrevI2CHwColAngle_Deg_M_f32 ligColPs_PrevI2CHwColAngle_Deg_M_f32 ligColPs_PrevI2CHwColAngle_Deg_M_f32 ligColPs_PrevI2CHwColAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Cnt_M_u16 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_PrevI2CHwSpurAngle_Deg_M_f32 ligColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 ligColPs_SpurAngleLPFKSV_Cnt_M_gc ligColPs_SpurParityErrorAcc_Cnt_M_u16 ligColPs_SpurParityErrorAcc_Cnt_M_u16 ligColPs_SpurParityError_Cnt_M_lgc	Actual Value Expected Value 1099.25391 1099.253906 ± 0.00048828125 0 0 577 577 0 0 2 2 0 0 0 0 0 0 0 0 19.2539063 19.25390625 ± 0.0001220703125 289.010742 289.0107422 ± 0.0001220703125 8 8 0 0 889 889 78.1347656 78.13476563 ± 0.0001220703125 1900 1900 166.992188 166.9921875 ± 0.0001220703125 4 4 2449.01074 2449.010742 ± 0.00048828125 1 1 250 0	Res
Lame DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_u16 DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CCHwColAngle_Deg_M_f32 DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u6 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_gc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	Actual Value	Res
Jame DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CCHwColAngle_Deg_M_f32 DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	Actual Value	Res
Name DigCoIPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_ColLPFInitDone_Cnt_M_lgc DigCoIPs_ColPs_ColParityErrorAcc_Cnt_M_u16 DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColParityError_Cnt_M_lgc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_ColSensorFault_Cnt_M_lgc DigCoIPs_I2COlSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	Actual Value	Res
Jame DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CCHwColAngle_Deg_M_f32 DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	Actual Value	Res

DigColPs_Per1

2014-10-14, 18:11:16+0530



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Actual Function Count Expected Function Count Result

2014-10-14, 18:11:16+0530



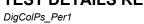
Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	120	120	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.67 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.7		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	126		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	175		
DigColPs_I2CHwColAngle_Cnt_M_u16	2584		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	25		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	275		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.235		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	412		
DigColPs_SpurRoughTurns_Cnt_M_s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	110		
k_SenseDetErrDiag_Cnt_str.PStep	24		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	270		
k_SenseParityErrDiag_Cnt_str.PStep	4		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	76		
Namo	Actual Value	Expected Value	Posult

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-121.696289	-121.6962891 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	130	130	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	169	169	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	238.303711	238.3037109 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	2.39589834	2.395898438 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	265	265	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	23.2910156	23.29101563 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	116	116	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	10.1953125	10.1953125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2.39589834	2.395898438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	270	270	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	249	249	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt GetData()	11		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	1600		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.55		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	536		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	169		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022		
DigColPs_I2CSensCommFlts_Cnt_M_u08	28		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	186		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	54		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	40		
k_SenseParityErrDiag_Cnt_str.Threshold	610		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	144		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColDs ColDarityErrorAcs Cnt M u16	548	548	

548

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DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_Per1

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200	2200	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	193.359375	193.359375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	366.679688	366.6796875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	





Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs ColParityErrorAcc Cnt M u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs I2CHwColAngle Cnt M u16	176		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs I2CHwSpurAngle Cnt M u16	1042		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	130.5		
DigColPs PrevI2CHwSpurAngle Cnt M u16	2300		
DigColPs PrevI2CHwSpurAngle Deg M f32	189		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55		
DigColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.152		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652		
DigColPs SpurRoughTurns Cnt M s16	-4		
· - · · ·	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_Igc	144		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	56		
<_SenseDetErrDiag_Cnt_str.Threshold < SenseDetErrDiag_Cnt_str.PStep	26		
	0		
k_SenseDetErrDiag_Cnt_str.NStep	620		
k_SenseParityErrDiag_Cnt_str.Threshold			
k_SenseParityErrDiag_Cnt_str.PStep	13		
k_SenseParityErrDiag_Cnt_str.NStep	13		
k_StepDetect_Deg_f32	146	I=	1_
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	932.130859	932.1308984 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	576	576	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	186	
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	
DigColPs_I2CHwColAngle_Deg_M_f32	212.130859	212.1308984 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.486572	218.4865625 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	
z.goo ozoopuroonom uui_oni_m_igo		1057	
	1057	1037	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057 92.9003906	92.90039063 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32			
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	92.9003906	92.90039063 ± 0.0001220703125	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	92.9003906 2300	92.90039063 ± 0.0001220703125 2300	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	92.9003906 2300 202.148438	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	92.9003906 2300 202.148438 3	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	92.9003906 2300 202.148438 3 -141.513428	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	92.9003906 2300 202.148438 3 -141.513428	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc	92.9003906 2300 202.148438 3 -141.513428 1 620	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc	92.9003906 2300 202.148438 3 -141.513428 1 620	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620 0	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	92.9003906 2300 202.148438 3 -141.513428 1 620 0	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620 0 -4	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_lgc	92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620 0 -4 0	
DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	92.9003906 2300 202.148438 3 -141.513428 1 620 0 -4 0	92.90039063 ± 0.0001220703125 2300 202.1484375 ± 0.0001220703125 3 -141.5134375 ± 0.00048828125 1 620 0 -4 0 144	



T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	624		
DigColPs ColRoughTurns Cnt M s16	1		
DigColPs ColSensorDiagFailed Cnt M lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	101		
DigColPs_I2CHwColAngle_Cnt_M_u16	294		
DigColPs I2CHwDataType Cnt M u08	2		
DigColPs I2CHwSpurAngle Cnt M u16	1382		
DigColPs I2CSensCommFlts Cnt M u08	17		
DigColPs PrevI2CHwColAngle Cnt M u16	1771		
DigColPs PrevI2CHwColAngle Deg M f32	180		
DigColPs PrevI2CHwSpurAngle Cnt M u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs SpurLPFInitDone Cnt M lgc	0		
DigColPs SpurParityErrorAcc Cnt M u16	965		
DigColPs SpurRoughTurns Cnt M s16	1		
DigColPs SpurSensorDiagFailed Cnt M lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
:_SenseDetErrDiag_Cnt_str.Threshold	90		
SenseDetErrDiag_Crit_str.Fileshold	43		
	18		
SenseDetErrDiag_Cnt_str.NStep	760		
_SenseParityErrDiag_Cnt_str.Threshold	30		
_SenseParityErrDiag_Cnt_str.PStep	30		
_SenseParityErrDiag_Cnt_str.NStep	174		
StepDetect_Deg_f32			
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	
ligColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	654	654	
ligColPs_ColParityError_Cnt_M_lgc	0	0	
0igColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
igColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	
igColPs_I2CColSensorFault_Cnt_M_Igc	1	1	
igColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	
igColPs_I2CHwSpurAngle_Deg_M_f32	132.358521	132.3585 ± 0.0001220703125	
igColPs_I2CSensCommFlts_Cnt_M_u08	6	6	
igColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	
		455 0540000 + 0.0004000700405	
)igColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	155.654297 4000	4000	

351.5625

852.358521

760

0

DigColPs_PrevI2CHwSpurAngle_Deg_M_f32

DigColPs_Reql2CSnsrDataType_Cnt_M_u08
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32

DigColPs_SpurLPFInitDone_Cnt_M_lgc

 ${\sf DigColPs_SpurParityError_Cnt_M_lgc}$

DigColPs_SpurParityErrorAcc_Cnt_M_u16

351.5625 ± 0.0001220703125

852.3585 ± 0.00048828125

0

0

760

DigColPs_Per1

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	6	6	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.71 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101		
DigColPs_I2CHwColAngle_Cnt_M_u16	295		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•

390

0

-1

1

14

390

-1

1

14

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T →					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	~	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~	





Test Step 2.73 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1440		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.276		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs ColParityErrorAcc Cnt M u16	286		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	204		
DigColPs I2CHwDataType Cnt M u08	4		
DigColPs I2CHwSpurAngle Cnt M u16	1122		
DigColPs_I2CSensCommFlts_Cnt_M_u08	4		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1225		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	150		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	201.4		
DigColPs_Previ2CHwSpurArigie_Deg_M_i32 DigColPs_Reql2CSnsrDataType_Cnt_M_u08	201.4		
DigColPs_ReqizeShsiDataType_Cht_w_uoo DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1275		
DigColPs_SpurAngleLPFKSV_Crit_M_str.Sv_Uis_132 DigColPs_SpurAngleLPFKSV_Crit_M_str.K_Uls_f32	0.216		
	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	999		
DigColPs_SpurParityErrorAcc_Cnt_M_u16			
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	100		
k_SenseDetErrDiag_Cnt_str.Threshold	64		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	5		
k_SenseParityErrDiag_Cnt_str.Threshold	660		
k_SenseParityErrDiag_Cnt_str.PStep	17		
k_SenseParityErrDiag_Cnt_str.NStep	17		
k_StepDetect_Deg_f32	154		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1112.20422	-1112.20418 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	303	303	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	181	181	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	•
DigCoIPs_I2CHwCoIAngle_Deg_M_f32	327.795776	327.7958203 ± 0.0001220703125	•
DIGOON 3_12OI IWOOIAIIGIE_DEG_IVI_I32	327.793770		
	253.097778	253.0978125 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32		253.0978125 ± 0.0001220703125 16	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08	253.097778		
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc	253.097778 16	16	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	253.097778 16 1	16 1	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	253.097778 16 1 1225	16 1 1225	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	253.097778 16 1 1 1225 107.666016	16 1 1225 107.6660156 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	253.097778 16 1 1 1225 107.666016 2700	16 1 1225 107.6660156 ± 0.0001220703125 2700	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	253.097778 16 1 1225 107.666016 2700 237.304688	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32	253.097778 16 1 1225 107.666016 2700 237.304688 2	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1	
DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurRoughTurns_Cnt_M_gc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0 -1 0 95	$16\\1\\1\\225\\107.6660156 \pm 0.0001220703125\\2700\\237.3046875 \pm 0.0001220703125\\2\\973.0978125 \pm 0.00048828125\\1\\660\\0\\-1\\0\\95$	
DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_gc DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	253.097778 16 1 1225 107.666016 2700 237.304688 2 973.097778 1 660 0 -1	16 1 1225 107.6660156 ± 0.0001220703125 2700 237.3046875 ± 0.0001220703125 2 973.0978125 ± 0.00048828125 1 660 0 -1 0	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.74 (Repeat Count = 1)	Innut Value		
Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs SpurSensorFaultAcc Cnt M u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k SenseDetErrDiag Cnt str.PStep	41		
k SenseDetErrDiag Cnt str.NStep	16		
	740		
k_SenseParityErrDiag_Cnt_str.Threshold	· ·		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	•
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	- ·
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	•
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	
DigColPs PrevI2CHwSpurAngle Deg M f32	333.984375	333.984375 ± 0.0001220703125	
DigColPs RegI2CSnsrDataType Cnt M u08	3	3	
0 = 1	437.289886	437.289875 ± 0.00048828125	
DIGCOIPS SpurAngieLPFKSV Cnt M Str.SV UIS 132			
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lac	0	0	
DigCoIPs_SpurAngieLPFKSV_Cnt_M_str.Sv_Uis_132 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 740	740	•

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.75 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	11		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	536		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	169		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1022		
DigColPs_I2CSensCommFlts_Cnt_M_u08	28		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	180		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-900		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.5		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	625		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	165		
k_SenseDetErrDiag_Cnt_str.Threshold	54		
k_SenseDetErrDiag_Cnt_str.PStep	25		
k_SenseDetErrDiag_Cnt_str.NStep	40		
k_SenseParityErrDiag_Cnt_str.Threshold	610		
k_SenseParityErrDiag_Cnt_str.PStep	12		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	144		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	144	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	90	90 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	450	450 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	5	5	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•

390

0

-1

1

14

390

-1

1

14

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

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Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	87.4535522	87.45354688 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	447.453552	447.4535469 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T ·				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.77 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
igColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
ligColPs_PrevI2CHwColAngle_Deg_M_f32	0		
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2047		
igColPs PrevI2CHwSpurAngle Deg M f32	360		
igColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
igColPs SpurAngleLPFKSV Cnt M str.K Uls f32	0.392		
bigColPs_SpurLPFInitDone_Cnt_M_lgc	0.392		
igCoIPs_SpurParityErrorAcc Cnt M u16	865		
bigCoIPs SpurRoughTurns Cnt M s16	-1		
· - · · ·	1		
igColPs_SpurSensorDiagFailed_Cnt_M_lgc	146		
igColPs_SpurSensorFaultAcc_Cnt_M_u16 _SenseDetErrDiag_Cnt_str.Threshold	86		
	41		
_SenseDetErrDiag_Cnt_str.PStep			
_SenseDetErrDiag_Cnt_str.NStep	16		
_SenseParityErrDiag_Cnt_str.Threshold	740		
_SenseParityErrDiag_Cnt_str.PStep	28		
SenseParityErrDiag_Cnt_str.NStep	25		
_StepDetect_Deg_f32	170.7		
lame	Actual Value	Expected Value	Res
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	
igColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
igColPs_ColParityErrorAcc_Cnt_M_u16	390	390	
igColPs_ColParityError_Cnt_M_lgc	0	0	
	-1	-1	
igColPs_ColRoughTurns_Cnt_M_s16	-1 1	1	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc			
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16	1	1	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc	1 14	1 14	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32	1 14 1	1 14 1	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32	1 14 1 138.148849	1 14 1 138.1488477 ± 0.0001220703125	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08	1 14 1 138.148849 158.01355	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125	
bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFlts_Cnt_M_u08 bigColPs_I2CSpurSensorFault_Cnt_M_lgc	1 14 1 138.148849 158.01355	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_lgc igColPs_PrevI2CHwColAngle_Cnt_M_u16	1 14 1 138.148849 158.01355 4	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_Igc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_Igc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_I2CSpurSensorFault_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	1 14 1 138.148849 158.01355 4 1	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_Igc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_Igc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32	1 14 1 138.148849 158.01355 4 1 1687 148.271484	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_Igc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_Igc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 igCoIPs_PrevI2CHwSpurAngle_Cnt_M_16	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125 2047	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125 2047 179.9121094 ± 0.0001220703125	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CCHwCoIAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125 2047 179.9121094 ± 0.0001220703125 3	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355	$\begin{array}{c} 1 \\ 14 \\ 1 \\ 138.1488477 \pm 0.0001220703125 \\ 158.0135469 \pm 0.0001220703125 \\ 4 \\ 1 \\ 1687 \\ 148.2714844 \pm 0.0001220703125 \\ 2047 \\ 179.9121094 \pm 0.0001220703125 \\ 3 \\ 518.0135469 \pm 0.00048828125 \\ \end{array}$	
igCoIPs_CoIRoughTurns_Cnt_M_s16 igCoIPs_CoISensorDiagFailed_Cnt_M_lgc igCoIPs_CoISensorFaultAcc_Cnt_M_u16 igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CCoISensorFault_Cnt_M_lgc igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CHwSpurAngle_Deg_M_f32 igCoIPs_I2CSensCommFlts_Cnt_M_u08 igCoIPs_I2CSpurSensorFault_Cnt_M_lgc igCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 igCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 igCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 igCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 igCoIPs_SpurLPFInitDone_Cnt_M_lgc igCoIPs_SpurParityErrorAcc_Cnt_M_u16	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355 0	$\begin{array}{c} 1 \\ 14 \\ 1 \\ 138.1488477 \pm 0.0001220703125 \\ 158.0135469 \pm 0.0001220703125 \\ 4 \\ 1 \\ 1687 \\ 148.2714844 \pm 0.0001220703125 \\ 2047 \\ 179.9121094 \pm 0.0001220703125 \\ 3 \\ 518.0135469 \pm 0.00048828125 \\ 0 \\ \end{array}$	
igColPs_ColRoughTurns_Cnt_M_s16 igColPs_ColSensorDiagFailed_Cnt_M_lgc igColPs_ColSensorFaultAcc_Cnt_M_u16 igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CColSensorFault_Cnt_M_lgc igColPs_I2CHwColAngle_Deg_M_f32 igColPs_I2CHwSpurAngle_Deg_M_f32 igColPs_I2CSensCommFlts_Cnt_M_u08 igColPs_I2CSpurSensorFault_Cnt_M_u6 igColPs_PrevI2CHwColAngle_Cnt_M_u16 igColPs_PrevI2CHwColAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_PrevI2CHwSpurAngle_Deg_M_f32 igColPs_SpurAngle_PFKSV_Cnt_M_u08 igColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityError_Cnt_M_lgc	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355 0 740	$ \begin{array}{c} 1 \\ 14 \\ 1 \\ 138.1488477 \pm 0.0001220703125 \\ 158.0135469 \pm 0.0001220703125 \\ 4 \\ 1 \\ 1687 \\ 148.2714844 \pm 0.0001220703125 \\ 2047 \\ 179.9121094 \pm 0.0001220703125 \\ 3 \\ 518.0135469 \pm 0.00048828125 \\ 0 \\ 740 \\ 0 \\ \end{array} $	
bigColPs_ColRoughTurns_Cnt_M_s16 bigColPs_ColSensorDiagFailed_Cnt_M_lgc bigColPs_ColSensorFaultAcc_Cnt_M_u16 bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CColSensorFault_Cnt_M_lgc bigColPs_I2CHwColAngle_Deg_M_f32 bigColPs_I2CHwSpurAngle_Deg_M_f32 bigColPs_I2CSensCommFits_Cnt_M_u08 bigColPs_I2CSensCommFits_Cnt_M_u6 bigColPs_PrevI2CHwColAngle_Cnt_M_u16 bigColPs_PrevI2CHwColAngle_Deg_M_f32 bigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 bigColPs_PrevI2CHwSpurAngle_Deg_M_f32 bigColPs_PrevI2CHwSpurAngle_Deg_M_f32 bigColPs_PrevI2CHwSpurAngle_Deg_M_f32 bigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 bigColPs_SpurAngleLPFKSV_Cnt_M_lgc bigColPs_SpurParityErrorAcc_Cnt_M_u16 bigColPs_SpurParityErrorAcc_Cnt_M_u16 bigColPs_SpurParityErrorCnt_M_lgc bigColPs_SpurAngleTrns_Cnt_M_lgc bigColPs_SpurRoughTurns_Cnt_M_s16	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355 0 740 0	$ \begin{array}{c} 1 \\ 14 \\ 1 \\ 138.1488477 \pm 0.0001220703125 \\ 158.0135469 \pm 0.0001220703125 \\ 4 \\ 1 \\ 1687 \\ 148.2714844 \pm 0.0001220703125 \\ 2047 \\ 179.9121094 \pm 0.0001220703125 \\ 3 \\ 518.0135469 \pm 0.00048828125 \\ 0 \\ 740 \\ \end{array} $	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355 0 740 0 0 1	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125 2047 179.9121094 ± 0.0001220703125 3 518.0135469 ± 0.00048828125 0 740 0 0 1	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSensCommFlts_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355 0 740 0 0 1 1 130	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125 2047 179.9121094 ± 0.0001220703125 3 518.0135469 ± 0.00048828125 0 740 0 0 1 130	
DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_u18 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurAngleLPFKSV_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1 14 1 138.148849 158.01355 4 1 1687 148.271484 2047 179.912109 3 518.01355 0 740 0 0 1	1 14 1 138.1488477 ± 0.0001220703125 158.0135469 ± 0.0001220703125 4 1 1687 148.2714844 ± 0.0001220703125 2047 179.9121094 ± 0.0001220703125 3 518.0135469 ± 0.00048828125 0 740 0 0 1	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enable12CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.78 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.306		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	241		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	239		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1222		
DigColPs_I2CSensCommFlts_Cnt_M_u08	9		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1435		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	175		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3200		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	216		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	76		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.296		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	245		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	145		
k_SenseDetErrDiag_Cnt_str.Threshold	74		
k_SenseDetErrDiag_Cnt_str.PStep	35		
k_SenseDetErrDiag_Cnt_str.NStep	10		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	22		
k_SenseParityErrDiag_Cnt_str.NStep	22		
k_StepDetect_Deg_f32	164		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	35.0736694	35.07365234 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	1	1	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	176	176	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	35.0736694	35.07365234 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	202.994019	202.994 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1435	1435	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	126.123047	126.1230469 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3200	3200	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	281.25	281.25 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	V
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	562.994019	562.994 ± 0.00048828125	V
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1	1	V
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓

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Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	135	135	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.79 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	5		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-480		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	246		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1242		
DigColPs_I2CSensCommFlts_Cnt_M_u08	10		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1477		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	219		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	186		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.312		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs_SpurRoughTurns_Cnt_M_s16	4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	76		
k_SenseDetErrDiag_Cnt_str.PStep	36		
k_SenseDetErrDiag_Cnt_str.NStep	11		
k_SenseParityErrDiag_Cnt_str.Threshold	1000		
k_SenseParityErrDiag_Cnt_str.PStep	23		
k_SenseParityErrDiag_Cnt_str.NStep	23		
k_StepDetect_Deg_f32	166		
Name	Actual Value	Expected Value	Result

K_0tep56test_569_162	100		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	159.542114	159.5421094 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	546	546	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	159.542114	159.5421094 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	307.740234	307.7401875 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1477	1477	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	129.814453	129.8144531 ± 0.0001220703125	~





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3300	3300	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	290.039063	290.0390625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	667.740234	667.7401875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	279	279	✓
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	6		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.318		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	365		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	253		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1262		
DigColPs_I2CSensCommFlts_Cnt_M_u08	11		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1519		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	185		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3400		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	222		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	296		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.328		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	263		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	78		
k_SenseDetErrDiag_Cnt_str.PStep	37		
k_SenseDetErrDiag_Cnt_str.NStep	12		
k_SenseParityErrDiag_Cnt_str.Threshold	500		
k_SenseParityErrDiag_Cnt_str.PStep	24		
k_SenseParityErrDiag_Cnt_str.NStep	24		
k_StepDetect_Deg_f32	168		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-633.705139	-633.7051367 ± 0.00048828125	-
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•

Name	Actual value	Expected value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-633.705139	-633.7051367 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	389	389	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	86.2948608	86.29486328 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	184.607605	184.607625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	6	6	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1519	1519	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	133.505859	133.5058594 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3400	3400	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	298.828125	298.828125 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-175.392395	-175.392375 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	287	287	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.81 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.324		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	156		
DigColPs_I2CHwColAngle_Cnt_M_u16	260		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1282		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1561		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	190		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3500		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	225		
DigCoIPs_PrevizcHwSpurArigie_Deg_w_isz DigCoIPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_Reqi2C3fisiDataType_Cfit_iw_u06 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	406		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uis_132 DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.344		
	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	254		
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168		
k_SenseDetErrDiag_Cnt_str.Threshold	80		
k_SenseDetErrDiag_Cnt_str.PStep	38		
k_SenseDetErrDiag_Cnt_str.NStep	13		
k_SenseParityErrDiag_Cnt_str.Threshold	710		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-413.628082	-413.6280859 ± 0.00048828125	'
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	281	281	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	143	143	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	306.371918	306.3719141 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	112.956299	112.9563125 ± 0.0001220703125	•
2.900 0_1201 mopan mgio_20g_in_102		I _	
	7	7	
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7 0	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc			
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	•
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	0 1561	0 1561	
DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	0 1561 137.197266	0 1561 137.1972656 ± 0.0001220703125	
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0 1561 137.197266 3500	0 1561 137.1972656 ± 0.0001220703125 3500	
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0 1561 137.197266 3500 307.617188	0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125	
DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32	0 1561 137.197266 3500 307.617188 0	0 1561 137.1972656 ± 0.0001220703125 3500 307.6171875 ± 0.0001220703125 0	
DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	0 1561 137.197266 3500 307.617188 0 -247.043701	0 1561 137.1972656 \pm 0.0001220703125 3500 307.6171875 \pm 0.0001220703125 0 -247.0436875 \pm 0.00048828125	
DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 1561 137.197266 3500 307.617188 0 -247.043701	0 1561 137.1972656 \pm 0.0001220703125 3500 307.6171875 \pm 0.0001220703125 0 -247.0436875 \pm 0.00048828125	
DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc	0 1561 137.197266 3500 307.617188 0 -247.043701 1	0 1561 137.1972656 \pm 0.0001220703125 3500 307.6171875 \pm 0.0001220703125 0 -247.0436875 \pm 0.00048828125 1 279	
DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc	0 1561 137.197266 3500 307.617188 0 -247.043701 1 279	0 1561 137.1972656 \pm 0.0001220703125 3500 307.6171875 \pm 0.0001220703125 0 -247.0436875 \pm 0.00048828125 1 279 0	
DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSensCommFits_Cnt_M_lgc DigColPs_12CSensCommFits_Cnt_M_lgc DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Req12CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0	0 1561 137.1972656 \pm 0.0001220703125 3500 307.6171875 \pm 0.0001220703125 0 -247.0436875 \pm 0.00048828125 1 279 0 -5	,
DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_gc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0 -5	$\begin{matrix} 0\\ 1561\\ 137.1972656 \pm 0.0001220703125\\ 3500\\ 307.6171875 \pm 0.0001220703125\\ 0\\ -247.0436875 \pm 0.00048828125\\ 1\\ 279\\ 0\\ -5\\ 0\\ \end{matrix}$	
DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_gc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	0 1561 137.197266 3500 307.617188 0 -247.043701 1 279 0 -5	$\begin{matrix} 0 \\ 1561 \\ 137.1972656 \pm 0.0001220703125 \\ 3500 \\ 307.6171875 \pm 0.0001220703125 \\ 0 \\ -247.0436875 \pm 0.00048828125 \\ 1 \\ 279 \\ 0 \\ -5 \\ 0 \\ 155 \\ \end{matrix}$	



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.82 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.33		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs ColParityErrorAcc Cnt M u16	365		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	134		
DigColPs I2CHwColAngle Cnt M u16	267		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1302		
DigColPs I2CSensCommFlts Cnt M u08	13		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1603		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	195		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3600		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	228		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	516		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.36		
DigColPs SpurLPFInitDone Cnt M Igc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	214		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs SpurSensorDiagFailed Cnt M Igc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	82		
	39		
k_SenseDetErrDiag_Cnt_str.PStep	14		
k_SenseDetErrDiag_Cnt_str.NStep	720		
k_SenseParityErrDiag_Cnt_str.Threshold	26		
k_SenseParityErrDiag_Cnt_str.PStep	26		
k_SenseParityErrDiag_Cnt_str.NStep	340		
k_StepDetect_Deg_f32		I=	1
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-309.906738	-309.9067383 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	391	391	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	50.0932617	50.09326172 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	55.3462219	55.34625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1603	1603	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	140.888672	140.8886719 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3600	3600	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	316.40625	316.40625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55.3462219	55.34625 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240	240	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	132	132	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.83 (Repeat Count = 1)	Innut Value		•
Name	Input Value		
DigColPsInt_GetData()	9		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	160		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.336		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	251		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	135		
DigColPs_I2CHwColAngle_Cnt_M_u16	274		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1322		
DigColPs_I2CSensCommFlts_Cnt_M_u08	14		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1645		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	200		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3700		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	231		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	626		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.376		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	256		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	123		
k_SenseDetErrDiag_Cnt_str.Threshold	84		
k_SenseDetErrDiag_Cnt_str.PStep	40		
k_SenseDetErrDiag_Cnt_str.NStep	15		
k_SenseParityErrDiag_Cnt_str.Threshold	730		
k_SenseParityErrDiag_Cnt_str.PStep	27		
k_SenseParityErrDiag_Cnt_str.NStep	27		
k_StepDetect_Deg_f32	180		
Name	A atual Value	Fyma ata d Value	Daguit

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-87.1010895	-87.10109375 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	278	278	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	120	120	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	272.898926	272.8989063 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	242.17746	242.1774375 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1645	1645	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	144.580078	144.5800781 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3700	3700	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	325.195313	325.1953125 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	242.17746	242.1774375 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	283	283	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	108	108	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	✓
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt GetData()	10		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	362		
DigColPs ColRoughTurns Cnt M s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColDo Coll DEInitDone Cot M Igo	1	1	

DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt GetData()	7		
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	640		
DigColPs ColAngleLPFKSV Cnt M str.K Uls f32	0.354		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	624		
DigColPs ColRoughTurns Cnt M s16	1		
DigColPs ColSensorDiagFailed Cnt M Igc	1		
DigColPs ColSensorFaultAcc Cnt M u16	101		
DigColPs I2CHwColAngle Cnt M u16	295		
DigColPs I2CHwDataType Cnt M u08	2		
DigColPs I2CHwSpurAngle Cnt M u16	1382		
DigColPs I2CSensCommFlts Cnt M u08	17		
DigColPs PrevI2CHwColAngle Cnt M u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	723.421631	723.4216211 ± 0.00048828125	
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DigColPs_ColLPFInitDone_Cnt_M_lgc DigColPs_ColParityErrorAcc_Cnt_M_u16

DigColPs_Per1





Name	Actual Value	Expected Value	Result
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	2	2	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	3.42163086	3.421621094 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	7	7	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.86 (Repeat Count = 1)	· ·
Name	Input Value
DigColPsInt_GetData()	12
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	294
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	180
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965
DigColPs_SpurRoughTurns_Cnt_M_s16	1
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186
k_SenseDetErrDiag_Cnt_str.Threshold	90
k_SenseDetErrDiag_Cnt_str.PStep	43
k_SenseDetErrDiag_Cnt_str.NStep	18
k_SenseParityErrDiag_Cnt_str.Threshold	760

DigColPs_SpurParityErrorAcc_Cnt_M_u16

DigColPs_SpurParityError_Cnt_M_lgc

DigColPs_SpurRoughTurns_Cnt_M_s16

DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16

 $Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$

Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

 $Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

2014-10-14, 18:11:16+0530



DigColPs_Per1 Input Value k_SenseParityErrDiag_Cnt_str.PStep 30 k_SenseParityErrDiag_Cnt_str.NStep 30 k_StepDetect_Deg_f32 174 Name **Actual Value Expected Value** Result DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32 595.981628 $595.9816211 \pm 0.00048828125$ DigColPs_ColLPFInitDone_Cnt_M_lgc 1 DigColPs_ColParityErrorAcc_Cnt_M_u16 654 654 DigColPs_ColParityError_Cnt_M_lgc 0 0 $DigColPs_ColRoughTurns_Cnt_M_s16$ 1 DigColPs_ColSensorDiagFailed_Cnt_M_lgc 1 ソソソソソソソソソソソソソ ${\sf DigColPs_ColSensorFaultAcc_Cnt_M_u16}$ 83 83 DigColPs_I2CColSensorFault_Cnt_M_lgc 1 DigColPs_I2CHwColAngle_Deg_M_f32 235.981628 235.9816211 ± 0.0001220703125 DigColPs_I2CHwSpurAngle_Deg_M_f32 132.358521 132.3585 ± 0.0001220703125 DigColPs I2CSensCommFlts Cnt M u08 12 12 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 1771 1771 DigColPs_PrevI2CHwColAngle_Deg_M_f32 155.654297 155.6542969 ± 0.0001220703125 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 4000 ${\tt DigColPs_PrevI2CHwSpurAngle_Deg_M_f32}$ $351.5625 \pm 0.0001220703125$ 351.5625 DigColPs_Reql2CSnsrDataType_Cnt_M_u08 0 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 852.358521 $852.3585 \pm 0.00048828125$ DigColPs_SpurLPFInitDone_Cnt_M_lgc 0 0

760

0

1

168

109

12

1

760

0

1

168

109

12

1

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.87 (Repeat Count = 1)	
Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	624
DigColPs_ColRoughTurns_Cnt_M_s16	1
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1
DigColPs_ColSensorFaultAcc_Cnt_M_u16	101
DigColPs_I2CHwColAngle_Cnt_M_u16	295
DigColPs_I2CHwDataType_Cnt_M_u08	2
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382
DigColPs_I2CSensCommFlts_Cnt_M_u08	17
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	965

DigColPs_SpurSensorDiagFailed_Cnt_M_lgc

DigColPs_SpurSensorFaultAcc_Cnt_M_u16

Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)

 $Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)$

 $Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

2014-10-14, 18:11:16+0530





Name	Input Value		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	35		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	659	659	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	235.981628	235.9816211 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	339.718506	339.7185 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771	1771	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	155.654297	155.6542969 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000	4000	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	351.5625	351.5625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	699.718506	699.7185 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	760	760	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	•

T ·				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

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Test Step 2.88 (Repeat Count = 1)		~
Name	Input Value	
DigColPsInt_GetData()	10	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	362	
DigColPs_ColRoughTurns_Cnt_M_s16	-1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30	
DigColPs_I2CHwColAngle_Cnt_M_u16	281	
DigColPs_I2CHwDataType_Cnt_M_u08	0	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342	
DigColPs_I2CSensCommFlts_Cnt_M_u08	15	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	





Name	Input Value		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.89 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetData()	11
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.55
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	536
DigColPs_ColRoughTurns_Cnt_M_s16	4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1

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DigColPs_Per1 Input Value DigColPs_ColSensorFaultAcc_Cnt_M_u16 184 DigColPs_I2CHwColAngle_Cnt_M_u16 169 DigColPs_I2CHwDataType_Cnt_M_u08 2 DigColPs_I2CHwSpurAngle_Cnt_M_u16 1022 DigColPs_I2CSensCommFlts_Cnt_M_u08 28 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 1015 DigColPs_PrevI2CHwColAngle_Deg_M_f32 125 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 2200 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 180.4 $DigColPs_Reql2CSnsrDataType_Cnt_M_u08$ 2 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 -900 DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32 0.5 DigColPs_SpurLPFInitDone_Cnt_M_lgc 0 DigColPs_SpurParityErrorAcc_Cnt_M_u16 625 DigColPs_SpurRoughTurns_Cnt_M_s16 4 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc 165 ${\tt DigColPs_SpurSensorFaultAcc_Cnt_M_u16}$ k_SenseDetErrDiag_Cnt_str.Threshold 54 $k_SenseDetErrDiag_Cnt_str.PStep$ 25 k_SenseDetErrDiag_Cnt_str.NStep 40 $k_SenseParityErrDiag_Cnt_str.Threshold$ 610 k_SenseParityErrDiag_Cnt_str.PStep 12 k_SenseParityErrDiag_Cnt_str.NStep 12 k StenDetect Dea f32 1//

k_StepDetect_Deg_f32	144		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1561.06494	1561.064941 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	548	548	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	4	4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	144	144	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	121.064941	121.0649414 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	6.6796875	6.6796875 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	11	11	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1015	1015	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	89.2089844	89.20898438 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2200	2200	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	193.359375	193.359375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	366.679688	366.6796875 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	610	610	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	125	125	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	11	11	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	-
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	✓





Test Step 2.90 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs ColSensorFaultAcc Cnt M u16	30		
DigColPs I2CHwColAngle Cnt M u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs PrevI2CHwSpurAngle Deg M f32	234		
DigColPs_PrevizenwspurArigie_Deg_w_isz DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_ReqizeStisiDataType_Crit_ivi_u06 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
	0.392		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	865		
	-1		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	28		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resu
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	_ ·
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
	'		
	14	14	
DigColPs_ColSensorFaultAcc_Cnt_M_u16		14 1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	14		
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32	14	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	14 1 138.148849	1 138.1488477 ± 0.0001220703125	
DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08	14 1 138.148849 77.2898865	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16	14 1 138.148849 77.2898865 16	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16	
DigCoIPs_CoISensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_lgc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	14 1 138.148849 77.2898865 16	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	14 1 138.148849 77.2898865 16 1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensorFault_Cnt_M_lgc DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSensCommFlts_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_st6	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740 0 -1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0 -1 1	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740 0 -1 1 1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0 -1 1 130	
DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_lgc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_lgc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc	14 1 138.148849 77.2898865 16 1 1687 148.271484 3800 333.984375 0 437.289886 0 740 0 -1	1 138.1488477 ± 0.0001220703125 77.289875 ± 0.0001220703125 16 1 1687 148.2714844 ± 0.0001220703125 3800 333.984375 ± 0.0001220703125 0 437.289875 ± 0.00048828125 0 740 0 -1 1	

DigColPs_Per1

2014-10-14, 18:11:16+0530



T
Actual Function Count Expected Function Count Result

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	168	168	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	5	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
EnableI2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓

Test Step 2.92 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	1		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	181	181	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	2		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1760		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.396		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	344		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1522		
DigColPs_I2CSensCommFlts_Cnt_M_u08	24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	270		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	245		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1780		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.536		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	104		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	830		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	37		
k_StepDetect_Deg_f32	188		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	707.231689	707.2316797 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
DigColPs ColParityErrorAcc Cnt M u16	523	523	,
Di o in o in il F			

0

-3

0

159

0

-3

0

159

DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	347.231689	347.2316797 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.2365723	80.2365625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	2	2	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065	2065	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	181.494141	181.4941406 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956	956	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84.0234375	84.0234375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1359.76343	-1359.763438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	830	830	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	161	161	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.94 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	3		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	640		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.354		
DigColPs ColLPFInitDone Cnt M Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	624		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs ColSensorFaultAcc Cnt M u16	101		
DigColPs_I2CHwColAngle_Cnt_M_u16	295		
DigColPs_I2CHwDataType_Cnt_M_u08	2		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1382		
DigColPs_I2CSensCommFlts_Cnt_M_u08	17		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1771		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	280		
DigColPs_PrevI2CHwColAngle_Deg_wi_32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4000		
DigColPs PrevI2CHwSpurAngle Deg M f32	240		
· · · · · · · · · · · · · · · · · · ·	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	956		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.424		
	0.424		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	965		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc			
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	90		
k_SenseDetErrDiag_Cnt_str.PStep	43		
k_SenseDetErrDiag_Cnt_str.NStep	18		
k_SenseParityErrDiag_Cnt_str.Threshold	760		
k_SenseParityErrDiag_Cnt_str.PStep	50		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	174	I=	I
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	595.981628	595.9816211 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	674	674	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	83	83	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc	83 1	83 1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_l2CColSensorFault_Cnt_M_lgc DigColPs_l2CHwColAngle_Deg_M_f32	83 1 235.981628	83 1 235.9816211 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32	83 1 235.981628 132.358521	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_l2CColSensorFault_Cnt_M_lgc DigColPs_l2CHwColAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CSensCommFlts_Cnt_M_u08	83 1 235.981628 132.358521 3	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125 3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_l2CColSensorFault_Cnt_M_lgc DigColPs_l2CHwColAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CSensCommFits_Cnt_M_u08 DigColPs_l2CSpurSensorFault_Cnt_M_lgc	83 1 235.981628 132.358521 3	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125 3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFlts_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16	83 1 235.981628 132.358521 3 1 1771	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125 3 1 1771	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32	83 1 235.981628 132.358521 3 1 1771 155.654297	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125 3 1 1771 155.6542969 ± 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_l2CColSensorFault_Cnt_M_lgc DigColPs_l2CHwColAngle_Deg_M_f32 DigColPs_l2CHwSpurAngle_Deg_M_f32 DigColPs_l2CSensCommFits_Cnt_M_u08 DigColPs_l2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	83 1 235.981628 132.358521 3 1 1771 155.654297 4000	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125 3 1 1771 155.6542969 ± 0.0001220703125 4000	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625	83 1 235.9816211 \pm 0.0001220703125 132.3585 \pm 0.0001220703125 3 1 1771 155.6542969 \pm 0.0001220703125 4000 351.5625 \pm 0.0001220703125	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFlts_Cnt_M_u08 DigColPs_L2CSensCommFlts_Cnt_M_lgc DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_lgc DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFlts_Cnt_M_u08 DigColPs_L2CSensCommFlts_Cnt_M_u08 DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFits_Cnt_M_u08 DigColPs_L2CSensCommFits_Cnt_M_lgc DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFits_Cnt_M_u08 DigColPs_L2CSensCommFits_Cnt_M_lgc DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760 0	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_L2CColSensorFault_Cnt_M_lgc DigColPs_L2CHwColAngle_Deg_M_f32 DigColPs_L2CHwSpurAngle_Deg_M_f32 DigColPs_L2CSensCommFlts_Cnt_M_u08 DigColPs_L2CSensCommFlts_Cnt_M_lgc DigColPs_L2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760 0 1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0 1	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760 0 1 1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0 1 1 168	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760 0 1 1 1 168	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0 1 1 168 109	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760 0 1 1 1 168 109	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CColSensorFault_Cnt_M_lgc DigColPs_I2CHwColAngle_Deg_M_f32 DigColPs_I2CHwSpurAngle_Deg_M_f32 DigColPs_I2CSensCommFits_Cnt_M_u08 DigColPs_I2CSpurSensorFault_Cnt_M_lgc DigColPs_PrevI2CHwColAngle_Cnt_M_u16 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_gc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0 1 1 168 109 3	83 1 235.9816211 ± 0.0001220703125 132.3585 ± 0.0001220703125 3 1 1771 155.6542969 ± 0.0001220703125 4000 351.5625 ± 0.0001220703125 0 852.3585 ± 0.00048828125 0 760 0 1 1 168 109 3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSensCommFits_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_lgc DigColPs_Prevl2CHwColAngle_Cnt_M_u16 DigColPs_Prevl2CHwColAngle_Deg_M_f32 DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigColPs_Prevl2CHwSpurAngle_Cnt_M_u16 DigColPs_Prevl2CHwSpurAngle_Deg_M_f32 DigColPs_Prevl2CHwSpurAngle_Deg_M_f32 DigColPs_Prevl2CHwSpurAngle_Drd_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_gc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	83 1 235.981628 132.358521 3 1 1771 155.654297 4000 351.5625 0 852.358521 0 760 0 1 1 168 109	83 1 $235.9816211 \pm 0.0001220703125$ $132.3585 \pm 0.0001220703125$ 3 1 1771 $155.6542969 \pm 0.0001220703125$ 4000 $351.5625 \pm 0.0001220703125$ 0 $852.3585 \pm 0.00048828125$ 0 760 0 1 1 1 168 109	

Test Step 2.95 (Repeat Count = 1)



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	4		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	25		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	483.743164	483.7430664 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	170	170	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	162	162	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	V
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	V
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	
DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_tgc	820 0	820	~



TEST DETAILS REPORT DigColPs_Per1	2014-10-14, 18:11:16+0530		razorrat
Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Ci	nt_T_enum) 109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_	Cnt_T_u08) 4	4	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_	Cnt_T_enum) 1	1	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.96 (Repeat Count = 1)	1		
Name	Input Value		
DigColPsInt_GetData()	12		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1700		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.252		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	563		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	176		
DigCoIPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1042		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	130.5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2300		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	189		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	55		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.152		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	652		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144		
k_SenseDetErrDiag_Cnt_str.Threshold	56		
k_SenseDetErrDiag_Cnt_str.PStep	26		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	620		
k_SenseParityErrDiag_Cnt_str.PStep	13		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	146		
	Actual Value	·	

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	932.130859	932.1308984 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	576	576	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186	186	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	✓
DigColPs_I2CHwColAngle_Deg_M_f32	212.130859	212.1308984 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	218.486572	218.4865625 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	12	12	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1057	1057	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	92.9003906	92.90039063 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	2300	2300	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	202.148438	202.1484375 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-141.513428	-141.5134375 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	620	620	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	144	144	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	12	12	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•

390

0

-1

1

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-1

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14

DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	437.289886	437.289875 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.98 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigCoIPs PrevI2CHwCoIAngle Cnt M u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Deg_wi_32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	138.148849	138.1488477 ± 0.00048828125	1,000
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
	-1	-1	
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc			
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	138.148849	138.1488477 ± 0.0001220703125	•
DigColPs_I2CHwSpurAngle_Deg_M_f32	77.2898865	77.289875 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs PrevI2CHwColAngle Cnt M u16	1687	1687	•
DIGOON S_1 TEVIZOTIWOOIATIGIE_OTIL_W_UTO		148.2714844 ± 0.0001220703125	
0 - 0	148.271484	110.21110112 0.0001220100120	
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484 3800	3800	•
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16			
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3800	3800	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3800 333.984375	3800 333.984375 ± 0.0001220703125	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32	3800 333.984375 3	3800 333.984375 ± 0.0001220703125 3	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc	3800 333.984375 3 437.289886	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16	3800 333.984375 3 437.289886 0	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityErrorAcc_Cnt_M_lgc	3800 333.984375 3 437.289886 0 740	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0	
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_lgc	3800 333.984375 3 437.289886 0 740 0	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1	
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc	3800 333.984375 3 437.289886 0 740 0 -1	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1	
DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigColPs_PrevI2CHwSpurAngle_Deg_M_f32 DigColPs_ReqI2CSnsrDataType_Cnt_M_u08 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	3800 333,984375 3 437,289886 0 740 0 -1 1 130	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1 1 130	
DigCoIPs_PrevI2CHwCoIAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_lgc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc	3800 333.984375 3 437.289886 0 740 0 -1	3800 333.984375 ± 0.0001220703125 3 437.289875 ± 0.00048828125 0 740 0 -1	



T v				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.99 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs ColParityErrorAcc Cnt M u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
	337		
DigColPs_I2CHwColAngle_Cnt_M_u16	3		
DigColPs_I2CHwDataType_Cnt_M_u08			
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	554.800049	554.8 ± 0.00048828125	
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	109	109	
DigColPs_ColParityError_Cnt_M_lgc	0	0	
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	
DigColPs I2CColSensorFault Cnt M Igc	1	1	
DigColPs_I2CHwColAngle_Deg_M_f32	194.800049	194.8 ± 0.0001220703125	
DigColPs_I2CHwSpurAngle_Deg_M_f32	214.080078	214.08 ± 0.0001220703125	
DigColPs I2CSensCommFlts Cnt M u08	0	0	
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	
DigColPs_PrevI2CHwColAngle_Deg_M_f32		0 ± 0.0001220703125	
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	214.080078	214.08 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	827	827	•
DigColPs SpurParityError Cnt M Igc	0	0	





Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.100 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1760		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.396		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	523		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	184		
DigColPs_I2CHwColAngle_Cnt_M_u16	344		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1522		
DigColPs_I2CSensCommFlts_Cnt_M_u08	24		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	270		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	245		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1780		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.536		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	-3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	104		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	25		
k_SenseParityErrDiag_Cnt_str.Threshold	830		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k_SenseParityErrDiag_Cnt_str.NStep	37		
k_StepDetect_Deg_f32	188		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	707.231689	707.2316797 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	523	523	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	159	159	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	•
DigColPs_I2CHwColAngle_Deg_M_f32	347.231689	347.2316797 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	80.2365723	80.2365625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2065	2065	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	181.494141	181.4941406 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530

DigColPs_Per1



Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	956	956	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	84.0234375	84.0234375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1359.76343	-1359.763438 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	830	830	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-3	-3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	161	161	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~	
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~	
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~	
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•	
DiagnosticThreshold	2	DiagnosticThreshold	2	•	
OddParityFault	2	OddParityFault	2	~	
DiagnosticThreshold	2	DiagnosticThreshold	2	~	
ComputeRoughTurns	2	ComputeRoughTurns	2	~	
ConstrainOneRev	2	ConstrainOneRev	2	•	
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•	
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•	
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.101 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	8		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.39		
DigColPs_ColLPFInitDone_Cnt_M_Igc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	145		
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	186		
DigColPs_I2CHwColAngle_Cnt_M_u16	337		
DigColPs_I2CHwDataType_Cnt_M_u08	3		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1502		
DigColPs_I2CSensCommFlts_Cnt_M_u08	23		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	260.3		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	244		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1616		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	863		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	184		
k_SenseDetErrDiag_Cnt_str.Threshold	102		
k_SenseDetErrDiag_Cnt_str.PStep	49		
k_SenseDetErrDiag_Cnt_str.NStep	24		
k_SenseParityErrDiag_Cnt_str.Threshold	820		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	36		
k_StepDetect_Deg_f32	186		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	483.743164	483.7430664 ± 0.00048828125	-
DigColPs ColLPFInitDone Cnt M Igc	0	0	-
DigColPs ColParityErrorAcc Cnt M u16	181	181	-
Di O ID O ID ii E O I M I			

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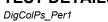
162

DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	123.743164	123.7430664 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	68.9726563	68.97257812 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	8	8	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2023	2023	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	177.802734	177.8027344 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	921	921	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	80.9472656	80.94726563 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	68.9726563	68.97257812 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	820	820	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	160	160	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8	8	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~





Test Step 2.102 (Repeat Count = 1)			
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	250		
DigColPs_ColRoughTurns_Cnt_M_s16	-3		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
0igColPs_I2CHwDataType_Cnt_M_u08	1		
0igColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
igColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
igColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
igColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
igColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
igColPs_SpurLPFInitDone_Cnt_M_lgc	0		
igColPs_SpurParityErrorAcc_Cnt_M_u16	512		
igColPs_SpurRoughTurns_Cnt_M_s16	0		
igColPs SpurSensorDiagFailed Cnt M lgc	0		
igColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
SenseDetErrDiag Cnt str.Threshold	122		
SenseDetErrDiag Cnt str.PStep	9		
SenseDetErrDiag Cnt str.NStep	34		
SenseParityErrDiag_Cnt_str.Threshold	920		
SenseParityErrDiag_Cnt_str.PStep	6		
senseParityErrDiag_Cnt_str.NStep	10		
stepDetect_Deg_f32	210.6		
lame	Actual Value	Expected Value	Res
igColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-719.912109	-719.9121094 ± 0.00048828125	1100
igCoIPs_ColLPFInitDone_Cnt_M_lgc	1	1	
	240	240	
igColPs_ColParityErrorAcc_Cnt_M_u16 igColPs_ColParityError_Cnt_M_lgc	0	0	
	-2	-2	
igColPs_ColRoughTurns_Cnt_M_s16	1	1	
igColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
igColPs_ColSensorFaultAcc_Cnt_M_u16			
igColPs_I2CColSensorFault_Cnt_M_lgc	1	1	
igColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
igColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
igColPs_I2CSensCommFlts_Cnt_M_u08	0	0	
igColPs_I2CSpurSensorFault_Cnt_M_lgc	0	0	
igColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	
igColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
igColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	
igColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	
igColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	
igColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	360.087891	360.0878906 ± 0.00048828125	
	1	1	
igColPs_SpurLPFInitDone_Cnt_M_lgc		502	
igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16	502		
igColPs_SpurLPFInitDone_Cnt_M_lgc igColPs_SpurParityErrorAcc_Cnt_M_u16 igColPs_SpurParityError_Cnt_M_lgc	0	0	
igCoIPs_SpurLPFInitDone_Cnt_M_Igc igCoIPs_SpurParityErrorAcc_Cnt_M_u16 igCoIPs_SpurParityError_Cnt_M_Igc igCoIPs_SpurRoughTurns_Cnt_M_s16	0	1	
oigColPs_SpurLPFInitDone_Cnt_M_Igc oigColPs_SpurParityErrorAcc_Cnt_M_u16 oigColPs_SpurParityError_Cnt_M_Igc oigColPs_SpurRoughTurns_Cnt_M_s16	0 1 0	1 0	
DigColPs_SpurAngleLPFRSV_Citt_M_str.Sv_Uis_132 DigColPs_SpurLPTInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	1	
olgColPs_SpurLPFInitDone_Cnt_M_Igc ligColPs_SpurParityErrorAcc_Cnt_M_u16 ligColPs_SpurParityError_Cnt_M_Igc ligColPs_SpurRoughTurns_Cnt_M_s16 ligColPs_SpurSensorDiagFailed_Cnt_M_Igc ligColPs_SpurSensorFaultAcc_Cnt_M_u16	0 1 0	1 0	
oigColPs_SpurLPFInitDone_Cnt_M_lgc oigColPs_SpurParityErrorAcc_Cnt_M_u16 oigColPs_SpurParityError_Cnt_M_lgc oigColPs_SpurRoughTurns_Cnt_M_s16 oigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0 1 0 0	0 0	

Test Step 2.103 (Repeat Count = 1)



T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 2.103 (Repeat Count = 1)			~
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	378		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.456		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	345		
DigColPs_ColRoughTurns_Cnt_M_s16	-2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2485		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125.7		
DigColPs PrevI2CHwSpurAngle Cnt M u16	1306		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	285.4		
DigColPs Regl2CSnsrDataType Cnt M u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1020		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.696		
DigColPs SpurLPFInitDone Cnt M lgc	1		
DigColPs SpurParityErrorAcc Cnt M u16	324		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k SenseDetErrDiag Cnt str.Threshold	124		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	930		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	321.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-122.647919	-122.6479219 ± 0.00048828125	Result
	1	1	-
DigColPs_ColLPFInitDone_Cnt_M_lgc	333	333	
DigColPs_ColParityErrorAcc_Cnt_M_u16			-
DigColPs_ColParityError_Cnt_M_lgc	0 -2	0	
DigColPs_ColRoughTurns_Cnt_M_s16		-2	-
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	· ·
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	Ž
DigColPs_I2CHwColAngle_Deg_M_f32	237.352081	237.3520781 ± 0.0001220703125	-
DigColPs_I2CHwSpurAngle_Deg_M_f32	200.701172	200.7011719 ± 0.0001220703125	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	V
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	V
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	560.701172	560.7011719 ± 0.00048828125	*
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColDo SpurBorityErrorAco Cot M u16	212	242	

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DigColPs_SpurParityErrorAcc_Cnt_M_u16

 ${\sf DigColPs_SpurParityError_Cnt_M_lgc}$

2014-10-14, 18:11:16+0530



Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 2.104 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	420		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.462		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	625		
DigColPs_ColRoughTurns_Cnt_M_s16	-1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	19		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2527		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	250.9		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1341		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	310.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1300		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.712		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	645		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	126		
k_SenseDetErrDiag_Cnt_str.PStep	11		
k_SenseDetErrDiag_Cnt_str.NStep	36		
k_SenseParityErrDiag_Cnt_str.Threshold	940		
k_SenseParityErrDiag_Cnt_str.PStep	8		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	105.8		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	226.000595	226.0006055 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	600	600	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	0	0	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	226.000595	226.0006055 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	2	2	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	620	620	~
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	462		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.468		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	412		
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	21		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2569		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	312.8		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1376		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	127.1		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1580		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.728		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	741		
DigColPs_SpurRoughTurns_Cnt_M_s16	3		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	128		
k_SenseDetErrDiag_Cnt_str.PStep	12		
k_SenseDetErrDiag_Cnt_str.NStep	37		
k_SenseParityErrDiag_Cnt_str.Threshold	950		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	30		
k_StepDetect_Deg_f32	120.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	360.087891	360.0878906 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	382	382	•
DI O ID O ID II E O I MI			

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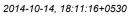
0

DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

 ${\tt DigColPs_ColSensorDiagFailed_Cnt_M_lgc}$

DigColPs_ColSensorFaultAcc_Cnt_M_u16





Name	Actual Value	Expected Value	Result
DigColPs_I2CColSensorFault_Cnt_M_Igc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	38.144043	38.14398437 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1478.14404	1478.143984 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	711	711	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	4	4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

T √						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	DisableI2CInterrupt	1	✓		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
EnableI2CInterrupt	1	EnableI2CInterrupt	1	~		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
OddParityFault	2	OddParityFault	2	~		
DiagnosticThreshold	2	DiagnosticThreshold	2	-		
ComputeRoughTurns	2	ComputeRoughTurns	2	~		
ConstrainOneRev	2	ConstrainOneRev	2	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		

DigColPs_Per1

2014-10-14, 18:11:16+0530



Test Case 3: Path Test

2014-10-14, 18:11:16+0530

DigColPs_Per1



Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 5049.00 Cycles
TS3.2 5174.00 Cycles
TS3.3 4828.00 Cycles
TS3.4 5030.00 Cycles
TS3.5 4671.00 Cycles
TS3.6 5045.00 Cycles
TS3.7 5082.00 Cycles
TS3.8 4914.00 Cycles
TS3.9 4722.00 Cycles
TS3.10 4791.00 Cycles
TS3.11 4742.00 Cycles
TS3.11 5002.00 Cycles



Description VECTOR DESCRIPTION:

```
TS3.1 "(I2CHwDataType_Cnt_T_u08 != D_ANGLEDATA_CNT_U08)=>TRUE (I2CSensCommFlts_Cnt_T_u08 != 0U)=>FALSE ((I2CHwColAngle_Cnt_T_u16 & 0x4000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x4000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE ((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) != 0U)=>FALSE ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE) || (ColParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE ((ColSensorEault_Cnt_T_lgc == TRUE))=>FALSE
   ((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)|| (ColParityOrCommErr_Cnt_T_lgc == TRUE)|=>FALSE ((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)|| (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>FALSE ((ColSensorFault_Cnt_T_lgc == TRUE)|| (SpurSensorFault_Cnt_T_lgc == TRUE)|| (SpurSensorFault_Cnt_T_u08 |= D_ANGLEDATA_CNT_U08)|| (SpurSensorFault_Cnt_T_u16 & 0x4000U) |= 0U)|| ((SCHwSpurAngle_Cnt_T_u16 & 0x4000U) |= 0U)|| ((SCHwSpur
((I2CHwSpurAngle_Cnt_T_u16 & 0x8000U) = 0U)F
((DigColPs_ColSensorDiagFailed_Cnt_M_lgc == TRUE)F || (ColParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((DigColPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((ColSensorFault_Cnt_T_lgc == TRUE)F || (SpurParityOrCommErr_Cnt_T_lgc == TRUE)T)=>TRUE
((ColParityGrorEvt_Cnt_T_lgc == TRUE)F || (ColParityGrorEvt_Cnt_T_lgc == TRUE)F || (SpurParityGrorEvt_Cnt_T_lgc == TRUE)F || (Sp
         ((DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoISensorFault_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurParityErrorEvt_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoIParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoIParityOrCommErr_Cnt_T_ugc == TRUE))=>TRUE ((I2CSensCommFlts_Cnt_T_uge == D_ANGLEDATA_CNT_Uge)F ((I2CHwDataType_Cnt_T_uge) == D_ANGLEDATA_CNT_Uge)F ((I2CHwDataType_Cnt_T_uge) == D_ANGLEDATA_CNT_Uge)F ((I2CHwCoIAngle_Cnt_T_ufe) & 0x4000U) != 0U)F ((I2CHwCoIAngle_Cnt_T_ufe) & 0x4000U) != 0U)F ((I2CHwCoIAngle_Cnt_T_ufe) & 0x8000U) != 0U)=>TRUE ((I2CHwSpurAngle_Cnt_T_ufe) & 0x8000U) != 0U)=>TRUE ((I2CHwSpurAngle_Cnt_T_ufe) & 0x8000U) != 0U)=>TRUE ((DigCoIPs_CoISensorDiagFailed_Cnt_M_lgc == TRUE) || (CoIParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((DigCoIPs_SpurSensorDiagFailed_Cnt_M_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE))=>TRUE ((CoISensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrComm
                      ( (ColSensorFault_Cnt_T_lgc == TRUE) ||
(SpurSensorFault_Cnt_T_lgc == TRUE) ||
```



```
(ColParityErrorEvt_Cnt_T_lgc == TRUE) ||
(SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurParityOrCommErr_Cnt_T_lgc == TRUE) || (SpurSensorFault_Cnt_T_lgc == TRUE) || (SpurSensorSampleOK_Cnt_T_lgc == TRUE
```

Test Step 3.1 (Repeat Count = 1)	Innut Value		
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_ColLPFInitDone_Cnt_M_Igc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	0		
DigColPs_ColRoughTurns_Cnt_M_s16	-5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0		
DigColPs_SpurRoughTurns_Cnt_M_s16	-11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	1		
k_SenseDetErrDiag_Cnt_str.PStep	0		
k_SenseDetErrDiag_Cnt_str.NStep	0		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	0		
k SenseParityErrDiag Cnt str.NStep	1		
k_StepDetect_Deg_f32	20		
Name	Actual Value	Expected Value	Result

k_StepDetect_Deg_132	20		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	0	0	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	-5	-5	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	0	0	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0	0 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	0	0	~

2014-10-14, 18:11:16+0530



DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0	0 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1800	-1800 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	0	0	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-11	-11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
DigColPsInt_GetData()	16		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000		
DigColPs_ColRoughTurns_Cnt_M_s16	5		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	255		
DigColPs_I2CHwColAngle_Cnt_M_u16	65535		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535		
DigColPs_I2CSensCommFlts_Cnt_M_u08	31		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	360		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	360		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1800		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	1		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000		
DigColPs_SpurRoughTurns_Cnt_M_s16	11		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	255		
k_SenseDetErrDiag_Cnt_str.Threshold	255		
k_SenseDetErrDiag_Cnt_str.PStep	50		
k_SenseDetErrDiag_Cnt_str.NStep	50		
k_SenseParityErrDiag_Cnt_str.Threshold	1000		
k_SenseParityErrDiag_Cnt_str.PStep	50		
k_SenseParityErrDiag_Cnt_str.NStep	50		
k_StepDetect_Deg_f32	340		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	2159.91211	2159.912109 ± 0.00048828125	•
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_ColParityErrorAcc_Cnt_M_u16	1000	1000	•
DigColPs_ColParityError_Cnt_M_lgc	0	0	
Discosine California Cat M add	_	-	

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DigColPs_ColRoughTurns_Cnt_M_s16

DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16

DigColPs_I2CColSensorFault_Cnt_M_Igc

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2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	16	16	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	4095	4095	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	359.912109	359.9121094 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	4	4	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	4319.91211	4319.912109 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1000	1000	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	11	11	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	205	205	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	16	16	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	-
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1) Name	Input Value		
	1		
DigColPsInt_GetData()			
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-500		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.12		
DigColPs_ColLPFInitDone_Cnt_M_lgc	100		
DigColPs_ColParityErrorAcc_Cnt_M_u16			
DigColPs_ColRoughTurns_Cnt_M_s16	-4		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1		
DigColPs_I2CHwColAngle_Cnt_M_u16	0		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	124		
DigColPs_I2CSensCommFlts_Cnt_M_u08	1		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	5		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	3		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1700		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.02		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	110		
DigColPs_SpurRoughTurns_Cnt_M_s16	-4		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	110		
k_SenseDetErrDiag_Cnt_str.Threshold	2		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	2		
k_SenseParityErrDiag_Cnt_str.Threshold	1		
k_SenseParityErrDiag_Cnt_str.PStep	1		
k_SenseParityErrDiag_Cnt_str.NStep	1		
k_StepDetect_Deg_f32	22		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	-612.747253	-612.7472656 ± 0.00048828125	~

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	1	1	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	107.252747	107.2527344 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	105.221069	105.2210938 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	1	1	•
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	•
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	5	5	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.439453125	0.439453125 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	12	12	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	1.0546875	1.0546875 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	•
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1694.77893	-1694.778906 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	•
DigColPs_SpurParityErrorAcc_Cnt_M_u16	1	1	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	•
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	•
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	108	108	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	•
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	•
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

Test Step 3.4 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetData()	5
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-100
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.28
DigColPs_ColLPFInitDone_Cnt_M_lgc	0
DigColPs_ColParityErrorAcc_Cnt_M_u16	110
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1
DigColPs_I2CHwColAngle_Cnt_M_u16	628
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	65535
DigColPs_I2CSensCommFlts_Cnt_M_u08	5
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35
DigColPs_PrevI2CHwColAngle_Deg_M_f32	45
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	15
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	0
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.26
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1
DigColPs_SpurParityErrorAcc_Cnt_M_u16	120
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	120
k_SenseDetErrDiag_Cnt_str.Threshold	10
k_SenseDetErrDiag_Cnt_str.PStep	25

DigColPs_SpurLPFInitDone_Cnt_M_lgc

DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16

DigColPs_SpurParityErrorAcc_Cnt_M_u16

DigColPs_SpurSensorDiagFailed_Cnt_M_lgc DigColPs_SpurSensorFaultAcc_Cnt_M_u16

 $Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$

Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

2014-10-14, 18:11:16+0530



DigColPs_Per1		Razo	orcat
Name	Input Value		
k_SenseDetErrDiag_Cnt_str.NStep	6		
k_SenseParityErrDiag_Cnt_str.Threshold	40		
k_SenseParityErrDiag_Cnt_str.PStep	9		
k_SenseParityErrDiag_Cnt_str.NStep	5		
k_StepDetect_Deg_f32	30		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-373.538666	-373.5386719 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_ColParityErrorAcc_Cnt_M_u16	40	40	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	•
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	•
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	346.461334	346.4613281 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	104.148438	104.1484375 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	5	5	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	35	35	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	3.07617188	3.076171875 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	24	24	•
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	2.109375	2.109375 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-1335.85156	-1335.851563 ± 0.00048828125	~

40

-4

10

109

5

40

-4

10

109

5

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
DisableI2CInterrupt	1	DisableI2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte Call DigColPs Per1 CP1 CheckpointReached	1	Rte Call DigColPs Per1 CP1 CheckpointReached	1	~

Test Step 3.5 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetData()	4
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.92
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	120
DigColPs_ColRoughTurns_Cnt_M_s16	-4
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1
DigColPs_I2CHwColAngle_Cnt_M_u16	1492
DigColPs_I2CHwDataType_Cnt_M_u08	1
DigColPs_I2CHwSpurAngle_Cnt_M_u16	202
DigColPs_I2CSensCommFlts_Cnt_M_u08	18
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195
DigColPs_PrevI2CHwColAngle_Deg_M_f32	205
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	63
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	300
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.165

DigColPs_Per1

2014-10-14, 18:11:16+0530



Name	Input Value
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0
DigColPs_SpurParityErrorAcc_Cnt_M_u16	130
DigColPs_SpurRoughTurns_Cnt_M_s16	-4
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130
k_SenseDetErrDiag_Cnt_str.Threshold	40
k_SenseDetErrDiag_Cnt_str.PStep	3
k_SenseDetErrDiag_Cnt_str.NStep	0
k_SenseParityErrDiag_Cnt_str.Threshold	200
k_SenseParityErrDiag_Cnt_str.PStep	41
k_SenseParityErrDiag_Cnt_str.NStep	21
k_StepDetect_Deg_f32	62

K_Conser unityEnblug_ont_strivetep			
k_StepDetect_Deg_f32	62		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-857.83252	-857.8324219 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	161	161	~
DigColPs_ColParityError_Cnt_M_lgc	1	1	~
DigColPs_ColRoughTurns_Cnt_M_s16	-3	-3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_ColSensorFaultAcc_Cnt_M_u16	1	1	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	0	0	~
DigColPs_I2CHwColAngle_Deg_M_f32	222.16748	222.1675781 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	14.1761475	14.17617188 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	4	4	✓
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	195	195	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	17.1386719	17.13867188 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	88	88	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	7.734375	7.734375 ± 0.0001220703125	~
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	14.1761475	14.17617188 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	171	171	~
DigColPs_SpurParityError_Cnt_M_lgc	1	1	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-4	-4	~
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	5	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	DisableI2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enable12CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	-
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.6 (Repeat Count = 1)		✓.
Name	Input Value	
DigColPsInt_GetData()	9	
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600	
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52	
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	
DigColPs_ColParityErrorAcc_Cnt_M_u16	300	
DigColPs_ColRoughTurns_Cnt_M_s16	-2	
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	
DigColPs_ColSensorFaultAcc_Cnt_M_u16	3	
DigColPs_I2CHwColAngle_Cnt_M_u16	32767	
DigColPs_I2CHwDataType_Cnt_M_u08	1	
DigColPs_I2CHwSpurAngle_Cnt_M_u16	32767	
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	

2014-10-14, 18:11:16+0530





Name	Input Value		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	132		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	296		
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.658		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	220		
DigColPs_SpurRoughTurns_Cnt_M_s16	-2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	200		
k_SenseDetErrDiag_Cnt_str.Threshold	150		
k_SenseDetErrDiag_Cnt_str.PStep	30		
k_SenseDetErrDiag_Cnt_str.NStep	20		
k_SenseParityErrDiag_Cnt_str.Threshold	28		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	39		
k_StepDetect_Deg_f32	169		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-630.636353	-630.6363281 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	28	28	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-2	-2	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	33	33	✓
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	89.3636475	89.36367188 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	320.592896	320.592832 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	9	9	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	695	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	61.0839844	61.08398438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	769	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	67.5878906	67.58789063 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320.592896	320.592832 ± 0.00048828125	•
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	28	28	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	150	150	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	9	9	•
Rte Call Sa DigColPs NxtrDiagMgr SetNTCStatus(Status Cnt T enum)		1	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	Enablel2CInterrupt	1	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.7 (Repeat Count = 1)	✓
Name	Input Value
DigColPsInt_GetData()	0
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52
DigColPs_ColLPFInitDone_Cnt_M_lgc	1
DigColPs_ColParityErrorAcc_Cnt_M_u16	310
DigColPs_ColRoughTurns_Cnt_M_s16	-1

k_SenseParityErrDiag_Cnt_str.NStep

2014-10-14, 18:11:16+0530



DigColPs_Per1 Input Value DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 3 DigColPs_I2CHwColAngle_Cnt_M_u16 32768 DigColPs_I2CHwDataType_Cnt_M_u08 DigColPs_I2CHwSpurAngle_Cnt_M_u16 32768 DigColPs_I2CSensCommFlts_Cnt_M_u08 0 DigColPs_PrevI2CHwColAngle_Cnt_M_u16 695 132 DigColPs_PrevI2CHwColAngle_Deg_M_f32 DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16 769 296 ${\tt DigColPs_PrevI2CHwSpurAngle_Deg_M_f32}$ DigColPs_Reql2CSnsrDataType_Cnt_M_u08 3 1500 ${\tt DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32}$ DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32 0.658 DigColPs_SpurLPFInitDone_Cnt_M_lgc 0 DigColPs_SpurParityErrorAcc_Cnt_M_u16 230 DigColPs_SpurRoughTurns_Cnt_M_s16 -1 ${\tt DigColPs_SpurSensorDiagFailed_Cnt_M_lgc}$ 0 DigColPs_SpurSensorFaultAcc_Cnt_M_u16 210 $k_SenseDetErrDiag_Cnt_str.Threshold$ 150 k_SenseDetErrDiag_Cnt_str.PStep 30 $k_SenseDetErrDiag_Cnt_str.NStep$ 20 k_SenseParityErrDiag_Cnt_str.Threshold 28 $k_SenseParityErrDiag_Cnt_str.PStep$ 36

39

k_deliser antyEndlag_dit_str.Notep	33		
k_StepDetect_Deg_f32	169		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-443.43634	-443.4363281 ± 0.00048828125	✓
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	28	28	✓
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	-1	-1	✓
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	✓
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	276.56366	276.5636719 ± 0.0001220703125	✓
DigColPs_I2CHwSpurAngle_Deg_M_f32	197.472839	197.472832 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	✓
DigColPs_I2CSpurSensorFault_Cnt_M_lgc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695	695	✓
DigColPs_PrevI2CHwColAngle_Deg_M_f32	61.0839844	61.08398438 ± 0.0001220703125	✓
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	769	769	✓
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	67.5878906	67.58789063 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	✓
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	557.472839	557.472832 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	✓
DigColPs_SpurParityErrorAcc_Cnt_M_u16	28	28	~
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	0	0	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	✓
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	190	190	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	65	65	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	✓





Test Step 3.8 (Repeat Count = 1) Name	Input Value		
DigColPsInt_GetData()	0		
DigColPsInt_GetData() DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-600		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.52		
DigCoIPS_COIATIGIELPFKSV_CITI_M_Str.K_OIS_132 DigCoIPS CoILPFInitDone Cnt M Igc	0.52		
	320		
DigColPs_ColParityErrorAcc_Cnt_M_u16			
DigColPs_ColRoughTurns_Cnt_M_s16	0		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	3		
DigColPs_I2CHwColAngle_Cnt_M_u16	256		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	255		
DigCoIPs_I2CSensCommFlts_Cnt_M_u08	0		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	695		
DigColPs_PrevI2CHwColAngle_On_M_f32	132		
DigCoIPs_PrevI2CHwSpurAngle_Deg_M_132 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16	769		
DigColPs PrevI2CHwSpurAngle Deg M f32	296		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_Reqi2C3fisiDataType_Cfit_M_u00 DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_UIs_f32	1500		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.658		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	240		
DigColPs SpurRoughTurns Cnt M s16	0		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	220		
k_SenseDetErrDiag_Cnt_str.Threshold	150		
k SenseDetErrDiag Cnt str.PStep	30		
k SenseDetErrDiag Cnt str.NStep	20		
k_SenseParityErrDiag_Ont_str.Threshold	28		
k_SenseParityErrDiag_Cnt_str.PStep	36		
k_SenseParityErrDiag_Cnt_str.NStep	39		
k_StepDetect_Deg_f32	169		
Name	Actual Value	Expected Value	Resul
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-256.236328	-256.2363281 ± 0.00048828125	Resul
DigColPs_ColLPFInitDone_Cnt_M_lgc	0	0	
	U	U	
	281	201	
	281	281	
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16	0	0	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0 0 1	0 0 1	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16	0 0 1 0	0 0 1 0	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc	0 0 1 0 1	0 0 1 0 1	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32	0 0 1 0 1 103.763672	0 0 1 0 1 103.7636719 ± 0.0001220703125	
DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32	0 0 1 0 1 103.763672 74.3528442	0 0 1 0 1 103.7636719 ± 0.0001220703125 74.35283203 ± 0.0001220703125	
DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwColAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSensCommFits_Cnt_M_u08	0 0 1 0 1 103.763672 74.3528442 0	0 0 1 0 1 103.7636719 ± 0.0001220703125 74.35283203 ± 0.0001220703125 0	
DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CCoISensorFault_Cnt_M_Igc DigCoIPs_12CHwCoIAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_Igc	0 0 1 0 1 103.763672 74.3528442 0	0 0 1 0 1 1 103.7636719 ± 0.0001220703125 74.35283203 ± 0.0001220703125 0	
DigCoIPs_CoIParityError_Cnt_M_Igc DigCoIPs_CoIRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CCoISensorFault_Cnt_M_Igc DigCoIPs_I2CCHwCoIAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwCoIAngle_Cnt_M_u16	0 0 1 0 1 103.763672 74.3528442 0 1 695	0 0 1 0 1 1 103.7636719 ± 0.0001220703125 74.35283203 ± 0.0001220703125 0 1 695	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844	0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769	0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906	0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigCoIPs_ColParityError_Cnt_M_igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_lgc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_12CSpurSensorFault_Cnt_M_lgc DigCoIPs_Prev12CHwColAngle_Cnt_M_u16 DigCoIPs_Prev12CHwColAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Cnt_M_u16 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Req12CSnsrDataType_Cnt_M_u08	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3	0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_DFKSV_Cnt_M_u5tr.SV_Uls_f32	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844	0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigCoIPs_ColParityError_Cnt_M_igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_igc DigCoIPs_I2CColSensorFault_Cnt_M_igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_igc	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844	0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
DigCoIPs_CoIParityError_Cnt_M_igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_lgc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_igc DigCoIPs_I2CColSensorFault_Cnt_M_igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_ReqI2CSnsrDataType_Cnt_M_u08 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16	0 0 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844 1	$\begin{array}{c} 0 \\ 0 \\ 1 \\ 0 \\ 1 \\ 103.7636719 \pm 0.0001220703125 \\ 74.35283203 \pm 0.0001220703125 \\ 0 \\ 1 \\ 695 \\ 61.08398438 \pm 0.0001220703125 \\ 769 \\ 67.58789063 \pm 0.0001220703125 \\ 3 \\ 794.352832 \pm 0.00048828125 \\ 1 \\ 28 \\ \end{array}$	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFlts_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHSSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_u08 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityErrorAcc_Cnt_M_lgc	0 0 1 1 0 1 103.763672 74.3528442 0 1 1 695 61.0839844 769 67.5878906 3 794.352844 1	0 0 1 1 0 0 1 1 1 0 0 1 1 1 0 1 1 1 1 1	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_Igc DigCoIPs_SpurRoughTurns_Cnt_M_Igc	0 0 1 0 1 1 0 1 103.763672 74.3528442 0 1 1 695 61.0839844 769 67.5878906 3 794.352844 1 1	0 0 1 1 0 1 103.7636719 ± 0.0001220703125 74.35283203 ± 0.0001220703125 0 1 695 61.08398438 ± 0.0001220703125 769 67.58789063 ± 0.0001220703125 3 794.352832 ± 0.00048828125 1 28 1	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_lgc DigCoIPs_SpurRoughTurns_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc	0 0 1 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844 1 28	0 0 1 1 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1	
DigColPs_ColParityErrorAcc_Cnt_M_u16 DigColPs_ColParityError_Cnt_M_lgc DigColPs_ColRoughTurns_Cnt_M_s16 DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_ColSensorFaultAcc_Cnt_M_u16 DigColPs_12CColSensorFault_Cnt_M_lgc DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CHwSpurAngle_Deg_M_f32 DigColPs_12CSpurSensorFault_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_u08 DigColPs_12CSpurSensorFault_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Cnt_M_u16 DigColPs_Prev12CHwColAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_Prev12CHwSpurAngle_Deg_M_f32 DigColPs_SpurAngle_PFKSV_Cnt_M_u08 DigColPs_SpurAngle_PFKSV_Cnt_M_str.SV_UIs_f32 DigColPs_SpurLPFInitDone_Cnt_M_lgc DigColPs_SpurParityErrorAcc_Cnt_M_u16 DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurParityError_Cnt_M_lgc DigColPs_SpurRoughTurns_Cnt_M_s16 DigColPs_SpurSensorDagHailed_Cnt_M_lgc DigColPs_SpurSensorFaultAtc_Cnt_M_u16 DigColPs_SpurSensorFaultAtc_Cnt_M_u16 DigColPs_SpurSensorFaultAtc_Cnt_M_u16 DigColPs_SpurSensorFaultAtc_Cnt_M_u16 DigColPs_SpurSensorFaultAtc_Cnt_M_u16 DigColPs_SpurSensorFaultAtc_Cnt_M_u16 DigColPs_SpurSensorFaultAtc_Cnt_M_u106	0 0 1 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844 1 28 1	0 0 1 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 0 1 0 1 0 1 1 0 1 0 1 1 0 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1 1 0 0 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 1 1 0 0 1 1 1 1 1 0 0 1 1 1 1 1 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 0 0 1	
DigCoIPs_ColParityError_Cnt_M_Igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_Igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CColSensorFault_Cnt_M_Igc DigCoIPs_I2CCHwColAngle_Deg_M_f32 DigCoIPs_I2CHwSpurAngle_Deg_M_f32 DigCoIPs_I2CSensCommFits_Cnt_M_u08 DigCoIPs_I2CSpurSensorFault_Cnt_M_Igc DigCoIPs_PrevI2CHwColAngle_Cnt_M_u16 DigCoIPs_PrevI2CHwColAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_PrevI2CHwSpurAngle_Deg_M_f32 DigCoIPs_SpurAngle_PFKSV_Cnt_M_str.Sv_Uls_f32 DigCoIPs_SpurLPFInitDone_Cnt_M_Igc DigCoIPs_SpurParityError_Cnt_M_Igc DigCoIPs_SpurParityError_Cnt_M_s16 DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_Igc DigCoIPs_SpurSensorFaultAcc_Cnt_M_u16 Rte_Call_Sa_DigCoIPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 1 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844 1 28 1	0 0 1 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 0 1 0 1 0 1 1 0 1 0 1 1 0 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1	
DigCoIPs_ColParityError_Cnt_M_igc DigCoIPs_ColRoughTurns_Cnt_M_s16 DigCoIPs_ColSensorDiagFailed_Cnt_M_igc DigCoIPs_ColSensorFaultAcc_Cnt_M_u16 DigCoIPs_12CColSensorFault_Cnt_M_igc DigCoIPs_12CHwColAngle_Deg_M_f32 DigCoIPs_12CHwSpurAngle_Deg_M_f32 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSensCommFits_Cnt_M_u08 DigCoIPs_12CSpurSensorFault_Cnt_M_igc DigCoIPs_Prev12CHwColAngle_Cnt_M_u16 DigCoIPs_Prev12CHwColAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_Prev12CHwSpurAngle_Deg_M_f32 DigCoIPs_SepurAngleLPFKSV_Cnt_M_str.SV_Uis_f32 DigCoIPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uis_f32 DigCoIPs_SpurParityErrorAcc_Cnt_M_u16 DigCoIPs_SpurParityError_Cnt_M_igc DigCoIPs_SpurSensorDiagFailed_Cnt_M_igc DigCoIPs_SpurSensorFaultAcc_Cnt_M_igc	0 0 1 1 0 1 103.763672 74.3528442 0 1 695 61.0839844 769 67.5878906 3 794.352844 1 28 1	0 0 1 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 0 1 0 1 0 1 1 0 1 0 1 1 0 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1 1 0 1 1 0 0 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 1 1 0 0 1 1 1 1 1 0 0 1 1 1 1 1 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 0 0 1	



Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	✓
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enable/2CInterrupt	1	Enablel2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	✓
ConstrainOneRev	2	ConstrainOneRev	2	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.9 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	7		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	168		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.426		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	256		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	379		
DigColPs_I2CHwDataType_Cnt_M_u08	4		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1622		
DigColPs_I2CSensCommFlts_Cnt_M_u08	29		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2275		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	320		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1131		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	250		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	-380		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.616		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	253		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	186		
k_SenseDetErrDiag_Cnt_str.Threshold	114		
k_SenseDetErrDiag_Cnt_str.PStep	5		
k_SenseDetErrDiag_Cnt_str.NStep	30		
k_SenseParityErrDiag_Cnt_str.Threshold	880		
k_SenseParityErrDiag_Cnt_str.PStep	2		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	198		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	334.971191	334.9711992 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	258	258	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs ColSensorDiagFailed Cnt M Igc	1	1	✓
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs I2CColSensorFault Cnt M Igc	1	1	✓
DigColPs_I2CHwColAngle_Deg_M_f32	334.971191	334.9711992 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	137.073059	137.0730469 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	7	7	~
DigColPs I2CSpurSensorFault Cnt M Igc	1	1	✓
DigColPs PrevI2CHwColAngle Cnt M u16	2275	2275	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	199.951172	199.9511719 ± 0.0001220703125	✓
DigColPs PrevI2CHwSpurAngle Cnt M u16	1131	1131	~
DigColPs PrevI2CHwSpurAngle Deg M f32	99.4042969	99.40429688 ± 0.0001220703125	✓
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	137.073059	137.0730469 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	255	255	~
DigColPs SpurParityError Cnt M Igc	0	0	~

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DigColPs_Per1

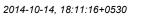
Name	Actual Value	Expected Value	Result
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	•
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	156	156	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	*none*	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	64	*none*	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

T	T					
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	•		
Enablel2CInterrupt	1	EnableI2CInterrupt	1	•		
DiagnosticThreshold	2	DiagnosticThreshold	2	•		
OddParityFault	2	OddParityFault	2	•		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
ComputeRoughTurns	2	ComputeRoughTurns	2	•		
ConstrainOneRev	2	ConstrainOneRev	2	•		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		

Test Step 3.10 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	336		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.45		
DigColPs_ColLPFInitDone_Cnt_M_lgc	0		
DigColPs_ColParityErrorAcc_Cnt_M_u16	250		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	12		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2443		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	222.6		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1271		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	240.6		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	740		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.68		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	512		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	122		
k_SenseDetErrDiag_Cnt_str.PStep	9		
k_SenseDetErrDiag_Cnt_str.NStep	34		
k_SenseParityErrDiag_Cnt_str.Threshold	920		
k_SenseParityErrDiag_Cnt_str.PStep	6		
k_SenseParityErrDiag_Cnt_str.NStep	10		
k_StepDetect_Deg_f32	210.6		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	240	240	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	✓
DigColPs_ColRoughTurns_Cnt_M_s16	3	3	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0	0	~
DigColPs_I2CColSensorFault_Cnt_M_lgc	1	1	•
DigColPs_I2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	0	0	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~

DigColPs_Per1





Name	Actual Value	Expected Value	Result
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	0	0	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1080.08789	1080.087891 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	502	502	✓
DigColPs_SpurParityError_Cnt_M_lgc	0	0	✓
DigColPs_SpurRoughTurns_Cnt_M_s16	3	3	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	0	0	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
Enablel2CInterrupt	1	EnableI2CInterrupt	1	~
DiagnosticThreshold	2	DiagnosticThreshold	2	-
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	~
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	~
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~

Test Step 3.11 (Repeat Count = 1)			✓
Name	Input Value		
DigColPsInt_GetData()	0		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	378		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.456		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	345		
DigColPs_ColRoughTurns_Cnt_M_s16	2		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	0		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	0		
DigColPs_I2CHwColAngle_Cnt_M_u16	1		
DigColPs_I2CHwDataType_Cnt_M_u08	1		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	2485		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	125.7		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1306		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	285.4		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	1		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	1020		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.696		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	324		
DigColPs_SpurRoughTurns_Cnt_M_s16	2		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0		
k_SenseDetErrDiag_Cnt_str.Threshold	124		
k_SenseDetErrDiag_Cnt_str.PStep	10		
k_SenseDetErrDiag_Cnt_str.NStep	35		
k_SenseParityErrDiag_Cnt_str.Threshold	930		
k_SenseParityErrDiag_Cnt_str.PStep	7		
k_SenseParityErrDiag_Cnt_str.NStep	12		
k_StepDetect_Deg_f32	321.4		
Name	Actual Value	Expected Value	Result
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	533.992065	533.9920781 ± 0.00048828125	~
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	✓
DigColPs_ColParityErrorAcc_Cnt_M_u16	333	333	~

0

2

0

0

0

0

2

0

0

0

DigColPs_ColParityError_Cnt_M_lgc

DigColPs_ColRoughTurns_Cnt_M_s16

DigColPs_ColSensorDiagFailed_Cnt_M_lgc DigColPs_ColSensorFaultAcc_Cnt_M_u16

DigColPs_I2CColSensorFault_Cnt_M_Igc

2014-10-14, 18:11:16+0530





Name	Actual Value	Expected Value	Result
DigColPs_I2CHwColAngle_Deg_M_f32	173.992065	173.9920781 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	91.2611694	91.26117188 ± 0.0001220703125	~
DigColPs_I2CSensCommFlts_Cnt_M_u08	0	0	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	✓
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1	1	•
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	•
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	1	1	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	0.087890625	0.087890625 ± 0.0001220703125	✓
DigColPs_ReqI2CSnsrDataType_Cnt_M_u08	1	1	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	811.261169	811.2611719 ± 0.00048828125	✓
DigColPs_SpurLPFInitDone_Cnt_M_lgc	1	1	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	312	312	•
DigColPs_SpurParityError_Cnt_M_lgc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	2	2	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T	<i>Τ</i> <i>Ψ</i>					
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~		
Disablel2CInterrupt	1	Disablel2CInterrupt	1	~		
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~		
Enablel2CInterrupt	1	Enablel2CInterrupt	1	~		
DiagnosticThreshold	2	DiagnosticThreshold	2	~		
OddParityFault	2	OddParityFault	2	✓		
DiagnosticThreshold	2	DiagnosticThreshold	2	-		
ComputeRoughTurns	2	ComputeRoughTurns	2	~		
ConstrainOneRev	2	ConstrainOneRev	2	~		
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	✓		
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	~		
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	~		

Test Step 3.12 (Repeat Count = 1) Name	Input Value		
	· ·		
DigColPsInt_GetData()	10		
DigColPs_ColAngleLPFKSV_Cnt_M_str.SV_Uls_f32	320		
DigColPs_ColAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.342		
DigColPs_ColLPFInitDone_Cnt_M_lgc	1		
DigColPs_ColParityErrorAcc_Cnt_M_u16	362		
DigColPs_ColRoughTurns_Cnt_M_s16	1		
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_ColSensorFaultAcc_Cnt_M_u16	30		
DigColPs_I2CHwColAngle_Cnt_M_u16	281		
DigColPs_I2CHwDataType_Cnt_M_u08	0		
DigColPs_I2CHwSpurAngle_Cnt_M_u16	1342		
DigColPs_I2CSensCommFlts_Cnt_M_u08	15		
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687		
DigColPs_PrevI2CHwColAngle_Deg_M_f32	0		
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800		
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	234		
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	736		
DigColPs_SpurAngleLPFKSV_Cnt_M_str.K_Uls_f32	0.392		
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0		
DigColPs_SpurParityErrorAcc_Cnt_M_u16	865		
DigColPs_SpurRoughTurns_Cnt_M_s16	1		
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1		
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	146		
k_SenseDetErrDiag_Cnt_str.Threshold	86		
k_SenseDetErrDiag_Cnt_str.PStep	41		
k_SenseDetErrDiag_Cnt_str.NStep	16		
k_SenseParityErrDiag_Cnt_str.Threshold	740		
k_SenseParityErrDiag_Cnt_str.PStep	28		
k_SenseParityErrDiag_Cnt_str.NStep	25		
k_StepDetect_Deg_f32	170.7		
Name	Actual Value	Expected Value	Result
DigColPs ColAngleLPFKSV Cnt M str.SV Uls f32	384.388855	384.3888477 ± 0.00048828125	

2014-10-14, 18:11:16+0530



DigColPs_Per1

Name	Actual Value	Expected Value	Result
DigColPs_ColLPFInitDone_Cnt_M_lgc	1	1	
DigColPs_ColParityErrorAcc_Cnt_M_u16	390	390	~
DigColPs_ColParityError_Cnt_M_lgc	0	0	•
DigColPs_ColRoughTurns_Cnt_M_s16	1	1	~
DigColPs_ColSensorDiagFailed_Cnt_M_lgc	1	1	•
DigColPs_ColSensorFaultAcc_Cnt_M_u16	14	14	~
DigColPs_I2CColSensorFault_Cnt_M_Igc	1	1	~
DigColPs_I2CHwColAngle_Deg_M_f32	24.388855	24.38884766 ± 0.0001220703125	~
DigColPs_I2CHwSpurAngle_Deg_M_f32	359.529846	359.529875 ± 0.0001220703125	✓
DigColPs_I2CSensCommFlts_Cnt_M_u08	10	10	~
DigColPs_I2CSpurSensorFault_Cnt_M_Igc	1	1	~
DigColPs_PrevI2CHwColAngle_Cnt_M_u16	1687	1687	~
DigColPs_PrevI2CHwColAngle_Deg_M_f32	148.271484	148.2714844 ± 0.0001220703125	~
DigColPs_PrevI2CHwSpurAngle_Cnt_M_u16	3800	3800	~
DigColPs_PrevI2CHwSpurAngle_Deg_M_f32	333.984375	333.984375 ± 0.0001220703125	~
DigColPs_Reql2CSnsrDataType_Cnt_M_u08	3	3	~
DigColPs_SpurAngleLPFKSV_Cnt_M_str.SV_Uls_f32	719.529846	719.529875 ± 0.00048828125	~
DigColPs_SpurLPFInitDone_Cnt_M_lgc	0	0	~
DigColPs_SpurParityErrorAcc_Cnt_M_u16	740	740	~
DigColPs_SpurParityError_Cnt_M_Igc	0	0	~
DigColPs_SpurRoughTurns_Cnt_M_s16	1	1	✓
DigColPs_SpurSensorDiagFailed_Cnt_M_lgc	1	1	~
DigColPs_SpurSensorFaultAcc_Cnt_M_u16	130	130	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	109	109	~
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	10	10	✓
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP0_CheckpointReached	1	~
Disablel2CInterrupt	1	Disablel2CInterrupt	1	•
DigColPsInt_GetData	1	DigColPsInt_GetData	1	~
EnableI2CInterrupt	1	Enablel2CInterrupt	1	✓
DiagnosticThreshold	2	DiagnosticThreshold	2	~
OddParityFault	2	OddParityFault	2	•
DiagnosticThreshold	2	DiagnosticThreshold	2	~
ComputeRoughTurns	2	ComputeRoughTurns	2	•
ConstrainOneRev	2	ConstrainOneRev	2	•
Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_DigColPs_NxtrDiagMgr_SetNTCStatus	1	•
DigColPsInt_StartRequest	1	DigColPsInt_StartRequest	1	•
Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	Rte_Call_DigColPs_Per1_CP1_CheckpointReached	1	•

2014-10-14, 18:24:01+0530



VernierLookup

Project	DigColPs
Module	DigColPs
Test Object	VernierLookup

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\DigColPs_C1XX
Configuration File	D:\Synergy_Work_Area\DigColPs_C1XX\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DigColPs\src\Sa_DigColPs.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-Dstatic= -Dconst= -D_DATA_ACCESS= -D_inline= -I\$(PROJECTROOT)\DigColPs\utp\contract -I\$(PROJECTROOT)\DigColPs\utp\contract\Sa_DigColPs -I\$(PROJECTROOT)\DigColPs\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

ame	Text
dodule 'DigColPs'	Name of Tester:Komal Sharma Code File(s) Under Test:Sa_DigColPs.c Code File(s) Version:8 Module Design Document:DigColPs_MDD.docx Module Design Document Version:9 Data Dictionary Version:9 Unit Test Plan Version:4 Optimization Level:Level 2 Compiller (CodeGen) Version:tms470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):3994 Total RAM Used (Bytes):108 Total CALS Used (Bytes):48 Special Test Requirements: Test Date:10-14-2014 Comments:"Note 1: Inline functions defined in GlobalMacro.h are not unit tested. Note 2: In the functions DigColPs_Init1() and DigColPs_SCom_CustSetTrim() extra codehas been added for the macro 'Redundant_Format_1_m' to imitate the source code. Note 4: In ""DigColPs_Init1()"" function, extra temporary variables are added in VBA for the implementation of 'Redundant_Format_1_m' mac."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	

2014-10-14, 18:24:01+0530



Attributes		
Name	Value	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	\$(PROJECTROOT)\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

TS1.1 358.00 Cycles Longest Execution Path TS1.2 16.00 Cycles Shortest Execution Path

VECTOR DESCRIPTION: Description

TS1.1 "Longest Execution Path => If (Level_Deg_T_f32 <= T2_VernierLUT_Cnt_u16(D_VERNIERLEVEL_CNT_U08, 0))=>FALSE For Search_Cnt_T_u08 = 0 To (TableSize_Cnt_T_u08 - 2)=>TRUE

For Search_Cht_1_u08 = 0 To (TableSize_Cht_1_u08 - 2)=>TRUE

If (MatchFound_Cnt_T_lgc = False)=>TRUE

If (Level_Deg_T_f32 < T2_VernierLUT_Cnt_u16(D_VERNIERLEVEL_CNT_U08, (Search_Cnt_T_u08 + 1)))=>TRUE

If (Level_Deg_T_f32 < Middle_Cnt_T_f32)=>FALSE

If (MatchFound_Cnt_T_lgc = False)=>FALSE"

TS1.2 "Shortest Execution Path =>

If (Level_Deg_T_f32 <= T2_VernierLUT_Cnt_u16(D_VERNIERLEVEL_CNT_U08, 0)) =>TRUE"

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2
VernierLUT_Cnt_T_s16[36]	3
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[40]	8
VernierLUT_Cnt_T_s16[41]	9
VernierLUT_Cnt_T_s16[42]	0
VernierLUT_Cnt_T_s16[43] VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[44] VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[45] VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[46] VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4

2014-10-14, 18:24:01+0530



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Name	Input Value		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u	08	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	
tgt_SpurRevPtr_Cnt_T_u08	0	0	_
tgt_VernierLevelNo_Cnt_T_u08	1	1	
3 1 00.2010.110_OIN_ 1 _400	1 '	l i	· · · · · · · · · · · · · · · · · · ·

Test Step 4.2 (Percet Count = 4)	
Test Step 1.2 (Repeat Count = 1)	·
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3

2014-10-14, 18:24:01+0530





Name	Input Value		
VernierLUT_Cnt_T_s16[20]	2		
VernierLUT_Cnt_T_s16[21]	1		
VernierLUT_Cnt_T_s16[22]	0		
	4		
VernierLUT_Cnt_T_s16[23]			
VernierLUT_Cnt_T_s16[24]	3		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	1		
VernierLUT_Cnt_T_s16[27]	0		
VernierLUT_Cnt_T_s16[28]	4		
VernierLUT_Cnt_T_s16[29]	3		
VernierLUT_Cnt_T_s16[30]	2		
VernierLUT_Cnt_T_s16[31]	1		
VernierLUT_Cnt_T_s16[32]	0		
	4		
VernierLUT_Cnt_T_s16[33]			
VernierLUT_Cnt_T_s16[34]	0		
VernierLUT_Cnt_T_s16[35]	8		
VernierLUT_Cnt_T_s16[36]	6		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	2		
VernierLUT_Cnt_T_s16[39]	0		
VernierLUT_Cnt_T_s16[40]	9		
VernierLUT_Cnt_T_s16[41]	7		
VernierLUT_Cnt_T_s16[42]	5		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
	14		
VernierLUT_Cnt_T_s16[52]			
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
	13		
VernierLUT_Cnt_T_s16[63]			
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
	-33		
VernierLUT_Cnt_T_s16[72]			
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
	294		
VernierLUT_Cnt_T_s16[82]			
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_	008	
Name	Actual Value	Expected Value	Resul
		· · · · · · · · · · · · · · · · · · ·	Resul
tgt_ColRevPtr_Cnt_T_u08	0	0	
tgt_SpurRevPtr_Cnt_T_u08	0	0	`
tgt_VernierLevelNo_Cnt_T_u08	1	1	· · · · · · · · · · · · · · · · · · ·

 $tgt_VernierLevelNo_Cnt_T_u08$



Test Case 2: Boundary Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

16.00 Cycles 378.00 Cycles 203.00 Cycles 358.00 Cycles 16.00 Cycles 203.00 Cycles 358.00 Cycles 358.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9

Description VECTOR DESCRIPTION:

TS2.1Level_Deg_T_f32=Min
TS2.2Level_Deg_T_f32=Max
TS2.3Level_Deg_T_f32=Zero
TS2.4Level_Deg_T_f32=Pos
TS2.5Level_Deg_T_f32=Neg
TS2.6LookupTableXSize_Cnt_T_u08=Min
TS2.7LookupTableXSize_Cnt_T_u08=Max
TS2.8All Min
TS2.9All Max

Test Step 2.1 (Repeat Count = 1)	
lame	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
.evel_Deg_T_f32	-792
.ookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
/ernierLUT_Cnt_T_s16[0]	-163
/ernierLUT_Cnt_T_s16[1]	-131
/ernierLUT_Cnt_T_s16[2]	-99
/ernierLUT_Cnt_T_s16[3]	-66
/ernierLUT_Cnt_T_s16[4]	-33
/ernierLUT_Cnt_T_s16[5]	0
/ernierLUT_Cnt_T_s16[6]	32
/ernierLUT_Cnt_T_s16[7]	65
/ernierLUT_Cnt_T_s16[8]	98
/ernierLUT_Cnt_T_s16[9]	130
/ernierLUT_Cnt_T_s16[10]	163
/ernierLUT_Cnt_T_s16[11]	196
/ernierLUT_Cnt_T_s16[12]	229
/ernierLUT_Cnt_T_s16[13]	261
/ernierLUT_Cnt_T_s16[14]	294
/ernierLUT_Cnt_T_s16[15]	327
/ernierLUT_Cnt_T_s16[16]	359
/ernierLUT_Cnt_T_s16[17]	0
/ernierLUT_Cnt_T_s16[18]	4
/ernierLUT_Cnt_T_s16[19]	3
/ernierLUT_Cnt_T_s16[20]	2
/ernierLUT_Cnt_T_s16[21]	1
/ernierLUT_Cnt_T_s16[22]	0
/ernierLUT_Cnt_T_s16[23]	4
/ernierLUT_Cnt_T_s16[24]	3
/ernierLUT_Cnt_T_s16[25]	2
/ernierLUT_Cnt_T_s16[26]	1
/ernierLUT_Cnt_T_s16[27]	0
/ernierLUT_Cnt_T_s16[28]	4
/ernierLUT_Cnt_T_s16[29]	3
/ernierLUT_Cnt_T_s16[30]	2
/ernierLUT_Cnt_T_s16[31]	1
/ernierLUT_Cnt_T_s16[32]	0
/ernierLUT_Cnt_T_s16[33]	4
/ernierLUT_Cnt_T_s16[34]	0
/ernierLUT_Cnt_T_s16[35]	8
/ernierLUT_Cnt_T_s16[36]	6
/ernierLUT_Cnt_T_s16[36] /ernierLUT_Cnt_T_s16[37]	4
/ernierLUT_Cnt_T_s16[37] /ernierLUT_Cnt_T_s16[38]	2
	0
/ernierLUT_Cnt_T_s16[39]	9
/ernierLUT_Cnt_T_s16[40] /ernierLUT_Cnt_T_s16[41]	7

VernierLookup

2014-10-14, 18:24:01+0530





Name	Input Value		
VernierLUT_Cnt_T_s16[42]	5		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	- Locale
tgt_SpurRevPtr_Cnt_T_u08	0	0	-
	1.5	1.	

Test Step 2.2 (Repeat Count = 1)		✓
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	360	
LookupTableXSize_Cnt_T_u08	22	
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
VernierLUT_Cnt_T_s16[0]	-396	
VernierLUT_Cnt_T_s16[1]	-360	
VernierLUT_Cnt_T_s16[2]	-324	
VernierLUT_Cnt_T_s16[3]	-288	
VernierLUT_Cnt_T_s16[4]	-252	
VernierLUT_Cnt_T_s16[5]	-216	
VernierLUT_Cnt_T_s16[6]	-180	
VernierLUT_Cnt_T_s16[7]	-144	
VernierLUT_Cnt_T_s16[8]	-108	
VernierLUT_Cnt_T_s16[9]	-72	
VernierLUT_Cnt_T_s16[10]	-36	
VernierLUT_Cnt_T_s16[11]	0	
VernierLUT_Cnt_T_s16[12]	36	

2014-10-14, 18:24:01+0530



VernierLookup	MACILA
Name	Input Value
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2
VernierLUT_Cnt_T_s16[36]	3
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[42]	9
VernierLUT_Cnt_T_s16[43]	0
VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	5
VernierLUT_Cnt_T_s16[50]	6
VernierLUT_Cnt_T_s16[51]	7
VernierLUT_Cnt_T_s16[52]	8
VernierLUT_Cnt_T_s16[53]	9
VernierLUT_Cnt_T_s16[54]	10
VernierLUT_Cnt_T_s16[55]	0
VernierLUT_Cnt_T_s16[56]	1
VernierLUT_Cnt_T_s16[57]	2
VernierLUT_Cnt_T_s16[58]	3
/ernierLUT_Cnt_T_s16[59]	4
/ernierLUT_Cnt_T_s16[60]	5
VernierLUT_Cnt_T_s16[61]	6
VernierLUT_Cnt_T_s16[62]	7
VernierLUT_Cnt_T_s16[63]	8
VernierLUT_Cnt_T_s16[64]	9
/ernierLUT_Cnt_T_s16[65]	10
/ernierLUT_Cnt_T_s16[66]	22
/ernierLUT_Cnt_T_s16[67]	2
/ernierLUT_Cnt_T_s16[68]	4
/ernierLUT_Cnt_T_s16[69]	6
/ernierLUT_Cnt_T_s16[70]	8
/ernierLUT_Cnt_T_s16[71]	10
/ernierLUT_Cnt_T_s16[72]	12 14
/ernierLUT_Cnt_T_s16[73]	
/ernierLUT_Cnt_T_s16[74]	16 18
/ernierLUT_Cnt_T_s16[75]	20
/ernierLUT_Cnt_T_s16[76]	
/ernierLUT_Cnt_T_s16[77]	1
/ernierLUT_Cnt_T_s16[78]	3
/ernierLUT_Cnt_T_s16[79]	5
VernierLUT_Cnt_T_s16[80]	7
VernierLUT_Cnt_T_s16[81]	9
VernierLUT_Cnt_T_s16[82]	11
VernierLUT_Cnt_T_s16[83]	13 15
	1.16
VernierLUT_Cnt_T_s16[84] VernierLUT_Cnt_T_s16[85]	17

VernierLookup

tgt_VernierLevelNo_Cnt_T_u08

2014-10-14, 18:24:01+0530

21



Name Input Value VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name **Actual Value Expected Value** Result tgt_ColRevPtr_Cnt_T_u08 0 10 tgt_SpurRevPtr_Cnt_T_u08 10

21

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5

2014-10-14, 18:24:01+0530



Name	Input Value		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	~
tgt_VernierLevelNo_Cnt_T_u08	2	2	~

Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	245.2
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4

2014-10-14, 18:24:01+0530



tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7	Nama	Innut Value		
VernetLIF_CN_T_s1609				
VermitLUT_CRT_15(Q1) 7 7 7 7 7 7 7 7 7	• •			
Names Name				
VermietUT_CRT_15(0)				
Variety Vari				
VernetUT_CRT_1=1524 VernetUT_CRT_1=1525 VernetUT_CRT_1=1525				
Varietating Cert 1, 15(55) 2 2 2 2 2 2 2 2 2				
Varietaria Cont. 1, 154529				
VarietarUT_Cet_T_151937 4				
Varies LUT, Co.T. 1, 151981 5 5 7 7 7 7 7 7 7 7				
VarmeCLUT_CRT_151690 6				
VernicutUT Cnt.T. 185691 7				
NormicLUT_CRT_181647 0 0 0 0 0 0 0 0 0				
VernietUT_Cot_T_15(4) VernietUT_Cot_T_15(
Varies LUT, Cet. T. 181849 0 0 0 0 0 0 0 0 0				
Vernicul U. Cot. 1.516 4 0				
VernieLUT_Col_T_stigles 1				
Vernied LT, Cn. T. 18(86) 2				
VernierLUT_CR_1_516(4) S VernierLUT_CR_1_516(4) S VernierLUT_CR_1_516(4) S VernierLUT_CR_1_516(4) S VernierLUT_CR_1_516(4) S VernierLUT_CR_1_516(5) S VernierLUT_CR_1_516(6) S VernierLUT_CR_				
VernieLUT_Cnt_1-s1628				
VernierLUT_CRLT_s16(0) 5				
Varient LT Cet. T. s16[50] 6				
VernierLUT_CNLT_s16[51] 7 7 7 7 7 1 1 1 1 1				
VernierLUT_Cnt_T_s16[52] 8 9 9 9 9 9 9 9 9 9				
VernierLUT_CnLT_s16[53] 9 10	•			
VernierLUT_Cnt_1 s16[54] 10 10 10 10 10 10 10 1				
VernierLUT_Cnt_1st8[55] 0 VernierLUT_Cnt_1st8[55] 1 VernierLUT_Cnt_1st8[57] 2 VernierLUT_Cnt_1st8[58] 3 VernierLUT_Cnt_1st8[59] 4 VernierLUT_Cnt_1st8[59] 4 VernierLUT_Cnt_1st8[50] 5 VernierLUT_Cnt_1st8[50] 6 VernierLUT_Cnt_1st8[50] 7 VernierLUT_Cnt_1st8[50] 8 VernierLUT_Cnt_1st8[50] 9 VernierLUT_Cnt_1st8[50] 9 VernierLUT_Cnt_1st8[65] 10 VernierLUT_Cnt_1st8[67] 2 VernierLUT_Cnt_1st8[67] 2 VernierLUT_Cnt_1st8[67] 10 VernierLUT_Cnt_1st8[67] 10 VernierLUT_Cnt_1st8[71] 10 VernierLUT_Cnt_1st8[71] 10 VernierLUT_Cnt_1st8[73] 14 VernierLUT_Cnt_1st8[74] 18 VernierLUT_Cnt_1st8[75] 18 VernierLUT_Cnt_1st8[76] 18 VernierLUT_C				
VernierLUT_Cnt_1s16 56 1				
VernierLUT_Cnt_T.s16[57] 2 2 2 2 2 2 2 2 2				
VernierLUT_CntT_s16[58] 3 4 5 5 5 5 5 5 5 5 5				
VernierLUT_Cnt_T_s16[59]				
VernierLUT_CntT_s16[60] 5				
VernierLUT_Cnt_T_s16[81] 6 7 7 7 7 7 7 7 7 7				
VernierLUT_Cnt_T_s16[62]				
VernierLUT_Cnt_T_s16[63] 8				
VernierLUT_Cnt_T_s16[61] 9				
VernierLUT_Cnt_T_s16[65] 10 22 25 25 25 25 25 25 2				
VernierLUT_Cnt_T_s16[66] 22 VernierLUT_Cnt_T_s16[67] 2 2 VernierLUT_Cnt_T_s16[68] 4 4 4 4 4 4 4 4 4		10		
VernierLUT_Cnt_T_s16[67] 2		22		
VernierLUT_Cnt_T_s16[69] 6		2		
VernierLUT_Cnts16[70] 8	VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[71]		6		
VernierLUT_Cnt_T_s16[71]		8		
VernierLUT_Cnt_T_s16[72] 12 VernierLUT_Cnt_T_s16[73] 14 VernierLUT_Cnt_T_s16[74] 16 VernierLUT_Cnt_T_s16[75] 18 VernierLUT_Cnt_T_s16[76] 20 VernierLUT_Cnt_T_s16[77] 1 VernierLUT_Cnt_T_s16[78] 3 VernierLUT_Cnt_T_s16[79] 5 VernierLUT_Cnt_T_s16[80] 7 VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 Ig_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Res tg_ColRevPtr_Cnt_T_u08 7 7 7		10		
VernierLUT_Cnt_T_s16[73]		12		
VernierLUT_Cnt_r_s16[74] 16 VernierLUT_Cnt_r_s16[75] 18 VernierLUT_Cnt_r_s16[76] 20 VernierLUT_Cnt_r_s16[77] 1 VernierLUT_Cnt_r_s16[78] 3 VernierLUT_Cnt_r_s16[80] 7 VernierLUT_Cnt_r_s16[81] 9 VernierLUT_Cnt_r_s16[82] 11 VernierLUT_Cnt_r_s16[83] 13 VernierLUT_Cnt_r_s16[84] 15 VernierLUT_Cnt_r_s16[85] 17 VernierLUT_Cnt_r_s16[86] 19 VernierLUT_Cnt_s16[87] 21 VernierLuT_Cnt_s16[87] 21 VernierLuT_Cnt_s16[87] 21 VernierLevelNo_Cnt_r_u08 Actual Value Expected Value Reserved ty_Collect_r_u08 Name Actual Value 7 7 tg_SpurRevPtr_Cnt_r_u08 7 7 7				
VernierLUT_Cnt_s16[75] 18 VernierLUT_Cnt_s16[76] 20 VernierLUT_Cnt_s16[77] 1 VernierLUT_Cnt_s16[78] 3 VernierLUT_Cnt_s16[79] 5 VernierLUT_Cnt_s16[80] 7 VernierLUT_Cnt_s16[81] 9 VernierLUT_Cnt_s16[82] 11 VernierLUT_Cnt_s16[83] 13 VernierLUT_Cnt_s16[84] 15 VernierLUT_Cnt_s16[85] 17 VernierLUT_Cnt_s16[86] 19 VernierLUT_Cnt_s16[87] 21 VernierLevelNo_Cnt_su08 15 Name Actual Value Expected Value Reserved Value tgt_SpurRevPtr_Cnt_su08 7 7 7				
VernierLUT_Cnt_T_s16[76] 20 VernierLUT_Cnt_T_s16[77] 1 VernierLUT_Cnt_T_s16[78] 3 VernierLUT_Cnt_T_s16[79] 5 VernierLUT_Cnt_T_s16[80] 7 VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[87] 21 VernierLUT_Cnt_T_u08 Actual Value Expected Value Reserved ty_ColRevPtr_Cnt_T_u08 Name Actual Value T 7 tgt_SpurRevPtr_Cnt_T_u08 7 7 7				
VernierLUT_Cnt_T_s16[77] 1 VernierLUT_Cnt_T_s16[78] 3 VernierLUT_Cnt_T_s16[79] 5 VernierLUT_Cnt_T_s16[80] 7 VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[78] 3 VernierLUT_Cnt_T_s16[79] 5 VernierLUT_Cnt_T_s16[80] 7 VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[79] 5 VernierLUT_Cnt_T_s16[80] 7 VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[80] 7 VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[81] 9 VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLuT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[82] 11 VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7		9		
VernierLUT_Cnt_T_s16[83] 13 VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[84] 15 VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLuT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Reserved tgt_ColRevPtr_Cnt_T_u08 tgt_SpurRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[85] 17 VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Reserved tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[86] 19 VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restructor tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLUT_Cnt_T_s16[87] 21 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Restruction tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 Name Actual Value Expected Value Res tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
Name Actual Value Expected Value Res tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7				
tgt_ColRevPtr_Cnt_T_u08 7 7 tgt_SpurRevPtr_Cnt_T_u08 7 7 7 7 7			Expected Value	Result
tgt_SpurRevPtr_Cnt_T_u08 7			-	Kesuii
tot Vernieri evelino (:nt. L. III)8 15	tgt_VernierLevelNo_Cnt_T_u08	15	15	

Test Step 2.5 (Repeat Count = 1)		✓
Name	Input Value	
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
Level_Deg_T_f32	-456.8	
LookupTableXSize Cnt T u08	17	

2014-10-14, 18:24:01+0530





<u> </u>	
Name	Input Value
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
VernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	
VernierLUT_Cnt_T_s16[27]	0
	4
VernierLUT_Cnt_T_s16[28]	
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
VernierLUT_Cnt_T_s16[39]	0
VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[41]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[49] VernierLUT_Cnt_T_s16[50]	10
	1
VernierLUT_Cnt_T_s16[51]	
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11
VernierLUT_Cnt_T_s16[54]	8
VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2
VernierLUT_Cnt_T_s16[57]	15
VernierLUT_Cnt_T_s16[58]	12
VernierLUT_Cnt_T_s16[59]	9
VernierLUT_Cnt_T_s16[60]	6
VernierLUT_Cnt_T_s16[61]	3
VernierLUT_Cnt_T_s16[62]	16
VernierLUT_Cnt_T_s16[63]	13
VernierLUT_Cnt_T_s16[64]	10
VernierLUT_Cnt_T_s16[65]	7
VernierLUT_Cnt_T_s16[66]	4
VernierLUT_Cnt_T_s16[67]	17
VernierLUT_Cnt_T_s16[68]	-163
VernierLUT_Cnt_T_s16[69]	-131
VernierLUT_Cnt_T_s16[70]	-99
VernierLUT_Cnt_T_s16[71]	-66

2014-10-14, 18:24:01+0530



Name	Input Value		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	-
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	1	1	✓

Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
/ernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[1]	-99
VernierLUT_Cnt_T_s16[2]	-66
/ernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32
/ernierLUT_Cnt_T_s16[0]	65
VernierLUT_Cnt_T_s16[7]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[9]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[11]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
/ernierLUT_Cnt_T_s16[14]	327
/ernierLUT_Cnt_T_s16[16]	359
/ernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[17]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
/ernierLUT_Cnt_T_s16[20]	1
/ernierLUT_Cnt_T_s16[22]	0
/ernierLUT_Cnt_T_s16[23]	4
/ernierLUT_Cnt_T_s16[23]	3
/ernierLUT_Cnt_T_s16[24]	2
/ernierLUT_Cnt_T_s16[26]	1
/ernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
/ernierLUT_Cnt_T_s16[31]	1
/ernierLUT_Cnt_T_s16[31]	0
/ernierLUT_Cnt_T_s16[32]	4
/ernierLUT_Cnt_1_s16[33] /ernierLUT_Cnt_T_s16[34]	0
/ernierLUT_Cnt_T_s16[35]	8
/ernierLUT_Cnt_T_s16[36]	6
/ernierLUT_Cnt_T_s16[37]	4
	2
/ernierLUT_Cnt_T_s16[38]	0
VernierLUT_Cnt_T_s16[39]	9
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41] VernierLUT_Cnt_T_s16[42]	5

2014-10-14, 18:24:01+0530



vernier Lookup			210
Name	Input Value		
VernierLUT_Cnt_T_s16[43]	3		
VernierLUT_Cnt_T_s16[44]	1		
VernierLUT_Cnt_T_s16[45]	10		
VernierLUT_Cnt_T_s16[46]	8		
VernierLUT_Cnt_T_s16[47]	6		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	✓
tgt_SpurRevPtr_Cnt_T_u08	0	0	·
tgt_VernierLevelNo_Cnt_T_u08	2	2	_
9	I .		1

Test Step 2.7 (Repeat Count = 1)	✓
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-7
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72

2014-10-14, 18:24:01+0530





Name	Input Value
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
VernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	9
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33] VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2
VernierLUT_Cnt_T_s16[36]	3
VernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	5
VernierLUT_Cnt_T_s16[39]	6
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[41]	8
VernierLUT_Cnt_T_s16[42]	9
VernierLUT_Cnt_T_s16[43]	0
VernierLUT_Cnt_T_s16[44]	0
VernierLUT_Cnt_T_s16[45]	1
VernierLUT_Cnt_T_s16[46]	2
VernierLUT_Cnt_T_s16[47]	3
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	5
VernierLUT_Cnt_T_s16[50]	6
VernierLUT_Cnt_T_s16[51]	7
VernierLUT_Cnt_T_s16[52]	8
VernierLUT_Cnt_T_s16[53]	9
VernierLUT_Cnt_T_s16[54]	10
VernierLUT_Cnt_T_s16[55]	0
VernierLUT_Cnt_T_s16[56] VernierLUT_Cnt_T_s16[57]	1 2
VernierLUT_Cnt_T_s16[58]	3
VernierLUT_Cnt_T_s16[59]	4
VernierLUT_Cnt_T_s16[60]	5
VernierLUT_Cnt_T_s16[61]	6
VernierLUT_Cnt_T_s16[62]	7
VernierLUT_Cnt_T_s16[63]	8
VernierLUT_Cnt_T_s16[64]	9
VernierLUT_Cnt_T_s16[65]	10
VernierLUT_Cnt_T_s16[66]	22
VernierLUT_Cnt_T_s16[67]	2
VernierLUT_Cnt_T_s16[68]	4
VernierLUT_Cnt_T_s16[69]	6
VernierLUT_Cnt_T_s16[70]	8
VernierLUT_Cnt_T_s16[71]	10
VernierLUT_Cnt_T_s16[72]	12
VernierLUT_Cnt_T_s16[73]	14
VernierLUT_Cnt_T_s16[74]	16
VernierLUT_Cnt_T_s16[75]	18
VernierLUT_Cnt_T_s16[76]	20
VernierLUT_Cnt_T_s16[77]	
VernierLUT_Cnt_T_s16[78]	3
VernierLUT_Cnt_T_s16[79]	5
VernierLUT_Cnt_T_s16[80]	7
VernierLUT_Cnt_T_s16[81]	9
VernierLUT_Cnt_T_s16[82]	11 13
VernierLUT_Cnt_T_s16[83]	15
Vernier LIT Cnt T s16[8/1]	
VernierLUT_Cnt_T_s16[84]	
VernierLUT_Cnt_T_s16[84] VernierLUT_Cnt_T_s16[85] VernierLUT_Cnt_T_s16[86]	17 19

2014-10-14, 18:24:01+0530



Name	Input Value		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08	S	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	✓
tgt_VernierLevelNo_Cnt_T_u08	1	1	✓

est Step 2.8 (Repeat Count = 1)		
lame	Input Value	
colRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08	
evel_Deg_T_f32	-792	
ookupTableXSize_Cnt_T_u08	17	
purRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08	
/ernierLUT_Cnt_T_s16[0]	-163	
/ernierLUT_Cnt_T_s16[1]	-131	
/ernierLUT_Cnt_T_s16[2]	-99	
/ernierLUT_Cnt_T_s16[3]	-66	
/ernierLUT_Cnt_T_s16[4]	-33	
/ernierLUT_Cnt_T_s16[5]	0	
ernierLUT_Cnt_T_s16[6]	32	
ernierLUT_Cnt_T_s16[7]	65	
ernierLUT_Cnt_T_s16[8]	98	
ernierLUT_Cnt_T_s16[9]	130	
ernierLUT_Cnt_T_s16[10]	163	
ernierLUT_Cnt_T_s16[11]	196	
ernierLUT_Cnt_T_s16[12]	229	
ernierLUT_Cnt_T_s16[13]	261	
ernierLUT_Cnt_T_s16[14]	294	
ernierLUT_Cnt_T_s16[15]	327	
ernierLUT_Cnt_T_s16[16]	359	
ernierLUT_Cnt_T_s16[17]	0	
ernierLUT_Cnt_T_s16[18]	4	
ernierLUT_Cnt_T_s16[19]	3	
ernierLUT_Cnt_T_s16[20]	2	
ernierLUT_Cnt_T_s16[21]	1	
ernierLUT_Cnt_T_s16[22]	0	
ernierLUT_Cnt_T_s16[23]	4	
ernierLUT_Cnt_T_s16[24]	3	
ernierLUT_Cnt_T_s16[25]	2	
ernierLUT_Cnt_T_s16[26]	1	
ernierLUT_Cnt_T_s16[27]	0	
ernierLUT_Cnt_T_s16[28]	4	
ernierLUT_Cnt_T_s16[29]	3	
ernierLUT_Cnt_T_s16[30]	2	
ernierLUT_Cnt_T_s16[31]	1	
ernierLUT_Cnt_T_s16[32]	0	
ernierLUT_Cnt_T_s16[33]	4	
ernierLUT_Cnt_T_s16[34]	0	
ernierLUT_Cnt_T_s16[35]	8	
ernierLUT_Cnt_T_s16[36]	6	
ernierLUT_Cnt_T_s16[37]	4	
ernierLUT_Cnt_T_s16[38]	2	
	0	
ernierLUT_Cnt_T_s16[39] ernierLUT_Cnt_T_s16[40]	9	
	7	
ernierLUT_Cnt_T_s16[41]	5	
ernierLUT_Cnt_T_s16[42]		
ernierLUT_Cnt_T_s16[43]	3	
ernierLUT_Cnt_T_s16[44]	1	
ernierLUT_Cnt_T_s16[45]	10	
ernierLUT_Cnt_T_s16[46]	8	
ernierLUT_Cnt_T_s16[47]	6	
ernierLUT_Cnt_T_s16[48]	4	
ernierLUT_Cnt_T_s16[49]	2	
ernierLUT_Cnt_T_s16[50]	10	
ernierLUT_Cnt_T_s16[51]	1	
ernierLUT_Cnt_T_s16[52]	14	
ernierLUT_Cnt_T_s16[53]	11	
ernierLUT_Cnt_T_s16[54]	8	
ernierLUT_Cnt_T_s16[55]	5	
ernierLUT_Cnt_T_s16[56]	2	
ernierLUT_Cnt_T_s16[57]	15	

VernierLookup

tgt_SpurRevPtr_Cnt_T_u08

 $tgt_VernierLevelNo_Cnt_T_u08$

2014-10-14, 18:24:01+0530



Name Input Value VernierLUT_Cnt_T_s16[58] 12 VernierLUT_Cnt_T_s16[59] 9 VernierLUT_Cnt_T_s16[60] 6 VernierLUT_Cnt_T_s16[61] 3 VernierLUT_Cnt_T_s16[62] 16 VernierLUT_Cnt_T_s16[63] 13 VernierLUT_Cnt_T_s16[64] 10 VernierLUT_Cnt_T_s16[65] 7 VernierLUT_Cnt_T_s16[66] 17 VernierLUT_Cnt_T_s16[67] VernierLUT_Cnt_T_s16[68] -163 VernierLUT_Cnt_T_s16[69] -131 VernierLUT_Cnt_T_s16[70] -99 VernierLUT_Cnt_T_s16[71] -66 VernierLUT_Cnt_T_s16[72] -33 VernierLUT_Cnt_T_s16[73] 0 VernierLUT_Cnt_T_s16[74] 32 VernierLUT_Cnt_T_s16[75] 65 VernierLUT_Cnt_T_s16[76] 98 VernierLUT_Cnt_T_s16[77] 130 VernierLUT_Cnt_T_s16[78] 163 VernierLUT_Cnt_T_s16[79] 196 VernierLUT_Cnt_T_s16[80] 229 VernierLUT_Cnt_T_s16[81] 261 VernierLUT_Cnt_T_s16[82] 294 VernierLUT_Cnt_T_s16[83] 327 VernierLUT_Cnt_T_s16[84] 359 VernierLUT_Cnt_T_s16[85] 0 VernierLUT_Cnt_T_s16[86] 4 VernierLUT_Cnt_T_s16[87] 3 VernierLevelNo_Cnt_T_u08 tgt_VernierLevelNo_Cnt_T_u08 **Actual Value Expected Value** tgt_ColRevPtr_Cnt_T_u08

0

0

1

0 0

1

Test Step 2.9 (Repeat Count = 1)	·
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	360
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288
VernierLUT_Cnt_T_s16[20]	324
VernierLUT_Cnt_T_s16[21]	360
VernierLUT_Cnt_T_s16[22]	9
VernierLUT_Cnt_T_s16[23]	0
VernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5

2014-10-14, 18:24:01+0530



			TOILCITO
Name	Input Value		
VernierLUT_Cnt_T_s16[29]	6		
VernierLUT_Cnt_T_s16[30]	7		
VernierLUT_Cnt_T_s16[31]	8		
VernierLUT_Cnt_T_s16[32]	9		
VernierLUT_Cnt_T_s16[33]	0		
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	2		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[47] VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
	8		
VernierLUT_Cnt_T_s16[52]	9		
VernierLUT_Cnt_T_s16[53]	10		
VernierLUT_Cnt_T_s16[54]	0		
VernierLUT_Cnt_T_s16[55]			
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Resul
	0	0	110001
tgt_ColRevPtr_Cnt_T_u08 tgt_SpurRevPtr_Cnt_T_u08	10	10	



Test Case 3: Path Test

Specification

Performance Metrics: (With "None" instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 16.00 Cycles TS3.2 378.00 Cycles TS3.3 203.00 Cycles TS3.4 358.00 Cycles

Description

VECTOR DESCRIPTION:

 $\label{eq:total_continuous_true} TS3.1 \quad \text{If (Level_Deg_T_f32} <= T2_VernierLUT0_Cnt_u16(0)) => \text{Frue} \\ TS3.2 \quad \text{If (Level_Deg_T_f32} <= T2_VernierLUT0_Cnt_u16(0)) => \text{False} \\ \text{For Search_Cnt_T_u08} = 0 \text{ To (TableSize_Cnt_T_u08} - 1) => \text{True} \\ \text{If (MatchFound_Cnt_T_lgc} = \text{False}) => \text{True} \\ \text{If (Level_Deg_T_f32} < T2_VernierLUT0_Cnt_u16(Search_Cnt_T_u08 + 1)) => \text{False} \\ \text{If (MatchFound_Cnt_T_lgc} = \text{False}) => \text{True} \\ \text{TS3.3} \quad \text{If (Level_Deg_T_f32} < \text{T2_VernierLUT0_Cnt_u16(Search_Cnt_T_u08 + 1)) => \text{True} \\ \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS3.4} \quad \text{If (Level_Deg_T_f32} < \text{Middle_Cnt_T_f32}) => \text{False} \\ \text{TS4.4} \quad \text{TS4$

Test Step 3.1 (Repeat Count = 1)	land Males
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	-792
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
/ernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
/ernierLUT_Cnt_T_s16[3]	-66
/ernierLUT_Cnt_T_s16[4]	-33
/ernierLUT_Cnt_T_s16[5]	0
/ernierLUT_Cnt_T_s16[6]	32
/ernierLUT_Cnt_T_s16[7]	65
VernierLUT_Cnt_T_s16[8]	98
/ernierLUT_Cnt_T_s16[9]	130
/ernierLUT_Cnt_T_s16[10]	163
/ernierLUT_Cnt_T_s16[11]	196
/ernierLUT_Cnt_T_s16[12]	229
/ernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
/ernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
/ernierLUT_Cnt_T_s16[18]	4
/ernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0
VernierLUT_Cnt_T_s16[23]	4
VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
/ernierLUT_Cnt_T_s16[27]	0
/ernierLUT_Cnt_T_s16[28]	4
/ernierLUT_Cnt_T_s16[29]	3
/ernierLUT_Cnt_T_s16[30]	2
/ernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
/ernierLUT_Cnt_T_s16[33]	4
/ernierLUT_Cnt_T_s16[34]	0
/ernierLUT_Cnt_T_s16[35]	8
/ernierLUT_Cnt_T_s16[36]	6
/ernierLUT_Cnt_T_s16[37]	4
VernierLUT_Cnt_T_s16[38]	2
/ernierLUT_Cnt_T_s16[39]	0
/ernierLUT_Cnt_T_s16[40]	9
/ernierLUT_Cnt_T_s16[41]	7
/ernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4

2014-10-14, 18:24:01+0530



		`	
Name	Input Value		
VernierLUT_Cnt_T_s16[49]	2		
VernierLUT_Cnt_T_s16[50]	10		
VernierLUT_Cnt_T_s16[51]	1		
VernierLUT_Cnt_T_s16[52]	14		
VernierLUT_Cnt_T_s16[53]	11		
VernierLUT_Cnt_T_s16[54]	8		
VernierLUT_Cnt_T_s16[55]	5		
VernierLUT_Cnt_T_s16[56]	2		
VernierLUT_Cnt_T_s16[57]	15		
VernierLUT_Cnt_T_s16[58]	12		
VernierLUT_Cnt_T_s16[59]	9		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61]	3		
VernierLUT_Cnt_T_s16[62]	16		
VernierLUT_Cnt_T_s16[63]	13		
VernierLUT_Cnt_T_s16[64]	10		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u0	08	
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	
tgt_SpurRevPtr_Cnt_T_u08	0	0	_
tgt_VernierLevelNo_Cnt_T_u08	1	1	
,	·	<u>'</u>	

Test Step 3.2 (Repeat Count = 1)	<u> </u>
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	360
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
VernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
VernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
VernierLUT_Cnt_T_s16[12]	36
VernierLUT_Cnt_T_s16[13]	72
VernierLUT_Cnt_T_s16[14]	108
VernierLUT_Cnt_T_s16[15]	144
VernierLUT_Cnt_T_s16[16]	180
VernierLUT_Cnt_T_s16[17]	216
VernierLUT_Cnt_T_s16[18]	252
VernierLUT_Cnt_T_s16[19]	288

2014-10-14, 18:24:01+0530



VernierLookup

Vernierzookap		(<i>y</i> =.0
Name	Input Value		
VernierLUT_Cnt_T_s16[20]	324		
VernierLUT_Cnt_T_s16[21]	360		
VernierLUT_Cnt_T_s16[22]	9		
VernierLUT_Cnt_T_s16[23]	0		
VernierLUT_Cnt_T_s16[24]	1		
VernierLUT_Cnt_T_s16[25]	2		
VernierLUT_Cnt_T_s16[26]	3		
VernierLUT_Cnt_T_s16[27]	4		
VernierLUT_Cnt_T_s16[28]	5		
VernierLUT_Cnt_T_s16[29]	6		
VernierLUT_Cnt_T_s16[30]	7		
VernierLUT_Cnt_T_s16[31]	8		
• •	9		
VernierLUT_Cnt_T_s16[32]	0		
VernierLUT_Cnt_T_s16[33]			
VernierLUT_Cnt_T_s16[34]	1		
VernierLUT_Cnt_T_s16[35]	3		
VernierLUT_Cnt_T_s16[36]			
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
VernierLUT_Cnt_T_s16[58]	3		
VernierLUT_Cnt_T_s16[59]	4		
VernierLUT_Cnt_T_s16[60]	5		
VernierLUT_Cnt_T_s16[61]	6		
VernierLUT_Cnt_T_s16[62]	7		
VernierLUT_Cnt_T_s16[63]	8		
VernierLUT_Cnt_T_s16[64]	9		
VernierLUT_Cnt_T_s16[65]	10		
VernierLUT_Cnt_T_s16[66]	22		
VernierLUT_Cnt_T_s16[67]	2		
VernierLUT_Cnt_T_s16[68]	4		
VernierLUT_Cnt_T_s16[69]	6		
VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
VernierLUT_Cnt_T_s16[72]	12		
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
	19		
VernierLUT_Cnt_T_s16[86] VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
		Expected Value	Pos.ult
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	
tgt_SpurRevPtr_Cnt_T_u08	10	10	7
tgt VernierLevelNo Cnt T u08	21	21	· •

21

21

tgt_VernierLevelNo_Cnt_T_u08



Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	0
LookupTableXSize_Cnt_T_u08	17
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-163
VernierLUT_Cnt_T_s16[1]	-131
VernierLUT_Cnt_T_s16[2]	-99
VernierLUT_Cnt_T_s16[3]	-66
VernierLUT_Cnt_T_s16[4]	-33
VernierLUT_Cnt_T_s16[5]	0
VernierLUT_Cnt_T_s16[6]	32 65
VernierLUT_Cnt_T_s16[7] VernierLUT_Cnt_T_s16[8]	98
VernierLUT_Cnt_T_s16[9]	130
VernierLUT_Cnt_T_s16[10]	163
VernierLUT_Cnt_T_s16[11]	196
VernierLUT_Cnt_T_s16[12]	229
VernierLUT_Cnt_T_s16[13]	261
VernierLUT_Cnt_T_s16[14]	294
VernierLUT_Cnt_T_s16[15]	327
VernierLUT_Cnt_T_s16[16]	359
VernierLUT_Cnt_T_s16[17]	0
VernierLUT_Cnt_T_s16[18]	4
VernierLUT_Cnt_T_s16[19]	3
VernierLUT_Cnt_T_s16[20]	2
VernierLUT_Cnt_T_s16[21]	1
VernierLUT_Cnt_T_s16[22]	0 4
VernierLUT_Cnt_T_s16[23] VernierLUT_Cnt_T_s16[24]	3
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	1
VernierLUT_Cnt_T_s16[27]	0
VernierLUT_Cnt_T_s16[28]	4
VernierLUT_Cnt_T_s16[29]	3
VernierLUT_Cnt_T_s16[30]	2
VernierLUT_Cnt_T_s16[31]	1
VernierLUT_Cnt_T_s16[32]	0
VernierLUT_Cnt_T_s16[33]	4
VernierLUT_Cnt_T_s16[34]	0
VernierLUT_Cnt_T_s16[35]	8
VernierLUT_Cnt_T_s16[36]	6
VernierLUT_Cnt_T_s16[37]	4 2
VernierLUT_Cnt_T_s16[38]	
VernierLUT_Cnt_T_s16[39] VernierLUT_Cnt_T_s16[40]	9
VernierLUT_Cnt_T_s16[40]	7
VernierLUT_Cnt_T_s16[42]	5
VernierLUT_Cnt_T_s16[43]	3
VernierLUT_Cnt_T_s16[44]	1
VernierLUT_Cnt_T_s16[45]	10
VernierLUT_Cnt_T_s16[46]	8
VernierLUT_Cnt_T_s16[47]	6
VernierLUT_Cnt_T_s16[48]	4
VernierLUT_Cnt_T_s16[49]	2
VernierLUT_Cnt_T_s16[50]	10
VernierLUT_Cnt_T_s16[51]	1
VernierLUT_Cnt_T_s16[52]	14
VernierLUT_Cnt_T_s16[53]	11 8
VernierLUT_Cnt_T_s16[54] VernierLUT_Cnt_T_s16[55]	5
VernierLUT_Cnt_T_s16[56]	2
VernierLUT_Cnt_T_s16[57]	15
VernierLUT_Cnt_T_s16[58]	12
VernierLUT_Cnt_T_s16[59]	9
VernierLUT_Cnt_T_s16[60]	6
VernierLUT_Cnt_T_s16[61]	3
VernierLUT_Cnt_T_s16[62]	16
VernierLUT_Cnt_T_s16[63]	13
VernierLUT_Cnt_T_s16[64]	10

2014-10-14, 18:24:01+0530



Name	Input Value		
VernierLUT_Cnt_T_s16[65]	7		
VernierLUT_Cnt_T_s16[66]	4		
VernierLUT_Cnt_T_s16[67]	17		
VernierLUT_Cnt_T_s16[68]	-163		
VernierLUT_Cnt_T_s16[69]	-131		
VernierLUT_Cnt_T_s16[70]	-99		
VernierLUT_Cnt_T_s16[71]	-66		
VernierLUT_Cnt_T_s16[72]	-33		
VernierLUT_Cnt_T_s16[73]	0		
VernierLUT_Cnt_T_s16[74]	32		
VernierLUT_Cnt_T_s16[75]	65		
VernierLUT_Cnt_T_s16[76]	98		
VernierLUT_Cnt_T_s16[77]	130		
VernierLUT_Cnt_T_s16[78]	163		
VernierLUT_Cnt_T_s16[79]	196		
VernierLUT_Cnt_T_s16[80]	229		
VernierLUT_Cnt_T_s16[81]	261		
VernierLUT_Cnt_T_s16[82]	294		
VernierLUT_Cnt_T_s16[83]	327		
VernierLUT_Cnt_T_s16[84]	359		
VernierLUT_Cnt_T_s16[85]	0		
VernierLUT_Cnt_T_s16[86]	4		
VernierLUT_Cnt_T_s16[87]	3		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08		
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	0	0	~
tgt_SpurRevPtr_Cnt_T_u08	0	0	~
tgt_VernierLevelNo_Cnt_T_u08	2	2	~

Test Step 3.4 (Repeat Count = 1)	i vi
Name	Input Value
ColRevPtr_Cnt_T_u08	tgt_ColRevPtr_Cnt_T_u08
Level_Deg_T_f32	245.2
LookupTableXSize_Cnt_T_u08	22
SpurRevPtr_Cnt_T_u08	tgt_SpurRevPtr_Cnt_T_u08
VernierLUT_Cnt_T_s16[0]	-396
/ernierLUT_Cnt_T_s16[1]	-360
VernierLUT_Cnt_T_s16[2]	-324
VernierLUT_Cnt_T_s16[3]	-288
VernierLUT_Cnt_T_s16[4]	-252
VernierLUT_Cnt_T_s16[5]	-216
/ernierLUT_Cnt_T_s16[6]	-180
VernierLUT_Cnt_T_s16[7]	-144
VernierLUT_Cnt_T_s16[8]	-108
VernierLUT_Cnt_T_s16[9]	-72
VernierLUT_Cnt_T_s16[10]	-36
VernierLUT_Cnt_T_s16[11]	0
/ernierLUT_Cnt_T_s16[12]	36
/ernierLUT_Cnt_T_s16[13]	72
/ernierLUT_Cnt_T_s16[14]	108
/ernierLUT_Cnt_T_s16[15]	144
/ernierLUT_Cnt_T_s16[16]	180
/ernierLUT_Cnt_T_s16[17]	216
/ernierLUT_Cnt_T_s16[18]	252
/ernierLUT_Cnt_T_s16[19]	288
/ernierLUT_Cnt_T_s16[20]	324
/ernierLUT_Cnt_T_s16[21]	360
/ernierLUT_Cnt_T_s16[22]	9
/ernierLUT_Cnt_T_s16[23]	0
/ernierLUT_Cnt_T_s16[24]	1
VernierLUT_Cnt_T_s16[25]	2
VernierLUT_Cnt_T_s16[26]	3
VernierLUT_Cnt_T_s16[27]	4
VernierLUT_Cnt_T_s16[28]	5
VernierLUT_Cnt_T_s16[29]	6
/ernierLUT_Cnt_T_s16[30]	7
VernierLUT_Cnt_T_s16[31]	8
VernierLUT_Cnt_T_s16[32]	9
VernierLUT_Cnt_T_s16[33]	0
VernierLUT_Cnt_T_s16[34]	1
VernierLUT_Cnt_T_s16[35]	2

VernierLookup

2014-10-14, 18:24:01+0530





Name	Input Value		
VernierLUT_Cnt_T_s16[36]	3		
VernierLUT_Cnt_T_s16[37]	4		
VernierLUT_Cnt_T_s16[38]	5		
VernierLUT_Cnt_T_s16[39]	6		
VernierLUT_Cnt_T_s16[40]	7		
VernierLUT_Cnt_T_s16[41]	8		
VernierLUT_Cnt_T_s16[42]	9		
VernierLUT_Cnt_T_s16[43]	0		
VernierLUT_Cnt_T_s16[44]	0		
VernierLUT_Cnt_T_s16[45]	1		
VernierLUT_Cnt_T_s16[46]	2		
VernierLUT_Cnt_T_s16[47]	3		
VernierLUT_Cnt_T_s16[48]	4		
VernierLUT_Cnt_T_s16[49]	5		
VernierLUT_Cnt_T_s16[50]	6		
VernierLUT_Cnt_T_s16[51]	7		
VernierLUT_Cnt_T_s16[52]	8		
VernierLUT_Cnt_T_s16[53]	9		
VernierLUT_Cnt_T_s16[54]	10		
VernierLUT_Cnt_T_s16[55]	0		
VernierLUT_Cnt_T_s16[56]	1		
VernierLUT_Cnt_T_s16[57]	2		
	3		
VernierLUT_Cnt_T_s16[58]			
VernierLUT_Cnt_T_s16[59]	5		
VernierLUT_Cnt_T_s16[60]	6		
VernierLUT_Cnt_T_s16[61] VernierLUT_Cnt_T_s16[62]	7		
· -			
VernierLUT_Cnt_T_s16[63]	9		
VernierLUT_Cnt_T_s16[64]			
VernierLUT_Cnt_T_s16[65]	10 22		
VernierLUT_Cnt_T_s16[66]	2		
VernierLUT_Cnt_T_s16[67]	4		
VernierLUT_Cnt_T_s16[68]	6		
VernierLUT_Cnt_T_s16[69] VernierLUT_Cnt_T_s16[70]	8		
VernierLUT_Cnt_T_s16[71]	10		
	12		
VernierLUT_Cnt_T_s16[72]			
VernierLUT_Cnt_T_s16[73]	14		
VernierLUT_Cnt_T_s16[74]	16		
VernierLUT_Cnt_T_s16[75]	18		
VernierLUT_Cnt_T_s16[76]	20		
VernierLUT_Cnt_T_s16[77]	1		
VernierLUT_Cnt_T_s16[78]	3		
VernierLUT_Cnt_T_s16[79]	5		
VernierLUT_Cnt_T_s16[80]	7		
VernierLUT_Cnt_T_s16[81]	9		
VernierLUT_Cnt_T_s16[82]	11		
VernierLUT_Cnt_T_s16[83]	13		
VernierLUT_Cnt_T_s16[84]	15		
VernierLUT_Cnt_T_s16[85]	17		
VernierLUT_Cnt_T_s16[86]	19		
VernierLUT_Cnt_T_s16[87]	21		
VernierLevelNo_Cnt_T_u08	tgt_VernierLevelNo_Cnt_T_u08	l=	1_
Name	Actual Value	Expected Value	Result
tgt_ColRevPtr_Cnt_T_u08	7	7	~
tgt_SpurRevPtr_Cnt_T_u08	7	7	V
tgt_VernierLevelNo_Cnt_T_u08	15	15	