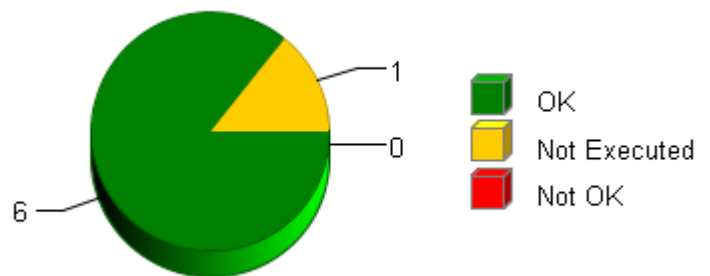


## Summary

**Total Test Objects:** 7  
**Successful:** 6  
**Failed:** 0  
**Not Executed:** 1  
**Date:** 2014-09-19  
**Time:** 16:38:41+0530

## Overall Test Object Results (including Coverage)



## Selected Project Items

Test Object "CBD\_UnitTest/FDD\_Inertia/ADDCoefCalc"  
Test Object "CBD\_UnitTest/FDD\_Inertia/DecelGain"  
Test Object "CBD\_UnitTest/FDD\_Inertia/DriverVelCalc"  
Test Object "CBD\_UnitTest/FDD\_Inertia/FilterCoefCalc"  
Test Object "CBD\_UnitTest/FDD\_Inertia/FrqDepDmpnInrtCmp\_Init"  
Test Object "CBD\_UnitTest/FDD\_Inertia/FrqDepDmpnInrtCmp\_Per1"  
Test Object "CBD\_UnitTest/FDD\_Inertia/GenFddlcCmd"

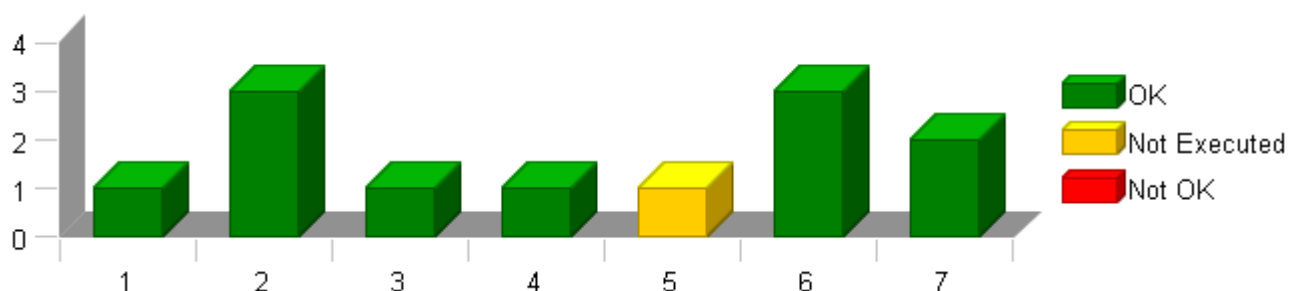
## Used Test Environments

TI TMS 570 PLS UDE (Default)

## Batch Operation Settings

**Check Interface:** No  
**Generate Driver:** Yes  
**Execute Test:** Yes  
**Create New Test Run:** No  
**Instrumentation:** Test Object Only  
**Coverage:** Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

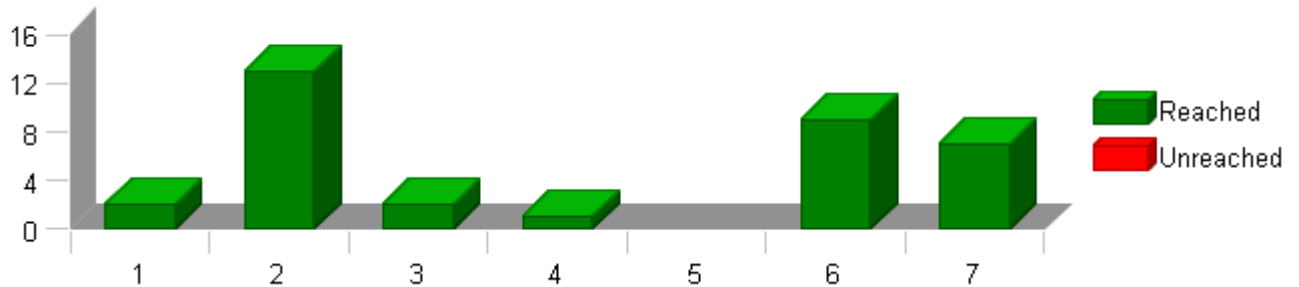
## Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results

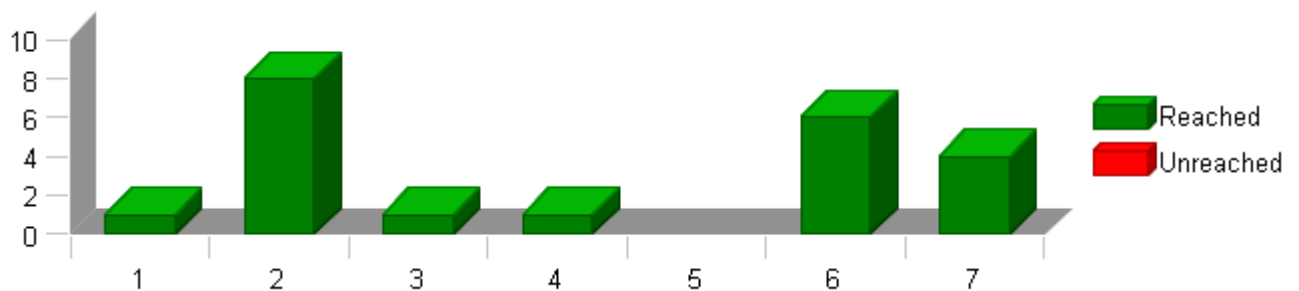
do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

### Statement (C0) Coverage: Total Statements for Each Test Object



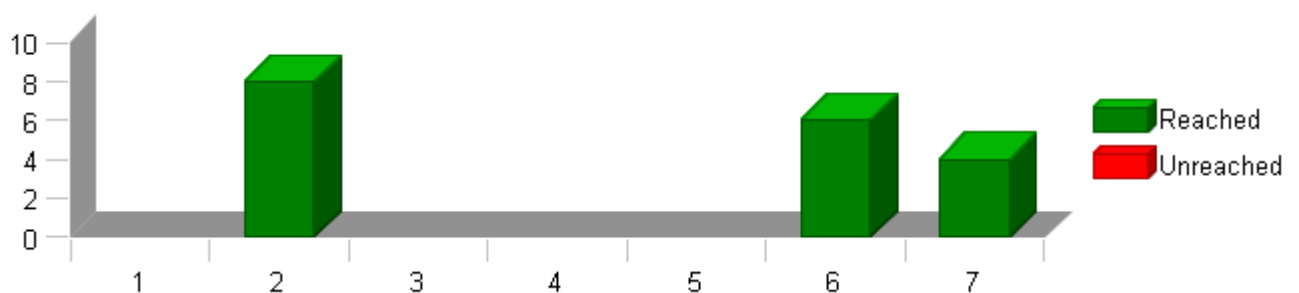
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

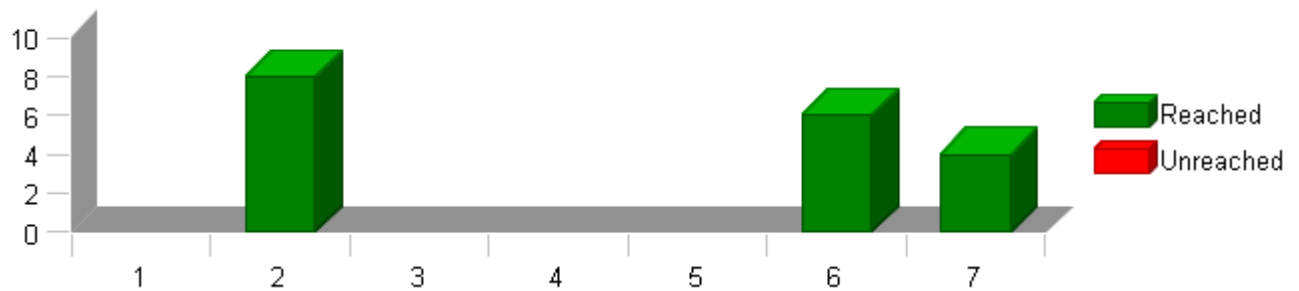
### Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

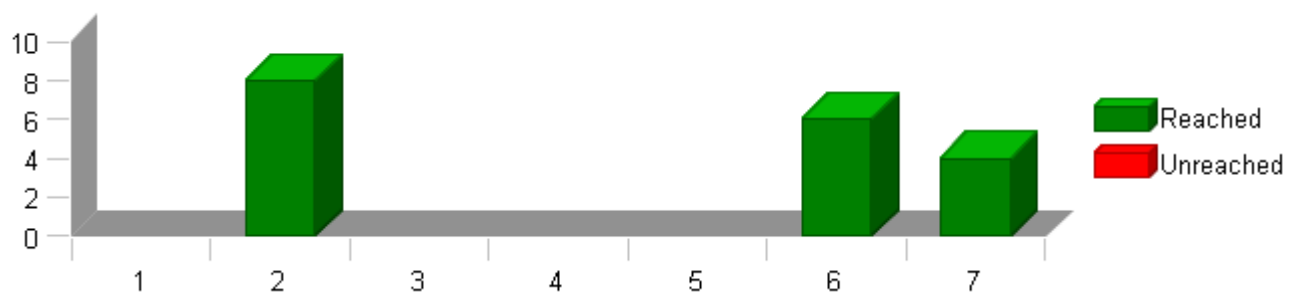
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

| No. | Name                                   | C0    | C1    | DC    | MC/DC | MCC   | Test Cases      | Result |
|-----|--|-------|-------|-------|-------|-------|-----------------|--------|
|     | FDD_Inertia                            | 100 % | 100 % | 100 % | 100 % | 100 % | 11 of 12 passed | ✓      |
|     | CBD_UnitTest                           | 100 % | 100 % | 100 % | 100 % | 100 % | 11 of 12 passed | ✓      |
|     | FDD_Inertia                            | 100 % | 100 % | 100 % | 100 % | 100 % | 11 of 12 passed | ✓      |
| 1   | <a href="#">ADDCoefCalc</a>            | 100 % | 100 % | -     | -     | -     | 1 of 1 passed   | ✓      |
| 2   | <a href="#">DecelGain</a>              | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed   | ✓      |
| 3   | <a href="#">DriverVelCalc</a>          | 100 % | 100 % | -     | -     | -     | 1 of 1 passed   | ✓      |
| 4   | <a href="#">FilterCoefCalc</a>         | 100 % | 100 % | -     | -     | -     | 1 of 1 passed   | ✓      |
| 5   | <a href="#">FrqDepDmpnInrtCmp_Init</a> | -     | -     | -     | -     | -     | -               | ⚠      |
| 6   | <a href="#">FrqDepDmpnInrtCmp_Per1</a> | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed   | ✓      |
| 7   | <a href="#">GenFddlcCmd</a>            | 100 % | 100 % | 100 % | 100 % | 100 % | 2 of 2 passed   | ✓      |

# TEST DETAILS REPORT

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GenFddlCmd



|             |             |
|-------------|-------------|
| Project     | FDD_Inertia |
| Module      | FDD_Inertia |
| Test Object | GenFddlCmd  |

## Instrumentation: Test Object Only

|                         |       |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage       | 100 % |
| Branch (C1) Coverage    | 100 % |
| MCC Coverage            | 100 % |
| MC/DC Coverage          | 100 % |

## Statistics

|                 |     |
|-----------------|-----|
| Total Testcases | 2   |
| Successful      | 2 ✓ |
| Failed          | 0   |
| Not Executed    | 0   |

# TEST DETAILS REPORT

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GenFddlcCmd



## Module Properties

|                        |   |
|------------------------|---|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp  |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml  |
| Target Environment     | TI TMS 570 PLS UDE (Default)  |
| Kind of Test           | Unit Test   |
| Linker Options         |   |
| Source File(s)         |   |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NxrLib\src\interpolation.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name                 | Text  |
|----------------------|---|
| Module 'FDD_Inertia' | <p>*****Unit Test Description*****</p> <p>Name of Tester: Spoorti Mali<br/>Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c<br/>Code File(s) Version: 13<br/>Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc<br/>Module Design Document Version: 18<br/>Data Dictionary Version: 16<br/>Unit Test Plan Version: 6<br/>Optimization Level: Level 2<br/>Compiler (CodeGen) Version: TMS470_4.9.5<br/>Model Type: Excel Macro<br/>Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30<br/>Total FLASH Used (Bytes): 1994<br/>Total RAM Used (Bytes): 60<br/>Total CALS Used (Bytes): 328<br/>Special Test Requirements:<br/>Test Date: 09-19-2014<br/>Comments:</p> <p>Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.</p> <p>Note2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.</p> <p>Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function.</p> <p>Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function.</p> <p>Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable ""filtCoef_Uls_T_Str.b0_Uls_f32"" is calculated as -2.74156205240179 to 0 and ""filtCoef_Uls_T_Str.b1_Uls_f32"" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16.</p> <p>Note6:In ""GenFddlcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range.</p> <p>Note 7:The range of the parameter ""VehicleSpeed_Kph_T_f32"" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication.</p> <p>Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DriverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"".</p> <p>*****</p> |

## Attributes

| Name                  | Value  |
|-----------------------|--|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision       | 9  |
| InitObjDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj                   |
| InitSrcDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src                   |
| Linker File           | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd          |
| Makefile Template     | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path   | \$(ProgramFiles)\pls\UDE 3.2   |
| Time Unit             | Cycles   |
| Timer Enabled         | false  |
| Timer Prescale        | 0  |
| Timer Resolution      | 1  |

# TEST DETAILS REPORT

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GenFddIcCmd



| Attributes      |  |
|-----------------|--|
| Name            | Value  |
| UDE Config File | \${PROJECTROOT}\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File  | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

# TEST DETAILS REPORT

GenFddlcCmd

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## Test Case 1: Metrics Test

|                      |   |
|----------------------|---|
| <b>Specification</b> | Performance Metrics (With "None" Instrumentation and "WithPS" Environment)<br><br>CPU Cycles:<br><br>TS1.1 360.00 Cycles<br>TS1.2 360.00 Cycles   |
| <b>Description</b>   | Test Vector Description:<br><br>TS1.1 "Shortest Execution Path:<br>(ScaledDriverVel_MtrRadpS_T_f32>=D_ATTENTBLMAXINPUT_MTRRADPS_F32)=True"<br>TS1.2 "Longest Execution Path:<br>(ScaledDriverVel_MtrRadpS_T_f32>=D_ATTENTBLMAXINPUT_MTRRADPS_F32)=False<br>(ScaledDriverVel_MtrRadpS_T_f32<=D_ATTENTBLMININPUT_MTRRADPS_F32)=False" |

## Test Step 1.1 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |  |
|-----------------------------------|------------------------|--------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 1.1                    |                          |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 22.2                   |                          |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 7.3                    |                          |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 10                     |                          |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -7226.652              |                          |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240                    |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 320                    |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 49                     |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 51                     |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.024534              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.124564               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0000456              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.0453                 |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3242                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.54523                |                          |        |  |
| Name                              | Actual Value           | Expected Value           | Result |  |
| GenFddlcCmd()                     | -0.330669165           | -0.330669151 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -1.6598295             | -1.659829464 ± 0.000009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -7226.65186            | -7226.652 ± 0.00390625   | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.10000002             | 1.1 ± 0.00048828125      | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 22.2000008             | 22.2 ± 0.00390625        | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.2 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |  |
|-----------------------------------|------------------------|--------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -2.2                   |                          |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -16.66                 |                          |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.2                   |                          |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -3                     |                          |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 10.2                   |                          |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 512                    |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 560                    |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 116                    |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 118                    |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02345               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.15457                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.32                   |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.766645               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.3242                 |                          |        |  |
| Name                              | Actual Value           | Expected Value           | Result |  |
| GenFddlcCmd()                     | -0.334564269           | -0.334564171 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -0.738348722           | -0.738348516 ± 0.0000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 10.1999998             | 10.2 ± 0.00390625        | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.20000005            | -2.2 ± 0.00048828125     | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -16.6599998            | -16.66 ± 0.00390625      | ✓      |  |



# TEST DETAILS REPORT

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GenFddIcCmd



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     |        | ✓ |

# TEST DETAILS REPORT

GenFddIcCmd

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## Test Case 2: Boundary Test





|               |  |
|---------------|--|
| Specification | Performance Metrics (With "None" Instrumentation and "WithPS" Environment) |
|               | CPU Cycles:  |
| TS2.1         | 360.00 Cycles  |
| TS2.2         | 360.00 Cycles  |
| TS2.3         | 360.00 Cycles  |
| TS2.4         | 360.00 Cycles  |
| TS2.5         | 360.00 Cycles  |
| TS2.6         | 360.00 Cycles  |
| TS2.7         | 360.00 Cycles  |
| TS2.8         | 372.00 Cycles  |
| TS2.9         | 360.00 Cycles  |
| TS2.10        | 360.00 Cycles  |
| TS2.11        | 360.00 Cycles  |
| TS2.12        | 372.00 Cycles  |
| TS2.13        | 372.00 Cycles  |
| TS2.14        | 372.00 Cycles  |
| TS2.15        | 360.00 Cycles  |
| TS2.16        | 360.00 Cycles  |
| TS2.17        | 360.00 Cycles  |
| TS2.18        | 360.00 Cycles  |
| TS2.19        | 360.00 Cycles  |
| TS2.20        | 372.00 Cycles  |
| TS2.21        | 372.00 Cycles  |
| TS2.22        | 372.00 Cycles  |
| TS2.23        | 372.00 Cycles  |
| TS2.24        | 360.00 Cycles  |
| TS2.25        | 360.00 Cycles  |
| TS2.26        | 360.00 Cycles  |
| TS2.27        | 428.00 Cycles  |
| TS2.28        | 360.00 Cycles  |
| TS2.29        | 360.00 Cycles  |
| TS2.30        | 360.00 Cycles  |
| TS2.31        | 360.00 Cycles  |
| TS2.32        | 360.00 Cycles  |
| TS2.33        | 360.00 Cycles  |
| TS2.34        | 360.00 Cycles  |
| TS2.35        | 372.00 Cycles  |
| TS2.36        | 360.00 Cycles  |
| TS2.37        | 360.00 Cycles  |
| TS2.38        | 360.00 Cycles  |
| TS2.39        | 360.00 Cycles  |
| TS2.40        | 372.00 Cycles  |
| TS2.41        | 372.00 Cycles  |
| TS2.42        | 360.00 Cycles  |
| TS2.43        | 372.00 Cycles  |
| TS2.44        | 360.00 Cycles  |
| TS2.45        | 360.00 Cycles  |
| TS2.46        | 372.00 Cycles  |
| TS2.47        | 360.00 Cycles  |
| TS2.48        | 360.00 Cycles  |
| TS2.49        | 360.00 Cycles  |
| TS2.50        | 360.00 Cycles  |
| TS2.51        | 360.00 Cycles  |

# TEST DETAILS REPORT

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GenFddlcCmd



## Description Test Vector Description

TS2.1 All min  
 TS2.2 All max  
 TS2.3 ScaledDriverVel\_MtrRadpS\_T\_f32 = min  
 TS2.4 ScaledDriverVel\_MtrRadpS\_T\_f32 = max  
 TS2.5 ScaledDriverVel\_MtrRadpS\_T\_f32 = zero  
 TS2.6 ScaledDriverVel\_MtrRadpS\_T\_f32 = pos  
 TS2.7 ScaledDriverVel\_MtrRadpS\_T\_f32 = neg  
 TS2.8 filtCoef\_Uls\_T\_Str.b0\_Uls\_f32 = min  
 TS2.9 filtCoef\_Uls\_T\_Str.b0\_Uls\_f32 = max  
 TS2.10 filtCoef\_Uls\_T\_Str.b0\_Uls\_f32 = mid  
 TS2.11 filtCoef\_Uls\_T\_Str.b1\_Uls\_f32 = min  
 TS2.12 filtCoef\_Uls\_T\_Str.b1\_Uls\_f32 = max  
 TS2.13 filtCoef\_Uls\_T\_Str.b1\_Uls\_f32 = mid  
 TS2.14 filtCoef\_Uls\_T\_Str.b2\_Uls\_f32 = min  
 TS2.15 filtCoef\_Uls\_T\_Str.b2\_Uls\_f32 = max  
 TS2.16 filtCoef\_Uls\_T\_Str.b2\_Uls\_f32 = mid  
 TS2.17 filtCoef\_Uls\_T\_Str.a0\_Uls\_f32 = min  
 TS2.18 filtCoef\_Uls\_T\_Str.a0\_Uls\_f32 = max  
 TS2.19 filtCoef\_Uls\_T\_Str.a0\_Uls\_f32 = mid  
 TS2.20 filtCoef\_Uls\_T\_Str.a1\_Uls\_f32 = min  
 TS2.21 filtCoef\_Uls\_T\_Str.a1\_Uls\_f32 = max  
 TS2.22 filtCoef\_Uls\_T\_Str.a1\_Uls\_f32 = mid  
 TS2.23 filtCoef\_Uls\_T\_Str.a2\_Uls\_f32 = min  
 TS2.24 filtCoef\_Uls\_T\_Str.a2\_Uls\_f32 = max  
 TS2.25 filtCoef\_Uls\_T\_Str.a2\_Uls\_f32 = mid  
 TS2.26 Prev2ScldrvVel\_RadpS\_M\_f32 = min  
 TS2.27 Prev2ScldrvVel\_RadpS\_M\_f32 = max  
 TS2.28 Prev2ScldrvVel\_RadpS\_M\_f32 = zero  
 TS2.29 Prev2ScldrvVel\_RadpS\_M\_f32 = neg  
 TS2.30 Prev2ScldrvVel\_RadpS\_M\_f32 = pos  
 TS2.31 Prev1ScldrvVel\_RadpS\_M\_f32 = min  
 TS2.32 Prev1ScldrvVel\_RadpS\_M\_f32 = max  
 TS2.33 Prev1ScldrvVel\_RadpS\_M\_f32 = zero  
 TS2.34 Prev1ScldrvVel\_RadpS\_M\_f32 = neg  
 TS2.35 Prev1ScldrvVel\_RadpS\_M\_f32 = pos  
 TS2.36 Prev1PreAttnComp\_MtrNm\_M\_f32 = min  
 TS2.37 Prev1PreAttnComp\_MtrNm\_M\_f32 = max  
 TS2.38 Prev1PreAttnComp\_MtrNm\_M\_f32 = zero  
 TS2.39 Prev1PreAttnComp\_MtrNm\_M\_f32 = neg  
 TS2.40 Prev1PreAttnComp\_MtrNm\_M\_f32 = pos  
 TS2.41 Prev2PreAttnComp\_MtrNm\_M\_f32 = min  
 TS2.42 Prev2PreAttnComp\_MtrNm\_M\_f32 = max  
 TS2.43 Prev2PreAttnComp\_MtrNm\_M\_f32 = zero  
 TS2.44 Prev2PreAttnComp\_MtrNm\_M\_f32 = neg  
 TS2.45 Prev2PreAttnComp\_MtrNm\_M\_f32 = pos  
 TS2.46 t\_FDD\_AttenTblX\_MtrRadpS\_u12p4[2] = min  
 TS2.47 t\_FDD\_AttenTblX\_MtrRadpS\_u12p4[2] = max  
 TS2.48 t\_FDD\_AttenTblX\_MtrRadpS\_u12p4[2] = mid  
 TS2.49 t\_FDD\_AttenTblY\_Uls\_u8p8[2] = min  
 TS2.50 t\_FDD\_AttenTblY\_Uls\_u8p8[2] = max  
 TS2.51 t\_FDD\_AttenTblY\_Uls\_u8p8[2] = mid

## Test Step 2.1 (Repeat Count = 1) ✓

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -8.8                   |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -12917.3               |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -8.8                   |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -12917.3               |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -7226.652              |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 0                      |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 0                      |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 0                      |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 0                      |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -2.741562052           |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0                      |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.160083862           |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.5525885              |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.9996842             |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.0504234              |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | 0                      | 0 ± 0.000009           | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 9012.61621             | 9012.617156 ± 0.009    | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | -7226.65186            | -7226.652 ± 0.00390625 | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -8.80000019            | -8.8 ± 0.00048828125   | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -12917.2998            | -12917.3 ± 0.00390625  | ✓      |

| T ✓                         |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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## Test Step 2.2 (Repeat Count = 1)

| Name                              | Input Value            |                       |        |  |
|-----------------------------------|------------------------|-----------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 8.8                    |                       |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 12917.3                |                       |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 8.8                    |                       |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 12917.3                |                       |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 7226.652               |                       |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                       |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 17600                  |                       |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 17600                  |                       |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 256                    |                       |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 256                    |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | 0                      |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330448               |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.411114052            |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.9498924              |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.8417266             |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 10.6056849             |                       |        |  |
| Name                              | Actual Value           | Expected Value        | Result |  |
| GenFddlcCmd()                     | 2046.13135             | 2046.131531 ± 0.009   | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 2046.13135             | 2046.131531 ± 0.009   | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 7226.65186             | 7226.652 ± 0.00390625 | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 8.80000019             | 8.8 ± 0.00048828125   | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 12917.2998             | 12917.3 ± 0.00390625  | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.3 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |  |
|-----------------------------------|------------------------|--------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 1.1                    |                          |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 22.2                   |                          |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 7.3                    |                          |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 10                     |                          |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -7226.652              |                          |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240                    |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 320                    |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 49                     |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 51                     |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.024534              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.124564               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0000456              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.0453                 |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3242                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.54523                |                          |        |  |
| Name                              | Actual Value           | Expected Value           | Result |  |
| GenFddlcCmd()                     | -0.330669165           | -0.330669151 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -1.6598295             | -1.659829464 ± 0.0000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -7226.65186            | -7226.652 ± 0.00390625   | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.10000002             | 1.1 ± 0.00048828125      | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 22.2000008             | 22.2 ± 0.00390625        | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.4 (Repeat Count = 1)

| Name                           | Input Value            |  |  |  |
|--------------------------------|------------------------|--|--|--|
| Prev1PreAttnComp_MtrNm_M_f32   | -1.1                   |  |  |  |
| Prev1ScIDrvVel_RadpS_M_f32     | -4.21                  |  |  |  |
| Prev2PreAttnComp_MtrNm_M_f32   | -6.8                   |  |  |  |
| Prev2ScIDrvVel_RadpS_M_f32     | -2                     |  |  |  |
| ScaledDriverVel_MtrRadpS_T_f32 | 7226.652               |  |  |  |
| filtCoef_Uls_T_Str             | tgt_filtCoef_Uls_T_Str |  |  |  |

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| Name                              | Input Value  |                         |        |
|-----------------------------------|--------------|-------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 352          |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 400          |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 65           |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 68           |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0332      |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.13456      |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0005345    |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.45675      |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.45654     |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.757645     |                         |        |
| Name                              | Actual Value | Expected Value          | Result |
| GenFddlcCmd()                     | 0.509668887  | 0.509668855 ± 0.0000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 1.91875339   | 1.918753337 ± 0.000009  | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 7226.65186   | 7226.652 ± 0.00390625   | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.1000002   | -1.1 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -4.2100004   | -4.21 ± 0.00390625      | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.5 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 6.6                    |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 26.1                   |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 8.3                    |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 17.03                  |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 0                      |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1088                   |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1120                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 129                    |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 131                    |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.006363              |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2574                 |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00145                |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.55765                |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.7898                |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.8534                 |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | 0.782138526            | 0.78213851 ± 0.0000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 1.55215085             | 1.552150842 ± 0.000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 0                      | 0 ± 0.00390625         | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 6.5999999              | 6.6 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 26.1000004             | 26.1 ± 0.00390625      | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.6 (Repeat Count = 1)

| Name                              | Input Value            |  |  |
|-----------------------------------|------------------------|--|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -2.2                   |  |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -16.66                 |  |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.2                   |  |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -3                     |  |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 10.2                   |  |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |  |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 512                    |  |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 560                    |  |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 116                    |  |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 118                    |  |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02345               |  |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.15457                |  |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.1                    |  |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.766645               |  |  |

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| Name                              | Input Value  |                         |        |
|-----------------------------------|--------------|-------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789      |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.3242       |                         |        |
| Name                              | Actual Value | Expected Value          | Result |
| GenFddlcCmd()                     | 0.157648206  | 0.157648289 ± 0.0000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 0.347913265  | 0.347913465 ± 0.0000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 10.1999998   | 10.2 ± 0.00390625       | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.20000005  | -2.2 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -16.6599998  | -16.66 ± 0.00390625     | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.7 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |
|-----------------------------------|------------------------|--------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 3.3                    |                          |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 26.45                  |                          |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.2                    |                          |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 17.12                  |                          |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -10.3                  |                          |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 512                    |                          |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 560                    |                          |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 144                    |                          |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 146                    |                          |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03123               |                          |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.16878                |                          |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.2                    |                          |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.27867                |                          |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.24234               |                          |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.67452                |                          |        |
| Name                              | Actual Value           | Expected Value           | Result |
| GenFddlcCmd()                     | -1.8318522             | -1.831852049 ± 0.0000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -3.25662613            | -3.256625864 ± 0.0000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | -10.3000002            | -10.3 ± 0.00390625       | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 3.29999995             | 3.3 ± 0.00048828125      | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 26.4500008             | 26.45 ± 0.00390625       | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.8 (Repeat Count = 1)

| Name                              | Input Value            |                      |        |
|-----------------------------------|------------------------|----------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -3.3                   |                      |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -4.21                  |                      |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.3                   |                      |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -33.32                 |                      |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 2562.6                 |                      |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                      |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 656                    |                      |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 720                    |                      |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 172                    |                      |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 174                    |                      |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -2.741562052           |                      |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.175634               |                      |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.8                    |                      |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.16756                |                      |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789                |                      |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.77453                |                      |        |
| Name                              | Actual Value           | Expected Value       | Result |
| GenFddlcCmd()                     | 470.300568             | 470.3005767 ± 0.0009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 691.936462             | 691.9364807 ± 0.0009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 2562.6001              | 2562.6 ± 0.00390625  | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -3.29999995            | -3.3 ± 0.00048828125 | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -4.2100004             | -4.21 ± 0.00390625   | ✓      |

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| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.9 (Repeat Count = 1)

| Name                              | Input Value            |                       |        |  |
|-----------------------------------|------------------------|-----------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 4.4                    |                       |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 1234.56                |                       |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 2.3                    |                       |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 4678.14                |                       |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -2.8                   |                       |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                       |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 768                    |                       |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 800                    |                       |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 218                    |                       |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 220                    |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | 0                      |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.184534               |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.9                    |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.92453                |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.535                 |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.452345               |                       |        |  |
| Name                              | Actual Value           | Expected Value        | Result |  |
| GenFddlcCmd()                     | 21.4257507             | 21.42575176 ± 0.00009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 25.1605148             | 25.16051583 ± 0.00009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -2.79999995            | -2.8 ± 0.00390625     | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 4.4000001              | 4.4 ± 0.00048828125   | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 1234.56006             | 1234.56 ± 0.00390625  | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.10 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |  |
|-----------------------------------|------------------------|-------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -4.4                   |                         |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -27.55                 |                         |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.7                   |                         |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -15                    |                         |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 3.5                    |                         |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 784                    |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 880                    |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 63                     |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 66                     |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.003467              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.1945645              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.9                    |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.823423               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.78987               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.6345                 |                         |        |  |
| Name                              | Actual Value           | Expected Value          | Result |  |
| GenFddlcCmd()                     | -0.823069274           | -0.82306927 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -3.34453535            | -3.344535448 ± 0.000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 3.5                    | 3.5 ± 0.00390625        | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -4.4000001             | -4.4 ± 0.00048828125    | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -27.5499992            | -27.55 ± 0.00390625     | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |



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## Test Step 2.11 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |  |
|-----------------------------------|------------------------|--------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 5.5                    |                          |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 6789.565               |                          |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.7                    |                          |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 5322.14                |                          |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -3.9                   |                          |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 944                    |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 960                    |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 78                     |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 80                     |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.004353              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0                      |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0016456              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.7234                 |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.64564               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.36567                |                          |        |  |
| Name                              | Actual Value           | Expected Value           | Result |  |
| GenFddlcCmd()                     | 0.0503453612           | 0.050345373 ± 0.00000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 0.165236056            | 0.165236095 ± 0.00000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -3.9000001             | -3.9 ± 0.00390625        | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.5                    | 5.5 ± 0.00048828125      | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 6789.56494             | 6789.565 ± 0.00390625    | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.12 (Repeat Count = 1)

| Name                              | Input Value            |                           |        |  |
|-----------------------------------|------------------------|---------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -5.5                   |                           |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -37.15                 |                           |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -8.3                   |                           |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -42.02                 |                           |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 1444.1                 |                           |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                           |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1008                   |                           |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1040                   |                           |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 106                    |                           |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 109                    |                           |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.005456              |                           |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330448               |                           |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.001767               |                           |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.65674                |                           |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4234                |                           |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.94645                |                           |        |  |
| Name                              | Actual Value           | Expected Value            | Result |  |
| GenFddlcCmd()                     | -0.619547307           | -0.619547276 ± 0.00000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -1.45508361            | -1.45508351 ± 0.00000009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 1444.09998             | 1444.1 ± 0.00390625       | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.5                   | -5.5 ± 0.00048828125      | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -37.1500015            | -37.15 ± 0.00390625       | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.13 (Repeat Count = 1)

| Name                           | Input Value            |  |  |  |
|--------------------------------|------------------------|--|--|--|
| Prev1PreAttnComp_MtrNm_M_f32   | 6.6                    |  |  |  |
| Prev1ScIDrvVel_RadpS_M_f32     | 26.1                   |  |  |  |
| Prev2PreAttnComp_MtrNm_M_f32   | 8.3                    |  |  |  |
| Prev2ScIDrvVel_RadpS_M_f32     | 17.03                  |  |  |  |
| ScaledDriverVel_MtrRadpS_T_f32 | -2234.7                |  |  |  |
| filtCoef_Uls_T_Str             | tgt_filtCoef_Uls_T_Str |  |  |  |

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| Name                              | Input Value  |                        |        |
|-----------------------------------|--------------|------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1088         |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1120         |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 129          |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 131          |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.006363    |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2574       |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00145      |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.55765      |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.7898      |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.8534       |                        |        |
| Name                              | Actual Value | Expected Value         | Result |
| GenFddlcCmd()                     | 0.625984669  | 0.62598471 ± 0.0000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 1.22329831   | 1.223298365 ± 0.000009 | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | -2234.69995  | -2234.7 ± 0.00390625   | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 6.5999999    | 6.6 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | 26.1000004   | 26.1 ± 0.00390625      | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.14 (Repeat Count = 1) ✓

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -6.6                   |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -33.1                  |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -7.5                   |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -22.04                 |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 1555.6                 |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1152                   |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1200                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 157                    |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 161                    |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745745            |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2454                 |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.160083862           |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.44564                |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.53524               |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.254                  |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | -18.191328             | -18.1913286 ± 0.00009  | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -28.9253426            | -28.92534236 ± 0.00009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 1555.59998             | 1555.6 ± 0.00390625    | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -6.5999999             | -6.6 ± 0.00048828125   | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -33.0999985            | -33.1 ± 0.00390625     | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.15 (Repeat Count = 1) ✓

| Name                              | Input Value            |
|-----------------------------------|------------------------|
| Prev1PreAttnComp_MtrNm_M_f32      | 7.7                    |
| Prev1ScIDrvVel_RadpS_M_f32        | 18                     |
| Prev2PreAttnComp_MtrNm_M_f32      | 7.5                    |
| Prev2ScIDrvVel_RadpS_M_f32        | 28.01                  |
| ScaledDriverVel_MtrRadpS_T_f32    | -5.8                   |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1232                   |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1280                   |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 183                    |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 185                    |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00864               |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.31545                |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.411114052            |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.3454                 |

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| Name                              | Input Value  |                        |        |
|-----------------------------------|--------------|------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.6353      |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.63432      |                        |        |
| Name                              | Actual Value | Expected Value         | Result |
| GenFddlcCmd()                     | 1.29496682   | 1.294967011 ± 0.000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 1.81153834   | 1.811538551 ± 0.000009 | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | -5.80000019  | -5.8 ± 0.00390625      | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 7.69999981   | 7.7 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | 18           | 18 ± 0.00390625        | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Step 2.16 (Repeat Count = 1) ✓

| Name                              | Input Value            |                         |        |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -7.7                   |                         |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -28.02                 |                         |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -6.5                   |                         |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -27                    |                         |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 6.2                    |                         |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1296                   |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1360                   |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 230                    |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 232                    |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.009585              |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.32554                |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.1496                |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.234535               |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.634453              |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.35435                |                         |        |
| Name                              | Actual Value           | Expected Value          | Result |
| GenFddlcCmd()                     | -3.82750082            | -3.827500822 ± 0.000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | -4.26017475            | -4.260174828 ± 0.000009 | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | 6.19999981             | 6.2 ± 0.00390625        | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | -7.69999981            | -7.7 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | -28.0200005            | -28.02 ± 0.00390625     | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Step 2.17 (Repeat Count = 1) ✓

| Name                              | Input Value            |                         |        |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 1.5                    |                         |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 24.06                  |                         |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 6.5                    |                         |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 32.56                  |                         |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -6.3                   |                         |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1344                   |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1440                   |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 71                     |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 74                     |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00365               |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.26745                |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00006456             |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.5525885              |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.4564                |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.134534               |                         |        |
| Name                              | Actual Value           | Expected Value          | Result |
| GenFddlcCmd()                     | 0.371916622            | 0.371916637 ± 0.0000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 1.34099519             | 1.340995197 ± 0.000009  | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | -6.30000019            | -6.3 ± 0.00390625       | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.5                    | 1.5 ± 0.00048828125     | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | 24.0599995             | 24.06 ± 0.00390625      | ✔      |

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| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.18 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |  |
|-----------------------------------|------------------------|-------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -1.5                   |                         |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -16.05                 |                         |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -4.5                   |                         |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -25.25                 |                         |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 7.4                    |                         |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1520                   |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1568                   |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 86                     |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 88                     |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01423               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27344                |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0014534              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.9498924              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4535                |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.34564                |                         |        |  |
| Name                              | Actual Value           | Expected Value          | Result |  |
| GenFddlcCmd()                     | 0.164055958            | 0.164056011 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 0.488352627            | 0.488352776 ± 0.0000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 7.4000001              | 7.4 ± 0.00390625        | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.5                   | -1.5 ± 0.00048828125    | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -16.0499992            | -16.05 ± 0.00390625     | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.19 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |  |
|-----------------------------------|------------------------|------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 2.5                    |                        |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 100.04                 |                        |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 4.5                    |                        |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 97                     |                        |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -7.5                   |                        |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1552                   |                        |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1600                   |                        |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 114                    |                        |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 116                    |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02342               |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.28546                |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.000745               |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.453723               |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.5345                |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.94534                |                        |        |  |
| Name                              | Actual Value           | Expected Value         | Result |  |
| GenFddlcCmd()                     | 1.44737673             | 1.447376757 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 3.25024962             | 3.25024956 ± 0.000009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -7.5                   | -7.5 ± 0.00390625      | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 2.5                    | 2.5 ± 0.00048828125    | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 100.040001             | 100.04 ± 0.00390625    | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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## Test Step 2.20 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |  |
|-----------------------------------|------------------------|-------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -2.5                   |                         |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -69.4                  |                         |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -3.5                   |                         |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -59.65                 |                         |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 1500.02                |                         |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1616                   |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1680                   |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 136                    |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 139                    |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03452               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2956                 |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00053453             |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.6345                 |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.9996842             |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.84563                |                         |        |  |
| Name                              | Actual Value           | Expected Value          | Result |  |
| GenFddlcCmd()                     | -2.45213747            | -2.452137655 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -4.51616669            | -4.516167192 ± 0.000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 1500.02002             | 1500.02 ± 0.00390625    | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.5                   | -2.5 ± 0.00048828125    | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -69.4000015            | -69.4 ± 0.00390625      | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.21 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |  |
|-----------------------------------|------------------------|--------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -3.5                   |                          |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -49.65                 |                          |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.4                   |                          |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -36.5                  |                          |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 2500.06                |                          |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1728                   |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1760                   |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 63                     |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 66                     |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.043453              |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2945                 |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00135                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.73453                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.8417266             |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.2325                 |                          |        |  |
| Name                              | Actual Value           | Expected Value           | Result |  |
| GenFddlcCmd()                     | -0.778024733           | -0.778024749 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -3.01779294            | -3.017792967 ± 0.000009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 2500.06006             | 2500.06 ± 0.00390625     | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -3.5                   | -3.5 ± 0.00048828125     | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -49.6500015            | -49.65 ± 0.00390625      | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.22 (Repeat Count = 1)

| Name                           | Input Value            |  |  |  |
|--------------------------------|------------------------|--|--|--|
| Prev1PreAttnComp_MtrNm_M_f32   | 4.5                    |  |  |  |
| Prev1ScIDrvVel_RadpS_M_f32     | 22.54                  |  |  |  |
| Prev2PreAttnComp_MtrNm_M_f32   | 2.4                    |  |  |  |
| Prev2ScIDrvVel_RadpS_M_f32     | 11                     |  |  |  |
| ScaledDriverVel_MtrRadpS_T_f32 | -2500.08               |  |  |  |
| filtCoef_Uls_T_Str             | tgt_filtCoef_Uls_T_Str |  |  |  |

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| Name                              | Input Value  |                        |        |
|-----------------------------------|--------------|------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1776         |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1840         |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 189          |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 191          |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.05342     |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.3036       |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0004234    |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.845555     |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.5474      |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.342        |                        |        |
| Name                              | Actual Value | Expected Value         | Result |
| GenFddlcCmd()                     | 2.5159831    | 2.515983222 ± 0.000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 3.37220788   | 3.372207879 ± 0.000009 | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | -2500.08008  | -2500.08 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 4.5          | 4.5 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | 22.5400009   | 22.54 ± 0.00390625     | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.23 (Repeat Count = 1) ✓

| Name                              | Input Value            |                         |        |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -4.5                   |                         |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -48.54                 |                         |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.1                   |                         |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -38.54                 |                         |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 3500.06                |                         |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 160                    |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1920                   |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 237                    |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 239                    |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01123               |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.30564                |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00023453             |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.95464                |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.345345              |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.0504234              |                         |        |
| Name                              | Actual Value           | Expected Value          | Result |
| GenFddlcCmd()                     | -9.47003937            | -9.470039831 ± 0.000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | -10.1436405            | -10.14364099 ± 0.00009  | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | 3500.06006             | 3500.06 ± 0.00390625    | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | -4.5                   | -4.5 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | -48.5400009            | -48.54 ± 0.00390625     | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.24 (Repeat Count = 1) ✓

| Name                              | Input Value            |
|-----------------------------------|------------------------|
| Prev1PreAttnComp_MtrNm_M_f32      | 6.5                    |
| Prev1ScIDrvVel_RadpS_M_f32        | 163.65                 |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.1                    |
| Prev2ScIDrvVel_RadpS_M_f32        | 175                    |
| ScaledDriverVel_MtrRadpS_T_f32    | -3.02                  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 176                    |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2000                   |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 49                     |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 51                     |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02123               |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.31564                |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.1                    |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.05678                |

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| Name                              | Input Value |
|-----------------------------------|-------------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.53454    |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 10.6056849  |

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| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.27 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |  |
|-----------------------------------|------------------------|------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -7.5                   |                        |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 250.45                 |                        |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -7.7                   |                        |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 12917.3                |                        |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -39.07                 |                        |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 224                    |                        |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2240                   |                        |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 116                    |                        |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 118                    |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.005534              |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.25856                |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.65                   |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.3678                 |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.734                 |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.245645               |                        |        |  |
| Name                              | Actual Value           | Expected Value         | Result |  |
| GenFddlcCmd()                     | -11.8644609            | -11.86446038 ± 0.00009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -26.1836376            | -26.18363669 ± 0.00009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -39.0699997            | -39.07 ± 0.00390625    | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -7.5                   | -7.5 ± 0.00048828125   | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 250.449997             | 250.45 ± 0.00390625    | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.28 (Repeat Count = 1)

| Name                              | Input Value            |                      |        |  |
|-----------------------------------|------------------------|----------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 8.5                    |                      |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 5000.65                |                      |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 7.7                    |                      |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 0                      |                      |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 6075.09                |                      |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                      |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240                    |                      |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2320                   |                      |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 144                    |                      |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 146                    |                      |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634               |                      |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.259346               |                      |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.35                   |                      |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.4786                 |                      |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.84764               |                      |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.365                  |                      |        |  |
| Name                              | Actual Value           | Expected Value       | Result |  |
| GenFddlcCmd()                     | 452.265015             | 452.2649718 ± 0.0009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 793.012634             | 793.0125532 ± 0.0009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 6075.08984             | 6075.09 ± 0.00390625 | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 8.5                    | 8.5 ± 0.00048828125  | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 5000.6499              | 5000.65 ± 0.00390625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |



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## Test Step 2.29 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |  |
|-----------------------------------|------------------------|-------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -8.5                   |                         |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -26.65                 |                         |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -6.6                   |                         |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -10.12                 |                         |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 6.02                   |                         |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 256                    |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2400                   |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 172                    |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 174                    |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.268567               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.24                   |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.5768                 |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.000456              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.4766                 |                         |        |  |
| Name                              | Actual Value           | Expected Value          | Result |  |
| GenFddlcCmd()                     | -5.66504765            | -5.665048067 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -8.4316988             | -8.431699448 ± 0.000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 6.01999998             | 6.02 ± 0.00390625       | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -8.5                   | -8.5 ± 0.00048828125    | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -26.6499996            | -26.65 ± 0.00390625     | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.30 (Repeat Count = 1)

| Name                              | Input Value            |                          |        |  |
|-----------------------------------|------------------------|--------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 1.3                    |                          |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 18.6                   |                          |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 6.6                    |                          |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 10.25                  |                          |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -6.06                  |                          |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 272                    |                          |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2480                   |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 218                    |                          |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 220                    |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27443                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.389                  |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.65675                |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.96456               |                          |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.57686                |                          |        |  |
| Name                              | Actual Value           | Expected Value           | Result |  |
| GenFddlcCmd()                     | -0.33675155            | -0.336751733 ± 0.0000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -0.395451367           | -0.395451576 ± 0.0000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -6.05999994            | -6.06 ± 0.00390625       | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.29999995             | 1.3 ± 0.00048828125      | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 18.6000004             | 18.6 ± 0.00390625        | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.31 (Repeat Count = 1)

| Name                           | Input Value            |  |  |  |
|--------------------------------|------------------------|--|--|--|
| Prev1PreAttnComp_MtrNm_M_f32   | 1.3                    |  |  |  |
| Prev1ScIDrvVel_RadpS_M_f32     | -12917.3               |  |  |  |
| Prev2PreAttnComp_MtrNm_M_f32   | -5.5                   |  |  |  |
| Prev2ScIDrvVel_RadpS_M_f32     | -900.36                |  |  |  |
| ScaledDriverVel_MtrRadpS_T_f32 | -4.02                  |  |  |  |
| filtCoef_Uls_T_Str             | tgt_filtCoef_Uls_T_Str |  |  |  |

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| Name                              | Input Value  |                         |        |
|-----------------------------------|--------------|-------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 288          |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2560         |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 63           |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 66           |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00845     |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.000564     |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.78         |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.745        |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3453      |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.6786       |                         |        |
| Name                              | Actual Value | Expected Value          | Result |
| GenFddlcCmd()                     | 0.722379088  | 0.722378984 ± 0.0000009 | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 2.93538165   | 2.935381268 ± 0.000009  | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | -4.01999998  | -4.02 ± 0.00390625      | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.29999995   | 1.3 ± 0.00048828125     | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | -12917.2998  | -12917.3 ± 0.00390625   | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.32 (Repeat Count = 1) ✓

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 2.3                    |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 12917.3                |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.5                    |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -2000.1                |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -1.05                  |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 304                    |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2640                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 78                     |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 80                     |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00945               |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.000654               |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.02                   |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.8453                 |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.873453              |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.15645                |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | 1.61534405             | 1.615344 ± 0.000009    | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 5.30164194             | 5.301641847 ± 0.000009 | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | -1.04999995            | -1.05 ± 0.00390625     | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 2.29999995             | 2.3 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | 12917.2998             | 12917.3 ± 0.00390625   | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.33 (Repeat Count = 1) ✓

| Name                              | Input Value            |
|-----------------------------------|------------------------|
| Prev1PreAttnComp_MtrNm_M_f32      | -2.3                   |
| Prev1ScIDrvVel_RadpS_M_f32        | 0                      |
| Prev2PreAttnComp_MtrNm_M_f32      | -4.4                   |
| Prev2ScIDrvVel_RadpS_M_f32        | 3000                   |
| ScaledDriverVel_MtrRadpS_T_f32    | 2.06                   |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1760                   |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2720                   |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 106                    |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 109                    |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01324               |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.3056                 |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.32                   |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.9454                 |

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| Name                              | Input Value  |                         |        |
|-----------------------------------|--------------|-------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.534       |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.74564      |                         |        |
| Name                              | Actual Value | Expected Value          | Result |
| GenFddlcCmd()                     | -2.96688271  | -2.966882443 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -7.1653018   | -7.165300993 ± 0.000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 2.05999994   | 2.06 ± 0.00390625       | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.29999995  | -2.3 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 0            | 0 ± 0.00390625          | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.34 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 3.4                    |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -2000.02               |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 4.4                    |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -3000.4                |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -2.05                  |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1920                   |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2800                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 129                    |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 131                    |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02234               |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.004678               |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0018576              |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.04564                |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.3453                |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.84534                |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | 6.05533695             | 6.055336888 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 12.0167923             | 12.01679258 ± 0.00009  | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | -2.04999995            | -2.05 ± 0.00390625     | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 3.4000001              | 3.4 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -2000.02002            | -2000.02 ± 0.00390625  | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.35 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -3.4                   |                         |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 2000.03                |                         |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -3.3                   |                         |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 4000.6                 |                         |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -350.02                |                         |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2080                   |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2880                   |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 157                    |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 161                    |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03234               |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.04784                |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.001645               |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.14564                |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3453                |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.9345                 |                         |        |
| Name                              | Actual Value           | Expected Value          | Result |
| GenFddlcCmd()                     | -4.80776691            | -4.807766498 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -7.64464808            | -7.64464735 ± 0.000009  | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | -350.019989            | -350.02 ± 0.00390625    | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -3.4000001             | -3.4 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 2000.03003             | 2000.03 ± 0.00390625    | ✓      |

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| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.36 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -8.8                   |                         |        |
| Prev1ScDrvVel_RadpS_M_f32         | -1000.4                |                         |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.5                   |                         |        |
| Prev2ScDrvVel_RadpS_M_f32         | -7500.6                |                         |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -3.05                  |                         |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2240                   |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2960                   |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 183                    |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 185                    |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.044564              |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.32555                |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.002342               |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.2454                 |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.53453               |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.3423                 |                         |        |
| Name                              | Actual Value           | Expected Value          | Result |
| GenFddlcCmd()                     | -3.7178309             | -3.71783362 ± 0.000009  | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | -5.20090008            | -5.200903862 ± 0.000009 | ✔      |
| Prev1ScDrvVel_RadpS_M_f32         | -3.04999995            | -3.05 ± 0.00390625      | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | -8.80000019            | -8.8 ± 0.00048828125    | ✔      |
| Prev2ScDrvVel_RadpS_M_f32         | -1000.40002            | -1000.4 ± 0.00390625    | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.37 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 8.8                    |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 980.6                  |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.2                   |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 6500.85                |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 4.05                   |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2400                   |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3040                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 230                    |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 232                    |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.053534              |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330264               |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0025235              |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.3675                 |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4234                |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.13453                |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | 5.50454187             | 5.5045434 ± 0.000009   | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32      | 6.12679434             | 6.126796132 ± 0.000009 | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32        | 4.05000019             | 4.05 ± 0.00390625      | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32      | 8.80000019             | 8.8 ± 0.00048828125    | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32        | 980.599976             | 980.6 ± 0.00390625     | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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## Test Step 2.38 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |  |
|-----------------------------------|------------------------|-------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 0                      |                         |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -1000                  |                         |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 2.2                    |                         |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -5000.41               |                         |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -4.8                   |                         |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2560                   |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3120                   |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 71                     |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 74                     |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.042342              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27566                |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.001535               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.456                  |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.84564               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.42342                |                         |        |  |
| Name                              | Actual Value           | Expected Value          | Result |  |
| GenFddlcCmd()                     | -2.99402881            | -2.994028926 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -10.7953711            | -10.7953719 ± 0.00009   | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -4.80000019            | -4.8 ± 0.00390625       | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 0                      | 0 ± 0.00048828125       | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -1000                  | -1000 ± 0.00390625      | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.39 (Repeat Count = 1)

| Name                              | Input Value            |                             |        |  |
|-----------------------------------|------------------------|-----------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -5.25                  |                             |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 1500.05                |                             |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.1                   |                             |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 6000.69                |                             |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 5.9                    |                             |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                             |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2720                   |                             |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3200                   |                             |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 86                     |                             |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 88                     |                             |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.053453              |                             |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.284564               |                             |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0012342              |                             |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.56575                |                             |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.32786               |                             |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.2564                 |                             |        |  |
| Name                              | Actual Value           | Expected Value              | Result |  |
| GenFddlcCmd()                     | 4.06544733             | 4.06544767986332 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 12.1017971             | 12.1017977447094 ± 0.00009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 5.9000001              | 5.9 ± 0.00390625            | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.25                  | -5.25 ± 0.00048828125       | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 1500.05005             | 1500.05 ± 0.00390625        | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.40 (Repeat Count = 1)

| Name                           | Input Value            |  |  |  |
|--------------------------------|------------------------|--|--|--|
| Prev1PreAttnComp_MtrNm_M_f32   | 5.25                   |  |  |  |
| Prev1ScIDrvVel_RadpS_M_f32     | 2500.06                |  |  |  |
| Prev2PreAttnComp_MtrNm_M_f32   | 1.1                    |  |  |  |
| Prev2ScIDrvVel_RadpS_M_f32     | 9000.45                |  |  |  |
| ScaledDriverVel_MtrRadpS_T_f32 | 2557                   |  |  |  |
| filtCoef_Uls_T_Str             | tgt_filtCoef_Uls_T_Str |  |  |  |

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| Name                              | Input Value  |                            |        |
|-----------------------------------|--------------|----------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2880         |                            |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3280         |                            |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 114          |                            |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 116          |                            |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01324     |                            |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2956       |                            |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0006345    |                            |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.6786       |                            |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.3123      |                            |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.5564       |                            |        |
| Name                              | Actual Value | Expected Value             | Result |
| GenFddlcCmd()                     | 45.0379448   | 45.0379399696766 ± 0.00009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 99.3940811   | 99.3940744158379 ± 0.00009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 2557         | 2557 ± 0.00390625          | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.25         | 5.25 ± 0.00048828125       | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 2500.06006   | 2500.06 ± 0.00390625       | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.41 (Repeat Count = 1)

| Name                              | Input Value            |                         |        |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 4.6                    |                         |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -1500.06               |                         |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -8.8                   |                         |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -9000.11               |                         |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 1646.7                 |                         |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 3040                   |                         |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3360                   |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 136                    |                         |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 139                    |                         |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0063                |                         |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.11345                |                         |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.000234               |                         |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.7765                 |                         |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.34534               |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.73523                |                         |        |
| Name                              | Actual Value           | Expected Value          | Result |
| GenFddlcCmd()                     | -4.42373562            | -4.423735974 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -8.14731121            | -8.147312297 ± 0.000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 1646.69995             | 1646.7 ± 0.00390625     | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 4.5999999              | 4.6 ± 0.00048828125     | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -1500.06006            | -1500.06 ± 0.00390625   | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.42 (Repeat Count = 1)

| Name                              | Input Value            |  |  |
|-----------------------------------|------------------------|--|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -4.6                   |  |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 600.07                 |  |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 8.8                    |  |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 9900.65                |  |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -6.8                   |  |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |  |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1920                   |  |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3440                   |  |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 63                     |  |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 66                     |  |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745               |  |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.15645                |  |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.25                   |  |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.84564                |  |  |

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| Name                              | Input Value  |                         |        |
|-----------------------------------|--------------|-------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.4342      |                         |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.845        |                         |        |
| Name                              | Actual Value | Expected Value          | Result |
| GenFddlcCmd()                     | -1.46749699  | -1.467496866 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -5.96316242  | -5.96316187 ± 0.000009  | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | -6.80000019  | -6.8 ± 0.00390625       | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -4.59999999  | -4.6 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 600.070007   | 600.07 ± 0.00390625     | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.43 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | 5.7                    |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 5000                   |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 0                      |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 8000.65                |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 2412.05                |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2080                   |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3520                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 189                    |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 191                    |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02342               |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.001234               |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00024378             |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.94564                |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.84564               |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.93453                |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | -14.621316             | -14.62131553 ± 0.00009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | -19.5971565            | -19.59715589 ± 0.00009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 2412.05005             | 2412.05 ± 0.00390625   | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.69999981             | 5.7 ± 0.00048828125    | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 5000                   | 5000 ± 0.00390625      | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.44 (Repeat Count = 1)

| Name                              | Input Value            |                             |        |
|-----------------------------------|------------------------|-----------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -5.7                   |                             |        |
| Prev1ScIDrvVel_RadpS_M_f32        | -9000.015              |                             |        |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.25                  |                             |        |
| Prev2ScIDrvVel_RadpS_M_f32        | -6000.12               |                             |        |
| ScaledDriverVel_MtrRadpS_T_f32    | -23.02                 |                             |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                             |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2240                   |                             |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3600                   |                             |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 237                    |                             |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 239                    |                             |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03234               |                             |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0156                 |                             |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.36                   |                             |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.0674                 |                             |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.458349              |                             |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.143                  |                             |        |
| Name                              | Actual Value           | Expected Value              | Result |
| GenFddlcCmd()                     | 3.19451404             | 3.19451007405634 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 3.45061421             | 3.45061003779925 ± 0.000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | -23.0200005            | -23.02 ± 0.00390625         | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.69999981            | -5.7 ± 0.00048828125        | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -9000.01465            | -9000.015 ± 0.00390625      | ✓      |

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GenFddlcCmd



| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 2.45 (Repeat Count = 1) |                        |                             |        |  |
|-----------------------------------|------------------------|-----------------------------|--------|--|
| Name                              | Input Value            |                             |        |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 6.8                    |                             |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 600.09                 |                             |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.25                   |                             |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 9000.62                |                             |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 34.06                  |                             |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                             |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2400                   |                             |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3680                   |                             |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 230                    |                             |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 232                    |                             |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00645               |                             |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.16777                |                             |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.54                   |                             |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.14564                |                             |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.864935              |                             |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.74564                |                             |        |  |
| Name                              | Actual Value           | Expected Value              | Result |  |
| GenFddlcCmd()                     | 9.78774643             | 9.78774586664643 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 10.894187              | 10.8941867037456 ± 0.00009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 34.0600014             | 34.06 ± 0.00390625          | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 6.80000019             | 6.8 ± 0.00048828125         | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 600.090027             | 600.09 ± 0.00390625         | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 2.46 (Repeat Count = 1) |                        |                         |        |  |
|-----------------------------------|------------------------|-------------------------|--------|--|
| Name                              | Input Value            |                         |        |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 1.5                    |                         |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -400.05                |                         |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 6.8                    |                         |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -7235.12               |                         |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 45.06                  |                         |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 0                      |                         |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 0                      |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 71                     |                         |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 74                     |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.005534              |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27344                |                         |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.000534               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.3678                 |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.24234               |                         |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.54523                |                         |        |  |
| Name                              | Actual Value           | Expected Value          | Result |  |
| GenFddlcCmd()                     | -2.39375806            | -2.393758233 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | -8.28110886            | -8.281109564 ± 0.000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 45.0600014             | 45.06 ± 0.00390625      | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 1.5                    | 1.5 ± 0.00048828125     | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -400.049988            | -400.05 ± 0.00390625    | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |



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GenFddlcCmd



## Test Step 2.47 (Repeat Count = 1)

| Name                              | Input Value            |                        |        |  |
|-----------------------------------|------------------------|------------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | -1.5                   |                        |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 289.65                 |                        |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -5.2                   |                        |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 8563.3                 |                        |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -4.05                  |                        |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 17600                  |                        |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 17600                  |                        |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 86                     |                        |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 88                     |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634               |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.28546                |                        |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.14                   |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.4786                 |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789                |                        |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.757645               |                        |        |  |
| Name                              | Actual Value           | Expected Value         | Result |  |
| GenFddlcCmd()                     | 1.24506903             | 1.245069116 ± 0.000009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 3.7062521              | 3.706252252 ± 0.000009 | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -4.05000019            | -4.05 ± 0.00390625     | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.5                   | -1.5 ± 0.00048828125   | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | 289.649994             | 289.65 ± 0.00390625    | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.48 (Repeat Count = 1)

| Name                              | Input Value            |                       |        |  |
|-----------------------------------|------------------------|-----------------------|--------|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 2.5                    |                       |        |  |
| Prev1ScIDrvVel_RadpS_M_f32        | -150                   |                       |        |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 5.2                    |                       |        |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -9358.2                |                       |        |  |
| ScaledDriverVel_MtrRadpS_T_f32    | 5266.06                |                       |        |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                       |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1005                   |                       |        |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 9383                   |                       |        |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 114                    |                       |        |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 116                    |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634               |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2956                 |                       |        |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.26                   |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.5768                 |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.535                 |                       |        |  |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.4563                 |                       |        |  |
| Name                              | Actual Value           | Expected Value        | Result |  |
| GenFddlcCmd()                     | 74.4717255             | 74.47172728 ± 0.00009 | ✓      |  |
| Prev1PreAttnComp_MtrNm_M_f32      | 164.351395             | 164.3513981 ± 0.0009  | ✓      |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 5266.06006             | 5266.06 ± 0.00390625  | ✓      |  |
| Prev2PreAttnComp_MtrNm_M_f32      | 2.5                    | 2.5 ± 0.00048828125   | ✓      |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -150                   | -150 ± 0.00390625     | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.49 (Repeat Count = 1)

| Name                           | Input Value            |  |  |  |
|--------------------------------|------------------------|--|--|--|
| Prev1PreAttnComp_MtrNm_M_f32   | -2.5                   |  |  |  |
| Prev1ScIDrvVel_RadpS_M_f32     | -2341.03               |  |  |  |
| Prev2PreAttnComp_MtrNm_M_f32   | -2.3                   |  |  |  |
| Prev2ScIDrvVel_RadpS_M_f32     | 9782.2                 |  |  |  |
| ScaledDriverVel_MtrRadpS_T_f32 | 4585.02                |  |  |  |
| filtCoef_Uls_T_Str             | tgt_filtCoef_Uls_T_Str |  |  |  |

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GenFddlcCmd

| Name                              | Input Value  |                       |        |
|-----------------------------------|--------------|-----------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1616         |                       |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3680         |                       |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 0            |                       |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 0            |                       |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745     |                       |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2945       |                       |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.38         |                       |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.65675      |                       |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.78987     |                       |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.3242       |                       |        |
| Name                              | Actual Value | Expected Value        | Result |
| GenFddlcCmd()                     | 0            | 0 ± 0.000009          | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 132.005234   | 132.0052327 ± 0.0009  | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 4585.02002   | 4585.02 ± 0.00390625  | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -2.5         | -2.5 ± 0.00048828125  | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | -2341.03003  | -2341.03 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.50 (Repeat Count = 1) ✓

| Name                              | Input Value            |                        |        |
|-----------------------------------|------------------------|------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32      | -3.5                   |                        |        |
| Prev1ScIDrvVel_RadpS_M_f32        | 500.012                |                        |        |
| Prev2PreAttnComp_MtrNm_M_f32      | 2.3                    |                        |        |
| Prev2ScIDrvVel_RadpS_M_f32        | 12000                  |                        |        |
| ScaledDriverVel_MtrRadpS_T_f32    | 3.02                   |                        |        |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1632                   |                        |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3696                   |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 256                    |                        |        |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 256                    |                        |        |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00845               |                        |        |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.3036                 |                        |        |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.5                    |                        |        |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.745                  |                        |        |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.64564               |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.67452                |                        |        |
| Name                              | Actual Value           | Expected Value         | Result |
| GenFddlcCmd()                     | 4.95908308             | 4.959080803 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 4.95908308             | 4.959080803 ± 0.000009 | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32        | 3.01999998             | 3.02 ± 0.00390625      | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | -3.5                   | -3.5 ± 0.00048828125   | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32        | 500.011993             | 500.012 ± 0.00390625   | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.51 (Repeat Count = 1) ✓

| Name                              | Input Value            |  |  |
|-----------------------------------|------------------------|--|--|
| Prev1PreAttnComp_MtrNm_M_f32      | 4.5                    |  |  |
| Prev1ScIDrvVel_RadpS_M_f32        | 385.032                |  |  |
| Prev2PreAttnComp_MtrNm_M_f32      | -1.7                   |  |  |
| Prev2ScIDrvVel_RadpS_M_f32        | -10712.32              |  |  |
| ScaledDriverVel_MtrRadpS_T_f32    | -7.02                  |  |  |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |  |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1648                   |  |  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3712                   |  |  |
| t_FDD_AttenTblY_Uls_u8p8[0]       | 63                     |  |  |
| t_FDD_AttenTblY_Uls_u8p8[1]       | 66                     |  |  |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00945               |  |  |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.30564                |  |  |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.62                   |  |  |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.8453                 |  |  |

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GenFddlcCmd

| Name                              | Input Value  |                        |        |
|-----------------------------------|--------------|------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4234      |                        |        |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.77453      |                        |        |
| Name                              | Actual Value | Expected Value         | Result |
| GenFddlcCmd()                     | 8.95816231   | 8.958162049 ± 0.000009 | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32      | 36.4014206   | 36.40142039 ± 0.00009  | ✓      |
| Prev1SclDrvVel_RadpS_M_f32        | -7.01999998  | -7.02 ± 0.00390625     | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32      | 4.5          | 4.5 ± 0.00048828125    | ✓      |
| Prev2SclDrvVel_RadpS_M_f32        | 385.032013   | 385.032 ± 0.00390625   | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

# TEST DETAILS REPORT

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FrqDepDmpnInrtCmp\_Init

|             |                        |
|-------------|------------------------|
| Project     | FDD_Inertia            |
| Module      | FDD_Inertia            |
| Test Object | FrqDepDmpnInrtCmp_Init |

## Statistics

|                 |   |
|-----------------|---|
| Total Testcases | 1 |
|-----------------|---|

## Module Properties

|                        |  |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp   |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml   |
| Target Environment     | TI TMS 570 PLS UDE (Default)   |
| Kind of Test           | Unit Test  |
| Linker Options         |  |
| Source File(s)         |  |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c   |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utpl\contract -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utpl\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NtrLib\src\interpolation.c   |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utpl\contract -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utpl\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name | Text |
|------|------|
|------|------|

# TEST DETAILS REPORT

FrqDepDmpnInrtCmp\_Init

2014-09-19, 16:38:06+0530



Module 'FDD\_Inertia'

## \*\*\*\*\*Unit Test Description\*\*\*\*\*

Name of Tester: Spoorti Mali  
Code File(s) Under Test: Ap\_FrqDepDmpnInrtCmp.c  
Code File(s) Version: 13  
Module Design Document: Frequency\_Dependent\_Damping\_And\_Inertia\_Compensation\_MDD.doc  
Module Design Document Version: 18  
Data Dictionary Version: 16  
Unit Test Plan Version: 6  
Optimization Level: Level 2  
Compiler (CodeGen) Version: TMS470\_4.9.5  
Model Type: Excel Macro  
Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30  
Total FLASH Used (Bytes): 1994  
Total RAM Used (Bytes): 60  
Total CALS Used (Bytes): 328  
Special Test Requirements:  
Test Date: 09-19-2014  
Comments:

Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.

Note2: ""CBD\_Sandbox\_dbg.map"" file is embedded for reference.

Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng\_HwDeg\_M\_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp\_Per1"" function.

Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function.

Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable ""filtCoef\_Uls\_T\_Str.b0\_Uls\_f32"" is calculated as -2.74156205240179 to 0 and ""filtCoef\_Uls\_T\_Str.b1\_Uls\_f32"" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16.

Note6:In ""GenFddlcCmd"" function, return value and output variable ""Prev1PreAttnComp\_MtrNm\_M\_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp\_Per1"" so here also output variable ""Prev1PreAttnComp\_MtrNm\_M\_f32"" is going out of range.

Note 7:The range of the parameter ""VehicleSpeed\_Kph\_T\_f32"" is mentioned in MDD as 0 to 512, but at line number 437, FPM\_FloatToFixed\_m macro is used for U9P7\_T, For All Max vector of parameter ""VehicleSpeed\_Kph\_T\_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication.

Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DriverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp\_Per1"" for the variable ""Prev1PreAttnComp\_MtrNm\_M\_f32"".

\*\*\*\*\*

| Attributes            |  |
|-----------------------|--|
| Name                  | Value  |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5               |
| Float Precision       | 9  |
| InitObjDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj                                 |
| InitSrcDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src                                 |
| Linker File           | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd                        |
| Makefile Template     | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl               |
| Target Install Path   | \$(ProgramFiles)\pls\UDE 3.2   |
| Time Unit             | Cycles   |
| Timer Enabled         | false  |
| Timer Prescale        | 0  |
| Timer Resolution      | 1  |
| UDE Config File       | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File        | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 116.00 Cycles  
 TS1.2 117.00 Cycles  
 TS1.3 116.00 Cycles  
 TS1.4 117.00 Cycles  
 TS1.5 117.00 Cycles  
 TS1.6 115.00 Cycles  
 TS1.7 115.00 Cycles  
 TS1.8 117.00 Cycles  
 TS1.9 117.00 Cycles  
 TS1.10 118.00 Cycles  
 TS1.11 118.00 Cycles  
 TS1.12 115.00 Cycles  
 TS1.13 115.00 Cycles

**Description** Test Vector Description:

TS1.1 All min  
 TS1.2 All max  
 TS1.3 k\_InrtCmp\_TBarVelLPFKn\_Hz\_f32 = min  
 TS1.4 k\_InrtCmp\_TBarVelLPFKn\_Hz\_f32 = max  
 TS1.5 k\_InrtCmp\_TBarVelLPFKn\_Hz\_f32 = mid  
 TS1.6 TbarVelFiltSv\_M\_str.K = min  
 TS1.7 TbarVelFiltSv\_M\_str.K = max  
 TS1.8 TbarVelFiltSv\_M\_str.K = mid  
 TS1.9 TbarVelFiltSv\_M\_str.SV = min  
 TS1.10 TbarVelFiltSv\_M\_str.SV = max  
 TS1.11 TbarVelFiltSv\_M\_str.SV = zero  
 TS1.12 TbarVelFiltSv\_M\_str.SV = pos  
 TS1.13 TbarVelFiltSv\_M\_str.SV = neg

## Test Step 1.1 (Repeat Count = 1)

| Name                           | Input Value                          |
|--------------------------------|--------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.66669989                          |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.00125584798                        |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 0.100000001                          |
| Name                           | Expected Value                       |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                           |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                       |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.00125584798 ± 0.000125655810790826 |

## Test Step 1.2 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.66669989                         |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.715390444                        |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 100                                |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.715390444 ± 0.000125655810790826 |

## Test Step 1.3 (Repeat Count = 1)

| Name                           | Input Value                          |
|--------------------------------|--------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.25460005                           |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.374119997                          |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 0.100000001                          |
| Name                           | Expected Value                       |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                           |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                       |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.00125584798 ± 0.000125655810790826 |

## Test Step 1.4 (Repeat Count = 1)

| Name                           | Input Value    |
|--------------------------------|----------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.68739986    |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.269800007    |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 100            |
| Name                           | Expected Value |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625     |

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| Name                           | Expected Value                     |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.715390444 ± 0.000125655810790826 |

## Test Step 1.5 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5632                             |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.145229995                        |
| k_InrtCmp_TBArVelLPFKn_Hz_f32  | 50.2299995                         |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.468051612 ± 0.000125655810790826 |

## Test Step 1.6 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.55769992                         |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.00125584798                      |
| k_InrtCmp_TBArVelLPFKn_Hz_f32  | 25.2000008                         |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.271430701 ± 0.000125655810790826 |

## Test Step 1.7 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.99850011                         |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.715390444                        |
| k_InrtCmp_TBArVelLPFKn_Hz_f32  | 26                                 |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.278718382 ± 0.000125655810790826 |

## Test Step 1.8 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.12300014                        |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.587459981                        |
| k_InrtCmp_TBArVelLPFKn_Hz_f32  | 35.25                              |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.357870042 ± 0.000125655810790826 |

## Test Step 1.9 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.66669989                        |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.532140017                        |
| k_InrtCmp_TBArVelLPFKn_Hz_f32  | 84                                 |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.652007759 ± 0.000125655810790826 |

## Test Step 1.10 (Repeat Count = 1)

| Name                           | Input Value  |
|--------------------------------|--------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.66669989   |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.0147850001 |

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| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 95.0100021                         |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.696972251 ± 0.000125655810790826 |

## Test Step 1.11 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0                                  |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.0258959997                       |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 41.2000008                         |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.404131025 ± 0.000125655810790826 |

## Test Step 1.12 (Repeat Count = 1)

| Name                           | Input Value                        |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.69869995                         |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.632139981                        |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 56.3499985                         |
| Name                           | Expected Value                     |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                         |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                     |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.507428169 ± 0.000125655810790826 |

## Test Step 1.13 (Repeat Count = 1)

| Name                           | Input Value                       |
|--------------------------------|-----------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.14230013                       |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.0147850001                      |
| k_InrtCmp_TBarVelLPFKn_Hz_f32  | 63.25                             |
| Name                           | Expected Value                    |
| PreDecelGain_Uls_M_f32         | 1 ± 0.0625                        |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625                    |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.54833883 ± 0.000125655810790826 |



# TEST DETAILS REPORT

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DecelGain



|             |             |
|-------------|-------------|
| Project     | FDD_Inertia |
| Module      | FDD_Inertia |
| Test Object | DecelGain   |

## Instrumentation: Test Object Only

|                         |       |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage       | 100 % |
| Branch (C1) Coverage    | 100 % |
| MCC Coverage            | 100 % |
| MC/DC Coverage          | 100 % |

## Statistics

|                 |     |
|-----------------|-----|
| Total Testcases | 3   |
| Successful      | 3 ✓ |
| Failed          | 0   |
| Not Executed    | 0   |

# TEST DETAILS REPORT

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DecelGain



## Module Properties

|                        |   |
|------------------------|---|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp  |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml  |
| Target Environment     | TI TMS 570 PLS UDE (Default)  |
| Kind of Test           | Unit Test   |
| Linker Options         |   |
| Source File(s)         |   |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NxrLib\src\interpolation.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name                 | Text  |
|----------------------|---|
| Module 'FDD_Inertia' | <p>*****Unit Test Description*****</p> <p>Name of Tester: Spoorti Mali<br/>Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c<br/>Code File(s) Version: 13<br/>Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc<br/>Module Design Document Version: 18<br/>Data Dictionary Version: 16<br/>Unit Test Plan Version: 6<br/>Optimization Level: Level 2<br/>Compiler (CodeGen) Version: TMS470_4.9.5<br/>Model Type: Excel Macro<br/>Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30<br/>Total FLASH Used (Bytes): 1994<br/>Total RAM Used (Bytes): 60<br/>Total CALS Used (Bytes): 328<br/>Special Test Requirements:<br/>Test Date: 09-19-2014<br/>Comments:</p> <p>Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.</p> <p>Note2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.</p> <p>Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function.</p> <p>Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function.</p> <p>Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable ""filtCoef_Uls_T_Str.b0_Uls_f32"" is calculated as -2.74156205240179 to 0 and ""filtCoef_Uls_T_Str.b1_Uls_f32"" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16.</p> <p>Note6:In ""GenFddlcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range.</p> <p>Note 7:The range of the parameter ""VehicleSpeed_Kph_T_f32"" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication.</p> <p>Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DriverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"".</p> <p>*****</p> |

## Attributes

| Name                  | Value  |
|-----------------------|--|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision       | 9  |
| InitObjDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj                   |
| InitSrcDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src                   |
| Linker File           | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd          |
| Makefile Template     | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path   | \$(ProgramFiles)\pls\UDE 3.2   |
| Time Unit             | Cycles   |
| Timer Enabled         | false  |
| Timer Prescale        | 0  |
| Timer Resolution      | 1  |

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DeceIGain



| Attributes      |  |
|-----------------|--|
| Name            | Value  |
| UDE Config File | \${PROJECTROOT}\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File  | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

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DecelGain



## Test Case 1: Metrics Test

|                      |  |
|----------------------|--|
| <b>Specification</b> | Performance Metrics (With "None" Instrumentation and "WithPS" Environment)<br><br>CPU Cycles:<br><br>TS1.1 320.00 Cycles<br>TS1.2 343.00 Cycles  |
| <b>Description</b>   | Test Vector Description:<br><br>TS1.1 "Shortest Execution Path:<br>(-VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32)=True<br>(RawDecelGain_Uls_T_f32>=(D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32)=True"<br>TS1.2 "Longest Execution Path:<br>(-VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32)=False<br>(-VehicleLonAccel_KphpS_T_f32 < k_DmpGainOffThresh_KphpS_f32)=False<br>(RawDecelGain_Uls_T_f32>=(D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32)=False<br>(RawDecelGain_Uls_T_f32<=(D_2MS_SEC_F32 * -k_DmpDecelGainFSlew_UlspS_f32)+ PreDecelGain_Uls_M_f32)=False" |

### Test Step 1.1 (Repeat Count = 1)

| Name                                  | Input Value  |                  |        |  |
|---------------------------------------|--------------|------------------|--------|--|
| CRFMotorVel_MtrRadpS_T_f32            | -1118        |                  |        |  |
| PreDecelGain_Uls_M_f32                | 1            |                  |        |  |
| VehicleLonAccel_KphpS_T_f32           | -10          |                  |        |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 1            |                  |        |  |
| k_DmpDecelGain_Uls_f32                | 2            |                  |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 0            |                  |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 0            |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0            |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0            |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0            |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0            |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0            |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0            |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 8            |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 8            |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 8            |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 8            |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 8            |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 8            |                  |        |  |
| Name                                  | Actual Value | Expected Value   | Result |  |
| DecelGain()                           | 1.00199997   | 1.002 ± 0.000009 | ✓      |  |
| PreDecelGain_Uls_M_f32                | 1.00199997   | 1.002 ± 0.0625   | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

### Test Step 1.2 (Repeat Count = 1)

| Name                                  | Input Value  |                  |        |  |
|---------------------------------------|--------------|------------------|--------|--|
| CRFMotorVel_MtrRadpS_T_f32            | 500.68       |                  |        |  |
| PreDecelGain_Uls_M_f32                | 127118.835   |                  |        |  |
| VehicleLonAccel_KphpS_T_f32           | -3.1         |                  |        |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 1700.02      |                  |        |  |
| k_DmpDecelGain_Uls_f32                | 2.1          |                  |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 0            |                  |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 44.45        |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192         |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224         |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256         |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288         |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320         |                  |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352         |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448          |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 456          |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 464          |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 472          |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 480          |                  |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 488          |                  |        |  |
| Name                                  | Actual Value | Expected Value   | Result |  |
| DecelGain()                           | 127118.836   | 127118.835 ± 0.9 | ✓      |  |

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DecelGain

| Name                   | Actual Value | Expected Value      | Result |
|------------------------|--------------|---------------------|--------|
| PreDecelGain_Uls_M_f32 | 127118.836   | 127118.835 ± 0.0625 | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Case 2: Path Test

|                      |   |
|----------------------|---|
| <b>Specification</b> | Performance Metrics (With "None" Instrumentation and "WithPS" Environment)<br><br>CPU Cycles:<br><br>TS2.1 326.00 Cycles<br>TS2.2 344.00 Cycles<br>TS2.3 342.00 Cycles<br>TS2.4 320.00 Cycles   |
| <b>Description</b>   | Test Vector Description:<br><br>TS2.1 "( -VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32 ) = True<br>and<br>(RawDecelGain_Uls_T_f32>= (D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32))=True"<br>TS2.2 "( -VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32 ) = False<br>and<br>( -VehicleLonAccel_KphpS_T_f32 < k_DmpGainOffThresh_KphpS_f32)=True<br>and<br>(RawDecelGain_Uls_T_f32>= (D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32))=False<br>and<br>(RawDecelGain_Uls_T_f32<=(D_2MS_SEC_F32 * k_DmpDecelGainFSlew_UlspS_f32)+ PreDecelGain_Uls_M_f32)=True"<br>TS2.3 "( -VehicleLonAccel_KphpS_T_f32 < k_DmpGainOffThresh_KphpS_f32)=False<br>TS2.4 (RawDecelGain_Uls_T_f32>= (D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32))=True |

## Test Step 2.1 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 100.02       |                     |        |
| PreDecelGain_Uls_M_f32                | 125487.235   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | -10          |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 100.02       |                     |        |
| k_DmpDecelGain_Uls_f32                | 2.1          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 11.5         |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 5.25         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 408          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 416          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 424          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 432          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 440          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 448          |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 125487.031   | 125487.035 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 125487.031   | 125487.035 ± 0.0625 | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Step 2.2 (Repeat Count = 1)

| Name                                  | Input Value |
|---------------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32            | 200.03      |
| PreDecelGain_Uls_M_f32                | 125589.21   |
| VehicleLonAccel_KphpS_T_f32           | 10          |
| k_DmpDecelGainFSlew_UlspS_f32         | 200.05      |
| k_DmpDecelGain_Uls_f32                | 3.5         |
| k_DmpGainOffThresh_KphpS_f32          | 22.25       |
| k_DmpGainOnThresh_KphpS_f32           | 10.12       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872        |

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DecelGain

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 2408         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 2416         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 2424         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 2432         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 2440         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 2448         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 125588.813   | 125588.8099 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 125588.813   | 125588.8099 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.3 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 500.68       |                     |        |
| PreDecelGain_Uls_M_f32                | 127118.835   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | -3.1         |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1700.02      |                     |        |
| k_DmpDecelGain_Uls_f32                | 2.1          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 0            |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 44.45        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 456          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 464          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 472          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 480          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 488          |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 127118.836   | 127118.835 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 127118.836   | 127118.835 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 2.4 (Repeat Count = 1)

| Name                                  | Input Value |  |  |
|---------------------------------------|-------------|--|--|
| CRFMotorVel_MtrRadpS_T_f32            | -1118       |  |  |
| PreDecelGain_Uls_M_f32                | 1           |  |  |
| VehicleLonAccel_KphpS_T_f32           | -10         |  |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 1           |  |  |
| k_DmpDecelGain_Uls_f32                | 2           |  |  |
| k_DmpGainOffThresh_KphpS_f32          | 0           |  |  |
| k_DmpGainOnThresh_KphpS_f32           | 0           |  |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0           |  |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0           |  |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0           |  |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0           |  |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0           |  |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0           |  |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 8           |  |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 8           |  |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 8           |  |  |

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DecelGain

| Name                               | Input Value  |                  |        |
|------------------------------------|--------------|------------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8            |                  |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8            |                  |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8            |                  |        |
| Name                               | Actual Value | Expected Value   | Result |
| DecelGain()                        | 1.00199997   | 1.002 ± 0.000009 | ✓      |
| PreDecelGain_Uls_M_f32             | 1.00199997   | 1.002 ± 0.0625   | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Case 3: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS3.1 328.00 Cycles  
 TS3.2 334.00 Cycles  
 TS3.3 326.00 Cycles  
 TS3.4 345.00 Cycles  
 TS3.5 334.00 Cycles  
 TS3.6 344.00 Cycles  
 TS3.7 344.00 Cycles  
 TS3.8 337.00 Cycles  
 TS3.9 337.00 Cycles  
 TS3.10 326.00 Cycles  
 TS3.11 326.00 Cycles  
 TS3.12 326.00 Cycles  
 TS3.13 334.00 Cycles  
 TS3.14 344.00 Cycles  
 TS3.15 344.00 Cycles  
 TS3.16 345.00 Cycles  
 TS3.17 345.00 Cycles  
 TS3.18 345.00 Cycles  
 TS3.19 342.00 Cycles  
 TS3.20 345.00 Cycles  
 TS3.21 345.00 Cycles  
 TS3.22 325.00 Cycles  
 TS3.23 334.00 Cycles  
 TS3.24 345.00 Cycles  
 TS3.25 345.00 Cycles  
 TS3.26 334.00 Cycles  
 TS3.27 345.00 Cycles  
 TS3.28 345.00 Cycles  
 TS3.29 345.00 Cycles  
 TS3.30 345.00 Cycles  
 TS3.31 345.00 Cycles  
 TS3.32 345.00 Cycles  
 TS3.33 334.00 Cycles

**Description** Test Vector Description:

TS3.1 All min  
 TS3.2 All max  
 TS3.3 VehicleLonAccel\_KphpS\_T\_f32 = min  
 TS3.4 VehicleLonAccel\_KphpS\_T\_f32 = max  
 TS3.5 VehicleLonAccel\_KphpS\_T\_f32 = zero  
 TS3.6 VehicleLonAccel\_KphpS\_T\_f32 = pos  
 TS3.7 VehicleLonAccel\_KphpS\_T\_f32 = neg  
 TS3.8 CRFMotorVel1\_MtrRadpS\_T\_f32 = min  
 TS3.9 CRFMotorVel1\_MtrRadpS\_T\_f32 = max  
 TS3.10 CRFMotorVel1\_MtrRadpS\_T\_f32 = zero  
 TS3.11 CRFMotorVel1\_MtrRadpS\_T\_f32 = pos  
 TS3.12 CRFMotorVel1\_MtrRadpS\_T\_f32 = neg  
 TS3.13 k\_DmpGainOnThresh\_KphpS\_f32 = min  
 TS3.14 k\_DmpGainOnThresh\_KphpS\_f32 = max  
 TS3.15 k\_DmpGainOnThresh\_KphpS\_f32 = pos  
 TS3.16 k\_DmpDecelGain\_Uls\_f32 = min  
 TS3.17 k\_DmpDecelGain\_Uls\_f32 = max  
 TS3.18 k\_DmpDecelGain\_Uls\_f32 = pos  
 TS3.19 k\_DmpGainOffThresh\_KphpS\_f32 = min  
 TS3.20 k\_DmpGainOffThresh\_KphpS\_f32 = max  
 TS3.21 k\_DmpGainOffThresh\_KphpS\_f32 = pos  
 TS3.22 PreDecelGain\_Uls\_M\_f32 = min  
 TS3.23 PreDecelGain\_Uls\_M\_f32 = max  
 TS3.24 PreDecelGain\_Uls\_M\_f32 = pos  
 TS3.25 t\_DmpDecelGainSlewX\_MtrRadpS\_u11p5[6] = min  
 TS3.26 t\_DmpDecelGainSlewX\_MtrRadpS\_u11p5[6] = max  
 TS3.27 t\_DmpDecelGainSlewX\_MtrRadpS\_u11p5[6] = pos  
 TS3.28 t\_DmpDecelGainSlewY\_UlspS\_u13p3[6] = min  
 TS3.29 t\_DmpDecelGainSlewY\_UlspS\_u13p3[6] = max  
 TS3.30 t\_DmpDecelGainSlewY\_UlspS\_u13p3[6] = pos  
 TS3.31 k\_DmpDecelGainFSlew\_UlspS\_f32 = min  
 TS3.32 k\_DmpDecelGainFSlew\_UlspS\_f32 = max  
 TS3.33 k\_DmpDecelGainFSlew\_UlspS\_f32 = pos

## Test Step 3.1 (Repeat Count = 1)

| Name                                  | Input Value |
|---------------------------------------|-------------|
| CRFMotorVel1_MtrRadpS_T_f32           | -1118       |
| PreDecelGain_Uls_M_f32                | 1           |
| VehicleLonAccel_KphpS_T_f32           | -10         |
| k_DmpDecelGainFSlew_UlspS_f32         | 1           |
| k_DmpDecelGain_Uls_f32                | 1           |
| k_DmpGainOffThresh_KphpS_f32          | 0           |
| k_DmpGainOnThresh_KphpS_f32           | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0           |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 8           |



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DecelGain

| Name                               | Input Value  |                |        |
|------------------------------------|--------------|----------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8            |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8            |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8            |                |        |
| Name                               | Actual Value | Expected Value | Result |
| DecelGain()                        | 1            | 1 ± 0.000009   | ✓      |
| PreDecelGain_Uls_M_f32             | 1            | 1 ± 0.0625     | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.2 (Repeat Count = 1)

| Name                                  | Input Value    |                     |        |
|---------------------------------------|----------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 1118           |                     |        |
| PreDecelGain_Uls_M_f32                | 4294967295     |                     |        |
| VehicleLonAccel_KphpS_T_f32           | 10             |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 4500           |                     |        |
| k_DmpDecelGain_Uls_f32                | 10             |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 50             |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 50             |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 35776          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 35776          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 35776          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 35776          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 35776          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 35776          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 4000           |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 4000           |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 4000           |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 4000           |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 4000           |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 4000           |                     |        |
| Name                                  | Actual Value   | Expected Value      | Result |
| DecelGain()                           | 4.2949673e+009 | 4294967286 ± 9999   | ✓      |
| PreDecelGain_Uls_M_f32                | 4.2949673e+009 | 4294967286 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.3 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 100.02       |                     |        |
| PreDecelGain_Uls_M_f32                | 125487.235   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | -10          |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 100.02       |                     |        |
| k_DmpDecelGain_Uls_f32                | 2.1          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 11.5         |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 5.25         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 408          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 416          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 424          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 432          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 440          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 448          |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 125487.031   | 125487.035 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 125487.031   | 125487.035 ± 0.0625 | ✓      |

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DecelGain



| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.4 (Repeat Count = 1)

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 200.03       |                      |        |
| PreDecelGain_Uls_M_f32                | 125589.21    |                      |        |
| VehicleLonAccel_KphpS_T_f32           | 10           |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 200.05       |                      |        |
| k_DmpDecelGain_Uls_f32                | 3.5          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          | 22.25        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           | 10.12        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 2408         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 2416         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 2424         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 2432         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 2440         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 2448         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 125588.813   | 125588.8099 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 125588.813   | 125588.8099 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.5 (Repeat Count = 1)

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -100.04      |                      |        |
| PreDecelGain_Uls_M_f32                | 125691.185   |                      |        |
| VehicleLonAccel_KphpS_T_f32           | 0            |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 300.06       |                      |        |
| k_DmpDecelGain_Uls_f32                | 4.2          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          | 33.35        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           | 15.32        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 456          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 464          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 472          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 480          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 488          |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 125690.586   | 125690.5849 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 125690.586   | 125690.5849 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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## Test Step 3.6 (Repeat Count = 1)

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -200.05      |                      |        |
| PreDecelGain_Uls_M_f32                | 125793.16    |                      |        |
| VehicleLonAccel_KphpS_T_f32           | 5.3          |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 400.04       |                      |        |
| k_DmpDecelGain_Uls_f32                | 6.1          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          | 44.45        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           | 20.25        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1208         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1216         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1224         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1232         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1240         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1248         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 125792.359   | 125792.3599 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 125792.359   | 125792.3599 ± 0.0625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.7 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 300.02       |                     |        |
| PreDecelGain_Uls_M_f32                | 125895.135   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | -5.4         |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 500.02       |                     |        |
| k_DmpDecelGain_Uls_f32                | 5.2          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 8.21         |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 25.12        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1608         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1616         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1624         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1632         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1640         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1648         |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 125894.133   | 125894.135 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 125894.133   | 125894.135 ± 0.0625 | ✓      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.8 (Repeat Count = 1)

| Name                          | Input Value |
|-------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32    | -1118       |
| PreDecelGain_Uls_M_f32        | 125997.11   |
| VehicleLonAccel_KphpS_T_f32   | -2.2        |
| k_DmpDecelGainFSlew_UlspS_f32 | 600.04      |
| k_DmpDecelGain_Uls_f32        | 7.8         |
| k_DmpGainOffThresh_KphpS_f32  | 16.62       |

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| Name                                  | Input Value |
|---------------------------------------|-------------|
| k_DmpGainOnThresh_KphpS_f32           | 1.25        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 2408        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 2416        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 2424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 2432        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 2440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 2448        |

| Name                   | Actual Value | Expected Value       | Result |
|------------------------|--------------|----------------------|--------|
| DecelGain()            | 125995.906   | 125995.9099 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32 | 125995.906   | 125995.9099 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 3.9 (Repeat Count = 1)      |  |              |                      | ✓      |
|---------------------------------------|--|--------------|----------------------|--------|
| Name                                  |  | Input Value  |                      |        |
| CRFMotorVel_MtrRadpS_T_f32            |  | 1118         |                      |        |
| PreDecelGain_Uls_M_f32                |  | 126099.085   |                      |        |
| VehicleLonAccel_KphpS_T_f32           |  | -3.3         |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         |  | 700.03       |                      |        |
| k_DmpDecelGain_Uls_f32                |  | 8.7          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          |  | 24.21        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           |  | 2.58         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] |  | 30592        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] |  | 30624        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] |  | 30656        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] |  | 30688        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] |  | 30720        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] |  | 30752        |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    |  | 448          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    |  | 456          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    |  | 464          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    |  | 472          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    |  | 480          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    |  | 488          |                      |        |
| Name                                  |  | Actual Value | Expected Value       | Result |
| DecelGain()                           |  | 126097.688   | 126097.6849 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                |  | 126097.688   | 126097.6849 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 3.10 (Repeat Count = 1)     |             |
|---------------------------------------|-------------|
| Name                                  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32            | 0           |
| PreDecelGain_Uls_M_f32                | 126201.06   |
| VehicleLonAccel_KphpS_T_f32           | -4.1        |
| k_DmpDecelGainFSlew_UlspS_f32         | 800.04      |
| k_DmpDecelGain_Uls_f32                | 9.2         |
| k_DmpGainOffThresh_KphpS_f32          | 11.21       |
| k_DmpGainOnThresh_KphpS_f32           | 3.21        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 27264       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 27296       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 27328       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 27360       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 27392       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 27424       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 3608        |

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| Name                               | Input Value  |                      |        |
|------------------------------------|--------------|----------------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 3616         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 3624         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 3632         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 3640         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 3648         |                      |        |
| Name                               | Actual Value | Expected Value       | Result |
| DecelGain()                        | 126199.461   | 126199.4599 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32             | 126199.461   | 126199.4599 ± 0.0625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.11 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 100.5        |                     |        |
| PreDecelGain_Uls_M_f32                | 126303.035   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | -5.6         |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 900.02       |                     |        |
| k_DmpDecelGain_Uls_f32                | 1.1          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 22.41        |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 4.62         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 14592        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 14624        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 14656        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 14688        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 14720        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 14752        |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 288          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 296          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 304          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 312          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 320          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 328          |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 126301.234   | 126301.235 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 126301.234   | 126301.235 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.12 (Repeat Count = 1)

| Name                                  | Input Value  |                    |        |
|---------------------------------------|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -100.2       |                    |        |
| PreDecelGain_Uls_M_f32                | 126405.01    |                    |        |
| VehicleLonAccel_KphpS_T_f32           | -6.1         |                    |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1000.01      |                    |        |
| k_DmpDecelGain_Uls_f32                | 1.5          |                    |        |
| k_DmpGainOffThresh_KphpS_f32          | 33.32        |                    |        |
| k_DmpGainOnThresh_KphpS_f32           | 5.64         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 20960        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 20992        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 21024        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 21056        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 21088        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 21120        |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 384          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 392          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 400          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 408          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 416          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 424          |                    |        |
| Name                                  | Actual Value | Expected Value     | Result |
| DecelGain()                           | 126403.008   | 126403.01 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 126403.008   | 126403.01 ± 0.0625 | ✓      |

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DecelGain

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 3.13 (Repeat Count = 1)     |              |                     |        |  |
|---------------------------------------|--------------|---------------------|--------|--|
| Name                                  | Input Value  |                     |        |  |
| CRFMotorVel_MtrRadpS_T_f32            | 200.12       |                     |        |  |
| PreDecelGain_Uls_M_f32                | 126506.985   |                     |        |  |
| VehicleLonAccel_KphpS_T_f32           | 6.2          |                     |        |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 1100.02      |                     |        |  |
| k_DmpDecelGain_Uls_f32                | 1.9          |                     |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 44.45        |                     |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 0            |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 25216        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 25248        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 25280        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 25312        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 25344        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 25376        |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 456          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 464          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 472          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 480          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 488          |                     |        |  |
| Name                                  | Actual Value | Expected Value      | Result |  |
| DecelGain()                           | 126504.781   | 126504.785 ± 0.9    | ✓      |  |
| PreDecelGain_Uls_M_f32                | 126504.781   | 126504.785 ± 0.0625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 3.14 (Repeat Count = 1)     |              |                    |        |  |
|---------------------------------------|--------------|--------------------|--------|--|
| Name                                  | Input Value  |                    |        |  |
| CRFMotorVel_MtrRadpS_T_f32            | -200.31      |                    |        |  |
| PreDecelGain_Uls_M_f32                | 126608.96    |                    |        |  |
| VehicleLonAccel_KphpS_T_f32           | 7.5          |                    |        |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 1200.02      |                    |        |  |
| k_DmpDecelGain_Uls_f32                | 2.5          |                    |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 8.62         |                    |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 50           |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3264         |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3296         |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3328         |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3360         |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3392         |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3424         |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 680          |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 688          |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 696          |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 704          |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 712          |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 720          |                    |        |  |
| Name                                  | Actual Value | Expected Value     | Result |  |
| DecelGain()                           | 126606.563   | 126606.56 ± 0.9    | ✓      |  |
| PreDecelGain_Uls_M_f32                | 126606.563   | 126606.56 ± 0.0625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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DecelGain

## Test Step 3.15 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 300.52       |                     |        |
| PreDecelGain_Uls_M_f32                | 126710.935   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | 8.2          |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1300.02      |                     |        |
| k_DmpDecelGain_Uls_f32                | 5.6          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 16.21        |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 25.25        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3776         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3808         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3840         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3872         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3904         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3936         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1536         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1544         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1552         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1560         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1568         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1576         |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 126708.336   | 126708.335 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 126708.336   | 126708.335 ± 0.0625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.16 (Repeat Count = 1)

| Name                                  | Input Value  |                    |        |
|---------------------------------------|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -300.63      |                    |        |
| PreDecelGain_Uls_M_f32                | 126812.91    |                    |        |
| VehicleLonAccel_KphpS_T_f32           | 9.3          |                    |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1400.01      |                    |        |
| k_DmpDecelGain_Uls_f32                | 1            |                    |        |
| k_DmpGainOffThresh_KphpS_f32          | 24.12        |                    |        |
| k_DmpGainOnThresh_KphpS_f32           | 11.21        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5280         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5312         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5344         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5376         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5408         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5440         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1480         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1488         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1496         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1504         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1512         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1520         |                    |        |
| Name                                  | Actual Value | Expected Value     | Result |
| DecelGain()                           | 126810.109   | 126810.11 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 126810.109   | 126810.11 ± 0.0625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.17 (Repeat Count = 1)

| Name                          | Input Value |
|-------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32    | 400.75      |
| PreDecelGain_Uls_M_f32        | 126914.885  |
| VehicleLonAccel_KphpS_T_f32   | -1.2        |
| k_DmpDecelGainFSlew_UlspS_f32 | 1500.04     |
| k_DmpDecelGain_Uls_f32        | 10          |
| k_DmpGainOffThresh_KphpS_f32  | 32.41       |

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DecelGain

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| k_DmpGainOnThresh_KphpS_f32           | 22.41        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 11680        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 11712        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 11744        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 11776        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 11808        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 11840        |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1608         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1616         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1624         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1632         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1640         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1648         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 126911.883   | 126911.8849 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 126911.883   | 126911.8849 ± 0.0625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.18 (Repeat Count = 1)

| Name                                  | Input Value  |                    |        |
|---------------------------------------|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -400.52      |                    |        |
| PreDecelGain_Uls_M_f32                | 127016.86    |                    |        |
| VehicleLonAccel_KphpS_T_f32           | -2.3         |                    |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1600.02      |                    |        |
| k_DmpDecelGain_Uls_f32                | 5.25         |                    |        |
| k_DmpGainOffThresh_KphpS_f32          | 40.52        |                    |        |
| k_DmpGainOnThresh_KphpS_f32           | 33.32        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 2408         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 2416         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 2424         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 2432         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 2440         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 2448         |                    |        |
| Name                                  | Actual Value | Expected Value     | Result |
| DecelGain()                           | 127013.656   | 127013.66 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 127013.656   | 127013.66 ± 0.0625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.19 (Repeat Count = 1)

| Name                                  | Input Value |
|---------------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32            | 500.68      |
| PreDecelGain_Uls_M_f32                | 127118.835  |
| VehicleLonAccel_KphpS_T_f32           | -3.1        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1700.02     |
| k_DmpDecelGain_Uls_f32                | 2.1         |
| k_DmpGainOffThresh_KphpS_f32          | 0           |
| k_DmpGainOnThresh_KphpS_f32           | 44.45       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448         |



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DecelGain

| Name                               | Input Value  |                     |        |
|------------------------------------|--------------|---------------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488          |                     |        |
| Name                               | Actual Value | Expected Value      | Result |
| DecelGain()                        | 127118.836   | 127118.835 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32             | 127118.836   | 127118.835 ± 0.0625 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Step 3.20 (Repeat Count = 1) ✓

| Name                                  | Input Value  |                    |        |
|---------------------------------------|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 600.46       |                    |        |
| PreDecelGain_Uls_M_f32                | 127220.81    |                    |        |
| VehicleLonAccel_KphpS_T_f32           | -4.2         |                    |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1800.01      |                    |        |
| k_DmpDecelGain_Uls_f32                | 2.2          |                    |        |
| k_DmpGainOffThresh_KphpS_f32          | 50           |                    |        |
| k_DmpGainOnThresh_KphpS_f32           | 8.62         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 3608         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 3616         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 3624         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 3632         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 3640         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 3648         |                    |        |
| Name                                  | Actual Value | Expected Value     | Result |
| DecelGain()                           | 127217.211   | 127217.21 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 127217.211   | 127217.21 ± 0.0625 | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

## Test Step 3.21 (Repeat Count = 1) ✓

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 700.02       |                      |        |
| PreDecelGain_Uls_M_f32                | 127322.785   |                      |        |
| VehicleLonAccel_KphpS_T_f32           | -5.2         |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 1900.03      |                      |        |
| k_DmpDecelGain_Uls_f32                | 2.6          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          | 25.45        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           | 16.21        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 288          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 296          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 304          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 312          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 320          |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 328          |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 127318.984   | 127318.9849 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 127318.984   | 127318.9849 ± 0.0625 | ✓      |

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DecelGain

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.22 (Repeat Count = 1)

| Name                                  | Input Value  |                |        |
|---------------------------------------|--------------|----------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 800.03       |                |        |
| PreDecelGain_Uls_M_f32                | 1            |                |        |
| VehicleLonAccel_KphpS_T_f32           | -6.5         |                |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2000.06      |                |        |
| k_DmpDecelGain_Uls_f32                | 2.8          |                |        |
| k_DmpGainOffThresh_KphpS_f32          | 11.21        |                |        |
| k_DmpGainOnThresh_KphpS_f32           | 24.12        |                |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320        |                |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352        |                |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384        |                |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416        |                |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448        |                |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480        |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448          |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 456          |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 464          |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 472          |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 480          |                |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 488          |                |        |
| Name                                  | Actual Value | Expected Value | Result |
| DecelGain()                           | 1            | 1 ± 0.000009   | ✓      |
| PreDecelGain_Uls_M_f32                | 1            | 1 ± 0.0625     | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.23 (Repeat Count = 1)

| Name                                  | Input Value    |                     |        |
|---------------------------------------|----------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 900.08         |                     |        |
| PreDecelGain_Uls_M_f32                | 4294967295     |                     |        |
| VehicleLonAccel_KphpS_T_f32           | -7.6           |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2100.02        |                     |        |
| k_DmpDecelGain_Uls_f32                | 3.5            |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 22.41          |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 32.41          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720          |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752          |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 448            |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 456            |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 464            |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 472            |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 480            |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 488            |                     |        |
| Name                                  | Actual Value   | Expected Value      | Result |
| DecelGain()                           | 4.2949673e+009 | 4294967291 ± 9999   | ✓      |
| PreDecelGain_Uls_M_f32                | 4.2949673e+009 | 4294967291 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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## Test Step 3.24 (Repeat Count = 1)

| Name                                  | Input Value  |                    |        |
|---------------------------------------|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 1000.12      |                    |        |
| PreDecelGain_Uls_M_f32                | 127628.71    |                    |        |
| VehicleLonAccel_KphpS_T_f32           | -8.2         |                    |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2200.02      |                    |        |
| k_DmpDecelGain_Uls_f32                | 3.9          |                    |        |
| k_DmpGainOffThresh_KphpS_f32          | 33.32        |                    |        |
| k_DmpGainOnThresh_KphpS_f32           | 40.52        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 27264        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 27296        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 27328        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 27360        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 27392        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 27424        |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 680          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 688          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 696          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 704          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 712          |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 720          |                    |        |
| Name                                  | Actual Value | Expected Value     | Result |
| DecelGain()                           | 127624.313   | 127624.31 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 127624.313   | 127624.31 ± 0.0625 | ✓      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.25 (Repeat Count = 1)

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | 1100.26      |                      |        |
| PreDecelGain_Uls_M_f32                | 127730.685   |                      |        |
| VehicleLonAccel_KphpS_T_f32           | -9.2         |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2300.04      |                      |        |
| k_DmpDecelGain_Uls_f32                | 3.7          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          | 44.45        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           | 48.62        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0            |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0            |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0            |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0            |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0            |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0            |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1536         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1544         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1552         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1560         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1568         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1576         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 127726.086   | 127726.0849 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 127726.086   | 127726.0849 ± 0.0625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.26 (Repeat Count = 1)

| Name                          | Input Value |
|-------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32    | -500.23     |
| PreDecelGain_Uls_M_f32        | 127832.66   |
| VehicleLonAccel_KphpS_T_f32   | 1.1         |
| k_DmpDecelGainFSlew_UlspS_f32 | 2400.08     |
| k_DmpDecelGain_Uls_f32        | 4.8         |
| k_DmpGainOffThresh_KphpS_f32  | 8.62        |

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| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| k_DmpGainOnThresh_KphpS_f32           | 4.21         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 35776        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 35776        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 35776        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 35776        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 35776        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 35776        |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1480         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1488         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1496         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1504         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1512         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1520         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 127827.859   | 127827.8598 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 127827.859   | 127827.8598 ± 0.0625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.27 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |
|---------------------------------------|--------------|---------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -600.52      |                     |        |
| PreDecelGain_Uls_M_f32                | 127934.635   |                     |        |
| VehicleLonAccel_KphpS_T_f32           | 1.2          |                     |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2500.02      |                     |        |
| k_DmpDecelGain_Uls_f32                | 5.9          |                     |        |
| k_DmpGainOffThresh_KphpS_f32          | 16.21        |                     |        |
| k_DmpGainOnThresh_KphpS_f32           | 8.85         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3200         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 6400         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9600         |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 12800        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 16000        |                     |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 19200        |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1208         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1216         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1224         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1232         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1240         |                     |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1248         |                     |        |
| Name                                  | Actual Value | Expected Value      | Result |
| DecelGain()                           | 127929.633   | 127929.635 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32                | 127929.633   | 127929.635 ± 0.0625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.28 (Repeat Count = 1)

| Name                                  | Input Value |
|---------------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32            | -700.14     |
| PreDecelGain_Uls_M_f32                | 128036.61   |
| VehicleLonAccel_KphpS_T_f32           | 1.6         |
| k_DmpDecelGainFSlew_UlspS_f32         | 2600.07     |
| k_DmpDecelGain_Uls_f32                | 5.8         |
| k_DmpGainOffThresh_KphpS_f32          | 24.12       |
| k_DmpGainOnThresh_KphpS_f32           | 12.61       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 8           |

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| Name                               | Input Value  |                      |        |
|------------------------------------|--------------|----------------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 8            |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 8            |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8            |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8            |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8            |                      |        |
| Name                               | Actual Value | Expected Value       | Result |
| DecelGain()                        | 128031.406   | 128031.4099 ± 0.9    | ✔      |
| PreDecelGain_Uls_M_f32             | 128031.406   | 128031.4099 ± 0.0625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.29 (Repeat Count = 1)

| Name                                  | Input Value  |                      |        |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -800.52      |                      |        |
| PreDecelGain_Uls_M_f32                | 128138.585   |                      |        |
| VehicleLonAccel_KphpS_T_f32           | 1.8          |                      |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2700.03      |                      |        |
| k_DmpDecelGain_Uls_f32                | 6.5          |                      |        |
| k_DmpGainOffThresh_KphpS_f32          | 32.41        |                      |        |
| k_DmpGainOnThresh_KphpS_f32           | 16.21        |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320         |                      |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 4000         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 4000         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 4000         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 4000         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 4000         |                      |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 4000         |                      |        |
| Name                                  | Actual Value | Expected Value       | Result |
| DecelGain()                           | 128133.188   | 128133.1849 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 128133.188   | 128133.1849 ± 0.0625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 3.30 (Repeat Count = 1)

| Name                                  | Input Value  |                    |        |
|---------------------------------------|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32            | -900.63      |                    |        |
| PreDecelGain_Uls_M_f32                | 128240.56    |                    |        |
| VehicleLonAccel_KphpS_T_f32           | -2.1         |                    |        |
| k_DmpDecelGainFSlew_UlspS_f32         | 2800.02      |                    |        |
| k_DmpDecelGain_Uls_f32                | 6.8          |                    |        |
| k_DmpGainOffThresh_KphpS_f32          | 40.52        |                    |        |
| k_DmpGainOnThresh_KphpS_f32           | 20.63        |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920         |                    |        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 2000         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 2008         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 2016         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 2024         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 2032         |                    |        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 2040         |                    |        |
| Name                                  | Actual Value | Expected Value     | Result |
| DecelGain()                           | 128234.961   | 128234.96 ± 0.9    | ✓      |
| PreDecelGain_Uls_M_f32                | 128234.961   | 128234.96 ± 0.0625 | ✓      |

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| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 3.31 (Repeat Count = 1)     |              |                     |        |  |
|---------------------------------------|--------------|---------------------|--------|--|
| Name                                  | Input Value  |                     |        |  |
| CRFMotorVel_MtrRadpS_T_f32            | -1000.25     |                     |        |  |
| PreDecelGain_Uls_M_f32                | 128342.535   |                     |        |  |
| VehicleLonAccel_KphpS_T_f32           | -2.5         |                     |        |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 1            |                     |        |  |
| k_DmpDecelGain_Uls_f32                | 6.9          |                     |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 48.62        |                     |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 24.14        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120         |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152         |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184         |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216         |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248         |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280         |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 680          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 688          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 696          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 704          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 712          |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 720          |                     |        |  |
| Name                                  | Actual Value | Expected Value      | Result |  |
| DecelGain()                           | 128342.531   | 128342.533 ± 0.9    | ✓      |  |
| PreDecelGain_Uls_M_f32                | 128342.531   | 128342.533 ± 0.0625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 3.32 (Repeat Count = 1)     |              |                    |        |  |
|---------------------------------------|--------------|--------------------|--------|--|
| Name                                  | Input Value  |                    |        |  |
| CRFMotorVel_MtrRadpS_T_f32            | -1100.85     |                    |        |  |
| PreDecelGain_Uls_M_f32                | 128444.51    |                    |        |  |
| VehicleLonAccel_KphpS_T_f32           | -2.9         |                    |        |  |
| k_DmpDecelGainFSlew_UlspS_f32         | 4500         |                    |        |  |
| k_DmpDecelGain_Uls_f32                | 3.8          |                    |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 4.21         |                    |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 28.18        |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320        |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352        |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384        |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416        |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448        |                    |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480        |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1536         |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1544         |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1552         |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1560         |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1568         |                    |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1576         |                    |        |  |
| Name                                  | Actual Value | Expected Value     | Result |  |
| DecelGain()                           | 128435.508   | 128435.51 ± 0.9    | ✓      |  |
| PreDecelGain_Uls_M_f32                | 128435.508   | 128435.51 ± 0.0625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

# TEST DETAILS REPORT

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DecelGain

## Test Step 3.33 (Repeat Count = 1)

| Name                                  | Input Value  |                     |        |  |
|---------------------------------------|--------------|---------------------|--------|--|
| CRFMotorVel_MtrRadpS_T_f32            | 458.62       |                     |        |  |
| PreDecelGain_Uls_M_f32                | 128546.485   |                     |        |  |
| VehicleLonAccel_KphpS_T_f32           | -8.1         |                     |        |  |
| k_DmpDecelGainFleW_UlspS_f32          | 2500.02      |                     |        |  |
| k_DmpDecelGain_Uls_f32                | 6.9          |                     |        |  |
| k_DmpGainOffThresh_KphpS_f32          | 8.85         |                     |        |  |
| k_DmpGainOnThresh_KphpS_f32           | 32.25        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720        |                     |        |  |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752        |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]    | 1208         |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]    | 1216         |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]    | 1224         |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]    | 1232         |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]    | 1240         |                     |        |  |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]    | 1248         |                     |        |  |
| Name                                  | Actual Value | Expected Value      | Result |  |
| DecelGain()                           | 128541.484   | 128541.485 ± 0.9    | ✓      |  |
| PreDecelGain_Uls_M_f32                | 128541.484   | 128541.485 ± 0.0625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

# TEST DETAILS REPORT

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FilterCoefCalc



|             |                |
|-------------|----------------|
| Project     | FDD_Inertia    |
| Module      | FDD_Inertia    |
| Test Object | FilterCoefCalc |

## Instrumentation: Test Object Only

|                         |       |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Branch (C1) Coverage    | 100 % |

## Statistics

|                 |     |
|-----------------|-----|
| Total Testcases | 1   |
| Successful      | 1 ✓ |
| Failed          | 0   |
| Not Executed    | 0   |



# TEST DETAILS REPORT

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FilterCoefCalc



## Module Properties

|                        |   |
|------------------------|---|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp  |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml  |
| Target Environment     | TI TMS 570 PLS UDE (Default)  |
| Kind of Test           | Unit Test   |
| Linker Options         |   |
| Source File(s)         |   |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NxrLib\src\interpolation.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name                 | Text  |
|----------------------|---|
| Module 'FDD_Inertia' | <p>*****Unit Test Description*****</p> <p>Name of Tester: Spoorti Mali<br/>Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c<br/>Code File(s) Version: 13<br/>Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc<br/>Module Design Document Version: 18<br/>Data Dictionary Version: 16<br/>Unit Test Plan Version: 6<br/>Optimization Level: Level 2<br/>Compiler (CodeGen) Version: TMS470_4.9.5<br/>Model Type: Excel Macro<br/>Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30<br/>Total FLASH Used (Bytes): 1994<br/>Total RAM Used (Bytes): 60<br/>Total CALS Used (Bytes): 328<br/>Special Test Requirements:<br/>Test Date: 09-19-2014<br/>Comments:</p> <p>Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.</p> <p>Note2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.</p> <p>Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function.</p> <p>Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function.</p> <p>Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable ""filtCoef_Uls_T_Str.b0_Uls_f32"" is calculated as -2.74156205240179 to 0 and ""filtCoef_Uls_T_Str.b1_Uls_f32"" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16.</p> <p>Note6:In ""GenFddlcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range.</p> <p>Note 7:The range of the parameter ""VehicleSpeed_Kph_T_f32"" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication.</p> <p>Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DriverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"".</p> <p>*****</p> |

## Attributes

| Name                  | Value  |
|-----------------------|--|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision       | 9  |
| InitObjDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj                   |
| InitSrcDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src                   |
| Linker File           | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd          |
| Makefile Template     | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path   | \$(ProgramFiles)\pls\UDE 3.2   |
| Time Unit             | Cycles   |
| Timer Enabled         | false  |
| Timer Prescale        | 0  |
| Timer Resolution      | 1  |

# TEST DETAILS REPORT

FilterCoefCalc

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| Attributes      |  |
|-----------------|--|
| Name            | Value  |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File  | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

# TEST DETAILS REPORT

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FilterCoefCalc



## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 1239.00 Cycles  
TS1.2 1283.00 Cycles  
TS1.3 1285.00 Cycles  
TS1.4 1274.00 Cycles  
TS1.5 1274.00 Cycles  
TS1.6 1251.00 Cycles  
TS1.7 1285.00 Cycles  
TS1.8 1274.00 Cycles  
TS1.9 1239.00 Cycles  
TS1.10 1250.00 Cycles  
TS1.11 1663.00 Cycles  
TS1.12 1272.00 Cycles  
TS1.13 1239.00 Cycles  
TS1.14 1652.00 Cycles  
TS1.15 1272.00 Cycles  
TS1.16 1274.00 Cycles  
TS1.17 1274.00 Cycles  
TS1.18 1274.00 Cycles  
TS1.19 1274.00 Cycles  
TS1.20 1274.00 Cycles  
TS1.21 1285.00 Cycles  
TS1.22 1274.00 Cycles  
TS1.23 1362.00 Cycles  
TS1.24 1274.00 Cycles  
TS1.25 1285.00 Cycles  
TS1.26 1285.00 Cycles  
TS1.27 1285.00 Cycles  
TS1.28 1285.00 Cycles  
TS1.29 1274.00 Cycles  
TS1.30 1274.00 Cycles  
TS1.31 1342.00 Cycles  
TS1.32 1274.00 Cycles

**Description** Test Vector Description

TS1.1 All min  
TS1.2 All max  
TS1.3 ADDCoef\_MtrNmSpRad\_T\_f32 min  
TS1.4 ADDCoef\_MtrNmSpRad\_T\_f32 max  
TS1.5 ADDCoef\_MtrNmSpRad\_T\_f32 pos  
TS1.6 VehicleSpeed2\_Kph\_T\_f32 min  
TS1.7 VehicleSpeed2\_Kph\_T\_f32 max  
TS1.8 VehicleSpeed2\_Kph\_T\_f32 pos  
TS1.9 WIRCmdAmpBlnd1\_MtrNm\_T\_f32 min  
TS1.10 WIRCmdAmpBlnd1\_MtrNm\_T\_f32 max  
TS1.11 WIRCmdAmpBlnd1\_MtrNm\_T\_f32 pos  
TS1.12 t\_CmnVehSpd\_Kph\_u9p7[12] min  
TS1.13 t\_CmnVehSpd\_Kph\_u9p7[12] max  
TS1.14 t\_CmnVehSpd\_Kph\_u9p7[12] pos  
TS1.15 t2\_FDD\_FreqTbIYM1\_Hz\_u12p4[12] min  
TS1.16 t2\_FDD\_FreqTbIYM1\_Hz\_u12p4[12] max  
TS1.17 t2\_FDD\_FreqTbIYM1\_Hz\_u12p4[12] pos  
TS1.18 t2\_FDD\_FreqTbIYM2\_Hz\_u12p4[12] min  
TS1.19 t2\_FDD\_FreqTbIYM2\_Hz\_u12p4[12] max  
TS1.20 t2\_FDD\_FreqTbIYM2\_Hz\_u12p4[12] pos  
TS1.21 t\_WIRBlndTbIX\_MtrNm\_u8p8[5] min  
TS1.22 t\_WIRBlndTbIX\_MtrNm\_u8p8[5] max  
TS1.23 t\_WIRBlndTbIX\_MtrNm\_u8p8[5] pos  
TS1.24 t\_DmpFiltKpWIRBlndY\_Uls\_u2p14[5] min  
TS1.25 t\_DmpFiltKpWIRBlndY\_Uls\_u2p14[5] max  
TS1.26 t\_DmpFiltKpWIRBlndY\_Uls\_u2p14[5] pos  
TS1.27 t\_InrtCmp\_ScaleFactorTbIY\_Uls\_u9p7[12] min  
TS1.28 t\_InrtCmp\_ScaleFactorTbIY\_Uls\_u9p7[12] max  
TS1.29 t\_InrtCmp\_ScaleFactorTbIY\_Uls\_u9p7[12] pos  
TS1.30 k\_InrtCmp\_MtrInertia\_KgmSq\_f32 min  
TS1.31 k\_InrtCmp\_MtrInertia\_KgmSq\_f32 max  
TS1.32 k\_InrtCmp\_MtrInertia\_KgmSq\_f32 pos

## Test Step 1.1 (Repeat Count = 1)

| Name                             | Input Value            |
|----------------------------------|------------------------|
| ADDCoef_MtrNmSpRad_T_f32         | 0                      |
| VehicleSpeed_Kph_T_f32           | 0                      |
| WIRCmdAmpBlnd_MtrNm_T_f32        | 0                      |
| filtCoef_Uls_T_Str               | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32   | 0.00001                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]  | 16                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10] | 16                     |

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FilterCoefCalc

| Name                                   | Input Value  |                         |        |
|--|--------------|-------------------------|--------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 16           |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 16           |                         |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 0            |                         |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 0            |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 0            |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 0            |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 0            |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 0            |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 0            |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 0            |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 0            |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 0            |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 0            |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 0            |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 0            |                         |        |
| Name                                   | Actual Value | Expected Value          | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | 0            | 0 ± 0.000009            | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0            | 0 ± 0.000009            | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | 0            | 0 ± 0.000009            | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 3.94989252   | 3.949892431 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -7.99968433  | -7.999684173 ± 0.000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 4.05042315   | 4.050423396 ± 0.000009  | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

| Test Step 1.2 (Repeat Count = 1) |                        | ✓ |
|----------------------------------|------------------------|---|
| Name                             | Input Value            |   |
| ADDCoef_MtrNmSpRad_T_f32         | 0.041306               |   |
| VehicleSpeed_Kph_T_f32           | 511.9921875            |   |
| WIRCmdAmpBlnd_MtrNm_T_f32        | 8.8                    |   |
| filtCoef_Uls_T_Str               | tgt_filtCoef_Uls_T_Str |   |
| k_InrtCmp_MtrInertia_KgmSq_f32   | 0.0005                 |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]  | 1600                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]  | 1600                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]  | 1600                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]  | 1600                   |   |

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| Name                                   | Input Value  |                         |        |
|--|--------------|-------------------------|--------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 1600         |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 1600         |                         |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 32640        |                         |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 32640        |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 16384        |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 16384        |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 16384        |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 16384        |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 16384        |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 384          |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 384          |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 2048         |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 2048         |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 2048         |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 2048         |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 2048         |                         |        |
| Name                                   | Actual Value | Expected Value          | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -2.74156237  | -2.741562052 ± 0.000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.330448002  | 0.330448 ± 0.0000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | 2.41111422   | 2.411114052 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 0.552588403  | 0.552588458 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -4.8417263   | -4.841726592 ± 0.000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 10.6056852   | 10.60568495 ± 0.00009   | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |

| Test Step 1.3 (Repeat Count = 1) |             |
|----------------------------------|-------------|
| Name                             | Input Value |
| ADDCoef_MtrNmSpRad_T_f32         | 0           |
| VehicleSpeed_Kph_T_f32           | 100.02      |

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| Name                                   | Input Value            |                             |        |
|--|------------------------|-----------------------------|--------|
| WIRCmdAmpBlnd_MtrNm_T_f32              | 2.5                    |                             |        |
| filtCoef_Uls_T_Str                     | tgt_filtCoef_Uls_T_Str |                             |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         | 0.00002                |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        | 16                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        | 32                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        | 48                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        | 64                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        | 80                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        | 96                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        | 112                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        | 128                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        | 144                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        | 160                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       | 176                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 192                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 32                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 48                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 64                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 80                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 96                     |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 112                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 128                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 144                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 160                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 176                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 192                    |                             |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 208                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 128                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 256                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 384                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 512                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 640                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 768                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 896                    |                             |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 1024                   |                             |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 1152                   |                             |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 1280                   |                             |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 1408                   |                             |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 1536                   |                             |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 1638                   |                             |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 3277                   |                             |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 4915                   |                             |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 6554                   |                             |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 8192                   |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 13                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 26                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 38                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 51                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 64                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 77                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 90                     |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 102                    |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 115                    |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 128                    |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 141                    |                             |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 154                    |                             |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 282                    |                             |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 307                    |                             |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 333                    |                             |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 358                    |                             |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 384                    |                             |        |
| Name                                   | Actual Value           | Expected Value              | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.00059381465         | -0.000593815 ± 0.0000000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0                      | 0 ± 0.000009                | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | 0.00059381465          | 0.000593815 ± 0.0000000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 3.39635515             | 3.39635548 ± 0.000009       | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -7.95065212            | -7.950651978 ± 0.000009     | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 4.65299273             | 4.652992542 ± 0.000009      | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

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| Test Step 1.4 (Repeat Count = 1)       |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.041306               |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 200.06                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.5                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00003                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 32                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 48                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 64                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 48                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 64                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 224                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 2560                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 3840                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5120                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7680                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 8960                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 10240                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11520                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 12800                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 14080                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 15360                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16640                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 3277                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 9830                   |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 26                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 38                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 166                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 538                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 563                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 589                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 614                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 640                    |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.170364141           | -0.170364138 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.330448002            | 0.330448 ± 0.0000009     | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.16008386            | -0.160083862 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 3.36400986             | 3.364009947 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.94497013            | -7.944970142 ± 0.0000009 | ✓      |

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FilterCoefCalc

| Name                              | Actual Value | Expected Value         | Result |
|-----------------------------------|--------------|------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.69101954   | 4.691019911 ± 0.000009 | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

| Test Step 1.5 (Repeat Count = 1)       |                        | ✓ |
|--|------------------------|---|
| Name                                   | Input Value            |   |
| ADDCoef_MtrNmSpRad_T_f32               | 0.02                   |   |
| VehicleSpeed_Kph_T_f32                 | 300.08                 |   |
| WIRCmdAmpBlnd_MtrNm_T_f32              | 0.5                    |   |
| filtCoef_Uls_T_Str                     | tgt_filtCoef_Uls_T_Str |   |
| k_InrtCmp_MtrInertia_KgmSq_f32         | 0.00004                |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        | 48                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        | 64                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        | 80                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        | 96                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        | 112                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        | 128                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        | 144                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        | 160                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        | 176                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        | 192                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       | 208                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 224                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 64                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 80                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 96                     |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 112                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 128                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 144                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 160                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 176                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 192                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 208                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 224                    |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 240                    |   |
| t_CmnVehSpd_Kph_u9p7[0]                | 6784                   |   |
| t_CmnVehSpd_Kph_u9p7[1]                | 6912                   |   |
| t_CmnVehSpd_Kph_u9p7[2]                | 7040                   |   |
| t_CmnVehSpd_Kph_u9p7[3]                | 7168                   |   |
| t_CmnVehSpd_Kph_u9p7[4]                | 7296                   |   |
| t_CmnVehSpd_Kph_u9p7[5]                | 7424                   |   |
| t_CmnVehSpd_Kph_u9p7[6]                | 7552                   |   |
| t_CmnVehSpd_Kph_u9p7[7]                | 7680                   |   |
| t_CmnVehSpd_Kph_u9p7[8]                | 7808                   |   |
| t_CmnVehSpd_Kph_u9p7[9]                | 7936                   |   |
| t_CmnVehSpd_Kph_u9p7[10]               | 8064                   |   |
| t_CmnVehSpd_Kph_u9p7[11]               | 8192                   |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 4915                   |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 6554                   |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 8192                   |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 9830                   |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 11469                  |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 38                     |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 51                     |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 64                     |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 77                     |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 90                     |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 102                    |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 115                    |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 128                    |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 141                    |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 154                    |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 166                    |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 179                    |   |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 794                    |   |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 819                    |   |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 845                    |   |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 870                    |   |



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FilterCoefCalc

| Name                              | Input Value   |                           |        |
|-----------------------------------|---------------|---------------------------|--------|
| t_WIRBlndTblX_MtrNm_u8p8[4]       | 896           |                           |        |
| Name                              | Actual Value  | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0846711174 | -0.084671116 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.1599999996  | 0.16 ± 0.0000009          | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0753288791 | -0.075328884 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.31349587    | 3.313495926 ± 0.000009    | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.9354167    | -7.935416577 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.75108767    | 4.751087497 ± 0.000009    | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |

| Test Step 1.6 (Repeat Count = 1)      |                        |
|---------------------------------------|------------------------|
| Name                                  | Input Value            |
| ADDCoef_MtrNmSpRad_T_f32              | 0.001                  |
| VehicleSpeed_Kph_T_f32                | 0                      |
| WIRCmdAmpBlnd_MtrNm_T_f32             | 6.5                    |
| filtCoef_Uls_T_Str                    | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32        | 0.00005                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]       | 64                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]       | 80                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]       | 96                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]       | 112                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]       | 128                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]       | 144                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]       | 160                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]       | 176                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]       | 192                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]       | 208                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]      | 224                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]      | 240                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]       | 80                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]       | 96                     |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]       | 112                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]       | 128                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]       | 144                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]       | 160                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]       | 176                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]       | 192                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]       | 208                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]       | 224                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]      | 240                    |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]      | 256                    |
| t_CmnVehSpd_Kph_u9p7[0]               | 128                    |
| t_CmnVehSpd_Kph_u9p7[1]               | 256                    |
| t_CmnVehSpd_Kph_u9p7[2]               | 384                    |
| t_CmnVehSpd_Kph_u9p7[3]               | 512                    |
| t_CmnVehSpd_Kph_u9p7[4]               | 640                    |
| t_CmnVehSpd_Kph_u9p7[5]               | 768                    |
| t_CmnVehSpd_Kph_u9p7[6]               | 896                    |
| t_CmnVehSpd_Kph_u9p7[7]               | 1024                   |
| t_CmnVehSpd_Kph_u9p7[8]               | 1152                   |
| t_CmnVehSpd_Kph_u9p7[9]               | 1280                   |
| t_CmnVehSpd_Kph_u9p7[10]              | 1408                   |
| t_CmnVehSpd_Kph_u9p7[11]              | 1536                   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]      | 6554                   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]      | 8192                   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]      | 9830                   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]      | 11469                  |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]      | 13107                  |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0] | 51                     |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1] | 64                     |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2] | 77                     |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3] | 90                     |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4] | 102                    |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5] | 115                    |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6] | 128                    |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7] | 141                    |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8] | 154                    |

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FilterCoefCalc

| Name                                   | Input Value |
|--|-------------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]  | 166         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 179         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 192         |
| t_WIRBlndTblX_MtrNm_u8p8[0]            | 1050        |
| t_WIRBlndTblX_MtrNm_u8p8[1]            | 1075        |
| t_WIRBlndTblX_MtrNm_u8p8[2]            | 1101        |
| t_WIRBlndTblX_MtrNm_u8p8[3]            | 1126        |
| t_WIRBlndTblX_MtrNm_u8p8[4]            | 1152        |

| Name                              | Actual Value   | Expected Value             | Result |
|-----------------------------------|----------------|----------------------------|--------|
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00400001789 | -0.004000018 ± 0.000000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.00800000038  | 0.008 ± 0.000000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00399998249 | -0.003999982 ± 0.000000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.76236439     | 3.76236461 ± 0.000009      | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.99272346    | -7.992723375 ± 0.000009    | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.24491215     | 4.244912015 ± 0.000009     | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

| Test Step 1.7 (Repeat Count = 1)      |                        |  | ✓ |
|---------------------------------------|------------------------|--|---|
| Name                                  | Input Value            |  |   |
| ADDCoef_MtrNmSpRad_T_f32              | 0.002                  |  |   |
| VehicleSpeed_Kph_T_f32                | 511.9921875            |  |   |
| WIRCmdAmpBlnd_MtrNm_T_f32             | 5.5                    |  |   |
| filtCoef_Uls_T_Str                    | tgt_filtCoef_Uls_T_Str |  |   |
| k_InrtCmp_MtrInertia_KgmSq_f32        | 0.00006                |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0]       | 80                     |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1]       | 96                     |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2]       | 112                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3]       | 128                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4]       | 144                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5]       | 160                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6]       | 176                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7]       | 192                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8]       | 208                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9]       | 224                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10]      | 240                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11]      | 256                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0]       | 96                     |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1]       | 112                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2]       | 128                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3]       | 144                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4]       | 160                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5]       | 176                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6]       | 192                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7]       | 208                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8]       | 224                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9]       | 240                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10]      | 256                    |  |   |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11]      | 272                    |  |   |
| t_CmnVehSpd_Kph_u9p7[0]               | 2560                   |  |   |
| t_CmnVehSpd_Kph_u9p7[1]               | 3840                   |  |   |
| t_CmnVehSpd_Kph_u9p7[2]               | 5120                   |  |   |
| t_CmnVehSpd_Kph_u9p7[3]               | 6400                   |  |   |
| t_CmnVehSpd_Kph_u9p7[4]               | 7680                   |  |   |
| t_CmnVehSpd_Kph_u9p7[5]               | 8960                   |  |   |
| t_CmnVehSpd_Kph_u9p7[6]               | 10240                  |  |   |
| t_CmnVehSpd_Kph_u9p7[7]               | 11520                  |  |   |
| t_CmnVehSpd_Kph_u9p7[8]               | 12800                  |  |   |
| t_CmnVehSpd_Kph_u9p7[9]               | 14080                  |  |   |
| t_CmnVehSpd_Kph_u9p7[10]              | 15360                  |  |   |
| t_CmnVehSpd_Kph_u9p7[11]              | 16640                  |  |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]      | 8192                   |  |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]      | 9830                   |  |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]      | 11469                  |  |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]      | 13107                  |  |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]      | 14746                  |  |   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 64                     |  |   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 77                     |  |   |

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| Name                                   | Input Value |
|--|-------------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]  | 90          |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]  | 102         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]  | 115         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]  | 128         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]  | 141         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]  | 154         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]  | 166         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]  | 179         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 192         |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 205         |
| t_WIRBlndTblX_MtrNm_u8p8[0]            | 1306        |
| t_WIRBlndTblX_MtrNm_u8p8[1]            | 1331        |
| t_WIRBlndTblX_MtrNm_u8p8[2]            | 1357        |
| t_WIRBlndTblX_MtrNm_u8p8[3]            | 1382        |
| t_WIRBlndTblX_MtrNm_u8p8[4]            | 1408        |

| Name                              | Actual Value   | Expected Value             | Result |
|-----------------------------------|----------------|----------------------------|--------|
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0128454715  | -0.012845471 ± 0.00000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0160000008   | 0.016 ± 0.00000009         | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00315452972 | -0.003154529 ± 0.000000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.1956141      | 3.195613973 ± 0.000009     | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.90979624    | -7.909796293 ± 0.000009    | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.89459038     | 4.894589734 ± 0.000009     | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |

| Test Step 1.8 (Repeat Count = 1) |                        |
|----------------------------------|------------------------|
| Name                             | Input Value            |
| ADDCoef_MtrNmSpRad_T_f32         | 0.003                  |
| VehicleSpeed_Kph_T_f32           | 255.25                 |
| WIRCmdAmpBlnd_MtrNm_T_f32        | 3.6                    |
| filtCoef_Uls_T_Str               | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32   | 0.00007                |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0]  | 96                     |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1]  | 112                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2]  | 128                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3]  | 144                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4]  | 160                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5]  | 176                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6]  | 192                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7]  | 208                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8]  | 224                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9]  | 240                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 256                    |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 272                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0]  | 336                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1]  | 352                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2]  | 368                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3]  | 384                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4]  | 400                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5]  | 416                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6]  | 432                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7]  | 448                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8]  | 464                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9]  | 480                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 496                    |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 512                    |
| t_CmnVehSpd_Kph_u9p7[0]          | 12800                  |
| t_CmnVehSpd_Kph_u9p7[1]          | 12928                  |
| t_CmnVehSpd_Kph_u9p7[2]          | 13056                  |
| t_CmnVehSpd_Kph_u9p7[3]          | 13184                  |
| t_CmnVehSpd_Kph_u9p7[4]          | 13312                  |
| t_CmnVehSpd_Kph_u9p7[5]          | 13440                  |
| t_CmnVehSpd_Kph_u9p7[6]          | 13568                  |
| t_CmnVehSpd_Kph_u9p7[7]          | 13696                  |
| t_CmnVehSpd_Kph_u9p7[8]          | 13824                  |
| t_CmnVehSpd_Kph_u9p7[9]          | 13952                  |
| t_CmnVehSpd_Kph_u9p7[10]         | 14080                  |
| t_CmnVehSpd_Kph_u9p7[11]         | 14208                  |

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FilterCoefCalc

| Name                                   | Input Value    |                                    |        |
|--|----------------|------------------------------------|--------|
| t_DmpFiltKpWIRBndY_Uls_u2p14[0]        | 1638           |                                    |        |
| t_DmpFiltKpWIRBndY_Uls_u2p14[1]        | 3277           |                                    |        |
| t_DmpFiltKpWIRBndY_Uls_u2p14[2]        | 4915           |                                    |        |
| t_DmpFiltKpWIRBndY_Uls_u2p14[3]        | 6554           |                                    |        |
| t_DmpFiltKpWIRBndY_Uls_u2p14[4]        | 8192           |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[0]  | 154            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[1]  | 166            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[2]  | 179            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[3]  | 192            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[4]  | 205            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[5]  | 218            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[6]  | 230            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[7]  | 243            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[8]  | 256            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[9]  | 269            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[10] | 282            |                                    |        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[11] | 294            |                                    |        |
| t_WIRBndTblyX_MtrNm_u8p8[0]            | 1562           |                                    |        |
| t_WIRBndTblyX_MtrNm_u8p8[1]            | 1587           |                                    |        |
| t_WIRBndTblyX_MtrNm_u8p8[2]            | 1613           |                                    |        |
| t_WIRBndTblyX_MtrNm_u8p8[3]            | 1638           |                                    |        |
| t_WIRBndTblyX_MtrNm_u8p8[4]            | 1664           |                                    |        |
| Name                                   | Actual Value   | Expected Value                     | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.022498928   | -0.0224989261685139 ± 0.00000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.0240000002   | 0.024 ± 0.00000009                 | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | -0.00150107313 | -0.00150107383148608 ± 0.000000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 3.12415075     | 3.12415079635252 ± 0.000009        | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -7.89191246    | -7.89191237196188 ± 0.000009       | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 4.98393726     | 4.98393683168561 ± 0.000009        | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

| Test Step 1.9 (Repeat Count = 1) |                        |  | ✔ |
|----------------------------------|------------------------|--|---|
| Name                             | Input Value            |  |   |
| ADDCoef_MtrNmSpRad_T_f32         | 0.004                  |  |   |
| VehicleSpeed_Kph_T_f32           | 16.25                  |  |   |
| WIRCmdAmpBlnd_MtrNm_T_f32        | 0                      |  |   |
| filtCoef_Uls_T_Str               | tgt_filtCoef_Uls_T_Str |  |   |
| k_InrtCmp_MtrInertia_KgmSq_f32   | 0.00008                |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][0]  | 336                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][1]  | 352                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][2]  | 368                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][3]  | 384                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][4]  | 400                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][5]  | 416                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][6]  | 432                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][7]  | 448                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][8]  | 464                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][9]  | 480                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][10] | 496                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[0][11] | 512                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][0]  | 656                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][1]  | 672                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][2]  | 688                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][3]  | 704                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][4]  | 720                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][5]  | 736                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][6]  | 752                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][7]  | 768                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][8]  | 784                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][9]  | 800                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][10] | 816                    |  |   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][11] | 832                    |  |   |
| t_CmnVehSpd_Kph_u9p7[0]          | 15488                  |  |   |
| t_CmnVehSpd_Kph_u9p7[1]          | 15616                  |  |   |
| t_CmnVehSpd_Kph_u9p7[2]          | 15744                  |  |   |
| t_CmnVehSpd_Kph_u9p7[3]          | 15872                  |  |   |
| t_CmnVehSpd_Kph_u9p7[4]          | 16000                  |  |   |

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| Name                                   | Input Value |
|--|-------------|
| t_CmnVehSpd_Kph_u9p7[5]                | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]               | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]               | 16896       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 9830        |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[0]  | 179         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[1]  | 192         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[2]  | 205         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[3]  | 218         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[4]  | 230         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[5]  | 243         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[6]  | 256         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[7]  | 269         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[8]  | 282         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[9]  | 294         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[10] | 307         |
| t_InrtCmp_ScaleFactorTbly_Uls_u9p7[11] | 320         |
| t_WIRBlndTbly_MtrNm_u8p8[0]            | 1766        |
| t_WIRBlndTbly_MtrNm_u8p8[1]            | 1792        |
| t_WIRBlndTbly_MtrNm_u8p8[2]            | 1818        |
| t_WIRBlndTbly_MtrNm_u8p8[3]            | 1843        |
| t_WIRBlndTbly_MtrNm_u8p8[4]            | 1869        |

| Name                              | Actual Value   | Expected Value            | Result |
|-----------------------------------|----------------|---------------------------|--------|
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0280437507  | -0.028043747 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0320000015   | 0.032 ± 0.00000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00395625085 | -0.003956253 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.84204841     | 2.842048638 ± 0.000009    | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.8026042     | -7.802604057 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.35534716     | 5.355347305 ± 0.000009    | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |

| Test Step 1.10 (Repeat Count = 1) |                        |
|-----------------------------------|------------------------|
| Name                              | Input Value            |
| ADDCoef_MtrNmSpRad_T_f32          | 0.005                  |
| VehicleSpeed_Kph_T_f32            | 32.28                  |
| WIRCmdAmpBlnd_MtrNm_T_f32         | 8.8                    |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32    | 0.00009                |
| t2_FDD_FreqTblyM_Hz_u12p4[0][0]   | 656                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][1]   | 672                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][2]   | 688                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][3]   | 704                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][4]   | 720                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][5]   | 736                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][6]   | 752                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][7]   | 768                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][8]   | 784                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][9]   | 800                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][10]  | 816                    |
| t2_FDD_FreqTblyM_Hz_u12p4[0][11]  | 832                    |
| t2_FDD_FreqTblyM_Hz_u12p4[1][0]   | 1296                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][1]   | 1312                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][2]   | 1328                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][3]   | 1344                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][4]   | 1360                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][5]   | 1376                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][6]   | 1392                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][7]   | 1408                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][8]   | 1424                   |
| t2_FDD_FreqTblyM_Hz_u12p4[1][9]   | 1440                   |

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| Name                                   | Input Value   |                           |        |
|--|---------------|---------------------------|--------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 1456          |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 1472          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 10368         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 10496         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 10624         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 10752         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 10880         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 11008         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 11136         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 11264         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 11392         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 11520         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 11648         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 11776         |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 4915          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 6554          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 8192          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 9830          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 11469         |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 141           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 154           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 166           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 179           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 192           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 205           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 218           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 230           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 243           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 256           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 269           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 282           |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 410           |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 435           |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 461           |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 486           |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 512           |                           |        |
| Name                                   | Actual Value  | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.0954187065 | -0.095418708 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.0399999991  | 0.04 ± 0.00000009         | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | 0.0554187112  | 0.055418708 ± 0.00000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 1.28349459    | 1.283494792 ± 0.000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -6.49632454   | -6.496324749 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 8.22018147    | 8.220180459 ± 0.000009    | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |

| Test Step 1.11 (Repeat Count = 1) |                        |
|-----------------------------------|------------------------|
| Name                              | Input Value            |
| ADDCoef_MtrNmSpRad_T_f32          | 0.006                  |
| VehicleSpeed_Kph_T_f32            | 48.52                  |
| WIRCmdAmpBlnd_MtrNm_T_f32         | 5.6                    |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32    | 0.0001                 |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]   | 1296                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]   | 1312                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]   | 1328                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]   | 1344                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]   | 1360                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]   | 1376                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]   | 1392                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]   | 1408                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]   | 1424                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]   | 1440                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]  | 1456                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]  | 1472                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]   | 1136                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]   | 1152                   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]   | 1168                   |

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| Name                                   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 1184         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 1200         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 1216         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 1232         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 1248         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 1264         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 1280         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 1296         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 1312         |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 5248         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 5376         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 5504         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 5632         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 5760         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 5888         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 6016         |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 6144         |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 6272         |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 6400         |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 6528         |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 6656         |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 6554         |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 8192         |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 9830         |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 11469        |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 13107        |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 166          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 179          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 192          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 205          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 218          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 230          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 243          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 256          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 269          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 282          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 294          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 307          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 666          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 691          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 717          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 742          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 768          |                          |        |
| Name                                   | Actual Value | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.246170521 | -0.246170482 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.0480000004 | 0.048 ± 0.00000009       | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | 0.198170513  | 0.198170482 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 0.976945579  | 0.976945693 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -5.9533534   | -5.953353668 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 9.06970024   | 9.06970064 ± 0.000009    | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

| Test Step 1.12 (Repeat Count = 1) |                        | ✓ |
|-----------------------------------|------------------------|---|
| Name                              | Input Value            |   |
| ADDCoef_MtrNmSpRad_T_f32          | 0.007                  |   |
| VehicleSpeed_Kph_T_f32            | 64.95                  |   |
| WIRCmdAmpBlnd_MtrNm_T_f32         | 1.1                    |   |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |   |
| k_InrtCmp_MtrInertia_KgmSq_f32    | 0.00011                |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]   | 1136                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]   | 1152                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]   | 1168                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]   | 1184                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]   | 1200                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]   | 1216                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]   | 1232                   |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]   | 1248                   |   |

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| Name                                   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        | 1264         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        | 1280         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       | 1296         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 1312         |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 176          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 192          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 208          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 224          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 240          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 256          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 272          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 288          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 304          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 320          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 336          |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 352          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 0            |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 0            |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 8192         |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 9830         |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 11469        |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 13107        |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 14746        |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 205          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 218          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 230          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 243          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 256          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 269          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 282          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 294          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 307          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 320          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 333          |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 346          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 922          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 947          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 973          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 998          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 1024         |                          |        |
| Name                                   | Actual Value | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.170547396 | -0.170547388 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.0560000017 | 0.056 ± 0.00000009       | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | 0.114547402  | 0.114547388 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 1.81319332   | 1.813193477 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -7.14600277  | -7.14600287 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 7.04080439   | 7.040803652 ± 0.000009   | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

| Test Step 1.13 (Repeat Count = 1) |                        | ✓ |
|-----------------------------------|------------------------|---|
| Name                              | Input Value            |   |
| ADDCoef_MtrNmSpRad_T_f32          | 0.008                  |   |
| VehicleSpeed_Kph_T_f32            | 80.35                  |   |
| WIRCmdAmpBlnd_MtrNm_T_f32         | 1.2                    |   |
| filtCoef_Uls_T_Str                | tgt_filtCoef_Uls_T_Str |   |
| k_InrtCmp_MtrInertia_KgmSq_f32    | 0.00012                |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]   | 176                    |   |



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| Name                                   | Input Value   |                           |        |
|--|---------------|---------------------------|--------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        | 192           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        | 208           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        | 224           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        | 240           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        | 256           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        | 272           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        | 288           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        | 304           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        | 320           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       | 336           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 352           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 496           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 512           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 528           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 544           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 560           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 576           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 592           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 608           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 624           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 640           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 656           |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 672           |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               | 32640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               | 32640         |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 1638          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 3277          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 4915          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 6554          |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 8192          |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 218           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 230           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 243           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 256           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 269           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 282           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 294           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 307           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 320           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 333           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 346           |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 358           |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 1178          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 1203          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 1229          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 1254          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 1280          |                           |        |
| Name                                   | Actual Value  | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.0392927453 | -0.039292744 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.0640000003  | 0.064 ± 0.00000009        | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | -0.0247072577 | -0.024707256 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 3.37325883    | 3.373258677 ± 0.000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -7.94662905   | -7.946629189 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 4.68011236    | 4.680112134 ± 0.000009    | ✔      |

| T                          |       |                            |       |        | ✓ |
|----------------------------|-------|----------------------------|-------|--------|---|
| Actual Function            | Count | Expected Function          | Count | Result |   |
| InplVarXY_u16_u16Xu16Y_Cnt | 4     | InplVarXY_u16_u16Xu16Y_Cnt | 4     | ✓      |   |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.14 (Repeat Count = 1)      |  |                        |                           | ✔      |
|--|--|------------------------|---------------------------|--------|
| Name                                   |  | Input Value            |                           |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.009                  |                           |        |
| VehicleSpeed_Kph_T_f32                 |  | 96.62                  |                           |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.3                    |                           |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                           |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00013                |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 496                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 512                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 528                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 544                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 560                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 576                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 592                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 608                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 624                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 640                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 656                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 672                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 64                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 80                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 96                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 112                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 128                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 144                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 160                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 176                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 192                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 208                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 224                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 240                    |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 2560                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 3840                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5120                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 6400                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7680                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 8960                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 10240                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11520                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 12800                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 14080                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 15360                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16640                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 3277                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 4915                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 6554                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 8192                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 9830                   |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 13                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 26                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 38                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 51                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 64                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 77                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 90                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 102                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 115                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 128                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 141                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 154                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1434                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1459                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1485                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1510                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1536                   |                           |        |
| Name                                   |  | Actual Value           | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.0544182248          | -0.054418228 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.0719999969           | 0.072 ± 0.00000009        | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0175817721          | -0.017581772 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.50426316             | 2.504263453 ± 0.000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.6513648             | -7.651364918 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 5.84437227             | 5.844371629 ± 0.000009    | ✔      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.15 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.01                   |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 112.41                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.4                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00014                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 16                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 240                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 256                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 6784                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 6912                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 7040                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 7168                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7296                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 7424                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 7552                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 7680                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 7808                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 7936                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 8064                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 11469                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 26                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 38                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 166                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1690                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1715                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1741                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1766                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1792                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.0412790775          | -0.04127908 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.0799999982           | 0.08 ± 0.00000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0387209207          | -0.03872092 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 3.72832537             | 3.728325621 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.99044704            | -7.990446859 ± 0.000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 4.28122759             | 4.28122752 ± 0.000009    | ✓      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.16 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.011                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 128.56                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.5                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00015                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 240                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 256                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 272                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 128                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 256                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 384                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 512                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 640                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 768                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 896                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 1024                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 1152                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 1280                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 1408                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 1536                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 11469                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 13107                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 38                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 179                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1894                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1920                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1946                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1971                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1997                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.199160993           | -0.199160956 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.0879999995           | 0.088 ± 0.00000009       | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.111160994            | 0.111160956 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 1.34697342             | 1.346973575 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -6.59078789            | -6.590788107 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 8.06223869             | 8.062238318 ± 0.0000009  | ✓      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.17 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.012                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 144.52                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.6                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00016                |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][0]        |  | 816                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][1]        |  | 832                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][2]        |  | 848                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][3]        |  | 864                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][4]        |  | 880                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][5]        |  | 896                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][6]        |  | 912                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][7]        |  | 928                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][8]        |  | 944                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][9]        |  | 960                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][10]       |  | 976                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[0][11]       |  | 992                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][0]        |  | 656                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][1]        |  | 672                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][2]        |  | 688                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][3]        |  | 704                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][4]        |  | 720                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][5]        |  | 736                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][6]        |  | 752                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][7]        |  | 768                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][8]        |  | 784                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][9]        |  | 800                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][10]       |  | 816                    |                          |        |
| t2_FDD_FreqTbLYM_Hz_u12p4[1][11]       |  | 832                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 2560                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 3840                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5120                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7680                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 8960                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 10240                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11520                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 12800                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 14080                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 15360                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16640                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 11469                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 13107                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 14746                  |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[0]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[1]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[2]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[3]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[4]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[5]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[6]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[7]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[8]  |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[9]  |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[10] |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbLY_Uls_u9p7[11] |  | 192                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 794                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 819                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 845                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 870                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 896                    |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.17973122            | -0.179731222 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.0960000008           | 0.096 ± 0.00000009       | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.0837312266           | 0.083731222 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 1.64792883             | 1.647929015 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -6.97387695            | -6.97387697 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 7.37819529             | 7.378194015 ± 0.000009   | ✓      |



# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.18 (Repeat Count = 1)      |  |                        |                           | ✓      |
|--|--|------------------------|---------------------------|--------|
| Name                                   |  | Input Value            |                           |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.013                  |                           |        |
| VehicleSpeed_Kph_T_f32                 |  | 160.63                 |                           |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.7                    |                           |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                           |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.0003                 |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 32                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 48                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 64                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 80                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 96                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 112                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 128                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 144                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 160                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 176                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 192                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 16                     |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 6784                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 6912                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 7040                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 7168                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7296                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 7424                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 7552                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 7680                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 7808                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 7936                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 8064                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 8192                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 1638                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 3277                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 4915                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 6554                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 8192                   |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 64                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 77                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 90                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 102                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 115                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 128                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 141                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 154                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 166                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 179                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 192                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 205                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1050                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1075                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1101                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1126                   |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1152                   |                           |        |
| Name                                   |  | Actual Value           | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.0631598011          | -0.063159799 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.104000002            | 0.104 ± 0.0000009         | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0408402011          | -0.040840201 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 3.47085524             | 3.47085539 ± 0.000009     | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.96247482            | -7.962474705 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 4.56666946             | 4.566669905 ± 0.000009    | ✓      |



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# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.19 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.014                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 176.85                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.8                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00031                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 32                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 48                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 64                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 1600                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 1600                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 128                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 256                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 384                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 512                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 640                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 768                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 896                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 1024                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 1152                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 1280                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 1408                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 1536                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 3277                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 9830                   |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 192                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 205                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 218                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 230                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 243                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 256                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 269                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 282                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 294                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1306                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1331                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1357                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1382                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1408                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.173795044           | -0.173795005 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.112000003            | 0.112 ± 0.0000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.0617950335           | 0.061795005 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.61782336             | 2.617823645 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.70810461            | -7.708104611 ± 0.000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 5.67407131             | 5.674071744 ± 0.000009   | ✓      |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530

FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530

FilterCoefCalc



| Test Step 1.20 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.015                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 192.52                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 1.9                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00032                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 48                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 64                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 656                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 672                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 688                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 704                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 720                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 736                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 752                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 768                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 784                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 800                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 816                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 832                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 2560                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 3840                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5120                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7680                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 8960                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 10240                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11520                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 12800                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 14080                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 15360                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16640                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 11469                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 192                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 205                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 218                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 230                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 243                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 256                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 269                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 282                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 294                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 307                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 320                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1562                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1587                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1613                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1638                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1664                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.155867472           | -0.155867459 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.119999997            | 0.12 ± 0.0000009         | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.0358674712           | 0.035867459 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.82515574             | 2.825155925 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.79624844            | -7.796248275 ± 0.000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 5.37859583             | 5.3785958 ± 0.000009     | ✓      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.21 (Repeat Count = 1)      |  |                        |                           | ✓      |
|--|--|------------------------|---------------------------|--------|
| Name                                   |  | Input Value            |                           |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.016                  |                           |        |
| VehicleSpeed_Kph_T_f32                 |  | 208.12                 |                           |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 2.2                    |                           |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                           |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00033                |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 64                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 80                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 96                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 112                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 128                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 144                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 160                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 176                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 192                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 208                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 224                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 240                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 16                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 32                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 48                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 64                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 80                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 96                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 112                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 128                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 144                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 160                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 176                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 192                    |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 12800                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 12928                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 13056                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 13184                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 13312                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 13440                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 13568                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 13696                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 13824                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 13952                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 14080                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 14208                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 6554                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 8192                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 9830                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 11469                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 13107                  |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 141                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 154                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 166                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 179                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 192                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 205                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 218                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 230                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 243                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 256                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 269                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 282                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 0                      |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 0                      |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 0                      |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 0                      |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 0                      |                           |        |
| Name                                   |  | Actual Value           | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.0883268192          | -0.088326814 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.128000006            | 0.128 ± 0.0000009         | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0396731868          | -0.039673186 ± 0.00000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 3.39172339             | 3.3917236 ± 0.000009      | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.94985914            | -7.94985896 ± 0.000009    | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 4.65841722             | 4.65841744 ± 0.000009     | ✓      |



# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530



FilterCoefCalc

| Test Step 1.22 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.017                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 224.01                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 2.1                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00034                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 240                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 256                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 32                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 48                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 64                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 208                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 15488                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 15616                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 15744                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 15872                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 16000                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 16128                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 16256                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 16384                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 16512                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 16640                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 16768                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16896                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 11469                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 13107                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 14746                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 192                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 205                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 218                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 230                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 243                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 256                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 269                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 282                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 294                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 307                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 2048                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 2048                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 2048                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 2048                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 2048                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.10374245            | -0.103742449 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.136000007            | 0.136 ± 0.0000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0322575532          | -0.032257551 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 3.30435205             | 3.304351854 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.93359709            | -7.933597302 ± 0.000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 4.76205063             | 4.762050845 ± 0.000009   | ✓      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.23 (Repeat Count = 1)      |                        |                          |        | ✓ |
|--|------------------------|--------------------------|--------|---|
| Name                                   | Input Value            |                          |        |   |
| ADDCoef_MtrNmSpRad_T_f32               | 0.018                  |                          |        |   |
| VehicleSpeed_Kph_T_f32                 | 240.02                 |                          |        |   |
| WIRCmdAmpBlnd_MtrNm_T_f32              | 3.5                    |                          |        |   |
| filtCoef_Uls_T_Str                     | tgt_filtCoef_Uls_T_Str |                          |        |   |
| k_InrtCmp_MtrInertia_KgmSq_f32         | 0.00035                |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        | 96                     |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        | 112                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        | 128                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        | 144                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        | 160                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        | 176                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        | 192                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        | 208                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        | 224                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        | 240                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       | 256                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       | 272                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        | 48                     |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        | 64                     |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        | 80                     |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        | 96                     |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        | 112                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        | 128                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        | 144                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        | 160                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        | 176                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        | 192                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       | 208                    |                          |        |   |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       | 224                    |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[0]                | 10368                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[1]                | 10496                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[2]                | 10624                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[3]                | 10752                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[4]                | 10880                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[5]                | 11008                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[6]                | 11136                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[7]                | 11264                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[8]                | 11392                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[9]                | 11520                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[10]               | 11648                  |                          |        |   |
| t_CmnVehSpd_Kph_u9p7[11]               | 11776                  |                          |        |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       | 3277                   |                          |        |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       | 4915                   |                          |        |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       | 6554                   |                          |        |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       | 8192                   |                          |        |   |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       | 9830                   |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  | 205                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  | 218                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  | 230                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  | 243                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  | 256                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  | 269                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  | 282                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  | 294                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  | 307                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  | 320                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] | 333                    |                          |        |   |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] | 346                    |                          |        |   |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            | 256                    |                          |        |   |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            | 512                    |                          |        |   |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            | 768                    |                          |        |   |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            | 1024                   |                          |        |   |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            | 1280                   |                          |        |   |
| Name                                   | Actual Value           | Expected Value           | Result |   |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      | -0.120654218           | -0.120654218 ± 0.0000009 | ✓      |   |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      | 0.143999994            | 0.144 ± 0.0000009        | ✓      |   |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      | -0.0233457759          | -0.023345782 ± 0.0000009 | ✓      |   |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      | 3.25202346             | 3.25202347 ± 0.000009    | ✓      |   |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      | -7.922647              | -7.92264714 ± 0.000009   | ✓      |   |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      | 4.82532883             | 4.82532939 ± 0.000009    | ✓      |   |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.24 (Repeat Count = 1)      |  |                        |                          | ✔      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.019                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 256.05                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 4.3                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00036                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 336                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 352                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 368                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 384                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 400                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 416                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 432                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 448                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 464                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 480                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 496                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 512                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 64                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 80                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 240                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 5248                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 5376                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5504                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 5632                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 5760                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 5888                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 6016                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 6144                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 6272                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 6528                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 6656                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 0                      |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 0                      |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 0                      |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 0                      |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 0                      |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 218                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 230                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 243                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 256                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 269                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 282                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 294                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 307                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 320                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 333                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 346                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 358                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1766                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1792                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1818                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1843                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1869                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.266277403           | -0.266277387 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.151999995            | 0.152 ± 0.0000009        | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.114277415            | 0.114277387 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.55320787             | 2.55320816 ± 0.000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.67659283            | -7.676592803 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 5.7701993              | 5.770199037 ± 0.000009   | ✔      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.25 (Repeat Count = 1)      |  |                        |                           | ✔      |
|--|--|------------------------|---------------------------|--------|
| Name                                   |  | Input Value            |                           |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.02                   |                           |        |
| VehicleSpeed_Kph_T_f32                 |  | 272.06                 |                           |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 5.1                    |                           |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                           |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00037                |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 656                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 672                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 688                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 704                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 720                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 736                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 752                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 768                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 784                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 800                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 816                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 832                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 80                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 96                     |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 112                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 128                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 144                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 160                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 176                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 192                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 208                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 224                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 240                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 256                    |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 3968                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 4096                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 4224                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 4352                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 4480                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 4608                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 4736                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 4864                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 4992                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 5120                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 5248                   |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 5376                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 16384                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 16384                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 16384                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 16384                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 16384                  |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 13                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 26                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 38                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 51                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 64                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 77                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 90                     |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 102                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 115                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 128                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 141                    |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 154                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 410                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 435                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 461                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 486                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 512                    |                           |        |
| Name                                   |  | Actual Value           | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.0996317267          | -0.099631729 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.159999996            | 0.16 ± 0.00000009         | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0603682697          | -0.060368271 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 3.23617816             | 3.23617818 ± 0.000009     | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.91914797            | -7.919148201 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 4.84467363             | 4.844673619 ± 0.000009    | ✔      |



# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.26 (Repeat Count = 1)      |  |                        |                          | ✔      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.021                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 288.08                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 6.4                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00038                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 1296                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 1312                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 1328                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 1344                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 1360                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 1376                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 1392                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 1408                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 1424                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 1440                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 1456                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 1472                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 96                     |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 112                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 128                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 144                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 160                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 240                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 256                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 272                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 12800                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 12928                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 13056                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 13184                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 13312                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 13440                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 13568                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 13696                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 13824                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 13952                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 14080                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 14208                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 11469                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 26                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 38                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 166                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 666                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 691                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 717                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 742                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 768                    |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.211607069           | -0.211607064 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.167999998            | 0.168 ± 0.0000009        | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.0436070785           | 0.043607064 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.26093268             | 2.260932845 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.50725317            | -7.507253234 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 6.23181343             | 6.231813921 ± 0.0000009  | ✔      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.27 (Repeat Count = 1)      |  |                        |                           | ✔      |
|--|--|------------------------|---------------------------|--------|
| Name                                   |  | Input Value            |                           |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.022                  |                           |        |
| VehicleSpeed_Kph_T_f32                 |  | 304.09                 |                           |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 7.1                    |                           |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                           |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00039                |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 1136                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 1152                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 1168                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 1184                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 1200                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 1216                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 1232                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 1248                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 1264                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 1280                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 1296                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 1312                   |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 336                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 352                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 368                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 384                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 400                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 416                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 432                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 448                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 464                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 480                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 496                    |                           |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 512                    |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 15488                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 15616                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 15744                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 15872                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 16000                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 16128                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 16256                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 16384                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 16512                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 16640                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 16768                  |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16896                  |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 1638                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 3277                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 4915                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 6554                   |                           |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 8192                   |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 0                      |                           |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 0                      |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 922                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 947                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 973                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 998                    |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1024                   |                           |        |
| Name                                   |  | Actual Value           | Expected Value            | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.0564835407          | -0.056483543 ± 0.00000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.175999999            | 0.176 ± 0.0000009         | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.119516462           | -0.119516457 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 1.64792883             | 1.647929015 ± 0.000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -6.97387695            | -6.97387697 ± 0.000009    | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 7.37819529             | 7.378194015 ± 0.000009    | ✔      |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530

FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.28 (Repeat Count = 1)      |  |                        |                         | ✔      |
|--|--|------------------------|-------------------------|--------|
| Name                                   |  | Input Value            |                         |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.023                  |                         |        |
| VehicleSpeed_Kph_T_f32                 |  | 320.07                 |                         |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 8.2                    |                         |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                         |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.0004                 |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 176                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 192                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 208                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 224                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 240                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 256                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 272                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 288                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 304                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 320                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 336                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 352                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 656                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 672                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 688                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 704                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 720                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 736                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 752                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 768                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 784                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 800                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 816                    |                         |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 832                    |                         |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 10368                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 10496                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 10624                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 10752                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 10880                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 11008                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 11136                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11264                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 11392                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 11520                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 11648                  |                         |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 11776                  |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 3277                   |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 4915                   |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 6554                   |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 8192                   |                         |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 9830                   |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 384                    |                         |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 384                    |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1178                   |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1203                   |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1229                   |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1254                   |                         |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1280                   |                         |        |
| Name                                   |  | Actual Value           | Expected Value          | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.44143194            | -0.44143189 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.184                  | 0.184 ± 0.0000009       | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.257431924            | 0.25743189 ± 0.0000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.24206972             | 2.242070137 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.49469471            | -7.49469476 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 6.26323557             | 6.263235103 ± 0.000009  | ✔      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530



FilterCoefCalc

| Test Step 1.29 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.024                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 336.06                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 4.5                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00041                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 496                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 512                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 528                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 544                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 560                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 576                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 592                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 608                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 624                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 640                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 656                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 672                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 1296                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 1312                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 1328                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 1344                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 1360                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 1376                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 1392                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 1408                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 1424                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 1440                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 1456                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 1472                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 5248                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 5376                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5504                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 5632                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 5760                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 5888                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 6016                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 6144                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 6272                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 6528                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 6656                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 11469                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 192                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 205                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 218                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 230                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 243                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 256                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 269                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 282                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 294                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 307                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1434                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1459                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1485                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1510                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1536                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.64859736            | -0.648597291 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.192000002            | 0.192 ± 0.0000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.456597328            | 0.456597291 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 1.64794874             | 1.647948707 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -6.97389889            | -6.973898945 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 7.37815237             | 7.378152348 ± 0.0000009  | ✓      |



# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530

FilterCoefCalc



| Test Step 1.30 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.025                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 352.05                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 4.9                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00001                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 816                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 832                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 848                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 864                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 880                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 896                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 912                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 928                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 944                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 960                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 976                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 992                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 1136                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 1152                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 1168                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 1184                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 1200                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 1216                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 1232                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 1248                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 1264                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 1280                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 1296                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 1312                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 3968                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 4096                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 4224                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 4352                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 4480                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 4608                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 4736                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 4864                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 4992                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 5120                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 5248                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 5376                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 11469                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 13107                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 38                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 179                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1690                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1715                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1741                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1766                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1792                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.12834549            | -0.128345472 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.200000003            | 0.2 ± 0.0000009          | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0716545135          | -0.071654528 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 1.25517929             | 1.255179464 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -6.45242405            | -6.45242444 ± 0.000009   | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 8.29239559             | 8.292396096 ± 0.000009   | ✓      |

# TEST DETAILS REPORT

2014-09-19, 16:32:29+0530

FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc

| Test Step 1.31 (Repeat Count = 1)      |  |                        |                          | ✓      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.026                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 368.01                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 7.5                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.0005                 |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 1392                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 1408                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 1424                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 1440                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 1456                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 1472                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 1488                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 1504                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 1520                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 1536                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 1552                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 1568                   |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 176                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 192                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 208                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 224                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 240                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 256                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 272                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 288                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 304                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 320                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 336                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 352                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 2560                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 3840                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5120                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7680                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 8960                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 10240                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11520                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 12800                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 14080                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 15360                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16640                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 9830                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 11469                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 13107                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 14746                  |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 51                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 64                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 77                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 90                     |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 102                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 115                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 128                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 141                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 154                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 166                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 192                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 1894                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 1920                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 1946                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 1971                   |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 1997                   |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.44634214            | -0.446342077 ± 0.0000009 | ✓      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.208000004            | 0.208 ± 0.0000009        | ✓      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | 0.238342136            | 0.238342077 ± 0.0000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 1.7996192              | 1.7996192 ± 0.000009     | ✓      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.13275242            | -7.132752506 ± 0.000009  | ✓      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 7.06762838             | 7.067628294 ± 0.000009   | ✓      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

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FilterCoefCalc



| Test Step 1.32 (Repeat Count = 1)      |  |                        |                          | ✔      |
|--|--|------------------------|--------------------------|--------|
| Name                                   |  | Input Value            |                          |        |
| ADDCoef_MtrNmSpRad_T_f32               |  | 0.027                  |                          |        |
| VehicleSpeed_Kph_T_f32                 |  | 384.02                 |                          |        |
| WIRCmdAmpBlnd_MtrNm_T_f32              |  | 2.5                    |                          |        |
| filtCoef_Uls_T_Str                     |  | tgt_filtCoef_Uls_T_Str |                          |        |
| k_InrtCmp_MtrInertia_KgmSq_f32         |  | 0.00003                |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]        |  | 496                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]        |  | 512                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]        |  | 528                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]        |  | 544                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]        |  | 560                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]        |  | 576                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]        |  | 592                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]        |  | 608                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]        |  | 624                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]        |  | 640                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]       |  | 656                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]       |  | 672                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]        |  | 496                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]        |  | 512                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]        |  | 528                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]        |  | 544                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]        |  | 560                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]        |  | 576                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]        |  | 592                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]        |  | 608                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]        |  | 624                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]        |  | 640                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]       |  | 656                    |                          |        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]       |  | 672                    |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                |  | 2560                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                |  | 3840                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                |  | 5120                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                |  | 6400                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                |  | 7680                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                |  | 8960                   |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                |  | 10240                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                |  | 11520                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                |  | 12800                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                |  | 14080                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]               |  | 15360                  |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]               |  | 16640                  |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]       |  | 3277                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]       |  | 4915                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]       |  | 6554                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]       |  | 8192                   |                          |        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]       |  | 9830                   |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]  |  | 179                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]  |  | 192                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]  |  | 205                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]  |  | 218                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]  |  | 230                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]  |  | 243                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]  |  | 256                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]  |  | 269                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]  |  | 282                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]  |  | 294                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10] |  | 307                    |                          |        |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11] |  | 320                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]            |  | 794                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]            |  | 819                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]            |  | 845                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]            |  | 870                    |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]            |  | 896                    |                          |        |
| Name                                   |  | Actual Value           | Expected Value           | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32      |  | -0.1716436             | -0.171643583 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32      |  | 0.216000006            | 0.216 ± 0.0000009        | ✔      |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32      |  | -0.0443564057          | -0.044356417 ± 0.0000009 | ✔      |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32      |  | 2.16740918             | 2.167409451 ± 0.000009   | ✔      |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32      |  | -7.44288063            | -7.442880571 ± 0.000009  | ✔      |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32      |  | 6.38971043             | 6.389709978 ± 0.000009   | ✔      |

# TEST DETAILS REPORT

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FilterCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4     | IntplVarXY_u16_u16Xu16Y_Cnt | 4     |        | ✓ |

# TEST DETAILS REPORT

2014-09-19, 16:26:53+0530

ADDCoefCalc



|             |             |
|-------------|-------------|
| Project     | FDD_Inertia |
| Module      | FDD_Inertia |
| Test Object | ADDCoefCalc |

## Instrumentation: Test Object Only

|                         |       |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Branch (C1) Coverage    | 100 % |

## Statistics

|                 |     |
|-----------------|-----|
| Total Testcases | 1   |
| Successful      | 1 ✓ |
| Failed          | 0   |
| Not Executed    | 0   |



# TEST DETAILS REPORT

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ADDCoefCalc



## Module Properties

|                        |   |
|------------------------|---|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp  |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml  |
| Target Environment     | TI TMS 570 PLS UDE (Default)  |
| Kind of Test           | Unit Test   |
| Linker Options         |   |
| Source File(s)         |   |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NtrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NtrLib\src\interpolation.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NtrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name                 | Text   |
|----------------------|--|
| Module 'FDD_Inertia' | <p>*****Unit Test Description*****</p> <p>Name of Tester: Spoorti Mali<br/>Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c<br/>Code File(s) Version: 13<br/>Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc<br/>Module Design Document Version: 18<br/>Data Dictionary Version: 16<br/>Unit Test Plan Version: 6<br/>Optimization Level: Level 2<br/>Compiler (CodeGen) Version: TMS470_4.9.5<br/>Model Type: Excel Macro<br/>Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30<br/>Total FLASH Used (Bytes): 1994<br/>Total RAM Used (Bytes): 60<br/>Total CALS Used (Bytes): 328<br/>Special Test Requirements:<br/>Test Date: 09-19-2014<br/>Comments:</p> <p>Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.</p> <p>Note2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.</p> <p>Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of iVM</p> |

# TEST DETAILS REPORT

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ADDCoefCalc



| Attributes      |  |
|-----------------|--|
| Name            | Value  |
| UDE Config File | \${PROJECTROOT}\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File  | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

# TEST DETAILS REPORT

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## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 1409.00 Cycles  
TS1.2 1399.00 Cycles  
TS1.3 1430.00 Cycles  
TS1.4 1487.00 Cycles  
TS1.5 1387.00 Cycles  
TS1.6 1432.00 Cycles  
TS1.7 1541.00 Cycles  
TS1.8 1375.00 Cycles  
TS1.9 1386.00 Cycles  
TS1.10 1398.00 Cycles  
TS1.11 1375.00 Cycles  
TS1.12 1681.00 Cycles  
TS1.13 1387.00 Cycles  
TS1.14 1556.00 Cycles  
TS1.15 1387.00 Cycles  
TS1.16 1387.00 Cycles  
TS1.17 1419.00 Cycles  
TS1.18 1387.00 Cycles  
TS1.19 1387.00 Cycles  
TS1.20 1387.00 Cycles  
TS1.21 1387.00 Cycles  
TS1.22 1387.00 Cycles  
TS1.23 1387.00 Cycles  
TS1.24 1419.00 Cycles  
TS1.25 1419.00 Cycles  
TS1.26 1398.00 Cycles  
TS1.27 1387.00 Cycles  
TS1.28 1387.00 Cycles  
TS1.29 1398.00 Cycles  
TS1.30 1601.00 Cycles  
TS1.31 1419.00 Cycles  
TS1.32 1387.00 Cycles  
TS1.33 1387.00 Cycles  
TS1.34 1387.00 Cycles  
TS1.35 1387.00 Cycles  
TS1.36 1398.00 Cycles  
TS1.37 1398.00 Cycles

**Description** Test Vector Description

TS1.1 All min  
TS1.2 All max  
TS1.3 BaseAssistCmd\_MtrNm\_T\_f32 min  
TS1.4 BaseAssistCmd\_MtrNm\_T\_f32 max  
TS1.5 BaseAssistCmd\_MtrNm\_T\_f32 zero  
TS1.6 BaseAssistCmd\_MtrNm\_T\_f32 pos  
TS1.7 BaseAssistCmd\_MtrNm\_T\_f32 neg  
TS1.8 WIRCmdAmpBlnd\_MtrNm\_T\_f32 min  
TS1.9 WIRCmdAmpBlnd\_MtrNm\_T\_f32 max  
TS1.10 WIRCmdAmpBlnd\_MtrNm\_T\_f32 pos  
TS1.11 VehicleSpeed1\_Kph\_T\_f32 min  
TS1.12 VehicleSpeed1\_Kph\_T\_f32 max  
TS1.13 VehicleSpeed1\_Kph\_T\_f32 pos  
TS1.14 t\_DmpADDCoefX\_MtrNm\_u4p12[10] min  
TS1.15 t\_DmpADDCoefX\_MtrNm\_u4p12[10] max  
TS1.16 t\_DmpADDCoefX\_MtrNm\_u4p12[10] pos  
TS1.17 t2\_FDD\_ADDRollingTblYM1\_MtrNmpRadpS\_um1p17[10] min  
TS1.18 t2\_FDD\_ADDRollingTblYM1\_MtrNmpRadpS\_um1p17[10] max  
TS1.19 t2\_FDD\_ADDRollingTblYM1\_MtrNmpRadpS\_um1p17[10] pos  
TS1.20 t2\_FDD\_ADDRollingTblYM2\_MtrNmpRadpS\_um1p17[10] min  
TS1.21 t2\_FDD\_ADDRollingTblYM2\_MtrNmpRadpS\_um1p17[10] max  
TS1.22 t2\_FDD\_ADDRollingTblYM2\_MtrNmpRadpS\_um1p17[10] pos  
TS1.23 t\_FDD\_ADDStaticTblY\_MtrNmpRadpS\_um1p17[10] min  
TS1.24 t\_FDD\_ADDStaticTblY\_MtrNmpRadpS\_um1p17[10] max  
TS1.25 t\_FDD\_ADDStaticTblY\_MtrNmpRadpS\_um1p17[10] pos  
TS1.26 t\_WIRBlndTblX\_MtrNm\_u8p8[5] min  
TS1.27 t\_WIRBlndTblX\_MtrNm\_u8p8[5] max  
TS1.28 t\_WIRBlndTblX\_MtrNm\_u8p8[5] pos  
TS1.29 t\_RIAstWIRBlndTblY\_Uls\_u2p14[5] min  
TS1.30 t\_RIAstWIRBlndTblY\_Uls\_u2p14[5] max  
TS1.31 t\_RIAstWIRBlndTblY\_Uls\_u2p14[5] pos  
TS1.32 t\_CmnVehSpd\_Kph\_u9p7[12] min  
TS1.33 t\_CmnVehSpd\_Kph\_u9p7[12] max  
TS1.34 t\_CmnVehSpd\_Kph\_u9p7[12] pos  
TS1.35 t\_FDD\_BlendTblY\_Uls\_u8p8[12] min  
TS1.36 t\_FDD\_BlendTblY\_Uls\_u8p8[12] max  
TS1.37 t\_FDD\_BlendTblY\_Uls\_u8p8[12] pos

## Test Step 1.1 (Repeat Count = 1)

| Name  | Input Value |
|---|-------------|
| BaseAssistCmd_MtrNm_T_f32                       | -8.8        |
| VehicleSpeed_Kph_T_f32                          | 0           |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 0           |

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| Name  | Input Value  |                |        |
|---|--------------|----------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 0            |                |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 0            |                |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 0            |                |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 0            |                |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 0            |                |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 0            |                |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 0            |                |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 0            |                |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 0            |                |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 0            |                |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 0            |                |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 0            |                |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 0            |                |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 0            |                |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 0            |                |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 0            |                |        |
| Name  | Actual Value | Expected Value | Result |
| ADDCoefCalc()                                   | 0            | 0 ± 0.000009   | ✔      |

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| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     |        | ✓ |

| Test Step 1.2 (Repeat Count = 1)                |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 8.8         |   |
| VehicleSpeed_Kph_T_f32                          | 511.9921875 |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 8.8         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 6554        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 6554        |   |
| t_CmnVehSpd_Kph_u9p7[0]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[1]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[2]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[3]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[4]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[5]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[6]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[7]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[8]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[9]                         | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[10]                        | 32640       |   |
| t_CmnVehSpd_Kph_u9p7[11]                        | 32640       |   |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 36045       |   |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 36045       |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 6554        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 6554        |   |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 256         |   |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 256         |   |

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| Name                            | Input Value  |                          |        |
|---------------------------------|--------------|--------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[9]     | 256          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 256          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 256          |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 16384        |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 2048         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 2048         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 2048         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 2048         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 2048         |                          |        |
| Name                            | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                   | 0.0500030518 | 0.050003052 ± 0.00000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.3 (Repeat Count = 1)                |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | -8.8        |
| VehicleSpeed_Kph_T_f32                          | 12.32       |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 5.2         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3409        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 8192        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0]       | 523         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1]       | 1038        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]       | 1553        |

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| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159         |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 3            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 5            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 8            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 10           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 13           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 15           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 18           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 20           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 23           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 26           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 28           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 31           |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]           | 1638         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]           | 3277         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]           | 4915         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]           | 6554         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]           | 8192         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]               | 282          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]               | 307          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]               | 333          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]               | 358          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]               | 384          |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0369348824 | 0.036934882 ± 0.00000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.4 (Repeat Count = 1)                |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 8.8         |
| VehicleSpeed_Kph_T_f32                          | 24          |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 6.5         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 342         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 683         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2728        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3409        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2583        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3099        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4129        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                         | 12800       |

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| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288        |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[0] | 704          |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[1] | 814          |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[2] | 924          |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[3] | 1034         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[4] | 1144         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[5] | 1254         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[6] | 1364         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[7] | 1475         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[8] | 1585         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[9] | 1695         |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[0]               | 5            |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[1]               | 8            |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[2]               | 10           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[3]               | 13           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[4]               | 15           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[5]               | 18           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[6]               | 20           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[7]               | 23           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[8]               | 26           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[9]               | 28           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[10]              | 31           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[11]              | 33           |                          |        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[0]           | 3277         |                          |        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[1]           | 4915         |                          |        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[2]           | 6554         |                          |        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[3]           | 8192         |                          |        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[4]           | 9830         |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]               | 538          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]               | 563          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]               | 589          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]               | 614          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]               | 640          |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.013426058  | 0.013426058 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.5 (Repeat Count = 1)                |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 0           |   |
| VehicleSpeed_Kph_T_f32                          | 36.25       |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 7.3         |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][0] | 523         |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][1] | 1038        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][2] | 1553        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][3] | 2068        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][4] | 2583        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][5] | 3099        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][6] | 3614        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][7] | 4129        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][8] | 4644        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][9] | 5159        |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[1][0] | 704         |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[1][1] | 814         |   |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[1][2] | 924         |   |



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| Name  | Input Value   |                          |        |
|---|---------------|--------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1034          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1144          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1254          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1364          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1475          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1585          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1695          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 6784          |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 6912          |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 7040          |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 7168          |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 7296          |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 7424          |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 7552          |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 7680          |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 7808          |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 7936          |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 8064          |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 8192          |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 12698         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 13107         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 13517         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 13926         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 14336         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 14746         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 15155         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 15565         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 15974         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 16384         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 885           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 986           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 1087          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 1188          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 1288          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 1389          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 1490          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 1591          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 1692          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 1793          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 10            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 13            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 15            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 18            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 20            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 23            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 26            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 28            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 31            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 33            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 36            |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 38            |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 4915          |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 6554          |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 8192          |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 9830          |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 11469         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 794           |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 819           |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 845           |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 870           |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 896           |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| ADDCoefCalc()                                   | 0.00668188976 | 0.00668189 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

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ADDCoefCalc

| Test Step 1.6 (Repeat Count = 1)                |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 5.25        |
| VehicleSpeed_Kph_T_f32                          | 48.12       |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 8.1         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][0] | 704         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][1] | 814         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][2] | 924         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][3] | 1034        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][4] | 1144        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][5] | 1254        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][6] | 1364        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][7] | 1475        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][8] | 1585        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][9] | 1695        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][0] | 885         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][1] | 986         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][2] | 1087        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][3] | 1188        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][4] | 1288        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][5] | 1389        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][6] | 1490        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][7] | 1591        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][8] | 1692        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][9] | 1793        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 16794       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 17203       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 17613       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 18022       |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 18432       |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 18842       |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 19251       |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 19661       |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 20070       |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 20480       |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[0]       | 1066        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[1]       | 1212        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[2]       | 1359        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[3]       | 1506        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[4]       | 1653        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[5]       | 1800        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[6]       | 1946        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[7]       | 2093        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[8]       | 2240        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[9]       | 2387        |
| t_FDD_BlendTbly_Uls_u8p8[0]                     | 13          |
| t_FDD_BlendTbly_Uls_u8p8[1]                     | 15          |
| t_FDD_BlendTbly_Uls_u8p8[2]                     | 18          |
| t_FDD_BlendTbly_Uls_u8p8[3]                     | 20          |
| t_FDD_BlendTbly_Uls_u8p8[4]                     | 23          |
| t_FDD_BlendTbly_Uls_u8p8[5]                     | 26          |
| t_FDD_BlendTbly_Uls_u8p8[6]                     | 28          |
| t_FDD_BlendTbly_Uls_u8p8[7]                     | 31          |
| t_FDD_BlendTbly_Uls_u8p8[8]                     | 33          |
| t_FDD_BlendTbly_Uls_u8p8[9]                     | 36          |
| t_FDD_BlendTbly_Uls_u8p8[10]                    | 38          |
| t_FDD_BlendTbly_Uls_u8p8[11]                    | 41          |
| t_RIAstWIRBlndTbly_Uls_u2p14[0]                 | 6554        |
| t_RIAstWIRBlndTbly_Uls_u2p14[1]                 | 8192        |
| t_RIAstWIRBlndTbly_Uls_u2p14[2]                 | 9830        |
| t_RIAstWIRBlndTbly_Uls_u2p14[3]                 | 11469       |

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ADDCoefCalc

| Name                            | Input Value  |                          |        |
|---------------------------------|--------------|--------------------------|--------|
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 13107        |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1050         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 1075         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 1101         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 1126         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 1152         |                          |        |
| Name                            | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                   | 0.0174616091 | 0.017461608 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.7 (Repeat Count = 1)                |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | -5.45       |   |
| VehicleSpeed_Kph_T_f32                          | 60          |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 5.2         |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 885         |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 986         |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1087        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1188        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1288        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1389        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1490        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1591        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1692        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1793        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1066        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1212        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1359        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1506        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1653        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1800        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1946        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2093        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 2240        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 2387        |   |
| t_CmnVehSpd_Kph_u9p7[0]                         | 2560        |   |
| t_CmnVehSpd_Kph_u9p7[1]                         | 3840        |   |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5120        |   |
| t_CmnVehSpd_Kph_u9p7[3]                         | 6400        |   |
| t_CmnVehSpd_Kph_u9p7[4]                         | 7680        |   |
| t_CmnVehSpd_Kph_u9p7[5]                         | 8960        |   |
| t_CmnVehSpd_Kph_u9p7[6]                         | 10240       |   |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11520       |   |
| t_CmnVehSpd_Kph_u9p7[8]                         | 12800       |   |
| t_CmnVehSpd_Kph_u9p7[9]                         | 14080       |   |
| t_CmnVehSpd_Kph_u9p7[10]                        | 15360       |   |
| t_CmnVehSpd_Kph_u9p7[11]                        | 16640       |   |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 20890       |   |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 21299       |   |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 21709       |   |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 22118       |   |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 22528       |   |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 22938       |   |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 23347       |   |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 23757       |   |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 24166       |   |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 24576       |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0]       | 1246        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1]       | 1638        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]       | 2030        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3]       | 2422        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4]       | 2814        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5]       | 3206        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6]       | 3598        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7]       | 3990        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8]       | 4382        |   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9]       | 4774        |   |

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ADDCoefCalc

| Name                            | Input Value  |                           |        |
|---------------------------------|--------------|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[0]     | 15           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[1]     | 18           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[2]     | 20           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[3]     | 23           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]     | 26           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]     | 28           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]     | 31           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]     | 33           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]     | 36           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]     | 38           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 41           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 44           |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 8192         |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 9830         |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 11469        |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 13107        |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 14746        |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1306         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 1331         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 1357         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 1382         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 1408         |                           |        |
| Name                            | Actual Value | Expected Value            | Result |
| ADDCoefCalc()                   | 0.0190629773 | 0.0190629773 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.8 (Repeat Count = 1)                |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 1.1         |
| VehicleSpeed_Kph_T_f32                          | 72.35       |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1066        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1212        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1359        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1506        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1653        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1800        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1946        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2093        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 2240        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1246        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1638        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2030        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2422        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2814        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3206        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3598        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3990        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4382        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4774        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 12800       |
| t_CmnVehSpd_Kph_u9p7[1]                         | 12928       |
| t_CmnVehSpd_Kph_u9p7[2]                         | 13056       |
| t_CmnVehSpd_Kph_u9p7[3]                         | 13184       |
| t_CmnVehSpd_Kph_u9p7[4]                         | 13312       |
| t_CmnVehSpd_Kph_u9p7[5]                         | 13440       |
| t_CmnVehSpd_Kph_u9p7[6]                         | 13568       |
| t_CmnVehSpd_Kph_u9p7[7]                         | 13696       |
| t_CmnVehSpd_Kph_u9p7[8]                         | 13824       |
| t_CmnVehSpd_Kph_u9p7[9]                         | 13952       |
| t_CmnVehSpd_Kph_u9p7[10]                        | 14080       |
| t_CmnVehSpd_Kph_u9p7[11]                        | 14208       |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 24986       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 25395       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 25805       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 26214       |

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ADDCoefCalc

| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672        |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1427         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1655         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1884         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2112         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2340         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2568         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2796         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3024         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3252         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3480         |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 18           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 20           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 23           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 26           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 28           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 31           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 33           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 36           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 38           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 41           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 44           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 46           |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]           | 1638         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]           | 3277         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]           | 4915         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]           | 6554         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]           | 8192         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]               | 1562         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]               | 1587         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]               | 1613         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]               | 1638         |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]               | 1664         |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0107031446 | 0.010703144 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.9 (Repeat Count = 1)                |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 1.2         |
| VehicleSpeed_Kph_T_f32                          | 84          |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 8.8         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2030        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2422        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3206        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4774        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1427        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1655        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1884        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2112        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2340        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2568        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2796        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3252        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3480        |

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ADDCoefCalc

| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                   | 15488        |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 15616        |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 15744        |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 15872        |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 16000        |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 16128        |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 16256        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 16384        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 16512        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 16640        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 16768        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16896        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 28262        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 28672        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 29082        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 29491        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 29901        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 30310        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 30720        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 31130        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 31539        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 31949        |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[0] | 1608         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[1] | 2032         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[2] | 2455         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[3] | 2878         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[4] | 3302         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[5] | 3725         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[6] | 4148         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[7] | 4572         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[8] | 4995         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[9] | 5419         |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[0]               | 20           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[1]               | 23           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[2]               | 26           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[3]               | 28           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[4]               | 31           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[5]               | 33           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[6]               | 36           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[7]               | 38           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[8]               | 41           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[9]               | 44           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[10]              | 46           |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[11]              | 49           |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[0]           | 3277         |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[1]           | 4915         |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[2]           | 6554         |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[3]           | 8192         |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[4]           | 9830         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]               | 1766         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]               | 1792         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]               | 1818         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]               | 1843         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]               | 1869         |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0121170254 | 0.012117026 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.10 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 1.3         |
| VehicleSpeed_Kph_T_f32                          | 96.14       |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 4.25        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][0] | 1427        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][1] | 1655        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][2] | 1884        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][3] | 2112        |

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ADDCoefCalc

| Name  | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2340         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2568         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2796         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 3024         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3252         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3480         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1608         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2032         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2455         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2878         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3302         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3725         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 4148         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4572         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4995         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5419         |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 10368        |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 10496        |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 10624        |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 10752        |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 10880        |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 11008        |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 11136        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11264        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 11392        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 11648        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 11776        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 24986        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 25395        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 25805        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 26214        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 26624        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 27034        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 27443        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 27853        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 28262        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 28672        |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 1789         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 2130         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 2471         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 2811         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 3152         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 3493         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 3834         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 4175         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 4515         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 4856         |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 49           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 51           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 54           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 57           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 60           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 63           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 66           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 68           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 71           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 74           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 77           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 80           |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 4915         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 6554         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 8192         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 9830         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 11469        |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 410          |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 435          |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 461          |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 486          |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 512          |                          |        |
| Name  | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                                   | 0.0130879935 | 0.013087993 ± 0.00000009 | ✓      |

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ADDCoefCalc

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     |        | ✓ |

| Test Step 1.11 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 1.4         |   |
| VehicleSpeed_Kph_T_f32                          | 0           |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.1         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1608        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2032        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2455        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2878        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3302        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3725        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 4148        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4572        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4995        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5419        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1789        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2130        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2471        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2811        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3152        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3493        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3834        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4175        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4515        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4856        |   |
| t_CmnVehSpd_Kph_u9p7[0]                         | 5248        |   |
| t_CmnVehSpd_Kph_u9p7[1]                         | 5376        |   |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5504        |   |
| t_CmnVehSpd_Kph_u9p7[3]                         | 5632        |   |
| t_CmnVehSpd_Kph_u9p7[4]                         | 5760        |   |
| t_CmnVehSpd_Kph_u9p7[5]                         | 5888        |   |
| t_CmnVehSpd_Kph_u9p7[6]                         | 6016        |   |
| t_CmnVehSpd_Kph_u9p7[7]                         | 6144        |   |
| t_CmnVehSpd_Kph_u9p7[8]                         | 6272        |   |
| t_CmnVehSpd_Kph_u9p7[9]                         | 6400        |   |
| t_CmnVehSpd_Kph_u9p7[10]                        | 6528        |   |
| t_CmnVehSpd_Kph_u9p7[11]                        | 6656        |   |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 28262       |   |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 28672       |   |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 29082       |   |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 29491       |   |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 29901       |   |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 30310       |   |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 30720       |   |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 31130       |   |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 31539       |   |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 31949       |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 161         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 328         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 494         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 661         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 827         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 994         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 1160        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 1326        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 1493        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 1659        |   |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 65          |   |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 68          |   |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 70          |   |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 73          |   |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 75          |   |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 78          |   |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 80          |   |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 83          |   |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 86          |   |



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| Name                            | Input Value   |                           |        |
|---------------------------------|---------------|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[9]     | 88            |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 91            |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 93            |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 6554          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 8192          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 9830          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 11469         |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 13107         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 666           |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 691           |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 717           |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 742           |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 768           |                           |        |
| Name                            | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                   | 0.00417164806 | 0.004171648 ± 0.000000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.12 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 1.5         |
| VehicleSpeed_Kph_T_f32                          | 511.9921875 |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.2         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1789        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2130        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2471        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2811        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3152        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3834        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4175        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4515        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4856        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3302        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3725        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 4148        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4572        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4995        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5419        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 3968        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 4096        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 4224        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 4352        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 4480        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 4608        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 4736        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 4864        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 4992        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 5120        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 5248        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 5376        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 8192        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0]       | 342         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1]       | 683         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]       | 1024        |

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| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409         |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 93           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 96           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 99           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 101          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 104          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 106          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 109          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 111          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 114          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 116          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 119          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 122          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]           | 8192         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]           | 9830         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]           | 11469        |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]           | 13107        |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]           | 14746        |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]               | 922          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]               | 947          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]               | 973          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]               | 998          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]               | 1024         |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0185419321 | 0.018541932 ± 0.00000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.13 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 1.6         |
| VehicleSpeed_Kph_T_f32                          | 100.21      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.3         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1608        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2032        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2455        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2878        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3302        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3725        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 4148        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4572        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4995        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5419        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1789        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2130        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2471        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2811        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3152        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3834        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4175        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4515        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4856        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152        |

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| Name                                      | Input Value   |                           |        |
|---|---------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280          |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408          |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288         |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 523           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1038          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1553          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2068          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2583          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3614          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4129          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4644          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5159          |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 116           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 118           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 121           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 123           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 126           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 129           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 131           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 134           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 136           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 139           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 141           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 144           |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]           | 1638          |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]           | 3277          |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]           | 4915          |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]           | 6554          |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[4]           | 8192          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]               | 1178          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]               | 1203          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]               | 1229          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]               | 1254          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]               | 1280          |                           |        |
| Name                                      | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                             | 0.00872414559 | 0.008724146 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.14 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 1.7         |   |
| VehicleSpeed_Kph_T_f32                          | 108         |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.4         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1789        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2130        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2471        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2811        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3152        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3493        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3834        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4175        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4515        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4856        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 161         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 328         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 494         |   |

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ADDCoefCalc

| Name  | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 661          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 827          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 994          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1160         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1326         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1493         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1659         |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 2560         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 3840         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5120         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 6400         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 8960         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 10240        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 12800        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 15360        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 16640        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 0            |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 0            |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 704          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 814          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 924          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 1034         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 1144         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 1254         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 1364         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 1475         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 1585         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 1695         |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 144          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 146          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 149          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 152          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 154          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 157          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 159          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 162          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 164          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 167          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 169          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 172          |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]                 | 3277         |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]                 | 4915         |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]                 | 6554         |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]                 | 8192         |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[4]                 | 9830         |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]                     | 1434         |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]                     | 1459         |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]                     | 1485         |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]                     | 1510         |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]                     | 1536         |                          |        |
| Name  | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                                   | 0.0254064538 | 0.025406454 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

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ADDCoefCalc

## Test Step 1.15 (Repeat Count = 1)

| Name   | Input Value |
|--|-------------|
| BaseAssistCmd_MtrNm_T_f32                      | 1.8         |
| VehicleSpeed_Kph_T_f32                         | 120.14      |
| WIRCmdAmpBlnd_MtrNm_T_f32                      | 1.5         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][0] | 161         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][1] | 328         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][2] | 494         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][3] | 661         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][4] | 827         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][5] | 994         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][6] | 1160        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][7] | 1326        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][8] | 1493        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[0][9] | 1659        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][0] | 342         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][1] | 683         |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][2] | 1024        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][3] | 1364        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][4] | 1705        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][5] | 2046        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][6] | 2387        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][7] | 2728        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][8] | 3068        |
| t2_FDD_ADDRollingTbIY_MtrNmpRadpS_um1p17[1][9] | 3409        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 6784        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 6912        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 7040        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 7168        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7296        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 7424        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 7552        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 7680        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 7808        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 7936        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 8064        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 8192        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[4]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[5]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[6]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[7]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[8]                   | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[9]                   | 36045       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]      | 885         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]      | 986         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]      | 1087        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]      | 1188        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]      | 1288        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]      | 1389        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]      | 1490        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]      | 1591        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]      | 1692        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]      | 1793        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                    | 172         |
| t_FDD_BlendTbIY_Uls_u8p8[1]                    | 174         |
| t_FDD_BlendTbIY_Uls_u8p8[2]                    | 176         |
| t_FDD_BlendTbIY_Uls_u8p8[3]                    | 178         |
| t_FDD_BlendTbIY_Uls_u8p8[4]                    | 180         |
| t_FDD_BlendTbIY_Uls_u8p8[5]                    | 183         |
| t_FDD_BlendTbIY_Uls_u8p8[6]                    | 185         |
| t_FDD_BlendTbIY_Uls_u8p8[7]                    | 187         |
| t_FDD_BlendTbIY_Uls_u8p8[8]                    | 189         |
| t_FDD_BlendTbIY_Uls_u8p8[9]                    | 191         |
| t_FDD_BlendTbIY_Uls_u8p8[10]                   | 193         |
| t_FDD_BlendTbIY_Uls_u8p8[11]                   | 195         |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]                | 4915        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]                | 6554        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]                | 8192        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]                | 9830        |

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ADDCoefCalc

| Name                            | Input Value |
|---------------------------------|-------------|
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 11469       |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1690        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     |             |

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ADDCoefCalc

| Name                            | Input Value   |                           |        |
|---------------------------------|---------------|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[0]     | 218           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[1]     | 220           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[2]     | 223           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[3]     | 225           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]     | 227           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]     | 230           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]     | 232           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]     | 234           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]     | 237           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]     | 239           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 241           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 243           |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 6554          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 8192          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 9830          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 11469         |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 13107         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1894          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 1920          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 1946          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 1971          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 1997          |                           |        |
| Name                            | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                   | 0.00236540218 | 0.002365402 ± 0.000000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.17 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 2           |
| VehicleSpeed_Kph_T_f32                          | 144.25      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.7         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 0           |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 161         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 328         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 494         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 661         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 827         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 994         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1160        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1326        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1659        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                         | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                         | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                        | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                        | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 5734        |

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ADDCoefCalc

| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1246         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1638         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2030         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2422         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2814         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3206         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3598         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3990         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4382         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4774         |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 3            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 5            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 8            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 10           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 13           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 15           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 18           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 20           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 23           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 26           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 28           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 31           |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]           | 8192         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]           | 9830         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]           | 11469        |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]           | 13107        |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]           | 14746        |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]               | 922          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]               | 947          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]               | 973          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]               | 998          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]               | 1024         |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0327785164 | 0.032778516 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.18 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | -1          |
| VehicleSpeed_Kph_T_f32                          | 156.12      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.8         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3409        |



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| Name                                      | Input Value   |                           |        |
|---|---------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                   | 12800         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 12928         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 13056         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 13184         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 13312         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 13440         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 13568         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 13696         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 13824         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 13952         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 14080         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 14208         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288         |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[0] | 342           |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[1] | 683           |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[2] | 1024          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[3] | 1364          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[4] | 1705          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[5] | 2046          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[6] | 2387          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[7] | 2728          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[8] | 3068          |                           |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[9] | 3409          |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[0]               | 5             |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[1]               | 8             |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[2]               | 10            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[3]               | 13            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[4]               | 15            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[5]               | 18            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[6]               | 20            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[7]               | 23            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[8]               | 26            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[9]               | 28            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[10]              | 31            |                           |        |
| t_FDD_BlendTbLY_Uls_u8p8[11]              | 33            |                           |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[0]           | 1638          |                           |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[1]           | 3277          |                           |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[2]           | 4915          |                           |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[3]           | 6554          |                           |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[4]           | 8192          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]               | 1178          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]               | 1203          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]               | 1229          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]               | 1254          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]               | 1280          |                           |        |
| Name                                      | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                             | 0.00810782239 | 0.008107823 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.19 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | -2          |
| VehicleSpeed_Kph_T_f32                          | 168         |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 1.9         |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][0] | 1427        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][1] | 1655        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][2] | 1884        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][3] | 2112        |

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ADDCoefCalc

| Name  | Input Value   |                           |        |
|---|---------------|---------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2340          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2568          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2796          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 3024          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3252          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3480          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 523           |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1038          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1553          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2068          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4644          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5159          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 15488         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 15616         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 15744         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 15872         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 16000         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 16128         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 16256         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 16384         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 16512         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 16640         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 16768         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 16896         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 12698         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 13107         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 13517         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 13926         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 14336         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 14746         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 15155         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 15565         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 15974         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 16384         |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 523           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 1038          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 1553          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 2068          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 2583          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 3099          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 3614          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 4129          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 4644          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 5159          |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 10            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 13            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 15            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 18            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 20            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 23            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 26            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 28            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 31            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 33            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 36            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 38            |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 3277          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 4915          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 6554          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 8192          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 9830          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 1434          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 1459          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 1485          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 1510          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 1536          |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                                   | 0.00480917655 | 0.004809176 ± 0.000000009 | ✓      |

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ADDCoefCalc

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     |        | ✓ |

| Test Step 1.20 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | -3          |   |
| VehicleSpeed_Kph_T_f32                          | 180.21      |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 0           |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 0           |   |
| t_CmnVehSpd_Kph_u9p7[0]                         | 10368       |   |
| t_CmnVehSpd_Kph_u9p7[1]                         | 10496       |   |
| t_CmnVehSpd_Kph_u9p7[2]                         | 10624       |   |
| t_CmnVehSpd_Kph_u9p7[3]                         | 10752       |   |
| t_CmnVehSpd_Kph_u9p7[4]                         | 10880       |   |
| t_CmnVehSpd_Kph_u9p7[5]                         | 11008       |   |
| t_CmnVehSpd_Kph_u9p7[6]                         | 11136       |   |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11264       |   |
| t_CmnVehSpd_Kph_u9p7[8]                         | 11392       |   |
| t_CmnVehSpd_Kph_u9p7[9]                         | 11520       |   |
| t_CmnVehSpd_Kph_u9p7[10]                        | 11648       |   |
| t_CmnVehSpd_Kph_u9p7[11]                        | 11776       |   |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 16794       |   |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 17203       |   |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 17613       |   |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 18022       |   |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 18432       |   |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 18842       |   |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 19251       |   |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 19661       |   |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 20070       |   |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 20480       |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 704         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 814         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 924         |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 1034        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 1144        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 1254        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 1364        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 1475        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 1585        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 1695        |   |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 13          |   |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 15          |   |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 18          |   |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 20          |   |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 23          |   |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 26          |   |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 28          |   |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 31          |   |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 33          |   |

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| Name                            | Input Value   |                          |        |
|---------------------------------|---------------|--------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[9]     | 36            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 38            |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 41            |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0] | 4915          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1] | 6554          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2] | 8192          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3] | 9830          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4] | 11469         |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]     | 1690          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]     | 1715          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]     | 1741          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]     | 1766          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]     | 1792          |                          |        |
| Name                            | Actual Value  | Expected Value           | Result |
| ADDCoefCalc()                   | 0.00464858953 | 0.00464859 ± 0.000000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.21 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | -4          |
| VehicleSpeed_Kph_T_f32                          | 192         |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.1         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 342         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 683         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2728        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3409        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 6554        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 6554        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 5248        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 5376        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5504        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 5632        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 5760        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 5888        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 6016        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 6144        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 6272        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 6400        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 6528        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 6656        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 20890       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 21299       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 21709       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 22118       |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 22528       |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 22938       |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 23347       |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 23757       |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 24166       |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 24576       |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0]       | 885         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1]       | 986         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]       | 1087        |

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ADDCoefCalc

| Name                                      | Input Value   |                         |        |
|---|---------------|-------------------------|--------|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188          |                         |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288          |                         |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389          |                         |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490          |                         |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591          |                         |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692          |                         |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1793          |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 15            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 18            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 20            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 23            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 26            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 28            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 31            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 33            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 36            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 38            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 41            |                         |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 44            |                         |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]           | 6554          |                         |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]           | 8192          |                         |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]           | 9830          |                         |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]           | 11469         |                         |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]           | 13107         |                         |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]               | 1894          |                         |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]               | 1920          |                         |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]               | 1946          |                         |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]               | 1971          |                         |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]               | 1997          |                         |        |
| Name                                      | Actual Value  | Expected Value          | Result |
| ADDCoefCalc()                             | 0.00929849967 | 0.0092985 ± 0.000000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.22 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | -5          |
| VehicleSpeed_Kph_T_f32                          | 204         |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.2         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 523         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1038        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1553        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2583        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3099        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3614        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4129        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4644        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5159        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3302        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3725        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 4148        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4572        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4995        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5419        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 3968        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 4096        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 4224        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 4352        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 4480        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 4608        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 4736        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 4864        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 4992        |

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| Name                                      | Input Value   |                           |        |
|---|---------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[9]                   | 5120          |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 5248          |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 5376          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672         |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 161           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1160          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1326          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1493          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659          |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 18            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 20            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 23            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 26            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 28            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 31            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 33            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 36            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 38            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 41            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 44            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 46            |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]           | 8192          |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]           | 9830          |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]           | 11469         |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]           | 13107         |                           |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[4]           | 14746         |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]               | 1178          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]               | 1203          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]               | 1229          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]               | 1254          |                           |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]               | 1280          |                           |        |
| Name                                      | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                             | 0.00246831775 | 0.002468318 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.23 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | -6          |   |
| VehicleSpeed_Kph_T_f32                          | 216.25      |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.3         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 704         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 814         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 924         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1034        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1144        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1254        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1364        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1475        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1585        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1695        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 523         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1038        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1553        |   |

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ADDCoefCalc

| Name  | Input Value   | Expected Value            | Result |
|---|---------------|---------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2068          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4644          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5159          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128           |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256           |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384           |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512           |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640           |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768           |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896           |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024          |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152          |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 1280          |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 1408          |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 1536          |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 28262         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 28672         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 29082         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 29491         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 29901         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 30310         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 30720         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 31130         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 31539         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 31949         |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 0             |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 0             |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 20            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 23            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 26            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 28            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 31            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 33            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 36            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 38            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 41            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 44            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 46            |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 49            |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 1638          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 3277          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 4915          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 6554          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 8192          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 1434          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 1459          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 1485          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 1510          |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 1536          |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                                   | 0.00100163568 | 0.001001636 ± 0.000000009 | ✓      |

T ✓

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ADDCoefCalc

## Test Step 1.24 (Repeat Count = 1)

| Name  | Input Value |
|---|-------------|
| BaseAssistCmd_MtrNm_T_f32                       | -7          |
| VehicleSpeed_Kph_T_f32                          | 228.25      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.4         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][0] | 885         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][1] | 986         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][2] | 1087        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][3] | 1188        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][4] | 1288        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][5] | 1389        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][6] | 1490        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][7] | 1591        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][8] | 1692        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[0][9] | 1793        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][0] | 704         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][1] | 814         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][2] | 924         |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][3] | 1034        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][4] | 1144        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][5] | 1254        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][6] | 1364        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][7] | 1475        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][8] | 1585        |
| t2_FDD_ADDRollingTblyM_MtrNmpRadpS_um1p17[1][9] | 1695        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                         | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                         | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                        | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                        | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 8192        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[0]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[1]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[2]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[3]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[4]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[5]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[6]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[7]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[8]       | 6554        |
| t_FDD_ADDStaticTbly_MtrNmpRadpS_um1p17[9]       | 6554        |
| t_FDD_BlendTbly_Uls_u8p8[0]                     | 49          |
| t_FDD_BlendTbly_Uls_u8p8[1]                     | 51          |
| t_FDD_BlendTbly_Uls_u8p8[2]                     | 54          |
| t_FDD_BlendTbly_Uls_u8p8[3]                     | 57          |
| t_FDD_BlendTbly_Uls_u8p8[4]                     | 60          |
| t_FDD_BlendTbly_Uls_u8p8[5]                     | 63          |
| t_FDD_BlendTbly_Uls_u8p8[6]                     | 66          |
| t_FDD_BlendTbly_Uls_u8p8[7]                     | 68          |
| t_FDD_BlendTbly_Uls_u8p8[8]                     | 71          |
| t_FDD_BlendTbly_Uls_u8p8[9]                     | 74          |
| t_FDD_BlendTbly_Uls_u8p8[10]                    | 77          |
| t_FDD_BlendTbly_Uls_u8p8[11]                    | 80          |
| t_RIAstWIRBlndTbly_Uls_u2p14[0]                 | 3277        |
| t_RIAstWIRBlndTbly_Uls_u2p14[1]                 | 4915        |
| t_RIAstWIRBlndTbly_Uls_u2p14[2]                 | 6554        |
| t_RIAstWIRBlndTbly_Uls_u2p14[3]                 | 8192        |



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ADDCoefCalc

| Name                            | Input Value |
|---------------------------------|-------------|
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1690        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 1715        |

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ADDCoefCalc

| Name                            | Input Value  |                          |        |
|---------------------------------|--------------|--------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[0]     | 65           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]     | 68           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]     | 70           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]     | 73           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]     | 75           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]     | 78           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]     | 80           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]     | 83           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]     | 86           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]     | 88           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 91           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 93           |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0] | 4915         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1] | 6554         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2] | 8192         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3] | 9830         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4] | 11469        |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]     | 1894         |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]     | 1920         |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]     | 1946         |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]     | 1971         |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]     | 1997         |                          |        |
| Name                            | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                   | 0.0226821322 | 0.022682133 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.26 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 3           |   |
| VehicleSpeed_Kph_T_f32                          | 252.24      |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.6         |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2030        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2422        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3206        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4774        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1066        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1212        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1359        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1506        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1653        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1800        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1946        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2093        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 2240        |   |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 2387        |   |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128         |   |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256         |   |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384         |   |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512         |   |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640         |   |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768         |   |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896         |   |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024        |   |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152        |   |
| t_CmnVehSpd_Kph_u9p7[9]                         | 1280        |   |
| t_CmnVehSpd_Kph_u9p7[10]                        | 1408        |   |
| t_CmnVehSpd_Kph_u9p7[11]                        | 1536        |   |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 12698       |   |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 13107       |   |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 13517       |   |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 13926       |   |

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ADDCoefCalc

| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 14336        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 14746        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 15155        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 15565        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 15974        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 16384        |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1608         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2032         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2455         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2878         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3302         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3725         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 4148         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4572         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4995         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5419         |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 93           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 96           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 99           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 101          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 104          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 106          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 109          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 111          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 114          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 116          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 119          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 122          |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]           | 6554         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]           | 8192         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]           | 9830         |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]           | 11469        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]           | 13107        |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]               | 0            |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]               | 0            |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]               | 0            |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]               | 0            |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]               | 0            |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0104283169 | 0.010428317 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.27 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 4           |
| VehicleSpeed_Kph_T_f32                          | 264         |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.7         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2340        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2568        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2796        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3252        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3480        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1246        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1638        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2030        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2422        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2814        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3206        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3598        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3990        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4382        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4774        |

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ADDCoefCalc

| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 16794        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 17203        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 17613        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 18022        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 18432        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 18842        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 19251        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 19661        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 20070        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 20480        |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[0] | 1789         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[1] | 2130         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[2] | 2471         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[3] | 2811         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[4] | 3152         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[5] | 3493         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[6] | 3834         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[7] | 4175         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[8] | 4515         |                          |        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[9] | 4856         |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[0]               | 116          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[1]               | 118          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[2]               | 121          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[3]               | 123          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[4]               | 126          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[5]               | 129          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[6]               | 131          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[7]               | 134          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[8]               | 136          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[9]               | 139          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[10]              | 141          |                          |        |
| t_FDD_BlendTbLY_Uls_u8p8[11]              | 144          |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[0]           | 8192         |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[1]           | 9830         |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[2]           | 11469        |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[3]           | 13107        |                          |        |
| t_RIAstWIRBIndTbLY_Uls_u2p14[4]           | 14746        |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]               | 2048         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]               | 2048         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]               | 2048         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]               | 2048         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]               | 2048         |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0117070675 | 0.011707067 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.28 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 5           |
| VehicleSpeed_Kph_T_f32                          | 276.14      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.8         |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][0] | 1608        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][1] | 2032        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][2] | 2455        |
| t2_FDD_ADDRollingTbLYM_MtrNmpRadpS_um1p17[0][3] | 2878        |

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ADDCoefCalc

| Name  | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3302         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3725         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 4148         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4572         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4995         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5419         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1427         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1655         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1884         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2112         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2340         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2568         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2796         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 3024         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3252         |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3480         |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 12800        |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 12928        |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 13056        |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 13184        |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 13312        |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 13440        |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 13568        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 13696        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 13824        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 13952        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 14208        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 20890        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 21299        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 21709        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 22118        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 22528        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 22938        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 23347        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 23757        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 24166        |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 24576        |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 1608         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 2032         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 2455         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 2878         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 3302         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 3725         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 4148         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 4572         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 4995         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 5419         |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 144          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 146          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 149          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 152          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 154          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 157          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 159          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 162          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 164          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 167          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 169          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 172          |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 6554         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 8192         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 9830         |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 11469        |                          |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 13107        |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 1178         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 1203         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 1229         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 1254         |                          |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 1280         |                          |        |
| Name  | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                                   | 0.0118969213 | 0.011896921 ± 0.00000009 | ✓      |

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ADDCoefCalc

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     |        | ✓ |

| Test Step 1.29 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 6           |   |
| VehicleSpeed_Kph_T_f32                          | 288         |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 2.9         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1789        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2130        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2471        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2811        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3152        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3493        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3834        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4175        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4515        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4856        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1608        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2032        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2455        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2878        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3302        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3725        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 4148        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4572        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4995        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5419        |   |
| t_CmnVehSpd_Kph_u9p7[0]                         | 15488       |   |
| t_CmnVehSpd_Kph_u9p7[1]                         | 15616       |   |
| t_CmnVehSpd_Kph_u9p7[2]                         | 15744       |   |
| t_CmnVehSpd_Kph_u9p7[3]                         | 15872       |   |
| t_CmnVehSpd_Kph_u9p7[4]                         | 16000       |   |
| t_CmnVehSpd_Kph_u9p7[5]                         | 16128       |   |
| t_CmnVehSpd_Kph_u9p7[6]                         | 16256       |   |
| t_CmnVehSpd_Kph_u9p7[7]                         | 16384       |   |
| t_CmnVehSpd_Kph_u9p7[8]                         | 16512       |   |
| t_CmnVehSpd_Kph_u9p7[9]                         | 16640       |   |
| t_CmnVehSpd_Kph_u9p7[10]                        | 16768       |   |
| t_CmnVehSpd_Kph_u9p7[11]                        | 16896       |   |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 24986       |   |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 25395       |   |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 25805       |   |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 26214       |   |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 26624       |   |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 27034       |   |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 27443       |   |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 27853       |   |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 28262       |   |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 28672       |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 1789        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 2130        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 2471        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 2811        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 3152        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 3493        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 3834        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 4175        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 4515        |   |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 4856        |   |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 172         |   |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 174         |   |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 176         |   |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 178         |   |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 180         |   |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 183         |   |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 185         |   |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 187         |   |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 189         |   |

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| Name                            | Input Value  |                          |        |
|---------------------------------|--------------|--------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[9]     | 191          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 193          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 195          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0] | 0            |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1] | 0            |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2] | 0            |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3] | 0            |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4] | 0            |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]     | 282          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]     | 307          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]     | 333          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]     | 358          |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]     | 384          |                          |        |
| Name                            | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                   | 0.0136489868 | 0.013648987 ± 0.00000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.30 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 7           |
| VehicleSpeed_Kph_T_f32                          | 300.25      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 3.2         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1789        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2130        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2471        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2811        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3152        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3834        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4175        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4515        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4856        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 10368       |
| t_CmnVehSpd_Kph_u9p7[1]                         | 10496       |
| t_CmnVehSpd_Kph_u9p7[2]                         | 10624       |
| t_CmnVehSpd_Kph_u9p7[3]                         | 10752       |
| t_CmnVehSpd_Kph_u9p7[4]                         | 10880       |
| t_CmnVehSpd_Kph_u9p7[5]                         | 11008       |
| t_CmnVehSpd_Kph_u9p7[6]                         | 11136       |
| t_CmnVehSpd_Kph_u9p7[7]                         | 11264       |
| t_CmnVehSpd_Kph_u9p7[8]                         | 11392       |
| t_CmnVehSpd_Kph_u9p7[9]                         | 11520       |
| t_CmnVehSpd_Kph_u9p7[10]                        | 11648       |
| t_CmnVehSpd_Kph_u9p7[11]                        | 11776       |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 28672       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 29082       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 29491       |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 29901       |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 30310       |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 30720       |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 31130       |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 31539       |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 31949       |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0]       | 161         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1]       | 328         |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]       | 494         |

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ADDCoefCalc

| Name                                      | Input Value  |                          |        |
|---|--------------|--------------------------|--------|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659         |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 218          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 220          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 223          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 225          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 227          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 230          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 232          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 234          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 237          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 239          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 241          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 243          |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]           | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]           | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]           | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]           | 16384        |                          |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]           | 16384        |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]               | 538          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]               | 563          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]               | 589          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]               | 614          |                          |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]               | 640          |                          |        |
| Name                                      | Actual Value | Expected Value           | Result |
| ADDCoefCalc()                             | 0.0155524611 | 0.015552461 ± 0.00000009 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.31 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 8           |
| VehicleSpeed_Kph_T_f32                          | 312         |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 3.1         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 342         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 683         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2728        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3409        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 161         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 328         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 494         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 661         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 827         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 994         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1160        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1326        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1493        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1659        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 5248        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 5376        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 5504        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 5632        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 5760        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 5888        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 6016        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 6144        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 6272        |



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| Name                                      | Input Value  |                        |        |
|---|--------------|------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[9]                   | 6400         |                        |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 6528         |                        |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 6656         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782         |                        |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 342          |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 683          |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1024         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1364         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1705         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 2046         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 2387         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2728         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 3068         |                        |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 3409         |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 15           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 18           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 20           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 23           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 26           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 28           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 31           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 33           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 36           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 38           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 41           |                        |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 44           |                        |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]           | 4915         |                        |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]           | 6554         |                        |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]           | 8192         |                        |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]           | 9830         |                        |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[4]           | 11469        |                        |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]               | 794          |                        |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]               | 819          |                        |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]               | 845          |                        |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]               | 870          |                        |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]               | 896          |                        |        |
| Name                                      | Actual Value | Expected Value         | Result |
| ADDCoefCalc()                             | 0.0253202002 | 0.0253202 ± 0.00000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.32 (Repeat Count = 1)               |             | ✓ |
|---|-------------|---|
| Name  | Input Value |   |
| BaseAssistCmd_MtrNm_T_f32                       | 1.5         |   |
| VehicleSpeed_Kph_T_f32                          | 324.14      |   |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 3.2         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 523         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1038        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1553        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2068        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2583        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3099        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3614        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4129        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4644        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5159        |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683         |   |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024        |   |

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ADDCoefCalc

| Name  | Input Value   |                          |        |
|---|---------------|--------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068          |                          |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 0             |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 0             |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 8602          |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 9011          |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 9421          |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 9830          |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 10240         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 10650         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 11059         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 11469         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 11878         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 12288         |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 161           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 328           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 494           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 661           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 827           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 994           |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 1160          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 1326          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 1493          |                          |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 1659          |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 116           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 118           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 121           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 123           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 126           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 129           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 131           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 134           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 136           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 139           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 141           |                          |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 144           |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]                 | 1638          |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]                 | 3277          |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]                 | 4915          |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]                 | 6554          |                          |        |
| t_RIAstWIRBlndTbIY_Uls_u2p14[4]                 | 8192          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]                     | 1050          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]                     | 1075          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]                     | 1101          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]                     | 1126          |                          |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]                     | 1152          |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| ADDCoefCalc()                                   | 0.00270421011 | 0.00270421 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

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ADDCoefCalc

## Test Step 1.33 (Repeat Count = 1)

| Name   | Input Value |
|--|-------------|
| BaseAssistCmd_MtrNm_T_f32                      | -1.5        |
| VehicleSpeed_Kph_T_f32                         | 336         |
| WIRCmdAmpBlnd_MtrNm_T_f32                      | 3.3         |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][0] | 704         |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][1] | 814         |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][2] | 924         |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][3] | 1034        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][4] | 1144        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][5] | 1254        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][6] | 1364        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][7] | 1475        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][8] | 1585        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[0][9] | 1695        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][0] | 523         |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][1] | 1038        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][2] | 1553        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][3] | 2068        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][4] | 2583        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][5] | 3099        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][6] | 3614        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][7] | 4129        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][8] | 4644        |
| t2_FDD_ADDRollingTbLY_MtrNmpRadpS_um1p17[1][9] | 5159        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[1]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[2]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[3]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[4]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[5]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[6]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[7]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[8]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[9]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[10]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[11]                       | 32640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]                   | 12698       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                   | 13107       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                   | 13517       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                   | 13926       |
| t_DmpADDCoefX_MtrNm_u4p12[4]                   | 14336       |
| t_DmpADDCoefX_MtrNm_u4p12[5]                   | 14746       |
| t_DmpADDCoefX_MtrNm_u4p12[6]                   | 15155       |
| t_DmpADDCoefX_MtrNm_u4p12[7]                   | 15565       |
| t_DmpADDCoefX_MtrNm_u4p12[8]                   | 15974       |
| t_DmpADDCoefX_MtrNm_u4p12[9]                   | 16384       |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[0]      | 161         |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[1]      | 328         |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[2]      | 494         |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[3]      | 661         |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[4]      | 827         |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[5]      | 994         |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[6]      | 1160        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[7]      | 1326        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[8]      | 1493        |
| t_FDD_ADDStaticTbLY_MtrNmpRadpS_um1p17[9]      | 1659        |
| t_FDD_BlendTbLY_Uls_u8p8[0]                    | 172         |
| t_FDD_BlendTbLY_Uls_u8p8[1]                    | 174         |
| t_FDD_BlendTbLY_Uls_u8p8[2]                    | 176         |
| t_FDD_BlendTbLY_Uls_u8p8[3]                    | 178         |
| t_FDD_BlendTbLY_Uls_u8p8[4]                    | 180         |
| t_FDD_BlendTbLY_Uls_u8p8[5]                    | 183         |
| t_FDD_BlendTbLY_Uls_u8p8[6]                    | 185         |
| t_FDD_BlendTbLY_Uls_u8p8[7]                    | 187         |
| t_FDD_BlendTbLY_Uls_u8p8[8]                    | 189         |
| t_FDD_BlendTbLY_Uls_u8p8[9]                    | 191         |
| t_FDD_BlendTbLY_Uls_u8p8[10]                   | 193         |
| t_FDD_BlendTbLY_Uls_u8p8[11]                   | 195         |
| t_RIAstWIRBlndTbLY_Uls_u2p14[0]                | 3277        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[1]                | 4915        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[2]                | 6554        |
| t_RIAstWIRBlndTbLY_Uls_u2p14[3]                | 8192        |

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ADDCoefCalc

| Name                            | Input Value   |                           |        |
|---------------------------------|---------------|---------------------------|--------|
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1306          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 1331          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 1357          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 1382          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 1408          |                           |        |
| Name                            | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                   | 0.00417356379 | 0.004173564 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |   |

| Test Step 1.34 (Repeat Count = 1)               |  |             |  |
|---|--|-------------|--|
| Name  |  | Input Value |  |
| BaseAssistCmd_MtrNm_T_f32                       |  | 2.9         |  |
| VehicleSpeed_Kph_T_f32                          |  | 348.14      |  |
| WIRCmdAmpBlnd_MtrNm_T_f32                       |  | 3.4         |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] |  | 885         |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] |  | 986         |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] |  | 1087        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] |  | 1188        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] |  | 1288        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] |  | 1389        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] |  | 1490        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] |  | 1591        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] |  | 1692        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] |  | 1793        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] |  | 704         |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] |  | 814         |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] |  | 924         |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] |  | 1034        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] |  | 1144        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] |  | 1254        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] |  | 1364        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] |  | 1475        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] |  | 1585        |  |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] |  | 1695        |  |
| t_CmnVehSpd_Kph_u9p7[0]                         |  | 12800       |  |
| t_CmnVehSpd_Kph_u9p7[1]                         |  | 12928       |  |
| t_CmnVehSpd_Kph_u9p7[2]                         |  | 13056       |  |
| t_CmnVehSpd_Kph_u9p7[3]                         |  | 13184       |  |
| t_CmnVehSpd_Kph_u9p7[4]                         |  | 13312       |  |
| t_CmnVehSpd_Kph_u9p7[5]                         |  | 13440       |  |
| t_CmnVehSpd_Kph_u9p7[6]                         |  | 13568       |  |
| t_CmnVehSpd_Kph_u9p7[7]                         |  | 13696       |  |
| t_CmnVehSpd_Kph_u9p7[8]                         |  | 13824       |  |
| t_CmnVehSpd_Kph_u9p7[9]                         |  | 13952       |  |
| t_CmnVehSpd_Kph_u9p7[10]                        |  | 14080       |  |
| t_CmnVehSpd_Kph_u9p7[11]                        |  | 14208       |  |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    |  | 16794       |  |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    |  | 17203       |  |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    |  | 17613       |  |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    |  | 18022       |  |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    |  | 18432       |  |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    |  | 18842       |  |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    |  | 19251       |  |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    |  | 19661       |  |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    |  | 20070       |  |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    |  | 20480       |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0]       |  | 342         |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1]       |  | 683         |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]       |  | 1024        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3]       |  | 1364        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4]       |  | 1705        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5]       |  | 2046        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6]       |  | 2387        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7]       |  | 2728        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8]       |  | 3068        |  |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9]       |  | 3409        |  |

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| Name                            | Input Value   |                           |        |
|---------------------------------|---------------|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[0]     | 218           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[1]     | 220           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[2]     | 223           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[3]     | 225           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]     | 227           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]     | 230           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]     | 232           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]     | 234           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]     | 237           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]     | 239           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]    | 241           |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]    | 243           |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 4915          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 6554          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 8192          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 9830          |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 11469         |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]     | 1562          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]     | 1587          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]     | 1613          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]     | 1638          |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]     | 1664          |                           |        |
| Name                            | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                   | 0.00614841701 | 0.006148417 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.35 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 3.7         |
| VehicleSpeed_Kph_T_f32                          | 360         |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 3.5         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1066        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1212        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1359        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1506        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1653        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1800        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1946        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2093        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 2240        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 2387        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 885         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 986         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1087        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1188        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1288        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1389        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1490        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1591        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1692        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1793        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 20890       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 21299       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 21709       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 22118       |

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ADDCoefCalc

| Name                                      | Input Value   |                           |        |
|---|---------------|---------------------------|--------|
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 22528         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 22938         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 23347         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 23757         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 24166         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 24576         |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523           |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644          |                           |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159          |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 0             |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 0             |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]           | 6554          |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]           | 8192          |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]           | 9830          |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]           | 11469         |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]           | 13107         |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]               | 1766          |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]               | 1792          |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]               | 1818          |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]               | 1843          |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]               | 1869          |                           |        |
| Name                                      | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                             | 0.00399017334 | 0.003990173 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.36 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | -3.69       |
| VehicleSpeed_Kph_T_f32                          | 372.14      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 3.6         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2030        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2422        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3206        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4774        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1066        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1212        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1359        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1506        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1653        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1800        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1946        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2093        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 2240        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 2387        |

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ADDCoefCalc

| Name                                      | Input Value   |                          |        |
|---|---------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560          |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840          |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120          |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400          |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680          |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960          |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240         |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520         |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800         |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080         |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360         |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262         |                          |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672         |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704           |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814           |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 924           |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1254          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585          |                          |        |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695          |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[0]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[1]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[2]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[3]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[4]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[5]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[6]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[7]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[8]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[9]               | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[10]              | 256           |                          |        |
| t_FDD_BlendTblY_Uls_u8p8[11]              | 256           |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]           | 8192          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]           | 9830          |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]           | 11469         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]           | 13107         |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]           | 14746         |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]               | 410           |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]               | 435           |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]               | 461           |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]               | 486           |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]               | 512           |                          |        |
| Name                                      | Actual Value  | Expected Value           | Result |
| ADDCoefCalc()                             | 0.00827023014 | 0.00827023 ± 0.000000009 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     | ✓      |

| Test Step 1.37 (Repeat Count = 1)               |             |
|---|-------------|
| Name  | Input Value |
| BaseAssistCmd_MtrNm_T_f32                       | 3.9         |
| VehicleSpeed_Kph_T_f32                          | 384.25      |
| WIRCmdAmpBlnd_MtrNm_T_f32                       | 3.7         |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884        |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112        |

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ADDCoefCalc

| Name  | Input Value   |                           |        |
|---|---------------|---------------------------|--------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2340          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2568          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2796          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 3024          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3252          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3480          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1246          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1638          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2030          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2422          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2814          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3206          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3598          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 3990          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4382          |                           |        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4774          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 12800         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                         | 12928         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                         | 13056         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                         | 13184         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                         | 13312         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                         | 13440         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                         | 13568         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                         | 13696         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 13824         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 13952         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 14080         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 14208         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 28262         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 28672         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 29082         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 29491         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 29901         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 30310         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 30720         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 31130         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 31539         |                           |        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 31949         |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 885           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 986           |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]       | 1087          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]       | 1188          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]       | 1288          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]       | 1389          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]       | 1490          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]       | 1591          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]       | 1692          |                           |        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]       | 1793          |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[0]                     | 116           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[1]                     | 118           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[2]                     | 121           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[3]                     | 123           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[4]                     | 126           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[5]                     | 129           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[6]                     | 131           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[7]                     | 134           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[8]                     | 136           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[9]                     | 139           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[10]                    | 141           |                           |        |
| t_FDD_BlendTbIY_Uls_u8p8[11]                    | 144           |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0]                 | 6554          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1]                 | 8192          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2]                 | 9830          |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3]                 | 11469         |                           |        |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4]                 | 13107         |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[0]                     | 666           |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[1]                     | 691           |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[2]                     | 717           |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[3]                     | 742           |                           |        |
| t_WIRBIndTbIX_MtrNm_u8p8[4]                     | 768           |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| ADDCoefCalc()                                   | 0.00845662132 | 0.008456621 ± 0.000000009 | ✓      |



# TEST DETAILS REPORT

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ADDCoefCalc



| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5     | IntplVarXY_u16_u16Xu16Y_Cnt | 5     |        | ✓ |

# TEST DETAILS REPORT

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DriverVelCalc

|             |               |
|-------------|---------------|
| Project     | FDD_Inertia   |
| Module      | FDD_Inertia   |
| Test Object | DriverVelCalc |

## Instrumentation: Test Object Only

|                         |       |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Branch (C1) Coverage    | 100 % |

## Statistics

|                 |     |
|-----------------|-----|
| Total Testcases | 1   |
| Successful      | 1 ✓ |
| Failed          | 0   |
| Not Executed    | 0   |

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DriverVelCalc



## Module Properties

|                        |  |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp   |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml   |
| Target Environment     | TI TMS 570 PLS UDE (Default)   |
| Kind of Test           | Unit Test  |
| Linker Options         |  |
| Source File(s)         |  |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c   |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NtrLib\src\interpolation.c   |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name                 | Text   |
|----------------------|--|
| Module 'FDD_Inertia' | <p>*****Unit Test Description*****</p> <p>Name of Tester: Spoorti Mali<br/>Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c<br/>Code File(s) Version: 13<br/>Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc<br/>Module Design Document Version: 18<br/>Data Dictionary Version: 16<br/>Unit Test Plan Version: 6<br/>Optimization Level: Level 2<br/>Compiler (CodeGen) Version: TMS470_4.9.5<br/>Model Type: Excel Macro<br/>Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30<br/>Total FLASH Used (Bytes): 1994<br/>Total RAM Used (Bytes): 60<br/>Total CALS Used (Bytes): 328<br/>Special Test Requirements:<br/>Test Date: 09-19-2014<br/>Comments:</p> <p>Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.</p> <p>Note2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.</p> <p>Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of iv□</p> |

# TEST DETAILS REPORT

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DriverVeICalc



| Attributes      |  |
|-----------------|--|
| Name            | Value  |
| UDE Config File | \${PROJECTROOT}\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File  | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



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DriverVelCalc



## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 329.00 Cycles  
TS1.2 341.00 Cycles  
TS1.3 341.00 Cycles  
TS1.4 341.00 Cycles  
TS1.5 341.00 Cycles  
TS1.6 341.00 Cycles  
TS1.7 497.00 Cycles  
TS1.8 341.00 Cycles  
TS1.9 329.00 Cycles  
TS1.10 329.00 Cycles  
TS1.11 329.00 Cycles  
TS1.12 417.00 Cycles  
TS1.13 329.00 Cycles  
TS1.14 341.00 Cycles  
TS1.15 417.00 Cycles  
TS1.16 341.00 Cycles  
TS1.17 397.00 Cycles  
TS1.18 329.00 Cycles  
TS1.19 329.00 Cycles  
TS1.20 329.00 Cycles  
TS1.21 427.00 Cycles  
TS1.22 341.00 Cycles  
TS1.23 341.00 Cycles  
TS1.24 341.00 Cycles  
TS1.25 341.00 Cycles  
TS1.26 407.00 Cycles  
TS1.27 341.00 Cycles  
TS1.28 341.00 Cycles  
TS1.29 341.00 Cycles  
TS1.30 341.00 Cycles  
TS1.31 329.00 Cycles  
TS1.32 341.00 Cycles  
TS1.33 341.00 Cycles  
TS1.34 329.00 Cycles  
TS1.35 341.00 Cycles  
TS1.36 341.00 Cycles  
TS1.37 341.00 Cycles  
TS1.38 341.00 Cycles  
TS1.39 341.00 Cycles  
TS1.40 341.00 Cycles  
TS1.41 341.00 Cycles  
TS1.42 341.00 Cycles  
TS1.43 341.00 Cycles

**Description** Test Vector Description

TS1.1 HwTroque\_HwNm\_T\_f32 = min  
TS1.2 HwTroque\_HwNm\_T\_f32 = max  
TS1.3 HwTroque\_HwNm\_T\_f32 = zero  
TS1.4 HwTroque\_HwNm\_T\_f32 = neg  
TS1.5 HwTroque\_HwNm\_T\_f32 = pos  
TS1.6 CRFMotorVel\_MtrRadpS\_T\_f32 = min  
TS1.7 CRFMotorVel\_MtrRadpS\_T\_f32 = max  
TS1.8 CRFMotorVel\_MtrRadpS\_T\_f32 = zero  
TS1.9 CRFMotorVel\_MtrRadpS\_T\_f32 = neg  
TS1.10 CRFMotorVel\_MtrRadpS\_T\_f32 = pos  
TS1.11 VehicleSpeed\_Kph\_T\_f32 = min  
TS1.12 VehicleSpeed\_Kph\_T\_f32 = max  
TS1.13 VehicleSpeed\_Kph\_T\_f32 = pos  
TS1.14 PrevTbarAng\_HwDeg\_M\_f32 = min  
TS1.15 PrevTbarAng\_HwDeg\_M\_f32 = max  
TS1.16 PrevTbarAng\_HwDeg\_M\_f32 = zero  
TS1.17 PrevTbarAng\_HwDeg\_M\_f32 = neg  
TS1.18 PrevTbarAng\_HwDeg\_M\_f32 = pos  
TS1.19 k\_CmnTbarStiff\_NmpDeg\_f32 = min  
TS1.20 k\_CmnTbarStiff\_NmpDeg\_f32 = max  
TS1.21 k\_CmnTbarStiff\_NmpDeg\_f32 = mid  
TS1.22 k\_CmnSysKinRatio\_MtrDegpHwDeg\_f32 = min  
TS1.23 k\_CmnSysKinRatio\_MtrDegpHwDeg\_f32 = max  
TS1.24 k\_CmnSysKinRatio\_MtrDegpHwDeg\_f32 = mid  
TS1.25 t\_CmnVehSpd\_Kph\_u9p7[12] = min  
TS1.26 t\_CmnVehSpd\_Kph\_u9p7[12] = max  
TS1.27 t\_CmnVehSpd\_Kph\_u9p7[12] = mid  
TS1.28 t\_InrtCmp\_TBarVel\_ScaleFactorTbly\_Uls\_u9p7[12] = min  
TS1.29 t\_InrtCmp\_TBarVel\_ScaleFactorTbly\_Uls\_u9p7[12] = max  
TS1.30 t\_InrtCmp\_TBarVel\_ScaleFactorTbly\_Uls\_u9p7[12] = mid  
TS1.31 k\_InrtCmp\_MtrVel\_ScaleFactor\_Uls\_f32 = min  
TS1.32 k\_InrtCmp\_MtrVel\_ScaleFactor\_Uls\_f32 = max  
TS1.33 k\_InrtCmp\_MtrVel\_ScaleFactor\_Uls\_f32 = mid  
TS1.34 TbarVelFiltSv\_M\_str.K = min  
TS1.35 TbarVelFiltSv\_M\_str.K = max  
TS1.36 TbarVelFiltSv\_M\_str.K = mid  
TS1.37 TbarVelFiltSv\_M\_str.SV = min  
TS1.38 TbarVelFiltSv\_M\_str.SV = max  
TS1.39 TbarVelFiltSv\_M\_str.SV = zero  
TS1.40 TbarVelFiltSv\_M\_str.SV = pos  
TS1.41 TbarVelFiltSv\_M\_str.SV = neg  
TS1.42 All min  
TS1.43 All max

## Test Step 1.1 (Repeat Count = 1)

| Name                       | Input Value |
|----------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32 | -1118       |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| HwTorque_HwNm_T_f32                            | -10          |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | -20          |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -6.6667      |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.001255848  |                           |        |
| VehicleSpeed_Kph_T_f32                         | 0            |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 1            |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 0.5          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 0            |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -0           | 0 ± 0.000009              | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -20          | -20 ± 0.00390625          | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -6.65832758  | -6.658327638 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.2 (Repeat Count = 1)              |             |
|---|-------------|
| Name  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32                    | 1118        |
| HwTorque_HwNm_T_f32                           | 10          |
| PrevTbarAng_HwDeg_M_f32                       | 20          |
| TbarVelFiltSv_M_str.SV_Uls_f32                | 6.6667      |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.715390457 |
| VehicleSpeed_Kph_T_f32                        | 511.9921875 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 100         |
| k_CmnTbarStiff_NmpDeg_f32                     | 10          |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 1           |
| t_CmnVehSpd_Kph_u9p7[0]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[1]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[2]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[3]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[4]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[5]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[6]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[7]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[8]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[9]                       | 32640       |
| t_CmnVehSpd_Kph_u9p7[10]                      | 32640       |
| t_CmnVehSpd_Kph_u9p7[11]                      | 32640       |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2] | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3] | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4] | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5] | 128         |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 128          |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 128          |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 128          |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 128          |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 128          |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 128          |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -10740.3115  | -10740.31169 ± 0.09       | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 1            | 1 ± 0.00390625            | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -6794.31201  | -6794.311935 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.3 (Repeat Count = 1) ✓

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | 100.5        |                           |        |
| HwTorque_HwNm_T_f32                            | -10          |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | -8.33        |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.2587       |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.1258       |                           |        |
| VehicleSpeed_Kph_T_f32                         | 100.02       |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 10.2         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 1.2          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.9          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 128          |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 256          |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 384          |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 512          |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 640          |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 768          |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 896          |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 1024         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536         |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0]  | 1            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1]  | 3            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 4            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 5            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 6            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 8            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 9            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 10           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 12           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 13           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 14           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 15           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | 90.4685822   | 90.46858168 ± 0.00009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -8.33333302  | -8.333333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 0.890704095  | 0.890688873 ± 0.00390625  | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.4 (Repeat Count = 1) ✓

| Name                           | Input Value |
|--------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32     | -100.6      |
| HwTorque_HwNm_T_f32            | 10          |
| PrevTbarAng_HwDeg_M_f32        | 3.9995      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.3697      |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.2365      |



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DriverVelCalc

| Name   | Input Value  |                         |        |
|--|--------------|-------------------------|--------|
| VehicleSpeed_Kph_T_f32                         | 200.03       |                         |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 20.3         |                         |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 2.5          |                         |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.8          |                         |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                         |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                         |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                         |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                         |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                         |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                         |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                         |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                         |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                         |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                         |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                         |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 3            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 4            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 5            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 6            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 8            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 9            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 10           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 12           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 13           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 14           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 15           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 17           |                         |        |
| Name   | Actual Value | Expected Value          | Result |
| DriverVelCalc()                                | -80.3920822  | -80.39208153 ± 0.00009  | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 4            | 4 ± 0.00390625          | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.86838663   | 1.86839095 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.5 (Repeat Count = 1)              |             |
|---|-------------|
| Name  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32                    | 200.2       |
| HwTorque_HwNm_T_f32                           | 0           |
| PrevTbarAng_HwDeg_M_f32                       | 0.01        |
| TbarVelFiltSv_M_str.SV_Uls_f32                | 3.2145      |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.35874     |
| VehicleSpeed_Kph_T_f32                        | 300.05      |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 30.4        |
| k_CmnTbarStiff_NmpDeg_f32                     | 3.4         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 0.7         |
| t_CmnVehSpd_Kph_u9p7[0]                       | 6784        |
| t_CmnVehSpd_Kph_u9p7[1]                       | 6912        |
| t_CmnVehSpd_Kph_u9p7[2]                       | 7040        |
| t_CmnVehSpd_Kph_u9p7[3]                       | 7168        |
| t_CmnVehSpd_Kph_u9p7[4]                       | 7296        |
| t_CmnVehSpd_Kph_u9p7[5]                       | 7424        |
| t_CmnVehSpd_Kph_u9p7[6]                       | 7552        |
| t_CmnVehSpd_Kph_u9p7[7]                       | 7680        |
| t_CmnVehSpd_Kph_u9p7[8]                       | 7808        |
| t_CmnVehSpd_Kph_u9p7[9]                       | 7936        |
| t_CmnVehSpd_Kph_u9p7[10]                      | 8064        |
| t_CmnVehSpd_Kph_u9p7[11]                      | 8192        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 5           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 6           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2] | 8           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3] | 9           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4] | 10          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5] | 12          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6] | 13          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7] | 14          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8] | 15          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9] | 17          |

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DriverVelCalc

| Name   | Input Value  |                         |        |
|--|--------------|-------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 18           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 19           |                         |        |
| Name   | Actual Value | Expected Value          | Result |
| DriverVelCalc()                                | 140.161072   | 140.161078 ± 0.0009     | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | 0            | 0 ± 0.00390625          | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 0.267630339  | 0.26763027 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.6 (Repeat Count = 1)

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | -200.1       |                          |        |
| HwTorque_HwNm_T_f32                            | -5.5         |                          |        |
| PrevTbarAng_HwDeg_M_f32                        | -1.221       |                          |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 4.5623       |                          |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.47856      |                          |        |
| VehicleSpeed_Kph_T_f32                         | 400.06       |                          |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 40.5         |                          |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 4.5          |                          |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.6          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 128          |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 256          |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 384          |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 512          |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 640          |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 768          |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 896          |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 1024         |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152         |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280         |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408         |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536         |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 6            |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 8            |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 9            |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 10           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 12           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 13           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 14           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 15           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 17           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 18           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 19           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 20           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | -119.829559  | -119.8295518 ± 0.0009    | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | -1.22222221  | -1.22222222 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 2.08650517   | 2.086512379 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.7 (Repeat Count = 1)

| Name                                 | Input Value |  |  |
|--------------------------------------|-------------|--|--|
| CRFMotorVel_MtrRadpS_T_f32           | 300.03      |  |  |
| HwTorque_HwNm_T_f32                  | 5.2         |  |  |
| PrevTbarAng_HwDeg_M_f32              | 0.92987     |  |  |
| TbarVelFiltSv_M_str.SV_Uls_f32       | 5.8745      |  |  |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.58963     |  |  |
| VehicleSpeed_Kph_T_f32               | 123.07      |  |  |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 50.6        |  |  |
| k_CmnTbarStiff_NmpDeg_f32            | 5.6         |  |  |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5         |  |  |

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DriverVelCalc

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0]  | 8            |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1]  | 9            |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 10           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 12           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 13           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 14           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 15           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 17           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 18           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 19           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 20           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 22           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | 150.29483    | 150.2948274 ± 0.0009     | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | 0.928571403  | 0.928571429 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 2.02786994   | 2.027880229 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function             | Count | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

| Test Step 1.8 (Repeat Count = 1)               |              |                       |        |  | ✓ |
|--|--------------|-----------------------|--------|--|---|
| Name   | Input Value  |                       |        |  |   |
| CRFMotorVel_MtrRadpS_T_f32                     | -1118        |                       |        |  |   |
| HwTorque_HwNm_T_f32                            | 1.6          |                       |        |  |   |
| PrevTbarAng_HwDeg_M_f32                        | 0.2461       |                       |        |  |   |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.369       |                       |        |  |   |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.63214      |                       |        |  |   |
| VehicleSpeed_Kph_T_f32                         | 150.08       |                       |        |  |   |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 60.8         |                       |        |  |   |
| k_CmnTbarStiff_NmpDeg_f32                      | 6.5          |                       |        |  |   |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.4          |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[0]                        | 12800        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[1]                        | 12928        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[2]                        | 13056        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[3]                        | 13184        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[4]                        | 13312        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[5]                        | 13440        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[6]                        | 13568        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[7]                        | 13696        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[8]                        | 13824        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[9]                        | 13952        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[10]                       | 14080        |                       |        |  |   |
| t_CmnVehSpd_Kph_u9p7[11]                       | 14208        |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0]  | 9            |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1]  | 10           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 12           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 13           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 14           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 15           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 17           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 18           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 19           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 20           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 22           |                       |        |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 23           |                       |        |  |   |
| Name   | Actual Value | Expected Value        | Result |  |   |
| DriverVelCalc()                                | -447.362946  | -447.3629225 ± 0.0009 | ✓      |  |   |

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DriverVelCalc

| Name                           | Actual Value | Expected Value            | Result |
|--------------------------------|--------------|---------------------------|--------|
| PrevTbarAng_HwDeg_M_f32        | 0.246153846  | 0.246153846 ± 0.00390625  | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.854439139 | -0.854441186 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.9 (Repeat Count = 1)

| Name   | Input Value |
|--|-------------|
| CRFMotorVel_MtrRadpS_T_f32                     | 1118        |
| HwTorque_HwNm_T_f32                            | -1.2        |
| PrevTbarAng_HwDeg_M_f32                        | -0.15321    |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -3.124      |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.014785    |
| VehicleSpeed_Kph_T_f32                         | 16.25       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 70.1        |
| k_CmnTbarStiff_NmpDeg_f32                      | 7.8         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.3         |
| t_CmnVehSpd_Kph_u9p7[0]                        | 15488       |
| t_CmnVehSpd_Kph_u9p7[1]                        | 15616       |
| t_CmnVehSpd_Kph_u9p7[2]                        | 15744       |
| t_CmnVehSpd_Kph_u9p7[3]                        | 15872       |
| t_CmnVehSpd_Kph_u9p7[4]                        | 16000       |
| t_CmnVehSpd_Kph_u9p7[5]                        | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                        | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                        | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                        | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                        | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]                       | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16896       |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 10          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 12          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 13          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 14          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 15          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 17          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 18          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 19          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 20          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 22          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 23          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 24          |

| Name                           | Actual Value | Expected Value            | Result |
|--------------------------------|--------------|---------------------------|--------|
| DriverVelCalc()                | 335.105377   | 335.1053608 ± 0.0009      | ✓      |
| PrevTbarAng_HwDeg_M_f32        | -0.15384616  | -0.153846154 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.08251452  | -3.082514427 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.10 (Repeat Count = 1)

| Name                                 | Input Value |
|--------------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32           | 0           |
| HwTorque_HwNm_T_f32                  | 2.2         |
| PrevTbarAng_HwDeg_M_f32              | 0.27        |
| TbarVelFiltSv_M_str.SV_Uls_f32       | -4.5511     |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.025896    |
| VehicleSpeed_Kph_T_f32               | 58.63       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 80.2        |
| k_CmnTbarStiff_NmpDeg_f32            | 8.1         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2         |
| t_CmnVehSpd_Kph_u9p7[0]              | 10368       |
| t_CmnVehSpd_Kph_u9p7[1]              | 10496       |
| t_CmnVehSpd_Kph_u9p7[2]              | 10624       |
| t_CmnVehSpd_Kph_u9p7[3]              | 10752       |

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DriverVelCalc

| Name   | Input Value |
|--|-------------|
| t_CmnVehSpd_Kph_u9p7[4]                        | 10880       |
| t_CmnVehSpd_Kph_u9p7[5]                        | 11008       |
| t_CmnVehSpd_Kph_u9p7[6]                        | 11136       |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11264       |
| t_CmnVehSpd_Kph_u9p7[8]                        | 11392       |
| t_CmnVehSpd_Kph_u9p7[9]                        | 11520       |
| t_CmnVehSpd_Kph_u9p7[10]                       | 11648       |
| t_CmnVehSpd_Kph_u9p7[11]                       | 11776       |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 24          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 26          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 27          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 29          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 30          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 31          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 33          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 34          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 36          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 37          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 38          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 40          |

| Name                           | Actual Value | Expected Value            | Result |
|--------------------------------|--------------|---------------------------|--------|
| DriverVelCalc()                | -1.15806818  | -1.15806835 ± 0.000009    | ✓      |
| PrevTbarAng_HwDeg_M_f32        | 0.271604925  | 0.271604938 ± 0.00390625  | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.41246414  | -4.412463974 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.11 (Repeat Count = 1)              |  |              |                           | ✔      |
|--|--|--------------|---------------------------|--------|
| Name   |  | Input Value  |                           |        |
| CRFMotorVel_MtrRadpS_T_f32                     |  | -450         |                           |        |
| HwTorque_HwNm_T_f32                            |  | -2.7         |                           |        |
| PrevTbarAng_HwDeg_M_f32                        |  | -0.292       |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 |  | -5.7412      |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  |  | 0.03698      |                           |        |
| VehicleSpeed_Kph_T_f32                         |  | 22.51        |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              |  | 90.5         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      |  | 9.2          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           |  | 0.1          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        |  | 5248         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        |  | 5376         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        |  | 5504         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        |  | 5632         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        |  | 5760         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        |  | 5888         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        |  | 6016         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        |  | 6144         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        |  | 6272         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        |  | 6400         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       |  | 6528         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       |  | 6656         |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  |  | 33           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  |  | 34           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  |  | 35           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  |  | 36           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  |  | 38           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  |  | 39           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  |  | 40           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  |  | 41           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  |  | 43           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  |  | 44           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] |  | 45           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] |  | 47           |                           |        |
| Name   |  | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                |  | -47.2626114  | -47.26260964 ± 0.00009    | ✔      |
| PrevTbarAng_HwDeg_M_f32                        |  | -0.29347828  | -0.293478261 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 |  | -5.55622387  | -5.556223467 ± 0.00390625 | ✔      |

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DriverVelCalc



| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.12 (Repeat Count = 1)

| Name   | Input Value  |                          |        |  |
|--|--------------|--------------------------|--------|--|
| CRFMotorVel_MtrRadpS_T_f32                     | 400          |                          |        |  |
| HwTorque_HwNm_T_f32                            | 3.6          |                          |        |  |
| PrevTbarAng_HwDeg_M_f32                        | 2.39         |                          |        |  |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.2587       |                          |        |  |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.02547      |                          |        |  |
| VehicleSpeed_Kph_T_f32                         | 33.25        |                          |        |  |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 11.2         |                          |        |  |
| k_CmnTbarStiff_NmpDeg_f32                      | 1.5          |                          |        |  |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.9          |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[0]                        | 3968         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[1]                        | 4096         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[2]                        | 4224         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[3]                        | 4352         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[4]                        | 4480         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[5]                        | 4608         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[6]                        | 4736         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[7]                        | 4864         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[8]                        | 4992         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[9]                        | 5120         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[10]                       | 5248         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[11]                       | 5376         |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 47           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 48           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 49           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 51           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 52           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 53           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 54           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 56           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 57           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 58           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 60           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 61           |                          |        |  |
| Name   | Actual Value | Expected Value           | Result |  |
| DriverVelCalc()                                | 360.101318   | 360.1013205 ± 0.0009     | ✓      |  |
| PrevTbarAng_HwDeg_M_f32                        | 2.39999986   | 2.4 ± 0.00390625         | ✓      |  |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.35398781   | 1.353990911 ± 0.00390625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.13 (Repeat Count = 1)

| Name                                 | Input Value |  |  |  |
|--------------------------------------|-------------|--|--|--|
| CRFMotorVel_MtrRadpS_T_f32           | -300.12     |  |  |  |
| HwTorque_HwNm_T_f32                  | -3.1        |  |  |  |
| PrevTbarAng_HwDeg_M_f32              | -1.239      |  |  |  |
| TbarVelFiltSv_M_str.SV_Uls_f32       | 2.3697      |  |  |  |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.02145     |  |  |  |
| VehicleSpeed_Kph_T_f32               | 0           |  |  |  |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 22.3        |  |  |  |
| k_CmnTbarStiff_NmpDeg_f32            | 2.5         |  |  |  |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.8         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[0]              | 128         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[1]              | 256         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[2]              | 384         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[3]              | 512         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[4]              | 640         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[5]              | 768         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[6]              | 896         |  |  |  |
| t_CmnVehSpd_Kph_u9p7[7]              | 1024        |  |  |  |

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| Name   | Input Value  |                          |        |  |
|--|--------------|--------------------------|--------|--|
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408         |                          |        |  |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536         |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 58           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 59           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 60           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 62           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 63           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 64           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 66           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 67           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 68           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 69           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 71           |                          |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 72           |                          |        |  |
| Name   | Actual Value | Expected Value           | Result |  |
| DriverVelCalc()                                | -239.688934  | -239.6889354 ± 0.0009    | ✓      |  |
| PrevTbarAng_HwDeg_M_f32                        | -1.2400001   | -1.24 ± 0.00390625       | ✓      |  |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 2.30814433   | 2.308144935 ± 0.00390625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.14 (Repeat Count = 1)

| Name   | Input Value  |                         |        |  |
|--|--------------|-------------------------|--------|--|
| CRFMotorVel_MtrRadpS_T_f32                     | 699.23       |                         |        |  |
| HwTorque_HwNm_T_f32                            | 4.2          |                         |        |  |
| PrevTbarAng_HwDeg_M_f32                        | 1.191        |                         |        |  |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 3.2145       |                         |        |  |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.03692      |                         |        |  |
| VehicleSpeed_Kph_T_f32                         | 511.9921875  |                         |        |  |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 33.5         |                         |        |  |
| k_CmnTbarStiff_NmpDeg_f32                      | 3.5          |                         |        |  |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.99         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                         |        |  |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 72           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 73           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 74           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 76           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 77           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 78           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 80           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 81           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 82           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 83           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 85           |                         |        |  |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 86           |                         |        |  |
| Name   | Actual Value | Expected Value          | Result |  |
| DriverVelCalc()                                | 693.519104   | 693.5191138 ± 0.0009    | ✓      |  |
| PrevTbarAng_HwDeg_M_f32                        | 1.19999993   | 1.2 ± 0.00390625        | ✓      |  |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 3.26195955   | 3.26196066 ± 0.00390625 | ✓      |  |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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## Test Step 1.15 (Repeat Count = 1)

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | -500.45      |                          |        |
| HwTorque_HwNm_T_f32                            | -4.5         |                          |        |
| PrevTbarAng_HwDeg_M_f32                        | -0.997       |                          |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 4.5623       |                          |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.01258      |                          |        |
| VehicleSpeed_Kph_T_f32                         | 55.52        |                          |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 44.4         |                          |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 4.5          |                          |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.6          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 6784         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 6912         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 7040         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 7168         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7296         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 7424         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 7552         |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 7808         |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 7936         |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 8064         |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 8192         |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 86           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 87           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 88           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 89           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 90           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 91           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 92           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 93           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 94           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 95           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 96           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 97           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | -297.880035  | -297.8800114 ± 0.0009    | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -1           | -1 ± 0.00390625          | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 4.4860363    | 4.486036266 ± 0.00390625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.16 (Repeat Count = 1)

| Name  | Input Value |
|---|-------------|
| CRFMotorVel_MtrRadpS_T_f32                    | 600.63      |
| HwTorque_HwNm_T_f32                           | -10         |
| PrevTbarAng_HwDeg_M_f32                       | -20         |
| TbarVelFiltSv_M_str.SV_Uls_f32                | 5.8745      |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.03257     |
| VehicleSpeed_Kph_T_f32                        | 17.17       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 55.6        |
| k_CmnTbarStiff_NmpDeg_f32                     | 0.5         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 0.5         |
| t_CmnVehSpd_Kph_u9p7[0]                       | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                       | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                       | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                       | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                       | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                       | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                       | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                       | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                       | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                       | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                      | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                      | 1536        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 109         |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 110         |



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| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 111          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 113          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 114          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 115          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 116          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 117          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 118          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 119          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 121          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 122          |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | 305.571442   | 305.5714494 ± 0.0009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -20          | -20 ± 0.00390625         | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 5.68316746   | 5.683167535 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.17 (Repeat Count = 1) ✓

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | -600.84      |                           |        |
| HwTorque_HwNm_T_f32                            | 10           |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | 20           |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.369       |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.096321     |                           |        |
| VehicleSpeed_Kph_T_f32                         | 27.95        |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 66.5         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 0.5          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.4          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 1            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 3            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 4            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 5            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 6            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 8            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 9            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 10           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 12           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 13           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 14           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 15           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -240.374832  | -240.3748238 ± 0.0009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 20           | 20 ± 0.00390625           | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.1408155   | -2.140815551 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.18 (Repeat Count = 1) ✓

| Name                       | Input Value |
|----------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32 | 150.14      |

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DriverVelCalc

| Name   | Input Value   |                           |        |
|--|---------------|---------------------------|--------|
| HwTorque_HwNm_T_f32                            | 0.05          |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | 0             |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -3.124        |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.047852      |                           |        |
| VehicleSpeed_Kph_T_f32                         | 37.02         |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 77.2          |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 10            |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.3           |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 12800         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 12928         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 13056         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 13184         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 13312         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 13440         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 13568         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 13696         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 13824         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 13952         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 14080         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 14208         |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 3             |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 4             |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 5             |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 6             |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 8             |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 9             |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 10            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 12            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 13            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 14            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 15            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 17            |                           |        |
| Name   | Actual Value  | Expected Value            | Result |
| DriverVelCalc()                                | 44.9518433    | 44.95184416 ± 0.00009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 0.00499999989 | 0.005 ± 0.00390625        | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.85488033   | -2.854880352 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.19 (Repeat Count = 1)             |             |
|---|-------------|
| Name  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32                    | -150.62     |
| HwTorque_HwNm_T_f32                           | -7.5        |
| PrevTbarAng_HwDeg_M_f32                       | -0.889      |
| TbarVelFiltSv_M_str.SV_Uls_f32                | -4.5511     |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.2356      |
| VehicleSpeed_Kph_T_f32                        | 11.03       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 88.2        |
| k_CmnTbarStiff_NmpDeg_f32                     | 8.5         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 0.2         |
| t_CmnVehSpd_Kph_u9p7[0]                       | 15488       |
| t_CmnVehSpd_Kph_u9p7[1]                       | 15616       |
| t_CmnVehSpd_Kph_u9p7[2]                       | 15744       |
| t_CmnVehSpd_Kph_u9p7[3]                       | 15872       |
| t_CmnVehSpd_Kph_u9p7[4]                       | 16000       |
| t_CmnVehSpd_Kph_u9p7[5]                       | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                       | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                       | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                       | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                       | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]                      | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]                      | 16896       |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 5           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 6           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2] | 8           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3] | 9           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4] | 10          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5] | 12          |

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| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 13           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 14           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 15           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 17           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 18           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 19           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -30.2861042  | -30.28610622 ± 0.00009    | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -0.882352948 | -0.882352941 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.69583821  | -2.695837311 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.20 (Repeat Count = 1) ✓

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | 250.24       |                           |        |
| HwTorque_HwNm_T_f32                            | 8.2          |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | 0.861        |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -5.7412      |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.3479       |                           |        |
| VehicleSpeed_Kph_T_f32                         | 33.04        |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 99.3         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 9.5          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.1          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 10368        |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 10496        |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 10624        |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 10752        |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 10880        |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 11008        |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 11136        |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11264        |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 11392        |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 11520        |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 11648        |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 11776        |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 6            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 8            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 9            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 10           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 12           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 13           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 14           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 15           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 17           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 18           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 19           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 20           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | 24.7503471   | 24.7503467 ± 0.00009      | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 0.863157868  | 0.863157895 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -3.36847568  | -3.368470731 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.21 (Repeat Count = 1) ✓

| Name                           | Input Value |
|--------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32     | -250.62     |
| HwTorque_HwNm_T_f32            | -8.5        |
| PrevTbarAng_HwDeg_M_f32        | -16.997     |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.2587      |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.2244      |

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DriverVelCalc

| Name   | Input Value  |                         |        |
|--|--------------|-------------------------|--------|
| VehicleSpeed_Kph_T_f32                         | 44.05        |                         |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 27.2         |                         |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 0.5          |                         |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.9          |                         |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 5248         |                         |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 5376         |                         |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5504         |                         |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 5632         |                         |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 5760         |                         |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 5888         |                         |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 6016         |                         |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 6144         |                         |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 6272         |                         |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 6400         |                         |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 6528         |                         |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 6656         |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 8            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 9            |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 10           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 12           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 13           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 14           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 15           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 17           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 18           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 19           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 20           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 22           |                         |        |
| Name   | Actual Value | Expected Value          | Result |
| DriverVelCalc()                                | -225.52951   | -225.5295319 ± 0.0009   | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -17          | -17 ± 0.00390625        | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 0.639618635  | 0.63964772 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.22 (Repeat Count = 1)             |             |
|---|-------------|
| Name  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32                    | 350.14      |
| HwTorque_HwNm_T_f32                           | 9.2         |
| PrevTbarAng_HwDeg_M_f32                       | 0.919       |
| TbarVelFiltSv_M_str.SV_Uls_f32                | 2.3697      |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.3366      |
| VehicleSpeed_Kph_T_f32                        | 376.06      |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 26.8        |
| k_CmnTbarStiff_NmpDeg_f32                     | 10          |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 1           |
| t_CmnVehSpd_Kph_u9p7[0]                       | 3968        |
| t_CmnVehSpd_Kph_u9p7[1]                       | 4096        |
| t_CmnVehSpd_Kph_u9p7[2]                       | 4224        |
| t_CmnVehSpd_Kph_u9p7[3]                       | 4352        |
| t_CmnVehSpd_Kph_u9p7[4]                       | 4480        |
| t_CmnVehSpd_Kph_u9p7[5]                       | 4608        |
| t_CmnVehSpd_Kph_u9p7[6]                       | 4736        |
| t_CmnVehSpd_Kph_u9p7[7]                       | 4864        |
| t_CmnVehSpd_Kph_u9p7[8]                       | 4992        |
| t_CmnVehSpd_Kph_u9p7[9]                       | 5120        |
| t_CmnVehSpd_Kph_u9p7[10]                      | 5248        |
| t_CmnVehSpd_Kph_u9p7[11]                      | 5376        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 9           |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 10          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2] | 12          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3] | 13          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4] | 14          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5] | 15          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6] | 17          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7] | 18          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8] | 19          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9] | 20          |

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DriverVelCalc

| Name   | Input Value  |                         |        |
|--|--------------|-------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 22           |                         |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 23           |                         |        |
| Name   | Actual Value | Expected Value          | Result |
| DriverVelCalc()                                | 350.286285   | 350.2862746 ± 0.0009    | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 0.919999957  | 0.92 ± 0.00390625       | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.74034667   | 1.74035898 ± 0.00390625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.23 (Repeat Count = 1) ✓

| Name   | Input Value  |                                |        |
|--|--------------|--------------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | -350.36      |                                |        |
| HwTorque_HwNm_T_f32                            | -9.21        |                                |        |
| PrevTbarAng_HwDeg_M_f32                        | -1.841       |                                |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 3.2145       |                                |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.0147850001 |                                |        |
| VehicleSpeed_Kph_T_f32                         | 265.02       |                                |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 53.5         |                                |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 5.25         |                                |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.7          |                                |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 128          |                                |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 256          |                                |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 384          |                                |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 512          |                                |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 640          |                                |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 768          |                                |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 896          |                                |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 1024         |                                |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152         |                                |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280         |                                |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408         |                                |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536         |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 10           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 12           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 13           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 14           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 15           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 17           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 18           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 19           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 20           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 22           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 23           |                                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 24           |                                |        |
| Name   | Actual Value | Expected Value                 | Result |
| DriverVelCalc()                                | -244.585281  | -244.585297                    | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | -1.75428569  | -1.75428571428571 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 3.80800867   | 3.80800891                     | ✓      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.24 (Repeat Count = 1) ✓

| Name                                 | Input Value |
|--------------------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32           | 450.52      |
| HwTorque_HwNm_T_f32                  | 1.5         |
| PrevTbarAng_HwDeg_M_f32              | 1.154       |
| TbarVelFiltSv_M_str.SV_Uls_f32       | 4.5623      |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.5599      |
| VehicleSpeed_Kph_T_f32               | 187.06      |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 1           |
| k_CmnTbarStiff_NmpDeg_f32            | 1.3         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6         |

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DriverVelCalc

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 24           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 26           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 27           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 29           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 30           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 31           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 33           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 34           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 36           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 37           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 38           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 40           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | 270.322723   | 270.3227163 ± 0.0009     | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | 1.15384614   | 1.153846154 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.96478438   | 1.964798999 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.25 (Repeat Count = 1)              |  |              |                       |  |
|--|--|--------------|-----------------------|---|
| Name   |  | Input Value  |                       |   |
| CRFMotorVel_MtrRadpS_T_f32                     |  | -450.58      |                       |   |
| HwTorque_HwNm_T_f32                            |  | -1.5         |                       |   |
| PrevTbarAng_HwDeg_M_f32                        |  | -0.551       |                       |   |
| TbarVelFiltSv_M_str.SV_Uls_f32                 |  | 5.8745       |                       |   |
| TbarVelFiltSv_M_str.K_Uls_f32                  |  | 0.1258       |                       |   |
| VehicleSpeed_Kph_T_f32                         |  | 166.08       |                       |   |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              |  | 100          |                       |   |
| k_CmnTbarStiff_NmpDeg_f32                      |  | 2.7          |                       |   |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           |  | 0.5          |                       |   |
| t_CmnVehSpd_Kph_u9p7[0]                        |  | 6784         |                       |   |
| t_CmnVehSpd_Kph_u9p7[1]                        |  | 6912         |                       |   |
| t_CmnVehSpd_Kph_u9p7[2]                        |  | 7040         |                       |   |
| t_CmnVehSpd_Kph_u9p7[3]                        |  | 7168         |                       |   |
| t_CmnVehSpd_Kph_u9p7[4]                        |  | 7296         |                       |   |
| t_CmnVehSpd_Kph_u9p7[5]                        |  | 7424         |                       |   |
| t_CmnVehSpd_Kph_u9p7[6]                        |  | 7552         |                       |   |
| t_CmnVehSpd_Kph_u9p7[7]                        |  | 7680         |                       |   |
| t_CmnVehSpd_Kph_u9p7[8]                        |  | 7808         |                       |   |
| t_CmnVehSpd_Kph_u9p7[9]                        |  | 7936         |                       |   |
| t_CmnVehSpd_Kph_u9p7[10]                       |  | 8064         |                       |   |
| t_CmnVehSpd_Kph_u9p7[11]                       |  | 8192         |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  |  | 33           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  |  | 34           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  |  | 35           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  |  | 36           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  |  | 38           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  |  | 39           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  |  | 40           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  |  | 41           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  |  | 43           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  |  | 44           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] |  | 45           |                       |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] |  | 47           |                       |   |
| Name   |  | Actual Value | Expected Value        | Result  |
| DriverVelCalc()                                |  | -222.18248   | -222.1824911 ± 0.0009 |  |

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DriverVelCalc

| Name                           | Actual Value | Expected Value            | Result |
|--------------------------------|--------------|---------------------------|--------|
| PrevTbarAng_HwDeg_M_f32        | -0.555555522 | -0.555555556 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.84894514   | 4.848943456 ± 0.00390625  | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.26 (Repeat Count = 1)

| Name   | Input Value |
|--|-------------|
| CRFMotorVel_MtrRadps_T_f32                     | -689.69     |
| HwTorque_HwNm_T_f32                            | 2.5         |
| PrevTbarAng_HwDeg_M_f32                        | 0.805       |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.369      |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.2365      |
| VehicleSpeed_Kph_T_f32                         | 2.06        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 25.45       |
| k_CmnTbarStiff_NmpDeg_f32                      | 3.1         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.89        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                        | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                        | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                        | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                        | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                        | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                        | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                        | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 47          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 48          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 49          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 51          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 52          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 53          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 54          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 56          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 57          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 58          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 60          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 61          |

| Name                           | Actual Value | Expected Value             | Result |
|--------------------------------|--------------|----------------------------|--------|
| DriverVelCalc()                | -614.096802  | -614.096787907239 ± 0.0009 | ✓      |
| PrevTbarAng_HwDeg_M_f32        | 0.806451619  | 0.806451613 ± 0.00390625   | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.6370784   | -1.637078274 ± 0.00390625  | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.27 (Repeat Count = 1)

| Name                                 | Input Value |
|--------------------------------------|-------------|
| CRFMotorVel_MtrRadps_T_f32           | -111.41     |
| HwTorque_HwNm_T_f32                  | -2.5        |
| PrevTbarAng_HwDeg_M_f32              | -0.518      |
| TbarVelFiltSv_M_str.SV_Uls_f32       | -3.124      |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.35874     |
| VehicleSpeed_Kph_T_f32               | 267.07      |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 75.5        |
| k_CmnTbarStiff_NmpDeg_f32            | 4.8         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3         |
| t_CmnVehSpd_Kph_u9p7[0]              | 0           |
| t_CmnVehSpd_Kph_u9p7[1]              | 0           |
| t_CmnVehSpd_Kph_u9p7[2]              | 0           |
| t_CmnVehSpd_Kph_u9p7[3]              | 0           |

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| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[4]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 0            |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 0            |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0]  | 58           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1]  | 59           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 60           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 62           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 63           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 64           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 66           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 67           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 68           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 69           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 71           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 72           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -35.2845802  | -35.2845812 ± 0.00009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -0.520833313 | -0.520833333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.51150656  | -2.51151124 ± 0.00390625  | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.28 (Repeat Count = 1)              |  |              |                          |  |
|--|--|--------------|--------------------------|---|
| Name   |  | Input Value  |                          |   |
| CRFMotorVel_MtrRadpS_T_f32                     |  | 222.62       |                          |   |
| HwTorque_HwNm_T_f32                            |  | 3.5          |                          |   |
| PrevTbarAng_HwDeg_M_f32                        |  | 0.671        |                          |   |
| TbarVelFiltSv_M_str.SV_Uls_f32                 |  | -4.5511      |                          |   |
| TbarVelFiltSv_M_str.K_Uls_f32                  |  | 0.47856      |                          |   |
| VehicleSpeed_Kph_T_f32                         |  | 510.03       |                          |   |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              |  | 46.2         |                          |   |
| k_CmnTbarStiff_NmpDeg_f32                      |  | 5.2          |                          |   |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           |  | 0.2          |                          |   |
| t_CmnVehSpd_Kph_u9p7[0]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[1]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[2]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[3]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[4]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[5]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[6]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[7]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[8]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[9]                        |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[10]                       |  | 32640        |                          |   |
| t_CmnVehSpd_Kph_u9p7[11]                       |  | 32640        |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  |  | 72           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  |  | 73           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  |  | 74           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  |  | 76           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  |  | 77           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  |  | 78           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  |  | 80           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  |  | 81           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  |  | 82           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  |  | 83           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] |  | 85           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] |  | 86           |                          |   |
| Name   |  | Actual Value | Expected Value           | Result  |
| DriverVelCalc()                                |  | 43.5075684   | 43.50756976 ± 0.00009    |  |
| PrevTbarAng_HwDeg_M_f32                        |  | 0.673076928  | 0.673076923 ± 0.00390625 |  |
| TbarVelFiltSv_M_str.SV_Uls_f32                 |  | -1.87615919  | -1.87615943 ± 0.00390625 |  |



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| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.29 (Repeat Count = 1)

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | -222.15      |                           |        |
| HwTorque_HwNm_T_f32                            | -3.5         |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | -0.5134      |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -5.7412      |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.58963      |                           |        |
| VehicleSpeed_Kph_T_f32                         | 467.08       |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 28.1         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 6.8          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.1          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 86           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 87           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 88           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 89           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 90           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 91           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 92           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 93           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 94           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 95           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 96           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 97           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -23.2337227  | -23.23372292 ± 0.00009    | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | -0.514705896 | -0.514705882 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.74100852  | -2.74100995 ± 0.00390625  | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.30 (Repeat Count = 1)

| Name                                 | Input Value |  |  |
|--------------------------------------|-------------|--|--|
| CRFMotorVel_MtrRadpS_T_f32           | 333.17      |  |  |
| HwTorque_HwNm_T_f32                  | 4.5         |  |  |
| PrevTbarAng_HwDeg_M_f32              | 0.614       |  |  |
| TbarVelFiltSv_M_str.SV_Uls_f32       | 1.2587      |  |  |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.63214     |  |  |
| VehicleSpeed_Kph_T_f32               | 166.92      |  |  |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 85.6        |  |  |
| k_CmnTbarStiff_NmpDeg_f32            | 7.3         |  |  |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4         |  |  |
| t_CmnVehSpd_Kph_u9p7[0]              | 128         |  |  |
| t_CmnVehSpd_Kph_u9p7[1]              | 256         |  |  |
| t_CmnVehSpd_Kph_u9p7[2]              | 384         |  |  |
| t_CmnVehSpd_Kph_u9p7[3]              | 512         |  |  |
| t_CmnVehSpd_Kph_u9p7[4]              | 640         |  |  |
| t_CmnVehSpd_Kph_u9p7[5]              | 768         |  |  |
| t_CmnVehSpd_Kph_u9p7[6]              | 896         |  |  |
| t_CmnVehSpd_Kph_u9p7[7]              | 1024        |  |  |

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| Name   | Input Value |                          |   |
|--|-------------|--------------------------|---|
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152        |                          |   |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280        |                          |   |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408        |                          |   |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536        |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 0           |                          |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 0           |                          |   |
| Name   |             | Expected Value           |   |
| DriverVelCalc()                                | 133.268005  | 133.268 ± 0.0009         | ✓ |
| PrevTbarAng_HwDeg_M_f32                        | 0.616438329 | 0.616438356 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 1.23370099  | 1.233716615 ± 0.00390625 | ✓ |

| T                           |   |                             |       |        | ✓ |
|-----------------------------|---|-----------------------------|-------|--------|---|
| Actual Function             |   | Expected Function           | Count | Result |   |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |   |

| Test Step 1.31 (Repeat Count = 1)              |             |  |  |  | ✓ |
|--|-------------|--|--|--|---|
| Name   | Input Value |  |  |  |   |
| CRFMotorVel_MtrRadpS_T_f32                     | -333.62     |  |  |  |   |
| HwTorque_HwNm_T_f32                            | -4.5        |  |  |  |   |
| PrevTbarAng_HwDeg_M_f32                        | -0.917      |  |  |  |   |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 2.3697      |  |  |  |   |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.014785    |  |  |  |   |
| VehicleSpeed_Kph_T_f32                         | 10.05       |  |  |  |   |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 36.8        |  |  |  |   |
| k_CmnTbarStiff_NmpDeg_f32                      | 4.9         |  |  |  |   |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.6         |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560        |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840        |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120        |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400        |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680        |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960        |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240       |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520       |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800       |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080       |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360       |  |  |  |   |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640       |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 128         |  |  |  |   |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 128         |  |  |  |   |

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## Test Step 1.32 (Repeat Count = 1)

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | 444.52       |                          |        |
| HwTorque_HwNm_T_f32                            | 5.5          |                          |        |
| PrevTbarAng_HwDeg_M_f32                        | 1.056        |                          |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 3.2145       |                          |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.1258       |                          |        |
| VehicleSpeed_Kph_T_f32                         | 377.06       |                          |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 85.5         |                          |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 5.2          |                          |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.9          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 6784         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 6912         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 7040         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 7168         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7296         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 7424         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 7552         |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 7808         |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 7936         |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 8064         |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 8192         |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 58           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 59           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 60           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 62           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 63           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 64           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 66           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 67           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 68           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 69           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 71           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 72           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | 402.516144   | 402.5161456 ± 0.0009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 1.05769229   | 1.057692308 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 2.91656113   | 2.916562054 ± 0.00390625 | ✔      |

| Actual Function             | Count | Expected Function           | Count | Result |
|-----------------------------|-------|-----------------------------|-------|--------|
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.33 (Repeat Count = 1)

| Name  | Input Value |
|---|-------------|
| CRFMotorVel_MtrRadpS_T_f32                    | -699.63     |
| HwTorque_HwNm_T_f32                           | -5.5        |
| PrevTbarAng_HwDeg_M_f32                       | -0.89       |
| TbarVelFiltSv_M_str.SV_Uls_f32                | 4.5623      |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.2365      |
| VehicleSpeed_Kph_T_f32                        | 38.17       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 29.2        |
| k_CmnTbarStiff_NmpDeg_f32                     | 6.1         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 0           |
| t_CmnVehSpd_Kph_u9p7[0]                       | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                       | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                       | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                       | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                       | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                       | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                       | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                       | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                       | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                       | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                      | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                      | 1536        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 86          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 87          |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 88           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 89           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 90           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 91           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 92           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 93           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 94           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 95           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 96           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 97           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | 0.81372714   | 0.813727562 ± 0.0000009   | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -0.901639342 | -0.901639344 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 2.10696244   | 2.106963591 ± 0.00390625  | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.34 (Repeat Count = 1) ✓

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | 555.74       |                          |        |
| HwTorque_HwNm_T_f32                            | 6.5          |                          |        |
| PrevTbarAng_HwDeg_M_f32                        | 0.83         |                          |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 5.8745       |                          |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.35874      |                          |        |
| VehicleSpeed_Kph_T_f32                         | 1.18         |                          |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 56.5         |                          |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 7.8          |                          |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 1            |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 109          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 110          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 111          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 113          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 114          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 115          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 116          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 117          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 118          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 119          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 121          |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 122          |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | 559.405396   | 559.4054289 ± 0.0009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 0.833333313  | 0.833333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 4.36498117   | 4.36498187 ± 0.00390625  | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.35 (Repeat Count = 1) ✓

| Name                       | Input Value |
|----------------------------|-------------|
| CRFMotorVel_MtrRadpS_T_f32 | -555.81     |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| HwTorque_HwNm_T_f32                            | -6.5         |                           |        |
| PrevTbarAng_HwDeg_M_f32                        | -0.78        |                           |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -2.369       |                           |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.47856      |                           |        |
| VehicleSpeed_Kph_T_f32                         | 276.19       |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 12.3         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 8.3          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.5          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 12800        |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 12928        |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 13056        |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 13184        |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 13312        |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 13440        |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 13568        |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 13696        |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 13824        |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 13952        |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 14080        |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 14208        |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 33           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 34           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 35           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 36           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 38           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 39           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 40           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 41           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 43           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 44           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 45           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 47           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -278.061462  | -278.0614576 ± 0.0009     | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | -0.783132493 | -0.78313253 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -1.98484111  | -1.984843167 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.36 (Repeat Count = 1)             |             |
|---|-------------|
| Name  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32                    | 666.86      |
| HwTorque_HwNm_T_f32                           | 7.5         |
| PrevTbarAng_HwDeg_M_f32                       | 0.799       |
| TbarVelFiltSv_M_str.SV_Uls_f32                | -3.124      |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.001255848 |
| VehicleSpeed_Kph_T_f32                        | 354.2       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 64.4        |
| k_CmnTbarStiff_NmpDeg_f32                     | 9.3         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 0.5         |
| t_CmnVehSpd_Kph_u9p7[0]                       | 15488       |
| t_CmnVehSpd_Kph_u9p7[1]                       | 15616       |
| t_CmnVehSpd_Kph_u9p7[2]                       | 15744       |
| t_CmnVehSpd_Kph_u9p7[3]                       | 15872       |
| t_CmnVehSpd_Kph_u9p7[4]                       | 16000       |
| t_CmnVehSpd_Kph_u9p7[5]                       | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                       | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                       | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                       | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                       | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]                      | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]                      | 16896       |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 47          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 48          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2] | 49          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3] | 51          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4] | 52          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5] | 53          |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 54           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 56           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 57           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 58           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 60           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 61           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | 331.76123    | 331.7612295 ± 0.0009      | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | 0.806451619  | 0.806451613 ± 0.00390625  | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -3.11539769  | -3.115397684 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.37 (Repeat Count = 1) ✓

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | -666.71      |                          |        |
| HwTorque_HwNm_T_f32                            | -7.5         |                          |        |
| PrevTbarAng_HwDeg_M_f32                        | -6.249       |                          |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -4.5511      |                          |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.715390457  |                          |        |
| VehicleSpeed_Kph_T_f32                         | 254.52       |                          |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 27.2         |                          |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 1.2          |                          |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.4          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 10368        |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 10496        |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 10624        |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 10752        |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 10880        |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 11008        |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 11136        |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11264        |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 11392        |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 11520        |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 11648        |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 11776        |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 58           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 59           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 60           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 62           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 63           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 64           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 66           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 67           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 68           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 69           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 71           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 72           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | -267.125366  | -267.1254046 ± 0.0009    | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | -6.24999952  | -6.25 ± 0.00390625       | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -1.6527853   | -1.65298172 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.38 (Repeat Count = 1) ✓

| Name                           | Input Value |  |  |
|--------------------------------|-------------|--|--|
| CRFMotorVel_MtrRadpS_T_f32     | 134.52      |  |  |
| HwTorque_HwNm_T_f32            | 8.5         |  |  |
| PrevTbarAng_HwDeg_M_f32        | 3.86        |  |  |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.7412     |  |  |
| TbarVelFiltSv_M_str.K_Uls_f32  | 0.58746     |  |  |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| VehicleSpeed_Kph_T_f32                         | 154.63       |                           |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 75.1         |                           |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 2.2          |                           |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.3          |                           |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 5248         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 5376         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5504         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 5632         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 5760         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 5888         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 6016         |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 6144         |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 6272         |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 6400         |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 6528         |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 6656         |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  | 24           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  | 26           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  | 27           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  | 29           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  | 30           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  | 31           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  | 33           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  | 34           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  | 36           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  | 37           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] | 38           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] | 40           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | 39.8233643   | 39.8233612 ± 0.00009      | ✔      |
| PrevTbarAng_HwDeg_M_f32                        | 3.86363626   | 3.863636364 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -1.30036688  | -1.300365557 ± 0.00390625 | ✔      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.39 (Repeat Count = 1)             |             |
|---|-------------|
| Name  | Input Value |
| CRFMotorVel_MtrRadpS_T_f32                    | -463.91     |
| HwTorque_HwNm_T_f32                           | -8.5        |
| PrevTbarAng_HwDeg_M_f32                       | -2.35       |
| TbarVelFiltSv_M_str.SV_Uls_f32                | -6.6667     |
| TbarVelFiltSv_M_str.K_Uls_f32                 | 0.35874     |
| VehicleSpeed_Kph_T_f32                        | 55.24       |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32             | 20.6        |
| k_CmnTbarStiff_NmpDeg_f32                     | 3.6         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32          | 0.2         |
| t_CmnVehSpd_Kph_u9p7[0]                       | 3968        |
| t_CmnVehSpd_Kph_u9p7[1]                       | 4096        |
| t_CmnVehSpd_Kph_u9p7[2]                       | 4224        |
| t_CmnVehSpd_Kph_u9p7[3]                       | 4352        |
| t_CmnVehSpd_Kph_u9p7[4]                       | 4480        |
| t_CmnVehSpd_Kph_u9p7[5]                       | 4608        |
| t_CmnVehSpd_Kph_u9p7[6]                       | 4736        |
| t_CmnVehSpd_Kph_u9p7[7]                       | 4864        |
| t_CmnVehSpd_Kph_u9p7[8]                       | 4992        |
| t_CmnVehSpd_Kph_u9p7[9]                       | 5120        |
| t_CmnVehSpd_Kph_u9p7[10]                      | 5248        |
| t_CmnVehSpd_Kph_u9p7[11]                      | 5376        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0] | 33          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1] | 34          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2] | 35          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3] | 36          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4] | 38          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5] | 39          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6] | 40          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7] | 41          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8] | 43          |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9] | 44          |

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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 45           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 47           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -93.6095047  | -93.60949919 ± 0.00009    | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | -2.36111116  | -2.361111111 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -6.26811457  | -6.268088042 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.40 (Repeat Count = 1) ✓

| Name   | Input Value  |                          |        |
|--|--------------|--------------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32                     | 263.42       |                          |        |
| HwTorque_HwNm_T_f32                            | 9.5          |                          |        |
| PrevTbarAng_HwDeg_M_f32                        | 2.25         |                          |        |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 6.6667       |                          |        |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.2874       |                          |        |
| VehicleSpeed_Kph_T_f32                         | 444.52       |                          |        |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 21.7         |                          |        |
| k_CmnTbarStiff_NmpDeg_f32                      | 4.2          |                          |        |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.1          |                          |        |
| t_CmnVehSpd_Kph_u9p7[0]                        | 128          |                          |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 256          |                          |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 384          |                          |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 512          |                          |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 640          |                          |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 768          |                          |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 896          |                          |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 1024         |                          |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152         |                          |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280         |                          |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408         |                          |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536         |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[0]  | 47           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[1]  | 48           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[2]  | 49           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[3]  | 51           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[4]  | 52           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[5]  | 53           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[6]  | 54           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[7]  | 56           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[8]  | 57           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]  | 58           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10] | 60           |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11] | 61           |                          |        |
| Name   | Actual Value | Expected Value           | Result |
| DriverVelCalc()                                | 27.5082359   | 27.50822923 ± 0.00009    | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | 2.26190495   | 2.261904762 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | 6.46143246   | 6.461404706 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.41 (Repeat Count = 1) ✓

| Name                                 | Input Value |  |  |
|--------------------------------------|-------------|--|--|
| CRFMotorVel_MtrRadpS_T_f32           | -522.63     |  |  |
| HwTorque_HwNm_T_f32                  | -9.5        |  |  |
| PrevTbarAng_HwDeg_M_f32              | -1.819      |  |  |
| TbarVelFiltSv_M_str.SV_Uls_f32       | 0           |  |  |
| TbarVelFiltSv_M_str.K_Uls_f32        | 0.025479    |  |  |
| VehicleSpeed_Kph_T_f32               | 333.62      |  |  |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32    | 45.8        |  |  |
| k_CmnTbarStiff_NmpDeg_f32            | 5.2         |  |  |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9         |  |  |



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DriverVelCalc

| Name   | Input Value  |                           |        |
|--|--------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0]                        | 2560         |                           |        |
| t_CmnVehSpd_Kph_u9p7[1]                        | 3840         |                           |        |
| t_CmnVehSpd_Kph_u9p7[2]                        | 5120         |                           |        |
| t_CmnVehSpd_Kph_u9p7[3]                        | 6400         |                           |        |
| t_CmnVehSpd_Kph_u9p7[4]                        | 7680         |                           |        |
| t_CmnVehSpd_Kph_u9p7[5]                        | 8960         |                           |        |
| t_CmnVehSpd_Kph_u9p7[6]                        | 10240        |                           |        |
| t_CmnVehSpd_Kph_u9p7[7]                        | 11520        |                           |        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 12800        |                           |        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 14080        |                           |        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 15360        |                           |        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 16640        |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0]  | 10           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1]  | 12           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]  | 13           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]  | 14           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]  | 15           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]  | 17           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]  | 18           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]  | 19           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]  | 20           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]  | 22           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10] | 23           |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11] | 24           |                           |        |
| Name   | Actual Value | Expected Value            | Result |
| DriverVelCalc()                                | -470.382141  | -470.3821283 ± 0.0009     | ✓      |
| PrevTbarAng_HwDeg_M_f32                        | -1.82692313  | -1.826923077 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -0.100936659 | -0.100936038 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

| Test Step 1.42 (Repeat Count = 1)              |  |              |                      |  |
|--|--|--------------|----------------------|---|
| Name   |  | Input Value  |                      |   |
| CRFMotorVel_MtrRadpS_T_f32                     |  | 357.25       |                      |   |
| HwTorque_HwNm_T_f32                            |  | 1.563        |                      |   |
| PrevTbarAng_HwDeg_M_f32                        |  | 0.251        |                      |   |
| TbarVelFiltSv_M_str.SV_Uls_f32                 |  | 5.6987       |                      |   |
| TbarVelFiltSv_M_str.K_Uls_f32                  |  | 0.03698      |                      |   |
| VehicleSpeed_Kph_T_f32                         |  | 222.42       |                      |   |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              |  | 76.9         |                      |   |
| k_CmnTbarStiff_NmpDeg_f32                      |  | 6.2          |                      |   |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           |  | 0.8          |                      |   |
| t_CmnVehSpd_Kph_u9p7[0]                        |  | 6784         |                      |   |
| t_CmnVehSpd_Kph_u9p7[1]                        |  | 6912         |                      |   |
| t_CmnVehSpd_Kph_u9p7[2]                        |  | 7040         |                      |   |
| t_CmnVehSpd_Kph_u9p7[3]                        |  | 7168         |                      |   |
| t_CmnVehSpd_Kph_u9p7[4]                        |  | 7296         |                      |   |
| t_CmnVehSpd_Kph_u9p7[5]                        |  | 7424         |                      |   |
| t_CmnVehSpd_Kph_u9p7[6]                        |  | 7552         |                      |   |
| t_CmnVehSpd_Kph_u9p7[7]                        |  | 7680         |                      |   |
| t_CmnVehSpd_Kph_u9p7[8]                        |  | 7808         |                      |   |
| t_CmnVehSpd_Kph_u9p7[9]                        |  | 7936         |                      |   |
| t_CmnVehSpd_Kph_u9p7[10]                       |  | 8064         |                      |   |
| t_CmnVehSpd_Kph_u9p7[11]                       |  | 8192         |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[0]  |  | 24           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[1]  |  | 26           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[2]  |  | 27           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[3]  |  | 29           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[4]  |  | 30           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[5]  |  | 31           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[6]  |  | 33           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[7]  |  | 34           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[8]  |  | 36           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[9]  |  | 37           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[10] |  | 38           |                      |   |
| t_InrtCmp_TBarVel_ScaleFactorTbiY_Uls_u9p7[11] |  | 40           |                      |   |
| Name   |  | Actual Value | Expected Value       | Result  |
| DriverVelCalc()                                |  | 288.110321   | 288.1102911 ± 0.0009 |  |

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DriverVelCalc

| Name                           | Actual Value | Expected Value           | Result |
|--------------------------------|--------------|--------------------------|--------|
| PrevTbarAng_HwDeg_M_f32        | 0.252096772  | 0.252096774 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.50824165   | 5.508241429 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

## Test Step 1.43 (Repeat Count = 1)

| Name   | Input Value |
|--|-------------|
| CRFMotorVel_MtrRadpS_T_f32                     | -464.25     |
| HwTorque_HwNm_T_f32                            | -2.645      |
| PrevTbarAng_HwDeg_M_f32                        | -0.3525     |
| TbarVelFiltSv_M_str.SV_Uls_f32                 | -5.1423     |
| TbarVelFiltSv_M_str.K_Uls_f32                  | 0.024588    |
| VehicleSpeed_Kph_T_f32                         | 111.52      |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32              | 42.5        |
| k_CmnTbarStiff_NmpDeg_f32                      | 7.5         |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32           | 0.7         |
| t_CmnVehSpd_Kph_u9p7[0]                        | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                        | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                        | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                        | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                        | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                        | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                        | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                        | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                        | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                        | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                       | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                       | 1536        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 33          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 34          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 35          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 36          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 38          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 39          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 40          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 41          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 43          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 44          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 45          |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 47          |

| Name                           | Actual Value | Expected Value            | Result |
|--------------------------------|--------------|---------------------------|--------|
| DriverVelCalc()                | -326.341705  | -326.3417122 ± 0.0009     | ✓      |
| PrevTbarAng_HwDeg_M_f32        | -0.352666676 | -0.352666667 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.01791048  | -5.017910128 ± 0.00390625 | ✓      |

| T                           |       |                             |       |        |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function             | Count | Expected Function           | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1     | IntplVarXY_u16_u16Xu16Y_Cnt | 1     | ✓      |

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FrqDepDmpnInrtCmp\_Per1

|             |                        |
|-------------|------------------------|
| Project     | FDD_Inertia            |
| Module      | FDD_Inertia            |
| Test Object | FrqDepDmpnInrtCmp_Per1 |

## Instrumentation: Test Object Only

|                         |       |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage       | 100 % |
| Branch (C1) Coverage    | 100 % |
| MCC Coverage            | 100 % |
| MC/DC Coverage          | 100 % |

## Statistics

|                 |     |
|-----------------|-----|
| Total Testcases | 3   |
| Successful      | 3 ✓ |
| Failed          | 0   |
| Not Executed    | 0   |

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## Module Properties

|                        |   |
|------------------------|---|
| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp  |
| Configuration File     | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml  |
| Target Environment     | TI TMS 570 PLS UDE (Default)  |
| Kind of Test           | Unit Test   |
| Linker Options         |   |
| Source File(s)         |   |
| File                   | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File                   | \$(PROJECTROOT)\NxrLib\src\interpolation.c  |
| Compiler Options       | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract -\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\$(PROJECTROOT)\NxrLib\include -\$(PROJECTROOT)\StdDef\include -\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

## Comments/Description/Specification

| Name                 | Text  |
|----------------------|---|
| Module 'FDD_Inertia' | <p>*****Unit Test Description*****</p> <p>Name of Tester: Spoorti Mali<br/>Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c<br/>Code File(s) Version: 13<br/>Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc<br/>Module Design Document Version: 18<br/>Data Dictionary Version: 16<br/>Unit Test Plan Version: 6<br/>Optimization Level: Level 2<br/>Compiler (CodeGen) Version: TMS470_4.9.5<br/>Model Type: Excel Macro<br/>Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30<br/>Total FLASH Used (Bytes): 1994<br/>Total RAM Used (Bytes): 60<br/>Total CALS Used (Bytes): 328<br/>Special Test Requirements:<br/>Test Date: 09-19-2014<br/>Comments:</p> <p>Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.</p> <p>Note2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.</p> <p>Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function.</p> <p>Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function.</p> <p>Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable ""filtCoef_Uls_T_Str.b0_Uls_f32"" is calculated as -2.74156205240179 to 0 and ""filtCoef_Uls_T_Str.b1_Uls_f32"" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16.</p> <p>Note6:In ""GenFddlcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range.</p> <p>Note 7:The range of the parameter ""VehicleSpeed_Kph_T_f32"" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication.</p> <p>Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DriverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"".</p> <p>*****</p> |

## Attributes

| Name                  | Value  |
|-----------------------|--|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision       | 9  |
| InitObjDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj                   |
| InitSrcDir            | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src                   |
| Linker File           | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd          |
| Makefile Template     | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path   | \$(ProgramFiles)\pls\UDE 3.2   |
| Time Unit             | Cycles   |
| Timer Enabled         | false  |
| Timer Prescale        | 0  |
| Timer Resolution      | 1  |

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| Attributes      |  |
|-----------------|--|
| Name            | Value  |
| UDE Config File | \${PROJECTROOT}\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg                       |
| Workspace File  | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

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| Usercode                                    |  |
|---|--|
| Stub Function Name                          | Stub Function Body   |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_Checkpo | \$stub void Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached() {<br>/* empty stub code created by TESSY */<br>} |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_Checkpo | \$stub void Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached() {<br>/* empty stub code created by TESSY */<br>} |

## Test Case 1: Metrics Test

|                      |  |
|----------------------|--|
| <b>Specification</b> | Performance Metrics (With "None" Instrumentation and "WithPS" Environment)<br><br>CPU Cycles:<br><br>TS1.1 5667.00 Cycles<br>TS1.2 5703.00 Cycles  |
| <b>Description</b>   | Test Vector Description:<br><br>TS1.1 "Shortest Execution Path:<br>(FDDDefSrvFlg_Cnt_T_lgc == TRUE)=False<br>(FrqDepDmpnInrtCmp_MtrNm_T_f32>=D_MTRTRQCMDHILMT_MTRNM_F32)=True"<br>TS1.2 "Longest Execution Path:<br>(FDDDefSrvFlg_Cnt_T_lgc == TRUE)=True<br>(FrqDepDmpnInrtCmp_MtrNm_T_f32>= D_MTRTRQCMDHILMT_MTRNM_F32)=False<br>(FrqDepDmpnInrtCmp_MtrNm_T_f32<=-D_MTRTRQCMDHILMT_MTRNM_F32)=False" |

### Test Step 1.1 (Repeat Count = 1)

| Name  | Input Value                       |
|---|-----------------------------------|
| PreDecelGain_Uls_M_f32                          | 1                                 |
| Prev1PreAttnComp_MtrNm_M_f32                    | 1.1                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 2205.3                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | 7.3                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 101.2                             |
| PrevTbarAng_HwDeg_M_f32                         | -8.32                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 3.5                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.1258                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 10.2                              |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 100.02                            |
| k_DmpDecelGain_Uls_f32                          | 2.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 16.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 30.2                              |
| k_InrtCmp_Mtrinertia_KgmSq_f32                  | 0.00008                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.9                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 112                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]          | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]           | 32          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]           | 48          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]           | 64          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]           | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 208         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3552        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3584        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3616        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3648        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 3680        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 3712        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 424         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 432         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 440         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 448         |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 523         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1038        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1553        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2583        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3614        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4129        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4644        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5159        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 240         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 320         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 49          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 51          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 3           |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 5           |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 8           |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[3]   | 10  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]   | 13  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]   | 15  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]   | 18  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]   | 20  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]   | 23  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]   | 26  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]  | 28  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]  | 31  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]                                   | 13  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]                                   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]                                   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]                                   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]                                   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]                                   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]                                   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]                                   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]                                   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]                                   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]                                  | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]                                  | 154   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]                           | 1   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]                           | 3   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]                           | 4   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]                           | 5   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]                           | 6   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]                           | 8   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]                           | 9   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]                           | 10  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]                           | 12  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]                           | 13  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]                          | 14  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]                          | 15  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 282   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 307   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 333   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 358   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 384   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                | 8.1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value               | 600.2   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                      | -10   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value              | -35.2   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                   | 100.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                | 1.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmc  | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_I  | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSi   | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwI   | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc  | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBl    | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 1.11199999  | 1.112 ± 0.0625            | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 128.764511  | 128.764510970637 ± 0.0009 | ✔      |
| Prev1SciDrvVel_RadpS_M_f32  | 540.226318  | 540.2263355 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 1.10000002  | 1.1 ± 0.00048828125       | ✔      |
| Prev2SciDrvVel_RadpS_M_f32  | 2205.30005  | 2205.3 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -8.33333302   | -8.333333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 2.22103405  | 2.221033333 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value            | 8.80000019  | 8.8 ± 0.00048828125       | ✔      |



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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 1.2 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125487.235                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 1.1                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 2205.3                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | 7.3                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 101.2                             |
| PrevTbarAng_HwDeg_M_f32                         | -8.32                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 3.5                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.1258                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 10.2                              |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 100.02                            |
| k_DmpDecelGain_Uls_f32                          | 2.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 16.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 30.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00008                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.9                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 80                                |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 208         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3552        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3584        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3616        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3648        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 3680        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 3712        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 424         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 432         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 440         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 448         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 523         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1038        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1553        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2583        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3614        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4129        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4644        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5159        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 240         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 320         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 49          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 51          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 3           |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 5           |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 8           |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 10          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 31          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 13  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 154   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 1   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 3   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 4   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 5   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 6   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 8   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 9   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 10  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 12  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 13  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 14  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 15  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 282   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 307   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 333   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 358   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 384   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 8.1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 600.2   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -10   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -21.32  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 100.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 1.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125487.234  | 125487.235 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 14899641  | 14899642.12 ± 99.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 540.226318  | 540.2263355 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 1.10000002  | 1.1 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 2205.30005  | 2205.3 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -8.33333302   | -8.333333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 2.22103405  | 2.221033333 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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| T  |       |  |       |        |
|--|-------|--|-------|--------|
| Actual Function                                      | Count | Expected Function                                    | Count | Result |
| Rte_Call_FrqDepDmpInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc  | 1     | ADDCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 5     | ✓      |
| DecelGain  | 1     | DecelGain  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | ✓      |
| DriverVelCalc  | 1     | DriverVelCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | ✓      |
| FilterCoefCalc                                       | 1     | FilterCoefCalc                                       | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 4     | ✓      |
| GenFddlcCmd  | 1     | GenFddlcCmd  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | ✓      |
| Rte_Call_FrqDepDmpInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

## Test Case 2: Path Test

|                      |   |
|----------------------|---|
| <b>Specification</b> | Performance Metrics (With "None" Instrumentation and "WithPS" Environment)<br><br>CPU Cycles:<br><br>TS2.1 5693.00 Cycles<br>TS2.2 5724.00 Cycles<br>TS2.3 6713.00 Cycles   |
| <b>Description</b>   | Test Vector Description:<br><br>TS2.1 "(FDDDefSrvFlg_Cnt_T_lgc = True) =False<br>(FrqDepDmpInrtCmp_MtrNm_T_f32=>D_MTRTRQCMDHILMT_MTRNM_F32)=True"<br>TS2.2 "(FDDDefSrvFlg_Cnt_T_lgc = True) =True<br>(FrqDepDmpInrtCmp_MtrNm_T_f32=>D_MTRTRQCMDHILMT_MTRNM_F32)=False<br>(FrqDepDmpInrtCmp_MtrNm_T_f32<=D_MTRTRQCMDHILMT_MTRNM_F32)=False"<br>TS2.3 (FrqDepDmpInrtCmp_MtrNm_T_f32<=D_MTRTRQCMDHILMT_MTRNM_F32)=True |

## Test Step 2.1 (Repeat Count = 1)

| Name  | Input Value                      |
|---|----------------------------------|
| PreDecelGain_Uls_M_f32                          | 125487.235                       |
| Prev1PreAttnComp_MtrNm_M_f32                    | 1.1                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | 2205.3                           |
| Prev2PreAttnComp_MtrNm_M_f32                    | 7.3                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | 101.2                            |
| PrevTbarAng_HwDeg_M_f32                         | -8.32                            |
| Rte_Inst_Ap_FrqDepDmpInrtCmp                    | tgt_Rte_Inst_Ap_FrqDepDmpInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 3.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.1258                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 10.2                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.2                              |
| k_DmpDecelGainFSlew_UlspS_f32                   | 100.02                           |
| k_DmpDecelGain_Uls_f32                          | 2.5                              |
| k_DmpGainOffThresh_KphpS_f32                    | 16.5                             |
| k_DmpGainOnThresh_KphpS_f32                     | 30.2                             |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00008                          |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.9                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409                             |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]           | 16          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]           | 32          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]           | 48          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]           | 64          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]           | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]          | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]           | 32          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]           | 48          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]           | 64          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]           | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 208         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3552        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3584        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3616        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3648        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 3680        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 3712        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 424         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 432         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 440         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 448         |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 523         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1038        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1553        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2583        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3614        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4129        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4644        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5159        |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0]                                       | 240   |                           |        |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1]                                       | 320   |                           |        |
| t_FDD_AttenTblY_Uls_u8p8[0]   | 49  |                           |        |
| t_FDD_AttenTblY_Uls_u8p8[1]   | 51  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[0]   | 3   |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[1]   | 5   |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[2]   | 8   |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[3]   | 10  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]   | 13  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]   | 15  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]   | 18  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]   | 20  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]   | 23  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]   | 26  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]  | 28  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]  | 31  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]                                   | 13  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]                                   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]                                   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]                                   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]                                   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]                                   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]                                   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]                                   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]                                   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]                                   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]                                  | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]                                  | 154   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]                           | 1   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]                           | 3   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]                           | 4   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]                           | 5   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]                           | 6   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]                           | 8   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]                           | 9   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]                           | 10  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]                           | 12  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]                           | 13  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]                          | 14  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]                          | 15  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 282   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 307   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 333   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 358   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 384   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                | 8.1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value               | 600.2   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                      | -10   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value              | 10.02   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                   | 100.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                | 1.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmc  | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_I  | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSi   | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwI   | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc  | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB     | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125487.031  | 125487.035 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 14899619  | 14899618.37 ± 99.9        | ✔      |
| Prev1ScldrvVel_RadpS_M_f32  | 540.226318  | 540.2263355 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 1.10000002  | 1.1 ± 0.00048828125       | ✔      |
| Prev2ScldrvVel_RadpS_M_f32  | 2205.30005  | 2205.3 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -8.33333302   | -8.333333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M str.SV_Uls_f32  | 2.22103405  | 2.221033333 ± 0.00390625  | ✔      |

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FrqDepDmpInrtCmp\_Per1

| Name   | Actual Value | Expected Value      | Result |
|--|--------------|---------------------|--------|
| tgt_FrqDepDmpInrtCmp_Per1_FrqDepDmpInrtCmp_MtrNm_f32.value | 8.80000019   | 8.8 ± 0.00048828125 | ✓      |

| T  |       |  |       |        |
|--|-------|--|-------|--------|
| Actual Function                                      | Count | Expected Function                                    | Count | Result |
| Rte_Call_FrqDepDmpInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc  | 1     | ADDCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 5     | ✓      |
| DecelGain  | 1     | DecelGain  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | ✓      |
| DriverVelCalc  | 1     | DriverVelCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | ✓      |
| FilterCoefCalc                                       | 1     | FilterCoefCalc                                       | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 4     | ✓      |
| GenFddlcCmd  | 1     | GenFddlcCmd  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                          | 1     | ✓      |
| Rte_Call_FrqDepDmpInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 2.2 (Repeat Count = 1)                |                                  |
|---|----------------------------------|
| Name  | Input Value                      |
| PreDecelGain_Uls_M_f32                          | 125589.21                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | -1.1                             |
| Prev1SclDrvVel_RadpS_M_f32                      | -445.3                           |
| Prev2PreAttnComp_MtrNm_M_f32                    | -6.8                             |
| Prev2SclDrvVel_RadpS_M_f32                      | -220.3                           |
| PrevTbarAng_HwDeg_M_f32                         | 4.339                            |
| Rte_Inst_Ap_FrqDepDmpInrtCmp                    | tgt_Rte_Inst_Ap_FrqDepDmpInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -2.5                             |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.2365                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 20.3                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 2.3                              |
| k_DmpDecelGainFSlew_UlspS_f32                   | 200.03                           |
| k_DmpDecelGain_Uls_f32                          | 3.6                              |
| k_DmpGainOffThresh_KphpS_f32                    | 20.2                             |
| k_DmpGainOnThresh_KphpS_f32                     | 35.3                             |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00009                          |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.8                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 342                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1024                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1364                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1705                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2046                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2387                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3068                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3409                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 523                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1038                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1553                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2068                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4644                             |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5159                             |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 32                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 48                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 64                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 80                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 96                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 112                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 128                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 144                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 160                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 176                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 192                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 208                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 48                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 64                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]           | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 224         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3872        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3904        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3936        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3968        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4000        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4032        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1480        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1488        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1496        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1504        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1512        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1520        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 704         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 814         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 924         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1034        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1144        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1254        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1475        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1585        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1695        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 352         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 400         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 65          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 5           |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 8           |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 10          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 28          |



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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[10]  | 31  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]  | 33  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]                                   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]                                   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]                                   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]                                   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]                                   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]                                   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]                                   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]                                   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]                                   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]                                   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]                                  | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]                                  | 166   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]                           | 15  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]                           | 17  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]                           | 18  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]                           | 19  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]                           | 20  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]                           | 22  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]                           | 23  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]                           | 24  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]                           | 26  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]                           | 27  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]                          | 28  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]                          | 29  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 9830  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 538   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 563   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 589   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 614   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 640   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                | -8.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value               | -600.3  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                      | 10  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value              | 20.03   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                   | 200.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                | 2.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmc  | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_f  | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSl   | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwI   | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc  | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBl    | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125588.813  | 125588.8099 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -321190.063   | -321190.1416 ± 0.9        | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32  | -480.309448   | -480.3094401 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -1.10000002   | -1.1 ± 0.00048828125      | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32  | -445.299988   | -445.3 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 4.347826  | 4.347826087 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -0.865101695  | -0.865065217 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value            | 0   | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 2.3 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125997.11                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -3.3                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -4021.3                           |
| Prev2PreAttnComp_MtrNm_M_f32                    | -2.3                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -363.2                            |
| PrevTbarAng_HwDeg_M_f32                         | 0.159                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -6.6                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.63214                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 60.05                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 6.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 400.05                            |
| k_DmpDecelGain_Uls_f32                          | 6.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 44.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 20.6                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00008                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.4                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1066                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1212                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1359                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1506                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1653                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1800                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1946                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2093                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 2240                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1246                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1638                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2030                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2422                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2814                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3206                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3598                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 3990                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4382                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4774                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 256                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 272                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 336                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 352                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 368                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 384                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 400         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 416         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 432         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 448         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 464         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 480         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 496         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 512         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 12928       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 13056       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 13184       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 13312       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 13440       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 13568       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 13696       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 13824       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 13952       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 14080       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 14208       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 32320       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 32352       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 32384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 32416       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 32448       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 32480       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 2408        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 2416        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 2424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 2432        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 2440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 2448        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1427        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1655        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1884        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2112        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2340        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 2568        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 2796        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 3024        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 3252        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 3480        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 656         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 720         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 172         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 174         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 44          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 46          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 192   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 205   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 282   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 294   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 77  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 78  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 79  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 81  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 82  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 83  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 84  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 86  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 87  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 88  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 90  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 91  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1562  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1587  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1613  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1638  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1664  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -6.3  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -1118   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 1.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -20.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 110.07  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 6.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125996.313  | 125996.3099 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -9984653  | -9984653.482 ± 9.9        | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32  | -447.704346   | -447.704346 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -3.29999995   | -3.3 ± 0.00048828125      | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32  | -4021.30005   | -4021.3 ± 0.00390625      | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0.164516136   | 0.164516129 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -0.684389591  | -0.684393097 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | -8.80000019   | -8.8 ± 0.00048828125      | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

## Test Case 3: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS3.1 5484.00 Cycles  
 TS3.2 5549.00 Cycles  
 TS3.3 5698.00 Cycles  
 TS3.4 5724.00 Cycles  
 TS3.5 5698.00 Cycles  
 TS3.6 5572.00 Cycles  
 TS3.7 5708.00 Cycles  
 TS3.8 6713.00 Cycles  
 TS3.9 5630.00 Cycles  
 TS3.10 5527.00 Cycles  
 TS3.11 5508.00 Cycles  
 TS3.12 5560.00 Cycles  
 TS3.13 5560.00 Cycles  
 TS3.14 5562.00 Cycles  
 TS3.15 5534.00 Cycles  
 TS3.16 5733.00 Cycles  
 TS3.17 5538.00 Cycles  
 TS3.18 5458.00 Cycles  
 TS3.19 5517.00 Cycles  
 TS3.20 5517.00 Cycles  
 TS3.21 5853.00 Cycles  
 TS3.22 5549.00 Cycles  
 TS3.23 5529.00 Cycles  
 TS3.24 5516.00 Cycles  
 TS3.25 5539.00 Cycles  
 TS3.26 5539.00 Cycles  
 TS3.27 5519.00 Cycles  
 TS3.28 5619.00 Cycles  
 TS3.29 5572.00 Cycles  
 TS3.30 5561.00 Cycles

**Description** Test Vector Description:

TS3.1 All min  
 TS3.2 All max  
 TS3.3 HwTorque\_HwNm\_f32 = min  
 TS3.4 HwTorque\_HwNm\_f32 = max  
 TS3.5 HwTorque\_HwNm\_f32 = zero  
 TS3.6 HwTorque\_HwNm\_f32 = neg  
 TS3.7 HwTorque\_HwNm\_f32 = pos  
 TS3.8 CRFMotorVel\_MtrRadpS\_f32 = min  
 TS3.9 CRFMotorVel\_MtrRadpS\_f32 = max  
 TS3.10 CRFMotorVel\_MtrRadpS\_f32 = zero  
 TS3.11 CRFMotorVel\_MtrRadpS\_f32 = neg  
 TS3.12 CRFMotorVel\_MtrRadpS\_f32 = pos  
 TS3.13 BaseAssistCmd\_MtrNm\_f32 = min  
 TS3.14 BaseAssistCmd\_MtrNm\_f32 = max  
 TS3.15 BaseAssistCmd\_MtrNm\_f32 = zero  
 TS3.16 BaseAssistCmd\_MtrNm\_f32 = neg  
 TS3.17 BaseAssistCmd\_MtrNm\_f32 = pos  
 TS3.18 VehicleSpeed\_Kph\_f32 = min  
 TS3.19 VehicleSpeed\_Kph\_f32 = max  
 TS3.20 VehicleSpeed\_Kph\_f32 = pos  
 TS3.21 WIRCmdAmpBlnd\_MtrNm\_f32 = min  
 TS3.22 WIRCmdAmpBlnd\_MtrNm\_f32 = max  
 TS3.23 WIRCmdAmpBlnd\_MtrNm\_f32 = pos  
 TS3.24 FreqDepDmpSrlComSvcDft\_Cnt\_lgc = min  
 TS3.25 FreqDepDmpSrlComSvcDft\_Cnt\_lgc = max  
 TS3.26 VehicleLonAccel\_KphpS\_f32 = min  
 TS3.27 VehicleLonAccel\_KphpS\_f32 = max  
 TS3.28 VehicleLonAccel\_KphpS\_f32 = zero  
 TS3.29 VehicleLonAccel\_KphpS\_f32 = neg  
 TS3.30 VehicleLonAccel\_KphpS\_f32 = pos

## Test Step 3.1 (Repeat Count = 1)

| Name                   | Input Value |
|------------------------|-------------|
| PreDecelGain_Uls_M_f32 | 1           |

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| Name  | Input Value                       |
|---|-----------------------------------|
| Prev1PreAttnComp_MtrNm_M_f32                    | -8.8                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -12917.3                          |
| Prev2PreAttnComp_MtrNm_M_f32                    | -8.8                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -12917.3                          |
| PrevTbarAng_HwDeg_M_f32                         | -20                               |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -6.6667                           |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.001255848                       |
| k_CmnSysKinRatio_MtrDegHwDeg_f32                | 1                                 |
| k_CmnTbarStiff_NmpDeg_f32                       | 0.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1                                 |
| k_DmpDecelGain_Uls_f32                          | 1                                 |
| k_DmpGainOffThresh_KphpS_f32                    | 0                                 |
| k_DmpGainOnThresh_KphpS_f32                     | 0                                 |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00001                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 0                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 0                                 |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]                | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]                | 16                                |
| t_CmnVehSpd_Kph_u9p7[0]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[1]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[2]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[3]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[4]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[5]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[6]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[7]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[8]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[9]                         | 0                                 |
| t_CmnVehSpd_Kph_u9p7[10]                        | 0                                 |
| t_CmnVehSpd_Kph_u9p7[11]                        | 0                                 |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 0                                 |

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| Name  | Input Value |
|---|-------------|
| t_DmpADDCoefX_MtrNm_u4p12[1]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[2]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[3]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[4]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[5]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[6]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[7]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[8]                  | 0           |
| t_DmpADDCoefX_MtrNm_u4p12[9]                  | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]         | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]         | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]         | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]         | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]         | 0           |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]         | 0           |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]            | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]            | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]            | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]            | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]            | 8           |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]            | 8           |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]              | 0           |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]              | 0           |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]              | 0           |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]              | 0           |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]              | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]     | 0           |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]     | 0           |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]             | 0           |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]             | 0           |
| t_FDD_AttenTbIY_Uls_u8p8[0]                   | 0           |
| t_FDD_AttenTbIY_Uls_u8p8[1]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[0]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[1]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[2]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[3]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[4]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[5]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[6]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[7]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[8]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[9]                   | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[10]                  | 0           |
| t_FDD_BlendTbIY_Uls_u8p8[11]                  | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]         | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10]        | 0           |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11]        | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7] | 0           |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8] | 0           |

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| Name   | Input Value   |                |        |
|--|---|----------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[9]                            | 0   |                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[10]                           | 0   |                |        |
| t_InrtCmp_TBarVel_ScaleFactorTbly_Uls_u9p7[11]                           | 0   |                |        |
| t_RIAstWIRBlndTbly_Uls_u2p14[0]  | 0   |                |        |
| t_RIAstWIRBlndTbly_Uls_u2p14[1]  | 0   |                |        |
| t_RIAstWIRBlndTbly_Uls_u2p14[2]  | 0   |                |        |
| t_RIAstWIRBlndTbly_Uls_u2p14[3]  | 0   |                |        |
| t_RIAstWIRBlndTbly_Uls_u2p14[4]  | 0   |                |        |
| t_WIRBlndTbIX_MtrNm_u8p8[0]  | 0   |                |        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]  | 0   |                |        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]  | 0   |                |        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]  | 0   |                |        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]  | 0   |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                 | -8.8  |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                | -1118   |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value          | 0   |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                       | -10   |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value               | -50   |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                    | 0   |                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                 | 0   |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmc   | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_I   | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrl   | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwI    | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I  | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBl     | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                |        |
| Name   | Actual Value  | Expected Value | Result |



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FrqDepDmpnInrtCmp\_Per1

| Name  | Input Value |
|---|-------------|
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.0005      |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 1           |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 6554        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 6554        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]                 | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]                | 1600        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]                | 1600        |
| t_CmnVehSpd_Kph_u9p7[0]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[1]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[2]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[3]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[4]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[5]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[6]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[7]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[8]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[9]                         | 32640       |
| t_CmnVehSpd_Kph_u9p7[10]                        | 32640       |
| t_CmnVehSpd_Kph_u9p7[11]                        | 32640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 36045       |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 36045       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]           | 35776       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]           | 35776       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]           | 35776       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]           | 35776       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]           | 35776       |

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FrqDepDmpnInrtCmp\_Per1

| Name   | Input Value |
|--|-------------|
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]                    | 35776       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]                       | 4000        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]                       | 4000        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]                       | 4000        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]                       | 4000        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]                       | 4000        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]                       | 4000        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]                         | 16384       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]                         | 16384       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]                         | 16384       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]                         | 16384       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]                         | 16384       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8]                | 6554        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9]                | 6554        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]                        | 17600       |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]                        | 17600       |
| t_FDD_AttenTbIY_Uls_u8p8[0]                              | 256         |
| t_FDD_AttenTbIY_Uls_u8p8[1]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[0]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[1]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[2]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[3]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[4]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[5]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[6]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[7]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[8]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[9]                              | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[10]                             | 256         |
| t_FDD_BlendTbIY_Uls_u8p8[11]                             | 256         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[0]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[1]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[2]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[3]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[6]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[7]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[9]                    | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[10]                   | 384         |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[11]                   | 384         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[0]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[1]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[3]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[4]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[5]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[6]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[7]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[8]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[9]            | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[10]           | 128         |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[11]           | 128         |
| t_RIAstWIRBlndTbIY_Uls_u2p14[0]                          | 16384       |
| t_RIAstWIRBlndTbIY_Uls_u2p14[1]                          | 16384       |
| t_RIAstWIRBlndTbIY_Uls_u2p14[2]                          | 16384       |
| t_RIAstWIRBlndTbIY_Uls_u2p14[3]                          | 16384       |
| t_RIAstWIRBlndTbIY_Uls_u2p14[4]                          | 16384       |
| t_WIRBlndTbIX_MtrNm_u8p8[0]                              | 2048        |
| t_WIRBlndTbIX_MtrNm_u8p8[1]                              | 2048        |
| t_WIRBlndTbIX_MtrNm_u8p8[2]                              | 2048        |
| t_WIRBlndTbIX_MtrNm_u8p8[3]                              | 2048        |
| t_WIRBlndTbIX_MtrNm_u8p8[4]                              | 2048        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 8.8         |

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FrqDepDmpnInrtCmp\_Per1

| Name  | Input Value   |                                |        |
|---|---|--------------------------------|--------|
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 1118  |                                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 1   |                                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 10  |                                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 50  |                                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 511.9921875   |                                |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 8.8   |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd                  | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel                    | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn                   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                                |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                                |        |
| Name  | Actual Value  | Expected Value                 | Result |
| PreDecelGain_Uls_M_f32  | 4.2949673e+009  | 4294967286 ± 0.0625            | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -2.45381431e+011  | -245381471607.646 ± 999999.9   | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32  | 1112.98718  | 1112.9872366867 ± 0.00390625   | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 8.80000019  | 8.8 ± 0.00048828125            | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32  | 12917.2998  | 12917.3 ± 0.00390625           | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 1   | 1 ± 0.00390625                 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -2.8721137  | -2.87210173650089 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125              | ✔      |

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.3 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125487.235                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 1.1                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 2205.3                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | 7.3                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 101.2                             |
| PrevTbarAng_HwDeg_M_f32                         | -8.32                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 3.5                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.1258                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 10.2                              |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 100.02                            |
| k_DmpDecelGain_Uls_f32                          | 2.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 16.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 30.2                              |
| k_InrtCmp_Mtrinertia_KgmSq_f32                  | 0.00008                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.9                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160                              |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326                              |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493                              |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659                              |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342                               |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683                               |

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| Name  | Input Value |
|---|-------------|
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068        |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409        |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 16          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 32          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 48          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 64          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 32          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 48          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 64          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]                 | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]                 | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]                 | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]                 | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]                 | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]                 | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]                | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]                | 208         |
| t_CmnVehSpd_Kph_u9p7[0]                         | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                         | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                         | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                         | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                         | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                         | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                         | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                         | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                         | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                         | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                        | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                        | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]                    | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]                    | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]                    | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]                    | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]                    | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]                    | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]                    | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]                    | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]                    | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]                    | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]           | 3552        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]           | 3584        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]           | 3616        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]           | 3648        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]           | 3680        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]           | 3712        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]              | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]              | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]              | 424         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]              | 432         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]              | 440         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]              | 448         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]                | 1638        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]                | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]                | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]                | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]                | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0]       | 523         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1]       | 1038        |

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| Name   | Input Value  |
|--|--|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2]  | 1553   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3]  | 2068   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4]  | 2583   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5]  | 3099   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6]  | 3614   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7]  | 4129   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8]  | 4644   |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9]  | 5159   |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0]  | 240  |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1]  | 320  |
| t_FDD_AttenTblY_Uls_u8p8[0]  | 49   |
| t_FDD_AttenTblY_Uls_u8p8[1]  | 51   |
| t_FDD_BlendTblY_Uls_u8p8[0]  | 3  |
| t_FDD_BlendTblY_Uls_u8p8[1]  | 5  |
| t_FDD_BlendTblY_Uls_u8p8[2]  | 8  |
| t_FDD_BlendTblY_Uls_u8p8[3]  | 10   |
| t_FDD_BlendTblY_Uls_u8p8[4]  | 13   |
| t_FDD_BlendTblY_Uls_u8p8[5]  | 15   |
| t_FDD_BlendTblY_Uls_u8p8[6]  | 18   |
| t_FDD_BlendTblY_Uls_u8p8[7]  | 20   |
| t_FDD_BlendTblY_Uls_u8p8[8]  | 23   |
| t_FDD_BlendTblY_Uls_u8p8[9]  | 26   |
| t_FDD_BlendTblY_Uls_u8p8[10]   | 28   |
| t_FDD_BlendTblY_Uls_u8p8[11]   | 31   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]  | 13   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]  | 26   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]  | 38   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]  | 51   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]  | 64   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]  | 77   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]  | 90   |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]  | 102  |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]  | 115  |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]  | 128  |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]   | 141  |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]   | 154  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 1  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 3  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 4  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 5  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 6  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 8  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 9  |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 10   |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 12   |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 13   |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]   | 14   |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]   | 15   |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]  | 1638   |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]  | 3277   |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]  | 4915   |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]  | 6554   |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]  | 8192   |
| t_WIRBIndTblX_MtrNm_u8p8[0]  | 282  |
| t_WIRBIndTblX_MtrNm_u8p8[1]  | 307  |
| t_WIRBIndTblX_MtrNm_u8p8[2]  | 333  |
| t_WIRBIndTblX_MtrNm_u8p8[3]  | 358  |
| t_WIRBIndTblX_MtrNm_u8p8[4]  | 384  |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                               | 8.1  |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                              | 600.2  |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 0  |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                     | -10  |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                             | 10.02  |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                  | 100.01   |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                               | 1.2  |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_lgc |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       |

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| Name   | Actual Value | Expected Value            | Result |
|--|--------------|---------------------------|--------|
| PreDecelGain_Uls_M_f32                                       | 125487.031   | 125487.035 ± 0.0625       | ✓      |
| Prev1PreAttnComp_MtrNm_M_f32                                 | 14899619     | 14899618.37 ± 99.9        | ✓      |
| Prev1ScIDrvVel_RadpS_M_f32                                   | 540.226318   | 540.2263355 ± 0.00390625  | ✓      |
| Prev2PreAttnComp_MtrNm_M_f32                                 | 1.10000002   | 1.1 ± 0.00048828125       | ✓      |
| Prev2ScIDrvVel_RadpS_M_f32                                   | 2205.30005   | 2205.3 ± 0.00390625       | ✓      |
| PrevTbarAng_HwDeg_M_f32                                      | -8.33333302  | -8.333333333 ± 0.00390625 | ✓      |
| TbarVelFiltSv_M_str.SV_Uls_f32                               | 2.22103405   | 2.221033333 ± 0.00390625  | ✓      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value | 8.80000019   | 8.8 ± 0.00048828125       | ✓      |

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.4 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125589.21                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -1.1                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -445.3                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | -6.8                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -220.3                            |
| PrevTbarAng_HwDeg_M_f32                         | 4.339                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -2.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.2365                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 20.3                              |
| k_CmnTbarStiff_NmpDeg_f32                       | 2.3                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 200.03                            |
| k_DmpDecelGain_Uls_f32                          | 3.6                               |
| k_DmpGainOffThresh_KphpS_f32                    | 20.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 35.3                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00009                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.8                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3409                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 523                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1038                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1553                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4644                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5159                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 128                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]          | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]           | 48          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]           | 64          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]           | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 224         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3872        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3904        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3936        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3968        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4000        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4032        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1480        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1488        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1496        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1504        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1512        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1520        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 704         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 814         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 924         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1034        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1144        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1254        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1475        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1585        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1695        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 352         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 400         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 65          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 5           |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 8           |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 10          |



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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[3]   | 13  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[4]   | 15  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[5]   | 18  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[6]   | 20  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[7]   | 23  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[8]   | 26  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[9]   | 28  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[10]  | 31  |                           |        |
| t_FDD_BlendTblY_Uls_u8p8[11]  | 33  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 166   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 15  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 17  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 18  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 19  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 20  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 22  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 23  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 24  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 26  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 27  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 28  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 29  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 9830  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 538   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 563   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 589   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 614   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 640   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -8.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -600.3  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 10  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 20.03   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 200.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 2.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125588.813  | 125588.8099 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -321190.063   | -321190.1416 ± 0.9        | ✔      |
| Prev1SciDrvVel_RadpS_M_f32  | -480.309448   | -480.3094401 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -1.10000002   | -1.1 ± 0.00048828125      | ✔      |
| Prev2SciDrvVel_RadpS_M_f32  | -445.299988   | -445.3 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 4.347826  | 4.347826087 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -0.865101695  | -0.865065217 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |



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| T   |       |                   |       |        | ✓ |
|---|-------|-------------------|-------|--------|---|
| Actual Function                                       | Count | Expected Function | Count | Result |   |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     |                   |       |        |   |

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FrqDepDmpnInrtCmp\_Per1

| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 240         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 6784        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 6912        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 7040        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 7168        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7296        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 7424        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 7552        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 7808        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 7936        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 8064        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 8192        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 12698       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 13107       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 13517       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 13926       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 14336       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 14746       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 15155       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 15565       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 15974       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 16384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 4192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 4224        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 4256        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 4288        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4320        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4352        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 2408        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 2416        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 2424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 2432        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 2440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 2448        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 11469       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 885         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 986         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1087        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1188        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1288        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1389        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1490        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1591        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1692        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1793        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 448         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 480         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 93          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 96          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 10          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 38          |

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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 38  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 51  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 64  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 77  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 90  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 102   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 115   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 128   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 141   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 154   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 166   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 179   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 31  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 32  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 33  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 35  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 36  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 37  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 38  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 40  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 41  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 42  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 44  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 45  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 4915  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 6554  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 8192  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 9830  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 11469   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 794   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 819   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 845   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 870   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 896   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 7.3   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 500.4   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 30.01   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 300.05  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 3.2   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 125690.984  | 125690.985 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 232822.953  | 232822.9685 ± 0.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 350.610321  | 350.6103097 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 2.20000005  | 2.2 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 292.600006  | 292.6 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0   | 0 ± 0.00390625           | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 1.78252006  | 1.78252 ± 0.00390625     | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125      | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.6 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125793.16                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -2.2                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -160.3                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | -5.2                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -301.2                            |
| PrevTbarAng_HwDeg_M_f32                         | -1.1549                           |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -1.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.47856                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 40.4                              |
| k_CmnTbarStiff_NmpDeg_f32                       | 4.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 200.05                            |
| k_DmpDecelGain_Uls_f32                          | 3.2                               |
| k_DmpGainOffThresh_KphpS_f32                    | 22.3                              |
| k_DmpGainOnThresh_KphpS_f32                     | 45.6                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00003                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.6                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 704                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 814                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 924                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1034                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1144                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1254                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1475                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1585                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1695                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 885                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 986                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1087                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1188                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1288                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1389                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1490                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1591                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1692                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1793                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 128                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 256         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 16794       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 17203       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 17613       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 18022       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 18432       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 18842       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 19251       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 19661       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 20070       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 20480       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 5792        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 5824        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 5856        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 5888        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 5920        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 5952        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1208        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1216        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1224        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1232        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1240        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1248        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 13107       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1066        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1212        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1359        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1506        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1653        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1800        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1946        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2093        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 2240        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 2387        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 512         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 560         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 116         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 118         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 41          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 192   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 46  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 47  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 49  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 50  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 51  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 52  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 54  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 55  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 56  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 58  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 59  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 60  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]   | 6554  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]   | 8192  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]   | 9830  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]   | 11469   |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]   | 13107   |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]   | 1050  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]   | 1075  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]   | 1101  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]   | 1126  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]   | 1152  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -7.1  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -500.5  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -5.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 40.02   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 400.06  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 4.1   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125792.758  | 125792.7599 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 512151.25   | 512151.2172 ± 0.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -300.610382   | -300.610367 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -2.20000005   | -2.2 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -160.300003   | -160.3 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -1.15555549   | -1.155555556 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -0.939015687  | -0.939021333 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.7 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125895.135                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 3.3                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 2625.3                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | 5.2                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 157.2                             |
| PrevTbarAng_HwDeg_M_f32                         | 1.009                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 1.5                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.58963                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 50.03                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 5.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 300.06                            |
| k_DmpDecelGain_Uls_f32                          | 4.2                               |
| k_DmpGainOffThresh_KphpS_f32                    | 33.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 15.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00004                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.5                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 885                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 986                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1087                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1188                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1288                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1389                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1490                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1591                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1692                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1793                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1066                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1212                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1359                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1506                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1653                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1800                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1946                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2093                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 2240                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 2387                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 256                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 144                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 256         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 272         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 20890       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 21299       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 21709       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 22118       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 22528       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 22938       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 23347       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 23757       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 24166       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 24576       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 9120        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 9152        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 9184        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 9216        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 9248        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 9280        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1608        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1616        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1624        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1632        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1640        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1648        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 13107       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 14746       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1246        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1638        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2030        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2422        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2814        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3206        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3598        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 3990        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4382        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 4774        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 512         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 560         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 144         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 146         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 44          |



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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 64  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 77  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 90  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 102   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 115   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 128   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 141   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 154   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 166   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 179   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 192   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 205   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 61  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 63  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 64  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 65  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 67  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 68  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 69  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 70  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 72  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 73  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 74  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 76  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 8192  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 9830  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 11469   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 13107   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 14746   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1306  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1331  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1357  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1382  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1408  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 6.2   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 400.6   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 5.3   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -10.05  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 500.08  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 5.2   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 125894.531  | 125894.5349 ± 0.0625     | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 16663430  | 16663430.49 ± 99.9       | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 202.182922  | 202.1828953 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 3.29999995  | 3.3 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 2625.30005  | 2625.3 ± 0.00390625      | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 1.01923084  | 1.019230769 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 3.63177729  | 3.631739231 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125        | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.8 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 125997.11                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -3.3                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -4021.3                           |
| Prev2PreAttnComp_MtrNm_M_f32                    | -2.3                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -363.2                            |
| PrevTbarAng_HwDeg_M_f32                         | 0.159                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -6.6                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.63214                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 60.05                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 6.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 400.05                            |
| k_DmpDecelGain_Uls_f32                          | 6.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 44.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 20.6                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00008                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.4                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1066                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1212                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1359                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1506                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1653                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1800                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1946                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2093                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 2240                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1246                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1638                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2030                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2422                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2814                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3206                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3598                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 3990                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4382                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4774                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 256                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 272                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 336                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 352                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 368                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 384                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 400         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 416         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 432         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 448         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 464         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 480         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 496         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 512         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 12928       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 13056       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 13184       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 13312       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 13440       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 13568       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 13696       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 13824       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 13952       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 14080       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 14208       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 32320       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 32352       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 32384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 32416       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 32448       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 32480       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 2408        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 2416        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 2424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 2432        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 2440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 2448        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1427        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1655        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1884        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2112        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2340        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 2568        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 2796        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 3024        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 3252        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 3480        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 656         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 720         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 172         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 174         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 44          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 46          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 192   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 205   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 282   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 294   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 77  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 78  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 79  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 81  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 82  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 83  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 84  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 86  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 87  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 88  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 90  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 91  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1562  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1587  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1613  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1638  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1664  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -6.3  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -1118   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 1.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -20.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 110.07  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 6.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 125996.313  | 125996.3099 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -9984653  | -9984653.482 ± 9.9        | ✔      |
| Prev1ScIDrvVel_RadpS_M_f32  | -447.704346   | -447.704346 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -3.29999995   | -3.3 ± 0.00048828125      | ✔      |
| Prev2ScIDrvVel_RadpS_M_f32  | -4021.30005   | -4021.3 ± 0.00390625      | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0.164516136   | 0.164516129 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -0.684389591  | -0.684393097 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | -8.80000019   | -8.8 ± 0.00048828125      | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.9 (Repeat Count = 1)                |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126099.085                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 4.4                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 1234.2                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | 2.3                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 4678.2                            |
| PrevTbarAng_HwDeg_M_f32                         | -0.129                            |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 6.2                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.014785                          |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 70.5                              |
| k_CmnTbarStiff_NmpDeg_f32                       | 7.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 500.02                            |
| k_DmpDecelGain_Uls_f32                          | 5.6                               |
| k_DmpGainOffThresh_KphpS_f32                    | 8.6                               |
| k_DmpGainOnThresh_KphpS_f32                     | 25.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00009                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.3                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1246                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1638                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2030                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2422                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2814                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3206                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3598                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 3990                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4382                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4774                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1427                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1655                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1884                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2112                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2340                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2568                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2796                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 3024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3252                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3480                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 336                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 352                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 368                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 384                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 400                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 416                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 432                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 448                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 464                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 480                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 496                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 512                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 688                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 704                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 720         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 736         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 752         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 768         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 784         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 800         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 816         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 832         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 15488       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 15616       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 15744       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 15872       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 16000       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16896       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 28672       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 29082       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 29491       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 29901       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 30310       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 30720       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 31130       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 31539       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 31949       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 30592       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 30624       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 30656       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 30688       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 30720       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 30752       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 384         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 392         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 400         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 424         |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1608        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2032        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2455        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2878        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 3302        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3725        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 4148        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4572        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4995        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5419        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 768         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 800         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 218         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 220         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 44          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 46          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 49          |

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| Name  | Input Value  |                           |        |
|---|--|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]                                   | 179  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]                                   | 192  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]                                   | 205  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]                                   | 218  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]                                   | 230  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]                                   | 243  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]                                   | 256  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]                                   | 269  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]                                   | 282  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]                                   | 294  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]                                  | 307  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]                                  | 320  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]                           | 92   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]                           | 93   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]                           | 95   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]                           | 96   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]                           | 97   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]                           | 99   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]                           | 100  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]                           | 101  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]                           | 102  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]                           | 104  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]                          | 105  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]                          | 106  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 3277   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 4915   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 6554   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 8192   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 9830   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1766   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1792   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1818   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1843   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1869   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                | 4.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value               | 1118   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_lgc.value          | 0  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                      | -1.03  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value              | -30.05   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                   | 120.08   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                | 7.1  |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmc  | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_I  | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpSi    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hwl   | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcco | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBl    | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       |                           |        |
| Name  | Actual Value   | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126098.086   | 126098.085 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -3128609.5   | -3128609.352 ± 9.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 340.747711   | 340.7476731 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 4.4000001  | 4.4 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 1234.19995   | 1234.2 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.137333333   | -0.137333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 6.04672861   | 6.046728833 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value            | -8.80000019  | -8.8 ± 0.00048828125      | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.10 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126201.06                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -4.4                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -270.2                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | -1.7                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -15.3                             |
| PrevTbarAng_HwDeg_M_f32                         | 0.279                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -5.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.025896                          |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 80.02                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 8.8                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 600.06                            |
| k_DmpDecelGain_Uls_f32                          | 7.2                               |
| k_DmpGainOffThresh_KphpS_f32                    | 16.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 30.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.0001                            |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.2                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1427                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1655                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1884                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2112                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2340                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2568                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2796                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 3024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3252                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3480                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1608                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2032                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2455                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2878                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3302                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3725                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 4148                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4572                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4995                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5419                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 688                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 704                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 720                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 736                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 752                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 768                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 784                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 800                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 816                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 832                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 1296                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 1312                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 1328                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 1344                              |



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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 1360        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 1376        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 1392        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 1408        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 1424        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 1440        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 1456        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 1472        |
| t_CmnVehSpd_Kph_u9p7[0]                   | 10368       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 10496       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 10624       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 10752       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 10880       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 11008       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 11136       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11264       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 11392       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 11648       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 11776       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 27264       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 27296       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 27328       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 27360       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 27392       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 27424       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 3608        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 3616        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 3624        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 3632        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 3640        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 3648        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 11469       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1789        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2130        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2471        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2811        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 3152        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3834        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4175        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4515        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 4856        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 784         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 880         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 63          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 66          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 49          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 51          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 54          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 57          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 60          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 63          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 66          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 71          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 74          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 77          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 80          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 192   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 205   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 282   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 1   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 3   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 4   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 5   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 6   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 8   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 9   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 10  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 12  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 13  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 14  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 15  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 11469   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 410   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 435   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 461   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 486   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 512   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -4.5  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 2.5   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -40.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 130.09  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 7.1   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126199.859  | 126199.8599 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -377091.875   | -377091.8717 ± 0.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -0.866061449  | -0.866061495 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -4.4000001  | -4.4 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -270.200012   | -270.2 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0.284090906   | 0.284090909 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -5.29165506   | -5.291654909 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.11 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126303.035                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 5.5                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 6789                              |
| Prev2PreAttnComp_MtrNm_M_f32                    | 1.7                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 5322.2                            |
| PrevTbarAng_HwDeg_M_f32                         | -0.269                            |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 5.2                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.03698                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 90.02                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 9.6                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 700.02                            |
| k_DmpDecelGain_Uls_f32                          | 8.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 24.1                              |
| k_DmpGainOnThresh_KphpS_f32                     | 35.3                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00008                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.1                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1608                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2032                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2455                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2878                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3302                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3725                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 4148                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4572                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4995                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5419                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1789                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2130                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2471                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2811                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3152                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3834                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4175                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4515                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4856                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 1296                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 1312                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 1328                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 1344                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 1360                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 1376                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 1392                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 1408                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 1424                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 1440                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 1456                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 1472                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 1136                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 1152                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 1168                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 1184                              |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 1200        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 1216        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 1232        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 1248        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 1264        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 1280        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 1296        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 1312        |
| t_CmnVehSpd_Kph_u9p7[0]                   | 5248        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 5376        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5504        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 5632        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 5760        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 5888        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 6016        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 6144        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 6272        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 6528        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 6656        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 28672       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 29082       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 29491       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 29901       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 30310       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 30720       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 31130       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 31539       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 31949       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 14592       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 14624       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 14656       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 14688       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 14720       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 14752       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 288         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 296         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 304         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 312         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 320         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 328         |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 11469       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 13107       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 161         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1160        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1326        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 944         |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 960         |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 78          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 80          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 65          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 70          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 73          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 75          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 78          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 80          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 83          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 86          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 88          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 91          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 93          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 192   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 205   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 282   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 294   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 307   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 15  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 17  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 18  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 19  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 20  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 22  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 23  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 24  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 26  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 27  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 28  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 29  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 11469   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 13107   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 666   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 691   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 717   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 742   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 768   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 3.1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -350.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -2.6  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 11.02   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 140.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 1.1   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126301.633  | 126301.635 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 1181610.88  | 1181610.552 ± 9.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -33.2495117   | -33.24951101 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 5.5   | 5.5 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 6789  | 6789 ± 0.00390625         | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.270833313  | -0.270833333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 4.9738059   | 4.973805667 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125       | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.12 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126405.01                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -5.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -37.03                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | -8.3                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -42.2                             |
| PrevTbarAng_HwDeg_M_f32                         | 2.459                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -4.2                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.02547                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 11.12                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 800.01                            |
| k_DmpDecelGain_Uls_f32                          | 9.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 32.3                              |
| k_DmpGainOnThresh_KphpS_f32                     | 40.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00009                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.9                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1789                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2130                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2471                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2811                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3152                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3834                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4175                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4515                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4856                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1608                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2032                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2455                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2878                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3302                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3725                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 4148                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4572                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4995                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5419                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 1136                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 1152                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 1168                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 1184                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 1200                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 1216                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 1232                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 1248                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 1264                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 1280                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 1296                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 1312                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 224                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 256         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 272         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 288         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 304         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 320         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 336         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 352         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 3968        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 4096        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 4224        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 4352        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 4480        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 4608        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 4736        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 4864        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 4992        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 5248        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 5376        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 20960       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 20992       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 21024       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 21056       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 21088       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 21120       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 384         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 392         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 400         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 424         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 13107       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 14746       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 342         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 683         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1024        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1705        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 2046        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 2387        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2728        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 3068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 3409        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1008        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1040        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 106         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 109         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 93          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 96          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 99          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 101         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 104         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 106         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 109         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 111         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 114         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 116         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 119         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 122         |

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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 205   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 218   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 230   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 243   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 256   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 269   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 282   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 294   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 307   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 320   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 333   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 346   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 31  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 32  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 33  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 35  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 36  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 37  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 38  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 40  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 41  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 42  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 44  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 45  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 8192  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 9830  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 11469   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 13107   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 14746   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 922   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 947   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 973   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 998   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1024  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -3.2  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 350.3   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 3.7   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 22.03   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 150.03  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 2.2   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 126403.406  | 126403.41 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -343428.688   | -343428.7798 ± 0.9       | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 314.997375  | 314.9973886 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -5.5  | -5.5 ± 0.00048828125     | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -37.0299988   | -37.03 ± 0.00390625      | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 2.4666667   | 2.466666667 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -3.99539185   | -3.995391 ± 0.00390625   | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125        | ✔      |



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| T   |       |                   |       |        | ✓ |
|---|-------|-------------------|-------|--------|---|
| Actual Function                                       | Count | Expected Function | Count | Result |   |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     |                   |       |        |   |

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FrqDepDmpnInrtCmp\_Per1

| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 560         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 576         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 592         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 608         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 624         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 640         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 656         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 672         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 25216       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 25248       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 25280       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 25312       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 25344       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 25376       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 448         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 456         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 464         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 472         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 480         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 488         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 523         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1038        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1553        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2583        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3614        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4129        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4644        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5159        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1088        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1120        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 129         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 131         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 116         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 118         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 121         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 123         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 126         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 129         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 131         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 134         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 136         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 139         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 141         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 144         |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 282   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 294   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 307   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 320   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 333   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 346   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 358   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 46  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 47  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 49  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 50  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 51  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 52  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 54  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 55  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 56  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 58  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 59  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 60  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1178  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1203  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1229  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1254  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1280  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -8.8  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -400.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -3.8  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 33.05   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 160.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 3.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126505.188  | 126505.1849 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 1010980   | 1010980.109 ± 9.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -319.417603   | -319.4175991 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 6.5999999   | 6.6 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 26.0200005  | 26.02 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -1.51999998   | -1.52 ± 0.00390625        | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 4.10051537  | 4.100515 ± 0.00390625     | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.14 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126608.96                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -6.6                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -33.05                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | -7.5                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -922.3                            |
| PrevTbarAng_HwDeg_M_f32                         | 1.16                              |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -3.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.03692                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 33.15                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 3.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1000.05                           |
| k_DmpDecelGain_Uls_f32                          | 1.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 48.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 47.6                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00011                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.99                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1789                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2130                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2471                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2811                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3152                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3834                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4175                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4515                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4856                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1659                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 496                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 512                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 528                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 544                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 560                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 576                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 592                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 608                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 624                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 640                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 112                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 240         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3264        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3296        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3328        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3360        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 3392        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 3424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 680         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 688         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 696         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 704         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 712         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 720         |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 704         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 814         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 924         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1034        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1144        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1254        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1475        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1585        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1695        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1152        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1200        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 157         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 161         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 144         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 146         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 149         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 152         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 154         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 157         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 159         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 162         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 164         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 167         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 169         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 172         |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 13  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 154   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 61  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 63  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 64  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 65  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 67  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 68  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 69  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 70  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 72  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 73  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 74  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 76  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 9830  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1434  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1459  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1485  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1510  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1536  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 8.8   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 300.6   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 4.1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -11.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 170.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 4.4   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126606.961  | 126606.9599 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 1334381.63  | 1334381.785 ± 9.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 296.508514  | 296.5085113 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -6.5999999  | -6.6 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -33.0499992   | -33.05 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 1.17142856  | 1.171428571 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -3.15980816   | -3.159808571 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125       | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.15 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126710.935                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 7.7                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 18.03                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | 7.5                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 28.5                              |
| PrevTbarAng_HwDeg_M_f32                         | -0.92                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 5.2                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.01258                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 44.51                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 4.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1100.02                           |
| k_DmpDecelGain_Uls_f32                          | 1.9                               |
| k_DmpGainOffThresh_KphpS_f32                    | 4.2                               |
| k_DmpGainOnThresh_KphpS_f32                     | 30.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00012                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.6                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 1392                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 1408                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 1424                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 1440                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 1456                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 1472                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 1488                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 1504                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 1520                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 1536                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 1552                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 1568                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 128                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 256         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 6784        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 6912        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 7040        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 7168        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7296        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 7424        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 7552        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 7808        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 7936        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 8064        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 8192        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3776        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3808        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3840        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3872        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 3904        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 3936        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1536        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1544        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1552        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1560        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1568        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1576        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 11469       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 885         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 986         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1087        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1188        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1288        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1389        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1490        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1591        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1692        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1793        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1232        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1280        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 183         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 185         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 172         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 174         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 176         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 178         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 180         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 183         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 185         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 187         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 189         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 191         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 193         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 195         |



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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 166   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 77  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 78  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 79  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 81  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 82  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 83  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 84  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 86  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 87  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 88  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 90  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 91  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 11469   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1690  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1715  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1741  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1766  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1792  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -300.1  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -4.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -22.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 180.05  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 6.6   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126710.938  | 126710.935 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 26591.9277  | 26591.92825 ± 0.09        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -177.270554   | -177.2705444 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 7.69999981  | 7.7 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 18.0300007  | 18.03 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.933333278  | -0.933333333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 5.05071735  | 5.050717333 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.16 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126812.91                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -7.7                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -28.5                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | -6.5                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -297.3                            |
| PrevTbarAng_HwDeg_M_f32                         | 1.145                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -4.2                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.03257                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 55.12                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 5.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1200.05                           |
| k_DmpDecelGain_Uls_f32                          | 2.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 8.2                               |
| k_DmpGainOnThresh_KphpS_f32                     | 35.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00013                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.5                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3409                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1659                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 496                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 512                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 528                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 544                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 560                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 576                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 592                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 608                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 624                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 640                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 144                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 256         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 272         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 12698       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 13107       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 13517       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 13926       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 14336       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 14746       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 15155       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 15565       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 15974       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 16384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 5280        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 5312        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 5344        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 5376        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 5408        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 5440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1480        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1488        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1496        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1504        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1512        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1520        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 13107       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1066        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1212        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1359        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1506        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1653        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1800        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1946        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2093        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 2240        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 2387        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1296        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1360        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 230         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 232         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 218         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 220         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 223         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 225         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 227         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 230         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 232         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 234         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 237         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 239         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 241         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 243         |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 179   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 92  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 93  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 95  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 96  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 97  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 99  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 100   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 101   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 102   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 104   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 105   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 106   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 11469   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 13107   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1894  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1920  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1946  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1971  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1997  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -5.4  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 200.2   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 6.3   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -33.05  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 190.05  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 7.7   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126812.906  | 126812.91 ± 0.0625        | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 267220.719  | 267220.7121 ± 0.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 96.8688278  | 96.86883293 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -7.69999981   | -7.7 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -28.5   | -28.5 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 1.14545453  | 1.145454545 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -4.05580378   | -4.055803727 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125       | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.17 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 126914.885                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | 1.5                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 24.6                              |
| Prev2PreAttnComp_MtrNm_M_f32                    | 6.5                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 382.2                             |
| PrevTbarAng_HwDeg_M_f32                         | -0.979                            |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 4.3                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.096321                          |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 66.13                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 6.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1300.06                           |
| k_DmpDecelGain_Uls_f32                          | 5.6                               |
| k_DmpGainOffThresh_KphpS_f32                    | 12.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 40.1                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00014                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.4                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3409                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1659                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 1136                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 1152                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 1168                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 1184                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 1200                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 1216                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 1232                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 1248                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 1264                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 1280                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 1296                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 1312                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 688                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 704                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 720         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 736         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 752         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 768         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 784         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 800         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 816         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 832         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 16794       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 17203       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 17613       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 18022       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 18432       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 18842       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 19251       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 19661       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 20070       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 20480       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 11680       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 11712       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 11744       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 11776       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 11808       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 11840       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1608        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1616        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1624        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1632        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1640        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1648        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 13107       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 14746       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1246        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1638        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2030        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2422        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2814        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3206        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3598        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 3990        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4382        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 4774        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1344        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1440        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 71          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 74          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 3           |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 5           |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 8           |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 10          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 31          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 192   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 1   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 3   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 4   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 5   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 6   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 8   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 9   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 10  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 12  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 13  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 14  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 15  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 11469   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 13107   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 14746   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 922   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 947   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 973   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 998   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1024  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 5.5   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -200.4  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -6.4  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -44.06  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 210.03  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 1.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 126912.281  | 126912.2849 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -756922.563   | -756922.4402 ± 0.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -79.67099   | -79.67099743 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 1.5   | 1.5 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 24.6000004  | 24.6 ± 0.00390625         | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.984615386  | -0.984615385 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 3.61537886  | 3.615379969 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | -8.80000019   | -8.8 ± 0.00048828125      | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.18 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 127016.86                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -1.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -16.2                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | -4.5                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -25.6                             |
| PrevTbarAng_HwDeg_M_f32                         | 0.989                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 1.5                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.047852                          |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 77.14                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 7.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1400.05                           |
| k_DmpDecelGain_Uls_f32                          | 2.1                               |
| k_DmpGainOffThresh_KphpS_f32                    | 16.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 45.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00015                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.3                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 523                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1038                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1553                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2583                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3099                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3614                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4129                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4644                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5159                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 224                               |



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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 256         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 272         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 288         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 304         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 320         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 336         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 352         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 12928       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 13056       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 13184       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 13312       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 13440       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 13568       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 13696       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 13824       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 13952       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 14080       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 14208       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 20890       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 21299       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 21709       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 22118       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 22528       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 22938       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 23347       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 23757       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 24166       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 24576       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3872        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3904        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3936        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3968        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4000        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4032        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 2408        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 2416        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 2424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 2432        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 2440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 2448        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 342         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 683         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1024        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1705        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 2046        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 2387        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2728        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 3068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 3409        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1520        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1536        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 86          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 88          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 5           |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 8           |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 10          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 33          |

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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 64  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 77  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 90  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 102   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 115   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 128   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 141   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 154   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 166   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 179   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 192   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 205   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 15  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 17  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 18  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 19  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 20  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 22  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 23  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 24  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 26  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 27  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 28  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 29  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1178  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1203  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1229  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1254  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1280  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 2.2   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 100.8   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 7.5   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 11.01   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 3.2   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 127014.063  | 127014.0599 ± 0.0625     | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -12284.4609   | -12284.45952 ± 0.09      | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 30.5068626  | 30.50686197 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -1.5  | -1.5 ± 0.00048828125     | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -16.2000008   | -16.2 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 1   | 1 ± 0.00390625           | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 1.69140744  | 1.691408 ± 0.00390625    | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | -8.80000019   | -8.8 ± 0.00048828125     | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |                   |       |        | ✓ |
|---|-------|-------------------|-------|--------|---|
| Actual Function                                       | Count | Expected Function | Count | Result |   |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     |                   |       |        |   |

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FrqDepDmpnInrtCmp\_Per1

| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 560         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 576         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 592         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 608         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 624         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 640         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 656         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 672         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 15488       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 15616       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 15744       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 15872       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 16000       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16896       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 4192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 4224        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 4256        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 4288        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4320        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4352        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 384         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 392         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 400         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 424         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 523         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1038        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1553        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 2583        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3614        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4129        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4644        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5159        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1552        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1600        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 114         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 116         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 10          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 38          |

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FrqDepDmpnInrtCmp\_Per1

| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 192   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 205   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 282   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 294   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 31  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 32  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 33  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 35  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 36  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 37  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 38  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 40  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 41  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 42  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 44  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 45  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[0]   | 3277  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[1]   | 4915  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[2]   | 6554  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[3]   | 8192  |                           |        |
| t_RIAstWIRBIndTblY_Uls_u2p14[4]   | 9830  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[0]   | 1434  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[1]   | 1459  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[2]   | 1485  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[3]   | 1510  |                           |        |
| t_WIRBIndTblX_MtrNm_u8p8[4]   | 1536  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -2.1  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -100.4  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -7.6  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 12.03   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 511.9921875   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 4.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 127115.836  | 127115.835 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -388429.438   | -388429.5001 ± 0.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -20.7490158   | -20.74901587 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 2.5   | 2.5 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 100.800003  | 100.8 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.894117653  | -0.894117647 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -1.23690033   | -1.236898824 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.20 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 127220.81                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | -2.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -69.6                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | -3.5                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -59.2                             |
| PrevTbarAng_HwDeg_M_f32                         | 0.909                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 1.2                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.3479                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 99.12                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 9.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1600.03                           |
| k_DmpDecelGain_Uls_f32                          | 2.6                               |
| k_DmpGainOffThresh_KphpS_f32                    | 22.3                              |
| k_DmpGainOnThresh_KphpS_f32                     | 33.5                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.0003                            |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.1                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1659                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 688                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 704                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 720         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 736         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 752         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 768         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 784         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 800         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 816         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 832         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 10368       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 10496       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 10624       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 10752       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 10880       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 11008       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 11136       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11264       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 11392       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 11648       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 11776       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 28672       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 29082       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 29491       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 29901       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 30310       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 30720       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 31130       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 31539       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 31949       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 5792        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 5824        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 5856        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 5888        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 5920        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 5952        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 3608        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 3616        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 3624        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 3632        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 3640        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 3648        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 11469       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 704         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 814         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 924         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1034        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1144        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1254        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1475        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1585        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1695        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1616        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1680        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 136         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 139         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 13          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 41          |

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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 179   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 192   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 205   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 218   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 230   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 243   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 256   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 269   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 282   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 294   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 307   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 320   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 46  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 47  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 49  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 50  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 51  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 52  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 54  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 55  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 56  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 58  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 59  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 60  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 4915  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 6554  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 8192  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 9830  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 11469   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1690  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1715  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1741  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1766  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1792  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 1.5   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 150.5   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 8.7   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 13.05   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 250.02  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 5.2   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 127217.609  | 127217.6099 ± 0.0625     | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -34957.4961   | -34957.49739 ± 0.09      | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 16.6422844  | 16.64228823 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -2.5  | -2.5 ± 0.00048828125     | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -69.5999985   | -69.6 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0.915789425   | 0.915789474 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 1.96354413  | 1.963548947 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | -8.80000019   | -8.8 ± 0.00048828125     | ✔      |



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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.21 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 127322.785                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | -3.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -49.2                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | -2.4                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -366.2                            |
| PrevTbarAng_HwDeg_M_f32                         | -6.771                            |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -1.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.2244                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 27.02                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.3                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1700.05                           |
| k_DmpDecelGain_Uls_f32                          | 2.1                               |
| k_DmpGainOffThresh_KphpS_f32                    | 16.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 44.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00031                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.9                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3409                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 683                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1705                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2046                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 16                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 64                                |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 80          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 192         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 5248        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 5376        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5504        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 5632        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 5760        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 5888        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 6016        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 6144        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 6272        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 6528        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 6656        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 9120        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 9152        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 9184        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 9216        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 9248        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 9280        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 288         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 296         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 304         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 312         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 320         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 328         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 13107       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 885         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 986         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1087        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1188        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1288        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1389        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1490        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1591        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1692        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1793        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1728        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1760        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 166         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 166         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 15          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 44          |

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| Name   | Input Value  |                           |        |
|--|--|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]  | 141  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]  | 154  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]  | 166  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]  | 179  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]  | 192  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]  | 205  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]  | 218  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]  | 230  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]  | 243  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]  | 256  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]   | 269  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]   | 282  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 61   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 63   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 64   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 65   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 67   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 68   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 69   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 70   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 72   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 73   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]   | 74   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]   | 76   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]  | 6554   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]  | 8192   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]  | 9830   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]  | 11469  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]  | 13107  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]  | 1894   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]  | 1920   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]  | 1946   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]  | 1971   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]  | 1997   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                               | -1.6   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                              | -150.6   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                     | -8.8   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                             | 14.06  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                  | 220.02   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                               | 0  |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       |                           |        |
| Name   | Actual Value   | Expected Value            | Result |
| PreDecelGain_Uls_M_f32   | 127319.383   | 127319.3849 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32   | 527959.5   | 527959.4157 ± 0.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32   | -135.810211  | -135.810175 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32   | -3.5   | -3.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32   | -49.2000008  | -49.2 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32  | -6.76923132  | -6.769230769 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32   | -0.96496433  | -0.964892308 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                           | 0  | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.22 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 8787                              |
| Prev1PreAttnComp_MtrNm_M_f32                    | 4.5                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 22.3                              |
| Prev2PreAttnComp_MtrNm_M_f32                    | 2.4                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 115.2                             |
| PrevTbarAng_HwDeg_M_f32                         | 3.403                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 2.6                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.3366                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 26.03                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 2.7                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1800.06                           |
| k_DmpDecelGain_Uls_f32                          | 2.2                               |
| k_DmpGainOffThresh_KphpS_f32                    | 20.3                              |
| k_DmpGainOnThresh_KphpS_f32                     | 8.5                               |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00032                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 1                                 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 523                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1038                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1553                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2583                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3099                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3614                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4129                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4644                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5159                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 523                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1038                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1553                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4644                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5159                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 256                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 32                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 80                                |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 96          |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 208         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 3968        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 4096        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 4224        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 4352        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 4480        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 4608        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 4736        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 4864        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 4992        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 5248        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 5376        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 32320       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 32352       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 32384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 32416       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 32448       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 32480       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 384         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 392         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 400         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 408         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 416         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 424         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 13107       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 14746       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 161         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1160        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1326        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1776        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1840        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 189         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 191         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 18          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 44          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 46          |

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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 166   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 179   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 192   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 205   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 218   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 230   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 243   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 256   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 269   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 282   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 294   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 307   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 77  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 78  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 79  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 81  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 82  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 83  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 84  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 86  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 87  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 88  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 90  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 91  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 8192  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 9830  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 11469   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 13107   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 14746   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1178  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1203  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1229  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1254  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1280  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 1.1   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 250.02  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 9.2   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 15.02   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 230.03  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 8.8   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 8783.39941  | 8783.39988 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -3935.75269   | -3935.753195 ± 0.009     | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 250.816666  | 250.8166781 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 4.5   | 4.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 22.2999992  | 22.3 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 3.40740728  | 3.407407407 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 2.46656632  | 2.466606667 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | -8.80000019   | -8.8 ± 0.00048828125     | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.23 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 45678                             |
| Prev1PreAttnComp_MtrNm_M_f32                    | -4.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -48.5                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | -1.1                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -380.2                            |
| PrevTbarAng_HwDeg_M_f32                         | -3.06                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -2.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.4488                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 53.25                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 3.1                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 1900.08                           |
| k_DmpDecelGain_Uls_f32                          | 2.6                               |
| k_DmpGainOffThresh_KphpS_f32                    | 22.5                              |
| k_DmpGainOnThresh_KphpS_f32                     | 16.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00033                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.7                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 704                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 814                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 924                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1034                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1144                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1254                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1475                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1585                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1695                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 523                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1038                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1553                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2068                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4644                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5159                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 144                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 160                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 256                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 272                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 48                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 96                                |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 112         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 224         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 12698       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 13107       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 13517       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 13926       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 14336       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 14746       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 15155       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 15565       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 15974       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 16384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 30592       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 30624       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 30656       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 30688       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 30720       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 30752       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 448         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 456         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 464         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 472         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 480         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 488         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 342         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 683         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1024        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 1364        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1705        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 2046        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 2387        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2728        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 3068        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 3409        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1760        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 1920        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 237         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 239         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 20          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 23          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 26          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 28          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 31          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 33          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 36          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 38          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 41          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 44          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 46          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 49          |



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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 205   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 218   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 230   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 243   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 256   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 269   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 282   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 294   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 307   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 320   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 333   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 346   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 92  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 93  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 95  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 96  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 97  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 99  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 100   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 101   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 102   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 104   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 105   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 106   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1434  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1459  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1485  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1510  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1536  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -1.1  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -250.03   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -9.5  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 35.01   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 240.05  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 5.5   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 45674.1992  | 45674.19984 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 57899.4453  | 57899.44082 ± 0.09        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -176.861588   | -176.8615543 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -4.5  | -4.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -48.5   | -48.5 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -3.06451631   | -3.064516129 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -2.39147186   | -2.391419355 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.24 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 127628.71                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | 6.5                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 163.6                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | 1.1                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | 175.3                             |
| PrevTbarAng_HwDeg_M_f32                         | 1.154                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 3.2                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.5599                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 27.06                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 1.3                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 200.09                            |
| k_DmpDecelGain_Uls_f32                          | 2.8                               |
| k_DmpGainOffThresh_KphpS_f32                    | 22.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 24.6                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00034                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.6                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 885                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 986                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1087                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1188                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1288                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1389                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1490                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1591                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1692                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1793                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 704                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 814                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 924                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1034                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1144                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1254                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1364                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1475                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1585                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1695                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 336                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 352                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 368                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 384                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 400                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 416                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 432                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 448                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 464                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 480                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 496                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 512                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 64                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 112                               |

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FrqDepDmpnInrtCmp\_Per1

| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 128         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 240         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 16794       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 17203       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 17613       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 18022       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 18432       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 18842       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 19251       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 19661       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 20070       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 20480       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 27264       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 27296       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 27328       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 27360       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 27392       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 27424       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 680         |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 688         |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 696         |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 704         |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 712         |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 720         |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 9830        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 11469       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 13107       |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 14746       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 161         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1160        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1326        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1760        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2000        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 49          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 51          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 49          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 51          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 54          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 57          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 60          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 63          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 66          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 71          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 74          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 77          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 80          |

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| Name   | Input Value  |                          |        |
|--|--|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]  | 218  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]  | 230  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]  | 243  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]  | 256  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]  | 269  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]  | 282  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]  | 294  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]  | 307  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]  | 320  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]  | 333  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]   | 346  |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]   | 358  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]  | 1  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]  | 3  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]  | 4  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]  | 5  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]  | 6  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]  | 8  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]  | 9  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]  | 10   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]  | 12   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]  | 13   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]   | 14   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]   | 15   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]  | 3277   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]  | 4915   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]  | 6554   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]  | 8192   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]  | 9830   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]  | 1690   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]  | 1715   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]  | 1741   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]  | 1766   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]  | 1792   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                               | 2.2  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                              | 450.25   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                     | 1.5  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                             | -35.06   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                  | 260.02   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                               | 6.2  |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32             |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32     |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32          |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32       |                          |        |
| Name   | Actual Value   | Expected Value           | Result |
| PreDecelGain_Uls_M_f32   | 127628.313   | 127628.3098 ± 0.0625     | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32   | -25875.293   | -25875.2916 ± 0.09       | ✔      |
| Prev1SclDrvVel_RadpS_M_f32   | 270.225586   | 270.2255612 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32   | 6.5  | 6.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32   | 163.600006   | 163.6 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32  | 1.15384614   | 1.153846154 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32   | 1.36523604   | 1.365250769 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                           | -8.80000019  | -8.8 ± 0.00048828125     | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.25 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 127730.685                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | -6.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -90.23                            |
| Prev2PreAttnComp_MtrNm_M_f32                    | -8.1                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | -120.1                            |
| PrevTbarAng_HwDeg_M_f32                         | -0.554                            |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 4.5                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.1258                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 26.02                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 2.7                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 300.06                            |
| k_DmpDecelGain_Uls_f32                          | 3.5                               |
| k_DmpGainOffThresh_KphpS_f32                    | 33.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 32.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00035                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.5                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1066                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1212                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1359                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 1506                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 1653                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1800                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1946                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2093                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 2240                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 2387                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 885                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 986                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1087                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1188                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1288                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1389                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1490                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 1591                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1692                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1793                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 688                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 704                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 720                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 736                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 752                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 768                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 784                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 800                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 816                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 832                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 80                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 128                               |

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FrqDepDmpInIntCmp\_Per1

| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 144         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 256         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 6784        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 6912        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 7040        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 7168        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7296        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 7424        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 7552        |
| t_CmnVehSpd_Kph_u9p7[7]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 7808        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 7936        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 8064        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 8192        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 20890       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 21299       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 21709       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 22118       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 22528       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 22938       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 23347       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 23757       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 24166       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 24576       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 9120        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 9152        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 9184        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 9216        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 9248        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 9280        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1536        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1544        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1552        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1560        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1568        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1576        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 161         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1160        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1326        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1920        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2080        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 65          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 65          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 68          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 70          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 73          |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 75          |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 78          |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 80          |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 83          |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 86          |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 88          |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 91          |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 93          |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 13  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 154   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 15  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 17  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 18  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 19  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 20  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 22  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 23  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 24  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 26  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 27  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 28  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 29  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 11469   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1894  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1920  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1946  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1971  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1997  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -2.2  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -450.14   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -1.5  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 30.02   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 270.06  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 7.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 127730.086  | 127730.0849 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 44157.7891  | 44157.78752 ± 0.09        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -224.675308   | -224.6753087 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -6.5  | -6.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -90.2300034   | -90.23 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.555555522  | -0.555555556 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 3.83605886  | 3.836055556 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.26 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 127832.66                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | 7.5                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | -1100.2                           |
| Prev2PreAttnComp_MtrNm_M_f32                    | 8.1                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | -36.2                             |
| PrevTbarAng_HwDeg_M_f32                         | 0.8                               |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -4.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.2365                            |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 53.12                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 3.1                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 200.02                            |
| k_DmpDecelGain_Uls_f32                          | 3.9                               |
| k_DmpGainOffThresh_KphpS_f32                    | 15.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 40.2                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00036                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.89                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1246                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1638                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2030                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2422                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 2814                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3206                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3598                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 3990                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4382                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4774                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1066                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1212                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1359                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1506                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 1653                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1800                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 1946                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2093                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 2240                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 2387                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 1296                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 1312                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 1328                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 1344                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 1360                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 1376                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 1392                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 1408                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 1424                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 1440                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 1456                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 1472                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 96                                |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 112                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 128                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 144                               |



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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 160         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 176         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 192         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 208         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 224         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 240         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 256         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 272         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 128         |
| t_CmnVehSpd_Kph_u9p7[1]                   | 256         |
| t_CmnVehSpd_Kph_u9p7[2]                   | 384         |
| t_CmnVehSpd_Kph_u9p7[3]                   | 512         |
| t_CmnVehSpd_Kph_u9p7[4]                   | 640         |
| t_CmnVehSpd_Kph_u9p7[5]                   | 768         |
| t_CmnVehSpd_Kph_u9p7[6]                   | 896         |
| t_CmnVehSpd_Kph_u9p7[7]                   | 1024        |
| t_CmnVehSpd_Kph_u9p7[8]                   | 1152        |
| t_CmnVehSpd_Kph_u9p7[9]                   | 1280        |
| t_CmnVehSpd_Kph_u9p7[10]                  | 1408        |
| t_CmnVehSpd_Kph_u9p7[11]                  | 1536        |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 24986       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 25395       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 25805       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 26214       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 26624       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 27034       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 27443       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 27853       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 28672       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 32320       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 32352       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 32384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 32416       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 32448       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 32480       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1480        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1488        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1496        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1504        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1512        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1520        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 11469       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1608        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2032        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2455        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2878        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 3302        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3725        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 4148        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4572        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4995        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5419        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 2080        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2160        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 93          |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 96          |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 93          |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 96          |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 99          |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 101         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 104         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 106         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 109         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 111         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 114         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 116         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 119         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 122         |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 166   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 31  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 32  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 33  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 35  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 36  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 37  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 38  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 40  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 41  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 42  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 44  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 45  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 11469   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 13107   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 794   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 819   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 845   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 870   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 896   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 3.3   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 550.2   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 2.5   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -50   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 280.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 5.2   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 127832.258  | 127832.26 ± 0.0625        | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -2236951.25   | -2236951.286 ± 9.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 488.806824  | 488.8068117 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 7.5   | 7.5 ± 0.00048828125       | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -1100.19995   | -1100.2 ± 0.00390625      | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0.806451619   | 0.806451613 ± 0.00390625  | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -2.67284751   | -2.672846774 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |                   |       |        | ✓ |
|---|-------|-------------------|-------|--------|---|
| Actual Function                                       | Count | Expected Function | Count | Result |   |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     |                   |       |        |   |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 400         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 416         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 432         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 448         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 464         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 480         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 496         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 512         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 2560        |
| t_CmnVehSpd_Kph_u9p7[1]                   | 3840        |
| t_CmnVehSpd_Kph_u9p7[2]                   | 5120        |
| t_CmnVehSpd_Kph_u9p7[3]                   | 6400        |
| t_CmnVehSpd_Kph_u9p7[4]                   | 7680        |
| t_CmnVehSpd_Kph_u9p7[5]                   | 8960        |
| t_CmnVehSpd_Kph_u9p7[6]                   | 10240       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 14080       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 15360       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16640       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 28262       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 28672       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 29082       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 29491       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 29901       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 30310       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 30720       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 31130       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 31539       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 31949       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 30592       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 30624       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 30656       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 30688       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 30720       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 30752       |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1208        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1216        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1224        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1232        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1240        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1248        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 1638        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 3277        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 8192        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1789        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2130        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2471        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2811        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 3152        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3834        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4175        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4515        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 4856        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1680        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2240        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 116         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 118         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 116         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 118         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 121         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 123         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 126         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 129         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 131         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 134         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 136         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 139         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 141         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 144         |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 154   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 166   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 179   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 192   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 46  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 47  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 49  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 50  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 51  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 52  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 54  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 55  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 56  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 58  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 59  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 60  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 8192  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 9830  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 11469   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 13107   |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 14746   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1050  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1075  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1101  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1126  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1152  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -3.3  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -550.3  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 0   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -2.5  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 50  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 290.01  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 1.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 127934.031  | 127934.0349 ± 0.0625      | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 415103.719  | 415103.7843 ± 0.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -164.116653   | -164.1166652 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -7.5  | -7.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 250.050003  | 250.05 ± 0.00390625       | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.520833313  | -0.520833333 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 1.58375692  | 1.583755 ± 0.00390625     | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125       | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.28 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 128036.61                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | 8.5                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 5000.03                           |
| Prev2PreAttnComp_MtrNm_M_f32                    | 7.7                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | -38.3                             |
| PrevTbarAng_HwDeg_M_f32                         | 0.66                              |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -5.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.47856                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 46.32                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 5.2                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 100.05                            |
| k_DmpDecelGain_Uls_f32                          | 4.8                               |
| k_DmpGainOffThresh_KphpS_f32                    | 25.3                              |
| k_DmpGainOnThresh_KphpS_f32                     | 4.2                               |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00038                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.2                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1608                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2032                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2455                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2878                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3302                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3725                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 4148                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4572                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4995                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5419                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1427                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 1655                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 1884                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2112                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2340                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 2568                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 2796                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 3024                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3252                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3480                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 176                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 192                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 208                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 224                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 240                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 256                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 272                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 288                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 304                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 320                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 336                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 352                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 688                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 704                               |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 720         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 736         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 752         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 768         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 784         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 800         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 816         |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 832         |
| t_CmnVehSpd_Kph_u9p7[0]                   | 12800       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 12928       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 13056       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 13184       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 13312       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 13440       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 13568       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 13696       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 13824       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 13952       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 14080       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 14208       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 4506        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 4915        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 5325        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 5734        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 6144        |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 6554        |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 6963        |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 7373        |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 7782        |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 8192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 3872        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 3904        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 3936        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 3968        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4000        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4032        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1480        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1488        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1496        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1504        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1512        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1520        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0]          | 3277        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1]          | 4915        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2]          | 6554        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3]          | 8192        |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4]          | 9830        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1608        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2032        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2455        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2878        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 3302        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3725        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 4148        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4572        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4995        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5419        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1648        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2320        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 144         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 146         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 144         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 146         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 149         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 152         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 154         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 157         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 159         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 162         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 164         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 167         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 169         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 172         |

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| Name  | Input Value   |                          |        |
|---|---|--------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 179   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 192   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 205   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 218   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 230   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 243   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 256   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 269   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 282   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 294   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 307   |                          |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 320   |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 61  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 63  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 64  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 65  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 67  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 68  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 69  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 70  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 72  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 73  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 74  |                          |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 76  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 6554  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 8192  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 9830  |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 11469   |                          |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 13107   |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 1306  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 1331  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 1357  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 1382  |                          |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 1408  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 4.4   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 650.01  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 3.5   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 0   |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 305.05  |                          |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 2.3   |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                          |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                          |        |
| Name  | Actual Value  | Expected Value           | Result |
| PreDecelGain_Uls_M_f32  | 128036.406  | 128036.4099 ± 0.0625     | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 34435492  | 34435493.31 ± 99.9       | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 130.127335  | 130.127343 ± 0.00390625  | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 8.5   | 8.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 5000.02979  | 5000.03 ± 0.00390625     | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 0.673076928   | 0.673076923 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 0.261120796   | 0.261126154 ± 0.00390625 | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125      | ✔      |



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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.29 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 128138.585                        |
| Prev1PreAttnComp_MtrNm_M_f32                    | -8.5                              |
| Prev1ScIDrvVel_RadpS_M_f32                      | -26.3                             |
| Prev2PreAttnComp_MtrNm_M_f32                    | -6.6                              |
| Prev2ScIDrvVel_RadpS_M_f32                      | 175.2                             |
| PrevTbarAng_HwDeg_M_f32                         | -0.51                             |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | 6.1                               |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.58963                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 28.12                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 6.8                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 200.02                            |
| k_DmpDecelGain_Uls_f32                          | 5.9                               |
| k_DmpGainOffThresh_KphpS_f32                    | 30.2                              |
| k_DmpGainOnThresh_KphpS_f32                     | 8.3                               |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.00039                           |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.1                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1789                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 2130                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2471                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2811                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 3152                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 3834                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 4175                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 4515                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4856                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1608                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2032                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2455                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2878                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3302                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3725                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 4148                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4572                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4995                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5419                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 496                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 512                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 528                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 544                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 560                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 576                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 592                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 608                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 624                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 640                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 656                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 672                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 1296                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 1312                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 1328                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 1344                              |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 1360        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 1376        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 1392        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 1408        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 1424        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 1440        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 1456        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 1472        |
| t_CmnVehSpd_Kph_u9p7[0]                   | 15488       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 15616       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 15744       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 15872       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 16000       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 16128       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 16256       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 16384       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 16512       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 16640       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 16768       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 16896       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 8602        |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 9011        |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 9421        |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 9830        |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 10240       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 10650       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 11059       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 11469       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 11878       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 12288       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 4192        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 4224        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 4256        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 4288        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 4320        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 4352        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 2408        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 2416        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 2424        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 2432        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 2440        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 2448        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 4915        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 11469       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1789        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2130        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2471        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 2811        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 3152        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 3834        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 4175        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4515        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 4856        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1616        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2400        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 172         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 174         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 172         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 174         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 176         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 178         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 180         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 183         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 185         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 187         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 189         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 191         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 193         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 195         |

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| Name  | Input Value   |                           |        |
|---|---|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 13  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 26  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 38  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 51  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 64  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 77  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 90  |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 102   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 115   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 128   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 141   |                           |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 154   |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 77  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 78  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 79  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 81  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 82  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 83  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 84  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 86  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 87  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 88  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 90  |                           |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 91  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 1638  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 3277  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 4915  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 6554  |                           |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 8192  |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 282   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 307   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 333   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 358   |                           |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 384   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | -4.4  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | -650.08   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value                         | 1   |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | -3.5  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | -10.02  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 315.04  |                           |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 4.3   |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                           |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                           |        |
| Name  | Actual Value  | Expected Value            | Result |
| PreDecelGain_Uls_M_f32  | 128138.188  | 128138.185 ± 0.0625       | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | -420468.938   | -420469.0063 ± 0.9        | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | -64.6186523   | -64.61864443 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | -8.5  | -8.5 ± 0.00048828125      | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | -26.2999992   | -26.3 ± 0.00390625        | ✔      |
| PrevTbarAng_HwDeg_M_f32   | -0.514705896  | -0.514705882 ± 0.00390625 | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | 1.11588478  | 1.115892294 ± 0.00390625  | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 0   | 0 ± 0.00048828125         | ✔      |

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FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |

| Test Step 3.30 (Repeat Count = 1)               |                                   |
|---|-----------------------------------|
| Name  | Input Value                       |
| PreDecelGain_Uls_M_f32                          | 128240.56                         |
| Prev1PreAttnComp_MtrNm_M_f32                    | 1.3                               |
| Prev1ScIDrvVel_RadpS_M_f32                      | 18.2                              |
| Prev2PreAttnComp_MtrNm_M_f32                    | 6.6                               |
| Prev2ScIDrvVel_RadpS_M_f32                      | -120.8                            |
| PrevTbarAng_HwDeg_M_f32                         | 20                                |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp                   | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32                  | -3.5                              |
| TbarVelFiltSv_M_str.K_Uls_f32                   | 0.63214                           |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32               | 85.13                             |
| k_CmnTbarStiff_NmpDeg_f32                       | 0.5                               |
| k_DmpDecelGainFSlew_UlspS_f32                   | 300.03                            |
| k_DmpDecelGain_Uls_f32                          | 5.8                               |
| k_DmpGainOffThresh_KphpS_f32                    | 35.3                              |
| k_DmpGainOnThresh_KphpS_f32                     | 12.5                              |
| k_InrtCmp_MtrInertia_KgmSq_f32                  | 0.0004                            |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32            | 0.4                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 161                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 328                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 494                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 661                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][4] | 827                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994                               |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] | 1160                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1326                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 1789                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 2130                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 2471                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 2811                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 3152                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3493                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3834                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4175                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4515                              |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4856                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0]                 | 816                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1]                 | 832                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2]                 | 848                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3]                 | 864                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4]                 | 880                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5]                 | 896                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][6]                 | 912                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7]                 | 928                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8]                 | 944                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9]                 | 960                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][10]                | 976                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11]                | 992                               |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0]                 | 1136                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][1]                 | 1152                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2]                 | 1168                              |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3]                 | 1184                              |

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| Name                                      | Input Value |
|---|-------------|
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4]           | 1200        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][5]           | 1216        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][6]           | 1232        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][7]           | 1248        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][8]           | 1264        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9]           | 1280        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][10]          | 1296        |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][11]          | 1312        |
| t_CmnVehSpd_Kph_u9p7[0]                   | 10368       |
| t_CmnVehSpd_Kph_u9p7[1]                   | 10496       |
| t_CmnVehSpd_Kph_u9p7[2]                   | 10624       |
| t_CmnVehSpd_Kph_u9p7[3]                   | 10752       |
| t_CmnVehSpd_Kph_u9p7[4]                   | 10880       |
| t_CmnVehSpd_Kph_u9p7[5]                   | 11008       |
| t_CmnVehSpd_Kph_u9p7[6]                   | 11136       |
| t_CmnVehSpd_Kph_u9p7[7]                   | 11264       |
| t_CmnVehSpd_Kph_u9p7[8]                   | 11392       |
| t_CmnVehSpd_Kph_u9p7[9]                   | 11520       |
| t_CmnVehSpd_Kph_u9p7[10]                  | 11648       |
| t_CmnVehSpd_Kph_u9p7[11]                  | 11776       |
| t_DmpADDCoefX_MtrNm_u4p12[0]              | 12698       |
| t_DmpADDCoefX_MtrNm_u4p12[1]              | 13107       |
| t_DmpADDCoefX_MtrNm_u4p12[2]              | 13517       |
| t_DmpADDCoefX_MtrNm_u4p12[3]              | 13926       |
| t_DmpADDCoefX_MtrNm_u4p12[4]              | 14336       |
| t_DmpADDCoefX_MtrNm_u4p12[5]              | 14746       |
| t_DmpADDCoefX_MtrNm_u4p12[6]              | 15155       |
| t_DmpADDCoefX_MtrNm_u4p12[7]              | 15565       |
| t_DmpADDCoefX_MtrNm_u4p12[8]              | 15974       |
| t_DmpADDCoefX_MtrNm_u4p12[9]              | 16384       |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0]     | 5792        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1]     | 5824        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2]     | 5856        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3]     | 5888        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4]     | 5920        |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5]     | 5952        |
| t_DmpDecelGainSlewY_UlspS_u13p3[0]        | 1208        |
| t_DmpDecelGainSlewY_UlspS_u13p3[1]        | 1216        |
| t_DmpDecelGainSlewY_UlspS_u13p3[2]        | 1224        |
| t_DmpDecelGainSlewY_UlspS_u13p3[3]        | 1232        |
| t_DmpDecelGainSlewY_UlspS_u13p3[4]        | 1240        |
| t_DmpDecelGainSlewY_UlspS_u13p3[5]        | 1248        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[0]          | 6554        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[1]          | 8192        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[2]          | 9830        |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[3]          | 11469       |
| t_DmpFiltKpWIRBlndY_Uls_u2p14[4]          | 13107       |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 161         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994         |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1160        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1326        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1493        |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[0]         | 1648        |
| t_FDD_AttenTbIX_MtrRadpS_u12p4[1]         | 2480        |
| t_FDD_AttenTbIY_Uls_u8p8[0]               | 218         |
| t_FDD_AttenTbIY_Uls_u8p8[1]               | 220         |
| t_FDD_BlendTbIY_Uls_u8p8[0]               | 218         |
| t_FDD_BlendTbIY_Uls_u8p8[1]               | 220         |
| t_FDD_BlendTbIY_Uls_u8p8[2]               | 223         |
| t_FDD_BlendTbIY_Uls_u8p8[3]               | 225         |
| t_FDD_BlendTbIY_Uls_u8p8[4]               | 227         |
| t_FDD_BlendTbIY_Uls_u8p8[5]               | 230         |
| t_FDD_BlendTbIY_Uls_u8p8[6]               | 232         |
| t_FDD_BlendTbIY_Uls_u8p8[7]               | 234         |
| t_FDD_BlendTbIY_Uls_u8p8[8]               | 237         |
| t_FDD_BlendTbIY_Uls_u8p8[9]               | 239         |
| t_FDD_BlendTbIY_Uls_u8p8[10]              | 241         |
| t_FDD_BlendTbIY_Uls_u8p8[11]              | 243         |

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| Name  | Input Value   |                               |        |
|---|---|-------------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0]   | 38  |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1]   | 51  |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2]   | 64  |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3]   | 77  |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4]   | 90  |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5]   | 102   |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6]   | 115   |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7]   | 128   |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8]   | 141   |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9]   | 154   |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10]  | 166   |                               |        |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]  | 179   |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0]   | 92  |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1]   | 93  |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2]   | 95  |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3]   | 96  |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4]   | 97  |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5]   | 99  |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6]   | 100   |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7]   | 101   |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8]   | 102   |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9]   | 104   |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]  | 105   |                               |        |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11]  | 106   |                               |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[0]   | 3277  |                               |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[1]   | 4915  |                               |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[2]   | 6554  |                               |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[3]   | 8192  |                               |        |
| t_RIAstWIRBlndTblY_Uls_u2p14[4]   | 9830  |                               |        |
| t_WIRBlndTblX_MtrNm_u8p8[0]   | 538   |                               |        |
| t_WIRBlndTblX_MtrNm_u8p8[1]   | 563   |                               |        |
| t_WIRBlndTblX_MtrNm_u8p8[2]   | 589   |                               |        |
| t_WIRBlndTblX_MtrNm_u8p8[3]   | 614   |                               |        |
| t_WIRBlndTblX_MtrNm_u8p8[4]   | 640   |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value                                | 5.5   |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value                               | 110.05  |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value                         | 0   |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value                                      | 10  |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value                              | 10.03   |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value                                   | 325.02  |                               |        |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value                                | 5.3   |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32        |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32       |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32    |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32              |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32      |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32           |                               |        |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32        |                               |        |
| Name  | Actual Value  | Expected Value                | Result |
| PreDecelGain_Uls_M_f32  | 128239.961  | 128239.9599 ± 0.0625          | ✔      |
| Prev1PreAttnComp_MtrNm_M_f32  | 224855.719  | 224855.71732493 ± 0.9         | ✔      |
| Prev1SclDrvVel_RadpS_M_f32  | 42.4358139  | 42.4358127289631 ± 0.00390625 | ✔      |
| Prev2PreAttnComp_MtrNm_M_f32  | 1.29999995  | 1.3 ± 0.00048828125           | ✔      |
| Prev2SclDrvVel_RadpS_M_f32  | 18.2000008  | 18.2 ± 0.00390625             | ✔      |
| PrevTbarAng_HwDeg_M_f32   | 20  | 20 ± 0.00390625               | ✔      |
| TbarVelFiltSv_M_str.SV_Uls_f32  | -1.28751016   | -1.28751 ± 0.00390625         | ✔      |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value                            | 8.80000019  | 8.8 ± 0.00048828125           | ✔      |

# TEST DETAILS REPORT

2014-09-19, 16:35:39+0530



FrqDepDmpnInrtCmp\_Per1

| T   |       |   |       |        |
|---|-------|---|-------|--------|
| Actual Function                                       | Count | Expected Function                                     | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1     | ✓      |
| ADDCoefCalc   | 1     | ADDCoefCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 5     | ✓      |
| DecelGain   | 1     | DecelGain   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| DriverVelCalc   | 1     | DriverVelCalc   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| FilterCoefCalc  | 1     | FilterCoefCalc  | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 4     | ✓      |
| GenFddlcCmd   | 1     | GenFddlcCmd   | 1     | ✓      |
| IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | IntplVarXY_u16_u16Xu16Y_Cnt                           | 1     | ✓      |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1     | ✓      |