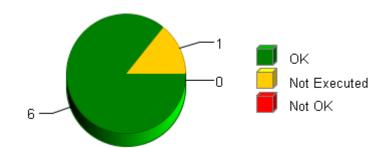


Summary

Overall Test Object Results (including Coverage)

Total Test Objects: 7
Successful: 6
Failed: 0
Not Executed: 1

Date: 2014-09-19 **Time:** 16:38:41+0530



Selected Project Items

Test Object "CBD UnitTest/FDD Inertia/ADDCoefCalc"

Test Object "CBD UnitTest/FDD Inertia/DecelGain"

Test Object "CBD_UnitTest/FDD_Inertia/DriverVelCalc"

Test Object "CBD_UnitTest/FDD_Inertia/FilterCoefCalc"

Test Object "CBD_UnitTest/FDD_Inertia/FrqDepDmpnInrtCmp_Init"

Test Object "CBD_UnitTest/FDD_Inertia/FrqDepDmpnInrtCmp_Per1"

Test Object "CBD_UnitTest/FDD_Inertia/GenFddIcCmd"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

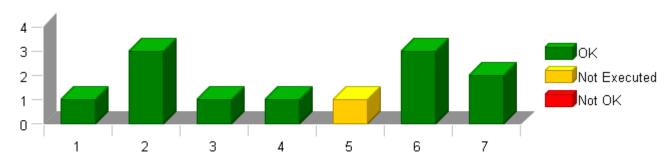
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)

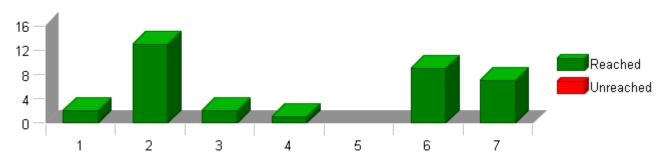


The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results



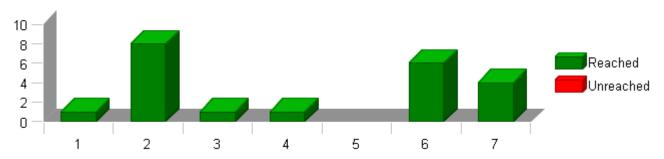
do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



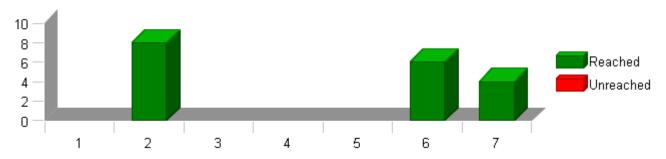
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

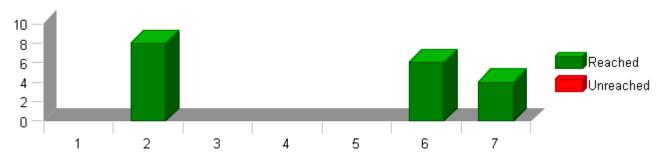


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



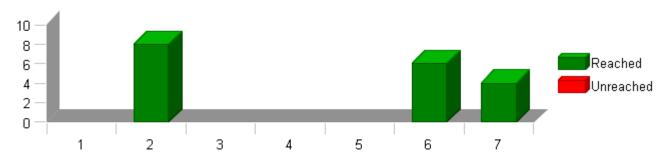
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

| No. | Name | C0 | C1 | DC | MC/DC | MCC | Test Cases Result |
|-----|------------------------|-------|-------|-------|-------|-------|-------------------|
| | FDD_Inertia | 100 % | 100 % | 100 % | 100 % | 100 % | 11 of 12 passed |
| | CBD_UnitTest | 100 % | 100 % | 100 % | 100 % | 100 % | 11 of 12 passed |
| | FDD_Inertia | 100 % | 100 % | 100 % | 100 % | 100 % | 11 of 12 passed |
| 1 | <u>ADDCoefCalc</u> | 100 % | 100 % | - | - | - | 1 of 1 passed |
| 2 | <u>DecelGain</u> | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed |
| 3 | <u>DriverVelCalc</u> | 100 % | 100 % | - | - | - | 1 of 1 passed |
| 4 | <u>FilterCoefCalc</u> | 100 % | 100 % | - | - | - | 1 of 1 passed |
| 5 | FrqDepDmpnInrtCmp_Init | - | - | - | - | - | _ |
| 6 | FrqDepDmpnInrtCmp Per1 | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed |
| 7 | <u>GenFddlcCmd</u> | 100 % | 100 % | 100 % | 100 % | 100 % | 2 of 2 passed |

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GenFddlcCmd

| Project | FDD_Inertia |
|-------------|-------------|
| Module | FDD_Inertia |
| Test Object | GenFddlcCmd |

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| Total Testcases | 2 |
|-----------------|---|
| Successful | 2 |
| Failed | 0 |
| Not Executed | 0 |



Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp |
|------------------------|---|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

| Comments/Description | on/Specification |
|----------------------|---|
| Name | Text |
| Module 'FDD_Inertia' | ************************************** |
| | Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c Code File(s) Version: 13 Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc Module Design Document Version: 18 Data Dictionary Version: 16 Unit Test Plan Version: 6 Optimization Level: Level 2 Compiler (CodeCoen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes): 1994 Total RAM Used (Bytes): 60 Total CALS Used (Bytes): 328 Special Test Requirements: Test Date: 09-19-2014 |
| | Comments: Note1:Inline Function defined in ""globalmacro.h"" are not unit tested. |
| | Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference. |
| | Note3:In ""DriverVelCalc"" function, difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1" function. |
| | Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function. |
| | Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable "filtCoef_Uls_T_Str.b0_Uls_f32" is calculated as -2.74156205240179 to 0 and "filtCoef_Uls_T_Str.b1_Uls_f32" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16. |
| | Note6:In ""GenFddIcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range. |
| | Note 7:The range of the parameter "VehicleSpeed_Kph_T_f32" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication. |
| | Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DriverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"". |
| | *************************************** |

| Attributes | | | | | |
|-----------------------|--|--|--|--|--|
| Name | Value | | | | |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 | | | | |
| Float Precision | 9 | | | | |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj | | | | |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src | | | | |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd | | | | |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl | | | | |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 | | | | |
| Time Unit | Cycles | | | | |
| Timer Enabled | false | | | | |
| Timer Prescale | 0 | | | | |
| Timer Resolution | 1 | | | | |

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| Attributes | |
|-----------------|--|
| Name | Value |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 360.00 Cycles TS1.2 360.00 Cycles

Description

Test Vector Description:

TS1.1 "Shortest Execution Path:
(ScaledDriverVel_MtrRadpS_T_f32>=D_ATTENTBLMAXINPUT_MTRRADPS_F32)=True"
TS1.2 "Longest Execution Path:
(ScaledDriverVel_MtrRadpS_T_f32>=D_ATTENTBLMAXINPUT_MTRRADPS_F32)=False
(ScaledDriverVel_MtrRadpS_T_f32<=D_ATTENTBLMININPUT_MTRRADPS_F32)=False"

| Test Step 1.1 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.1 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 22.2 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 10 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -7226.652 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 320 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 49 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 51 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.024534 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.124564 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0000456 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.0453 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3242 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.54523 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.330669165 | -0.330669151 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.6598295 | -1.659829464 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -7226.65186 | -7226.652 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.10000002 | 1.1 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 22.2000008 | 22.2 ± 0.00390625 | ~ |

| T | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | |

| Test Step 1.2 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -2.2 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -16.66 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.2 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -3 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 10.2 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 512 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 560 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 116 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 118 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02345 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.15457 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.32 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.766645 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.3242 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.334564269 | -0.334564171 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -0.738348722 | -0.738348516 ± 0.0000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 10.1999998 | 10.2 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.20000005 | -2.2 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -16.6599998 | -16.66 ± 0.00390625 | · |

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| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

GenFddlcCmd

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Test Case 2: Boundary Test

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GenFddlcCmd



Specification

Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

| TS2.1 | 360.00 Cycles |
|--------|---------------|
| TS2.2 | 360.00 Cycles |
| TS2.3 | 360.00 Cycles |
| TS2.4 | 360.00 Cycles |
| TS2.5 | 360.00 Cycles |
| TS2.6 | 360.00 Cycles |
| | |
| TS2.7 | 360.00 Cycles |
| TS2.8 | 372.00 Cycles |
| TS2.9 | 360.00 Cycles |
| TS2.10 | 360.00 Cycles |
| TS2.11 | 360.00 Cycles |
| TS2.12 | 372.00 Cycles |
| TS2.13 | 372.00 Cycles |
| TS2.14 | 372.00 Cycles |
| TS2.15 | 360.00 Cycles |
| TS2.16 | 360.00 Cycles |
| TS2.17 | 360.00 Cycles |
| TS2.18 | 360.00 Cycles |
| TS2.19 | 360.00 Cycles |
| TS2.20 | 372.00 Cycles |
| TS2.21 | 372.00 Cycles |
| 152.21 | |
| TS2.22 | 372.00 Cycles |
| TS2.23 | 372.00 Cycles |
| TS2.24 | 360.00 Cycles |
| TS2.25 | 360.00 Cycles |
| TS2.26 | 360.00 Cycles |
| TS2.27 | 428.00 Cycles |
| TS2.28 | 360.00 Cycles |
| TS2.29 | 360.00 Cycles |
| TS2.30 | 360.00 Cycles |
| TS2.31 | 360.00 Cycles |
| TS2.32 | 360.00 Cycles |
| TS2.33 | 360.00 Cycles |
| TS2.34 | 360.00 Cycles |
| TS2.35 | 372.00 Cycles |
| TS2.36 | 360.00 Cycles |
| TS2.37 | 360.00 Cycles |
| TS2.38 | 360.00 Cycles |
| TS2.38 | |
| | 360.00 Cycles |
| TS2.40 | 372.00 Cycles |
| TS2.41 | 372.00 Cycles |
| TS2.42 | 360.00 Cycles |
| TS2.43 | 372.00 Cycles |
| TS2.44 | 360.00 Cycles |
| TS2.45 | 360.00 Cycles |
| TS2.46 | 372.00 Cycles |
| TS2.47 | 360.00 Cycles |
| TS2.48 | 360.00 Cycles |
| TS2.49 | 360.00 Cycles |
| TS2.50 | 360.00 Cycles |
| TS2.51 | 360.00 Cycles |
| | JUJ.00 CYCLES |





```
Description Test Vector Description
```

```
TS2.1 All min
TS2.2 All max
TS2.3 ScaledDriverVel_MtrRadpS_T_f32 = min
TS2.4 ScaledDriverVel_MtrRadpS_T_f32 = max
TS2.5 ScaledDriverVel_MtrRadpS_T_f32 = pos
TS2.6 ScaledDriverVel_MtrRadpS_T_f32 = pos
TS2.7 ScaledDriverVel_MtrRadpS_T_f32 = pos
TS2.7 ScaledDriverVel_MtrRadpS_T_f32 = neg
TS2.8 filtCoef_Uls_T_Str.b0_Uls_f32 = min
TS2.9 filtCoef_Uls_T_Str.b0_Uls_f32 = min
TS2.10 filtCoef_Uls_T_Str.b0_Uls_f32 = mid
TS2.11 filtCoef_Uls_T_Str.b1_Uls_f32 = mid
TS2.12 filtCoef_Uls_T_Str.b1_Uls_f32 = mid
TS2.13 filtCoef_Uls_T_Str.b1_Uls_f32 = mid
TS2.14 filtCoef_Uls_T_Str.b1_Uls_f32 = mid
TS2.15 filtCoef_Uls_T_Str.b2_Uls_f32 = mid
TS2.16 filtCoef_Uls_T_Str.b2_Uls_f32 = mid
TS2.17 filtCoef_Uls_T_Str.a0_Uls_f32 = mid
TS2.18 filtCoef_Uls_T_Str.a0_Uls_f32 = mid
TS2.19 filtCoef_Uls_T_Str.a0_Uls_f32 = mid
TS2.20 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.21 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.22 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.23 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.24 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.25 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.26 filtCoef_Uls_T_Str.a1_Uls_f32 = mid
TS2.27 prev2ScIDrvVel_RadpS_M_f32 = max
TS2.28 prev2ScIDrvVel_RadpS_M_f32 = neg
    TS2.1 All min
    TS2.2
                           All max
    TS2.28
                                Prev2ScIDrvVel_RadpS_M_f32 = zero
                               Prev2ScIDrvVel_RadpS_M_f32 = neg
Prev2ScIDrvVel_RadpS_M_f32 = pos
Prev1ScIDrvVel_RadpS_M_f32 = min
    TS2.29
    TS2.30
    TS2.31
                               Prev1ScIDrvVel_RadpS_M_f32 = max
Prev1ScIDrvVel_RadpS_M_f32 = zero
Prev1ScIDrvVel_RadpS_M_f32 = neg
    TS2.32
    TS2.33
    TS2.34
                               Prev1ScIDrvVel_RadpS_M_f32 = pos
Prev1PreAttnComp_MtrNm_M_f32 = min
Prev1PreAttnComp_MtrNm_M_f32 = max
    TS2.35
TS2.36
    TS2.37
                                Prev1PreAttnComp_MtrNm_M_f32 = zero
Prev1PreAttnComp_MtrNm_M_f32 = neg
Prev1PreAttnComp_MtrNm_M_f32 = pos
    TS2.38
    TS2 39
    TS2.40
                                Prev2PreAttnComp_MtrNm_M_f32 = min
Prev2PreAttnComp_MtrNm_M_f32 = max
Prev2PreAttnComp_MtrNm_M_f32 = zero
    TS2.41
    TS2 42
    TS2.43
    TS2.44
                                Prev2PreAttnComp_MtrNm_M_f32 = neg
                                Prev2PreAttnComp_MtrNm_M_f32 = pos
t_FDD_AttenTbIX_MtrRadpS_u12p4[2] = min
   TS2.45
TS2.46
```

| Test Step 2.1 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp MtrNm M f32 | -8.8 | | |
| Prev1SclDrvVel RadpS M f32 | -12917.3 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.8 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -12917.3 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -7226.652 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 0 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 0 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 0 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 0 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -2.741562052 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.160083862 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.5525885 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.9996842 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.0504234 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0 | 0 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 9012.61621 | 9012.617156 ± 0.009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -7226.65186 | -7226.652 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.80000019 | -8.8 ± 0.00048828125 | ~ |
| Prev2ScIDrvVel_RadpS_M_f32 | -12917.2998 | -12917.3 ± 0.00390625 | ~ |

| Т | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |





| Test Step 2.2 (Repeat Count = 1) | | | V |
|-----------------------------------|------------------------|-----------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 8.8 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 12917.3 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.8 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 12917.3 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 7226.652 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 17600 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 17600 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 256 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 256 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | 0 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330448 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.411114052 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.9498924 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.8417266 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 10.6056849 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 2046.13135 | 2046.131531 ± 0.009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 2046.13135 | 2046.131531 ± 0.009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 7226.65186 | 7226.652 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.80000019 | 8.8 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 12917.2998 | 12917.3 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.3 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.1 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | 22.2 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 10 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -7226.652 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 320 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 49 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 51 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.024534 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.124564 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0000456 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.0453 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3242 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.54523 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.330669165 | -0.330669151 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.6598295 | -1.659829464 ± 0.000009 | ~ |
| Prev1ScIDrvVel_RadpS_M_f32 | -7226.65186 | -7226.652 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.10000002 | 1.1 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 22.2000008 | 22.2 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 2.4 (Repeat Count = 1) | | ✓ |
|----------------------------------|------------------------|---|
| Name | Input Value | |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.1 | |
| Prev1SclDrvVel_RadpS_M_f32 | -4.21 | |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.8 | |
| Prev2SclDrvVel_RadpS_M_f32 | -2 | |
| ScaledDriverVel_MtrRadpS_T_f32 | 7226.652 | |
| filtCoef Uls T Str | tgt filtCoef Uls T Str | |

GenFddlcCmd

Prev2ScIDrvVel_RadpS_M_f32

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-4.21 ± 0.00390625

Input Value t_FDD_AttenTblX_MtrRadpS_u12p4[0] 352 t_FDD_AttenTblX_MtrRadpS_u12p4[1] 400 t_FDD_AttenTblY_Uls_u8p8[0] 65 t_FDD_AttenTblY_Uls_u8p8[1] 68 tgt_filtCoef_Uls_T_Str.b0_Uls_f32 -0.0332 tgt_filtCoef_Uls_T_Str.b1_Uls_f32 0.13456 tgt_filtCoef_Uls_T_Str.b2_Uls_f32 0.0005345 tgt_filtCoef_Uls_T_Str.a0_Uls_f32 2.45675 tgt_filtCoef_Uls_T_Str.a1_Uls_f32 -6.45654 tgt_filtCoef_Uls_T_Str.a2_Uls_f32 6.757645 Result **Actual Value** Name **Expected Value** GenFddlcCmd() 0.509668887 0.509668855 ± 0.0000009 1.91875339 Prev1PreAttnComp_MtrNm_M_f32 1.918753337 ± 0.000009 Prev1SclDrvVel_RadpS_M_f32 7226.65186 7226.652 ± 0.00390625 Prev2PreAttnComp_MtrNm_M_f32 -1.10000002 -1.1 ± 0.00048828125

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|-----|-----|
| Actual Function | Count | Expected Function | Count | Res | ult |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | • |

-4.21000004

| Test Step 2.5 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 6.6 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 26.1 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 17.03 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 0 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1088 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1120 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 129 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 131 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.006363 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2574 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00145 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.55765 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.7898 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.8534 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0.782138526 | 0.78213851 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.55215085 | 1.552150842 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 0 | 0 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.5999999 | 6.6 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 26.1000004 | 26.1 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntolVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | |

| Test Step 2.6 (Repeat Count = 1) | |
|-----------------------------------|------------------------|
| Name | Input Value |
| Prev1PreAttnComp_MtrNm_M_f32 | -2.2 |
| Prev1SclDrvVel_RadpS_M_f32 | -16.66 |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.2 |
| Prev2SclDrvVel_RadpS_M_f32 | -3 |
| ScaledDriverVel_MtrRadpS_T_f32 | 10.2 |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 512 |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 560 |
| t_FDD_AttenTblY_Uls_u8p8[0] | 116 |
| t_FDD_AttenTblY_Uls_u8p8[1] | 118 |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02345 |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.15457 |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.1 |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.766645 |

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| Name | Input Value | | |
|-----------------------------------|--------------|-------------------------|----------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.3242 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0.157648206 | 0.157648289 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 0.347913265 | 0.347913465 ± 0.0000009 | ✓ |
| Prev1ScIDrvVel_RadpS_M_f32 | 10.1999998 | 10.2 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.20000005 | -2.2 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -16.6599998 | -16.66 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.7 (Repeat Count = 1) | | | V |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 3.3 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 26.45 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.2 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 17.12 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -10.3 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 512 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 560 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 144 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 146 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03123 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.16878 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.2 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.27867 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.24234 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.67452 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -1.8318522 | -1.831852049 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.25662613 | -3.256625864 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -10.3000002 | -10.3 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 3.2999995 | 3.3 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 26.4500008 | 26.45 ± 0.00390625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.8 (Repeat Count = 1) | | | ` |
|-----------------------------------|------------------------|----------------------|-------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.3 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -4.21 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -33.32 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 2562.6 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 656 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 720 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 172 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 174 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -2.741562052 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.175634 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.8 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.16756 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.77453 | | |
| Name | Actual Value | Expected Value | Resul |
| GenFddlcCmd() | 470.300568 | 470.3005767 ± 0.0009 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | 691.936462 | 691.9364807 ± 0.0009 | |
| Prev1ScIDrvVel_RadpS_M_f32 | 2562.6001 | 2562.6 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.2999995 | -3.3 ± 0.00048828125 | • |
| Prev2SclDrvVel RadpS M f32 | -4.21000004 | -4.21 ± 0.00390625 | |



Count Result

GenFddlcCmd

Actual Function

IntplVarXY_u16_u16Xu16Y_Cnt

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.9 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-----------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.4 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | 1234.56 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.3 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | 4678.14 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -2.8 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 768 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 800 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 218 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 220 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | 0 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.184534 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.9 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.92453 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.535 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.452345 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 21.4257507 | 21.42575176 ± 0.00009 | ✓ |
| Prev1PreAttnComp_MtrNm_M_f32 | 25.1605148 | 25.16051583 ± 0.00009 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -2.79999995 | -2.8 ± 0.00390625 | - |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.4000001 | 4.4 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 1234.56006 | 1234.56 ± 0.00390625 | ✓ |

Count Expected Function

IntplVarXY_u16_u16Xu16Y_Cnt

| Name | Input Value | | |
|-----------------------------------|------------------------|-------------------------|--------|
| Prev1PreAttnComp_MtrNm_M_f32 | -4.4 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -27.55 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.7 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -15 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 3.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 784 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 880 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 63 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 66 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.003467 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.1945645 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.9 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.823423 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.78987 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.6345 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.823069274 | -0.82306927 ± 0.0000009 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.34453535 | -3.344535448 ± 0.000009 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 3.5 | 3.5 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -4.400001 | -4.4 ± 0.00048828125 | • |
| | | | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | ✓ |

-27.5499992

Prev2SclDrvVel_RadpS_M_f32

-27.55 ± 0.00390625





| Test Step 2.11 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 5.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 6789.565 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.7 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 5322.14 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -3.9 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 944 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 960 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 78 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 80 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.004353 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0016456 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.7234 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.64564 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.36567 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0.0503453612 | 0.050345373 ± 0.00000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 0.165236056 | 0.165236095 ± 0.0000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -3.9000001 | -3.9 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.5 | 5.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 6789.56494 | 6789.565 ± 0.00390625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.12 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -5.5 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | -37.15 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -42.02 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 1444.1 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1008 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1040 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 106 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 109 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.005456 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330448 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.001767 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.65674 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4234 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.94645 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.619547307 | -0.619547276 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.45508361 | -1.45508351 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 1444.09998 | 1444.1 ± 0.00390625 | - |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.5 | -5.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -37.1500015 | -37.15 ± 0.00390625 | · |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 2.13 (Repeat Count = 1) | | ✓ |
|-----------------------------------|------------------------|---|
| Name | Input Value | |
| Prev1PreAttnComp_MtrNm_M_f32 | 6.6 | |
| Prev1SclDrvVel_RadpS_M_f32 | 26.1 | |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.3 | |
| Prev2SclDrvVel_RadpS_M_f32 | 17.03 | |
| ScaledDriverVel_MtrRadpS_T_f32 | -2234.7 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | |

GenFddlcCmd

Prev1SclDrvVel_RadpS_M_f32

Prev2PreAttnComp_MtrNm_M_f32 Prev2SclDrvVel_RadpS_M_f32 2014-09-19, 16:38:05+0530



-2234.7 ± 0.00390625

6.6 ± 0.00048828125

26.1 ± 0.00390625

| Name | Input Value | | |
|-----------------------------------|--------------|------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1088 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1120 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 129 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 131 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.006363 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2574 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00145 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.55765 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.7898 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.8534 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0.625984669 | 0.62598471 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.22329831 | 1.223298365 ± 0.000009 | ~ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|-----|-----|
| Actual Function | Count | Expected Function | Count | Res | ult |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | • |

-2234.69995

6.5999999

26.1000004

| Name | Input Value | | |
|-----------------------------------|------------------------|------------------------|-------|
| Prev1PreAttnComp MtrNm M f32 | -6.6 | | |
| Prev1ScIDrvVel RadpS M f32 | -33.1 | | |
| Prev2PreAttnComp MtrNm M f32 | -7.5 | | |
| Prev2ScIDrvVel RadpS M f32 | -22.04 | | |
| ScaledDriverVel MtrRadpS T f32 | 1555.6 | | |
| filtCoef Uls T Str | tgt filtCoef Uls T Str | | |
| t FDD AttenTblX MtrRadpS u12p4[0] | 1152 | | |
| t FDD AttenTblX MtrRadpS u12p4[1] | 1200 | | |
| t FDD AttenTblY Uls u8p8[0] | 157 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 161 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745745 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2454 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.160083862 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.44564 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.53524 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.254 | | |
| Name | Actual Value | Expected Value | Resul |
| GenFddlcCmd() | -18.191328 | -18.1913286 ± 0.00009 | |
| Prev1PreAttnComp_MtrNm_M_f32 | -28.9253426 | -28.92534236 ± 0.00009 | |
| Prev1SclDrvVel_RadpS_M_f32 | 1555.59998 | 1555.6 ± 0.00390625 | |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.5999999 | -6.6 ± 0.00048828125 | |
| Prev2ScIDrvVel RadpS M f32 | -33.0999985 | -33.1 ± 0.00390625 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntolVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | |

| Test Step 2.15 (Repeat Count = 1) | |
|-----------------------------------|------------------------|
| Name | Input Value |
| Prev1PreAttnComp_MtrNm_M_f32 | 7.7 |
| Prev1SclDrvVel_RadpS_M_f32 | 18 |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.5 |
| Prev2SclDrvVel_RadpS_M_f32 | 28.01 |
| ScaledDriverVel_MtrRadpS_T_f32 | -5.8 |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1232 |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1280 |
| t_FDD_AttenTblY_Uls_u8p8[0] | 183 |
| t_FDD_AttenTblY_Uls_u8p8[1] | 185 |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00864 |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.31545 |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.411114052 |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.3454 |

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| Name | Input Value | | |
|-----------------------------------|--------------|------------------------|----------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.6353 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.63432 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 1.29496682 | 1.294967011 ± 0.000009 | ✓ |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.81153834 | 1.811538551 ± 0.000009 | ✓ |
| Prev1ScIDrvVel_RadpS_M_f32 | -5.80000019 | -5.8 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.69999981 | 7.7 ± 0.00048828125 | ✓ |
| Prev2ScIDrvVel_RadpS_M_f32 | 18 | 18 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.16 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -7.7 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -28.02 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.5 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -27 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 6.2 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1296 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1360 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 230 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 232 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.009585 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.32554 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.1496 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.234535 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.634453 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.35435 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -3.82750082 | -3.827500822 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -4.26017475 | -4.260174828 ± 0.000009 | ✓ |
| Prev1ScIDrvVel_RadpS_M_f32 | 6.19999981 | 6.2 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -7.6999981 | -7.7 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -28.0200005 | -28.02 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.17 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 24.06 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.5 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 32.56 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -6.3 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1344 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1440 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 71 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 74 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00365 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.26745 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00006456 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.5525885 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.4564 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.134534 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0.371916622 | 0.371916637 ± 0.0000009 | - |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.34099519 | 1.340995197 ± 0.000009 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -6.30000019 | -6.3 ± 0.00390625 | - |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.5 | 1.5 ± 0.00048828125 | • |
| Prev2SclDrvVel RadpS M f32 | 24.0599995 | 24.06 ± 0.00390625 | - |





| Τ | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - | |

| Test Step 2.18 (Repeat Count = 1) | Test Step 2.18 (Repeat Count = 1) | | | |
|-----------------------------------|-----------------------------------|-------------------------|--------|--|
| Name | Input Value | | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.5 | | | |
| Prev1SclDrvVel_RadpS_M_f32 | -16.05 | | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -4.5 | | | |
| Prev2SclDrvVel_RadpS_M_f32 | -25.25 | | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 7.4 | | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1520 | | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1568 | | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 86 | | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 88 | | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01423 | | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27344 | | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0014534 | | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.9498924 | | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4535 | | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.34564 | | | |
| Name | Actual Value | Expected Value | Result | |
| GenFddlcCmd() | 0.164055958 | 0.164056011 ± 0.0000009 | ~ | |
| Prev1PreAttnComp_MtrNm_M_f32 | 0.488352627 | 0.488352776 ± 0.0000009 | ~ | |
| Prev1ScIDrvVel_RadpS_M_f32 | 7.4000001 | 7.4 ± 0.00390625 | ~ | |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.5 | -1.5 ± 0.00048828125 | ~ | |
| Prev2SclDrvVel_RadpS_M_f32 | -16.0499992 | -16.05 ± 0.00390625 | ~ | |

| T | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | |

| Test Step 2.19 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 2.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 100.04 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.5 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | 97 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -7.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1552 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1600 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 114 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 116 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02342 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.28546 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.000745 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.453723 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.5345 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.94534 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 1.44737673 | 1.447376757 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 3.25024962 | 3.25024956 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -7.5 | -7.5 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.5 | 2.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 100.040001 | 100.04 ± 0.00390625 | ~ |

| T | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | |





| Test Step 2.20 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -2.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -69.4 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.5 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -59.65 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 1500.02 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1616 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1680 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 136 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 139 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03452 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2956 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00053453 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.6345 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.9996842 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.84563 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -2.45213747 | -2.452137655 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -4.51616669 | -4.516167192 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 1500.02002 | 1500.02 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.5 | -2.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -69.4000015 | -69.4 ± 0.00390625 | |

| au | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | |

| Test Step 2.21 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.5 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | -49.65 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.4 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -36.5 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 2500.06 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1728 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1760 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 63 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 66 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.043453 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2945 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00135 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.73453 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.8417266 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.2325 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.778024733 | -0.778024749 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.01779294 | -3.017792967 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 2500.06006 | 2500.06 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.5 | -3.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -49.6500015 | -49.65 ± 0.00390625 | ~ |

| T . | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ | | |

| Test Step 2.22 (Repeat Count = 1) | | |
|-----------------------------------|------------------------|--|
| Name | Input Value | |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.5 | |
| Prev1SclDrvVel_RadpS_M_f32 | 22.54 | |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.4 | |
| Prev2SclDrvVel_RadpS_M_f32 | 11 | |
| ScaledDriverVel_MtrRadpS_T_f32 | -2500.08 | |
| filtCoef Uls T Str | tgt filtCoef Uls T Str | |

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| Name | Input Value | | |
|-----------------------------------|--------------|------------------------|----------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1776 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1840 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 189 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 191 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.05342 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.3036 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0004234 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.845555 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.5474 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.342 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 2.5159831 | 2.515983222 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 3.37220788 | 3.372207879 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -2500.08008 | -2500.08 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.5 | 4.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel RadpS M f32 | 22.5400009 | 22.54 ± 0.00390625 | ✓ |

| T | | | | | | |
|-----------------------------|-------|-----------------------------|-------|-----|-----|--|
| Actual Function | Count | Expected Function | Count | Res | ult | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | • | |

| Test Step 2.23 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -4.5 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | -48.54 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.1 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -38.54 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 3500.06 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 160 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1920 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 237 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 239 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01123 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.30564 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00023453 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.95464 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.345345 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.0504234 | | |
| Name | Actual Value Expe | ected Value | Result |
| GenFddlcCmd() | -9.47003937 -9.470 | 0039831 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -10.1436405 -10.14 | 4364099 ± 0.00009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 3500.06006 3500. | .06 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -4.5 ± | £ 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -48.5400009 -48.5 | 4 ± 0.00390625 | ~ |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Name | Input Value |
|-----------------------------------|------------------------|
| Prev1PreAttnComp_MtrNm_M_f32 | 6.5 |
| Prev1SclDrvVel_RadpS_M_f32 | 163.65 |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.1 |
| Prev2SclDrvVel_RadpS_M_f32 | 175 |
| ScaledDriverVel_MtrRadpS_T_f32 | -3.02 |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 176 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 2000 |
| t_FDD_AttenTblY_Uls_u8p8[0] | 49 |
| _FDD_AttenTblY_Uls_u8p8[1] | 51 |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02123 |
| gt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.31564 |
| gt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.1 |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.05678 |

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| Name | Input Value |
|-----------------------------------|-------------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.53454 |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 10.6056849 |





| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - |

| Test Step 2.27 (Repeat Count = 1) | | | V |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -7.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 250.45 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -7.7 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 12917.3 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -39.07 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 224 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2240 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 116 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 118 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.005534 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.25856 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.65 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.3678 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.734 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.245645 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -11.8644609 | -11.86446038 ± 0.00009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -26.1836376 | -26.18363669 ± 0.00009 | ~ |
| Prev1ScIDrvVel_RadpS_M_f32 | -39.0699997 | -39.07 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -7.5 | -7.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 250.449997 | 250.45 ± 0.00390625 | |

| T | | | | ~ |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.28 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|----------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 8.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 5000.65 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.7 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | 0 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 6075.09 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2320 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 144 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 146 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.259346 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.35 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.4786 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.84764 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.365 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 452.265015 | 452.2649718 ± 0.0009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 793.012634 | 793.0125532 ± 0.0009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 6075.08984 | 6075.09 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.5 | 8.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 5000.6499 | 5000.65 ± 0.00390625 | ✓ |

| T | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ | |





| Test Step 2.29 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -8.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -26.65 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.6 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -10.12 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 6.02 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 256 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2400 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 172 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 174 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.268567 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.24 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.5768 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.000456 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.4766 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -5.66504765 | -5.665048067 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -8.4316988 | -8.431699448 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 6.01999998 | 6.02 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.5 | -8.5 ± 0.00048828125 | ✓ |
| Prev2ScIDrvVel_RadpS_M_f32 | -26.6499996 | -26.65 ± 0.00390625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.30 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|--------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.3 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | 18.6 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.6 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 10.25 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -6.06 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 272 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2480 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 218 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 220 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27443 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.389 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.65675 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.96456 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.57686 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -0.33675155 | -0.336751733 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -0.395451367 | -0.395451576 ± 0.0000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -6.05999994 | -6.06 ± 0.00390625 | - |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.2999995 | 1.3 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 18.6000004 | 18.6 ± 0.00390625 | ~ |

| T | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Test Step 2.31 (Repeat Count = 1) | |
|-----------------------------------|------------------------|
| Name | Input Value |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.3 |
| Prev1SclDrvVel_RadpS_M_f32 | -12917.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.5 |
| Prev2SclDrvVel_RadpS_M_f32 | -900.36 |
| ScaledDriverVel_MtrRadpS_T_f32 | -4.02 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |

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| Name | Input Value | | |
|-----------------------------------|--------------|-------------------------|----------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 288 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2560 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 63 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 66 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00845 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.000564 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.78 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.745 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3453 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.6786 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0.722379088 | 0.722378984 ± 0.0000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 2.93538165 | 2.935381268 ± 0.000009 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -4.01999998 | -4.02 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.2999995 | 1.3 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | -12917.2998 | -12917.3 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.32 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 2.3 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 12917.3 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.5 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -2000.1 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -1.05 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 304 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2640 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 78 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 80 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00945 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.000654 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.02 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.8453 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -4.873453 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.15645 | | |
| Name | Actual Value Expe | ected Value | Result |
| GenFddlcCmd() | 1.61534405 1.6153 | 344 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 5.30164194 5.3016 | 641847 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -1.04999995 -1.05 : | ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.29999995 2.3 ± 0 | 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 12917.2998 12917 | 7.3 ± 0.00390625 | ~ |

| T | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Name | Input Value | |
|-----------------------------------|------------------------|--|
| Prev1PreAttnComp MtrNm M f32 | -2.3 | |
| Prev1ScIDrvVel RadpS M f32 | 0 | |
| Prev2PreAttnComp MtrNm M f32 | -4.4 | |
| Prev2ScIDrvVel RadpS M f32 | 3000 | |
| ScaledDriverVel_MtrRadpS_T_f32 | 2.06 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1760 | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2720 | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 106 | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 109 | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01324 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.3056 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 1.32 | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.9454 | |

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| Name | Input Value | | |
|-----------------------------------|--------------|-------------------------|----------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.534 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.74564 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -2.96688271 | -2.966882443 ± 0.000009 | ✓ |
| Prev1PreAttnComp_MtrNm_M_f32 | -7.1653018 | -7.165300993 ± 0.000009 | ✓ |
| Prev1ScIDrvVel_RadpS_M_f32 | 2.0599994 | 2.06 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.2999995 | -2.3 ± 0.00048828125 | ✓ |
| Prev2ScIDrvVel_RadpS_M_f32 | 0 | 0 ± 0.00390625 | ~ |

| T | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Test Step 2.34 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 3.4 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | -2000.02 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.4 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -3000.4 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -2.05 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1920 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2800 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 129 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 131 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02234 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.004678 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0018576 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.04564 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.3453 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.84534 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 6.05533695 | 6.055336888 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 12.0167923 | 12.01679258 ± 0.00009 | ~ |
| Prev1ScIDrvVel_RadpS_M_f32 | -2.04999995 | -2.05 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 3.4000001 | 3.4 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -2000.02002 | -2000.02 ± 0.00390625 | ~ |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.35 (Repeat Count = 1) | | | |
|-----------------------------------|------------------------|-------------------------|-------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.4 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | 2000.03 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 4000.6 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -350.02 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2080 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2880 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 157 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 161 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03234 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.04784 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.001645 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.14564 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.3453 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.9345 | | |
| Name | Actual Value | Expected Value | Resul |
| GenFddlcCmd() | -4.80776691 | -4.807766498 ± 0.000009 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | -7.64464808 | -7.64464735 ± 0.000009 | |
| Prev1SclDrvVel_RadpS_M_f32 | -350.019989 | -350.02 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.4000001 | -3.4 ± 0.00048828125 | • |
| Prev2SclDrvVel RadpS M f32 | 2000.03003 | 2000.03 ± 0.00390625 | |





| Т | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Test Step 2.36 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -8.8 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -1000.4 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.5 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -7500.6 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -3.05 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2240 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 2960 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 183 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 185 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.044564 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.32555 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.002342 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.2454 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.53453 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.3423 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -3.7178309 | -3.71783362 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -5.20090008 | -5.200903862 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -3.04999995 | -3.05 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.80000019 | -8.8 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -1000.40002 | -1000.4 ± 0.00390625 | ~ |

| T | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | |

| Test Step 2.37 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 8.8 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 980.6 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.2 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | 6500.85 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 4.05 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2400 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3040 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 230 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 232 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.053534 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330264 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0025235 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.3675 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4234 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.13453 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 5.50454187 | 5.5045434 ± 0.000009 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | 6.12679434 | 6.126796132 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 4.05000019 | 4.05 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.80000019 | 8.8 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 980.599976 | 980.6 ± 0.00390625 | ~ |

| T | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|
| Actual Function | Count | Expected Function | Count | Result | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | |





| Test Step 2.38 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 0 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -1000 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.2 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -5000.41 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -4.8 | | |
| filtCoef_UIs_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2560 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3120 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 71 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 74 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.042342 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27566 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.001535 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.456 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.84564 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.42342 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -2.99402881 | -2.994028926 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -10.7953711 | -10.7953719 ± 0.00009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -4.80000019 | -4.8 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 0 | 0 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -1000 | -1000 ± 0.00390625 | ~ |

| T . | | | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|--|--|
| Actual Function | Count | Expected Function | Count | Result | | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | | |

| Test Step 2.39 (Repeat Count = 1) | Test Step 2.39 (Repeat Count = 1) | | | | |
|-----------------------------------|-----------------------------------|-----------------------------|--------|--|--|
| Name | Input Value | | | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -5.25 | | | | |
| Prev1SclDrvVel_RadpS_M_f32 | 1500.05 | | | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.1 | | | | |
| Prev2SclDrvVel_RadpS_M_f32 | 6000.69 | | | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 5.9 | | | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2720 | | | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3200 | | | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 86 | | | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 88 | | | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.053453 | | | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.284564 | | | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0012342 | | | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.56575 | | | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.32786 | | | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.2564 | | | | |
| Name | Actual Value | Expected Value | Result | | |
| GenFddlcCmd() | 4.06544733 | 4.06544767986332 ± 0.000009 | ~ | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 12.1017971 | 12.1017977447094 ± 0.00009 | ~ | | |
| Prev1ScIDrvVel_RadpS_M_f32 | 5.9000001 | 5.9 ± 0.00390625 | ~ | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.25 | -5.25 ± 0.00048828125 | ~ | | |
| Prev2ScIDrvVel_RadpS_M_f32 | 1500.05005 | 1500.05 ± 0.00390625 | ~ | | |

| Τ | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 2.40 (Repeat Count = 1) | |
|-----------------------------------|------------------------|
| Name | Input Value |
| Prev1PreAttnComp_MtrNm_M_f32 | 5.25 |
| Prev1ScIDrvVel_RadpS_M_f32 | 2500.06 |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.1 |
| Prev2SclDrvVel_RadpS_M_f32 | 9000.45 |
| ScaledDriverVel_MtrRadpS_T_f32 | 2557 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |

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| Name | Input Value | | |
|-----------------------------------|--------------|----------------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2880 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3280 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 114 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 116 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.01324 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2956 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0006345 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.6786 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.3123 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.5564 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 45.0379448 | 45.0379399696766 ± 0.00009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 99.3940811 | 99.3940744158379 ± 0.00009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 2557 | 2557 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.25 | 5.25 ± 0.00048828125 | ~ |
| Prev2ScIDrvVel_RadpS_M_f32 | 2500.06006 | 2500.06 ± 0.00390625 | ~ |

| T . | | | | | | |
|-----------------------------|-------|-----------------------------|-------|-----|-----|--|
| Actual Function | Count | Expected Function | Count | Res | ult | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | • | |

| Test Step 2.41 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.6 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -1500.06 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.8 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -9000.11 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 1646.7 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 3040 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3360 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 136 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 139 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0063 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.11345 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.000234 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.7765 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.34534 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.73523 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -4.42373562 | -4.423735974 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -8.14731121 | -8.147312297 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 1646.69995 | 1646.7 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.5999999 | 4.6 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -1500.06006 | -1500.06 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntolVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | |

| Test Step 2.42 (Repeat Count = 1) | |
|-----------------------------------|------------------------|
| Name | Input Value |
| Prev1PreAttnComp_MtrNm_M_f32 | -4.6 |
| Prev1SclDrvVel_RadpS_M_f32 | 600.07 |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.8 |
| Prev2SclDrvVel_RadpS_M_f32 | 9900.65 |
| ScaledDriverVel_MtrRadpS_T_f32 | -6.8 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1920 |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3440 |
| t_FDD_AttenTblY_Uls_u8p8[0] | 63 |
| t_FDD_AttenTblY_Uls_u8p8[1] | 66 |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745 |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.15645 |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.25 |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.84564 |

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| Name | Input Value | | |
|-----------------------------------|--------------|-------------------------|----------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.4342 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.845 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -1.46749699 | -1.467496866 ± 0.000009 | ✓ |
| Prev1PreAttnComp_MtrNm_M_f32 | -5.96316242 | -5.96316187 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -6.80000019 | -6.8 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -4.5999999 | -4.6 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 600.070007 | 600.07 ± 0.00390625 | ~ |

| Τ | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Test Step 2.43 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 5.7 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 5000 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 0 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 8000.65 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 2412.05 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2080 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3520 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 189 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 191 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.02342 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.001234 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00024378 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.94564 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.84564 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.93453 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -14.621316 | -14.62131553 ± 0.00009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -19.5971565 | -19.59715589 ± 0.00009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 2412.05005 | 2412.05 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.6999981 | 5.7 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 5000 | 5000 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.44 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-----------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -5.7 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -9000.015 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.25 | | |
| Prev2SclDrvVel_RadpS_M_f32 | -6000.12 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -23.02 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2240 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3600 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 237 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 239 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.03234 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0156 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.36 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.0674 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.458349 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.143 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 3.19451404 | 3.19451007405634 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 3.45061421 | 3.45061003779925 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -23.0200005 | -23.02 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.69999981 | -5.7 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -9000.01465 | -9000.015 ± 0.00390625 | - |





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|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - | |

| Test Step 2.45 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-----------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 6.8 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 600.09 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.25 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 9000.62 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 34.06 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 2400 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3680 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 230 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 232 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00645 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.16777 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.54 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.14564 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.864935 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.74564 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 9.78774643 | 9.78774586664643 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 10.894187 | 10.8941867037456 ± 0.00009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 34.0600014 | 34.06 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.80000019 | 6.8 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 600.090027 | 600.09 ± 0.00390625 | |

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|--|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| T - 2 - 12 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - | | 1 4 1 2 2 2 1 2 1 2 1 | | |

| Test Step 2.46 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-------------------------|--------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.5 | | |
| Prev1ScIDrvVel_RadpS_M_f32 | -400.05 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.8 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -7235.12 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 45.06 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 0 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 0 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 71 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 74 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.005534 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.27344 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.000534 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.3678 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.24234 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.54523 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | -2.39375806 | -2.393758233 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -8.28110886 | -8.281109564 ± 0.000009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 45.0600014 | 45.06 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.5 | 1.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -400.049988 | -400.05 ± 0.00390625 | ~ |

| Т | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |





| Test Step 2.47 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 289.65 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.2 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | 8563.3 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | -4.05 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 17600 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 17600 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 86 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 88 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.28546 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.14 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.4786 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.9789 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.757645 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 1.24506903 | 1.245069116 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 3.7062521 | 3.706252252 ± 0.000009 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -4.05000019 | -4.05 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.5 | -1.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 289.649994 | 289.65 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.48 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|-----------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 2.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | -150 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.2 | | |
| Prev2ScIDrvVel_RadpS_M_f32 | -9358.2 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 5266.06 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1005 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 9383 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 114 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 116 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00634 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2956 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.26 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.5768 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.535 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.4563 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 74.4717255 | 74.47172728 ± 0.00009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 164.351395 | 164.3513981 ± 0.0009 | ✓ |
| Prev1ScIDrvVel_RadpS_M_f32 | 5266.06006 | 5266.06 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.5 | 2.5 ± 0.00048828125 | ✓ |
| Prev2ScIDrvVel_RadpS_M_f32 | -150 | -150 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.49 (Repeat Count = 1) | ✓ |
|-----------------------------------|------------------------|
| Name | Input Value |
| Prev1PreAttnComp_MtrNm_M_f32 | -2.5 |
| Prev1SclDrvVel_RadpS_M_f32 | -2341.03 |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.3 |
| Prev2ScIDrvVel_RadpS_M_f32 | 9782.2 |
| ScaledDriverVel_MtrRadpS_T_f32 | 4585.02 |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str |

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| Name | Input Value | | |
|-----------------------------------|--------------|-----------------------|--------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1616 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3680 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 0 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 0 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00745 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.2945 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.38 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.65675 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.78987 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.3242 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 0 | 0 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 132.005234 | 132.0052327 ± 0.0009 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 4585.02002 | 4585.02 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.5 | -2.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -2341.03003 | -2341.03 ± 0.00390625 | ~ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|-----|----------|
| Actual Function | Count | Expected Function | Count | Res | ult |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 2.50 (Repeat Count = 1) | | | ✓ |
|-----------------------------------|------------------------|------------------------|----------|
| Name | Input Value | | |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.5 | | |
| Prev1SclDrvVel_RadpS_M_f32 | 500.012 | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.3 | | |
| Prev2SclDrvVel_RadpS_M_f32 | 12000 | | |
| ScaledDriverVel_MtrRadpS_T_f32 | 3.02 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1632 | | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3696 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 256 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 256 | | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00845 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.3036 | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.5 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.745 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.64564 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.67452 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 4.95908308 | 4.959080803 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.95908308 | 4.959080803 ± 0.000009 | ~ |
| Prev1ScIDrvVel_RadpS_M_f32 | 3.01999998 | 3.02 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.5 | -3.5 ± 0.00048828125 | ~ |
| Prev2ScIDrvVel_RadpS_M_f32 | 500.011993 | 500.012 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.51 (Repeat Count = 1) | | |
|-----------------------------------|------------------------|--|
| Name | Input Value | |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.5 | |
| Prev1SclDrvVel_RadpS_M_f32 | 385.032 | |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.7 | |
| Prev2SclDrvVel_RadpS_M_f32 | -10712.32 | |
| ScaledDriverVel_MtrRadpS_T_f32 | -7.02 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 1648 | |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 3712 | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 63 | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 66 | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00945 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.30564 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.62 | |
| tgt filtCoef Uls T Str.a0 Uls f32 | 1.8453 | |

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| Name | Input Value | | |
|-----------------------------------|--------------|------------------------|--------|
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.4234 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.77453 | | |
| Name | Actual Value | Expected Value | Result |
| GenFddlcCmd() | 8.95816231 | 8.958162049 ± 0.000009 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 36.4014206 | 36.40142039 ± 0.00009 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -7.01999998 | -7.02 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.5 | 4.5 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 385.032013 | 385.032 ± 0.00390625 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

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FrqDepDmpnInrtCmp_Init

Project FDD_Inertia

Module FDD_Inertia

Test Object FrqDepDmpnInrtCmp_Init

Statistics

| Total Testcases | 1 |
|-----------------|---|
|-----------------|---|

Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp |
|------------------------|---|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | $\label{eq:condition} $$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c$ |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\-I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\\NxtrLib\utp\contract\Ap_FrqDepDmpnInrtCmp\\State(PROJECTROOT) \NxtrLib\utp\contract\Ap_FrqDepDmpnInrtCmp\\State(PROJECTROOT) |

| Comments/Description/Specification | | | | |
|------------------------------------|------|--|--|--|
| Name | Text | | | |



Module 'FDD_Inertia'

Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c

Code File(s) Version: 13

Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc
Module Design Document Version: 18

Data Dictionary Version: 16 Unit Test Plan Version: 6 Optimization Level: Level 2

Optimization Level. Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30
Total FLASH Used (Bytes): 1994
Total FLASH Used (Bytes): 60

Total CALS Used (Bytes): 328 Special Test Requirements: Test Date: 09-19-2014 Comments:

Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.

Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference.

Note3:In ""DriverVelCalc" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1" function.

Note4:In ""ADDCoefCalc"" function, return value is going out of range due to conversion happening in the function.

Note5:In ""FilterCoefCalc" function,the Range of the Structure Variable "filtCoef_Uls_T_Str.b0_Uls_f32" is calculated as -2.74156205240179 to 0 and "filtCoef_Uls_T_Str.b1_Uls_f32" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16.

Note6:In ""GenFddlcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of

Note 7:The range of the parameter "VehicleSpeed_Kph_T_f32" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication.

Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DirverVelCalc"", ""FitterCoefCalc"",""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"".

| Attributes | |
|-----------------------|--|
| Name | Value |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre> |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre> |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path | <pre>\$(ProgramFiles)\pls\UDE 3.2</pre> |
| Time Unit | Cycles |
| Timer Enabled | false |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 116.00 Cycles
TS1.2 117.00 Cycles
TS1.3 116.00 Cycles
TS1.4 117.00 Cycles
TS1.5 117.00 Cycles
TS1.5 117.00 Cycles
TS1.6 115.00 Cycles
TS1.7 115.00 Cycles
TS1.8 117.00 Cycles
TS1.9 117.00 Cycles
TS1.10 118.00 Cycles
TS1.11 118.00 Cycles
TS1.11 118.00 Cycles
TS1.12 115.00 Cycles
TS1.13 115.00 Cycles

Description

Test Vector Description:

TS1.1 All min

TS1.2 All max

TS1.2 All max
TS1.3 k_InrtCmp_TBarVell_PFKn_Hz_f32 = min
TS1.4 k_InrtCmp_TBarVell_PFKn_Hz_f32 = max
TS1.5 k_InrtCmp_TBarVell_PFKn_Hz_f32 = mid
TS1.6 TbarVelFiltSv_M_str.K = min
TS1.7 TbarVelFiltSv_M_str.K = max
TS1.8 TbarVelFiltSv_M_str.K = mid
TS1.9 TbarVelFiltSv_M_str.SV = min
TS1.10 TbarVelFiltSv_M_str.SV = max
TS1.11 TbarVelFiltSv_M_str.SV = zero
TS1.12 TbarVelFiltSv_M_str.SV = pos
TS1.13 TbarVelFiltSv_M_str.SV = neg

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value |
|--------------------------------|--------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.66669989 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.00125584798 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 0.100000001 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv M str.K Uls f32 | 0.00125584798 ± 0.000125655810790826 |

Test Step 1.2 (Repeat Count = 1)

| Name | input value |
|--------------------------------|---------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.66669989 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.715390444 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 100 |
| | |
| Name | Expected Value |
| Name PreDecelGain_Uls_M_f32 | Expected Value 1 ± 0.0625 |
| | · |

Test Step 1.3 (Repeat Count = 1)

| root otop no (respont obtain 1) | |
|---------------------------------|--------------------------------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.25460005 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.374119997 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 0.100000001 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv M str.K Uls f32 | 0.00125584798 ± 0.000125655810790826 |

Test Step 1.4 (Repeat Count = 1)

| Name | Input Value |
|--------------------------------|----------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.68739986 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.269800007 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 100 |
| Name | Expected Value |
| PreDecelGain Uls M f32 | 1+0.0625 |

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FrqDepDmpnInrtCmp_Init

| Name | Expected Value |
|--------------------------------|------------------------------------|
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.715390444 ± 0.000125655810790826 |

| Test Step 1.5 (Repeat Count = 1) | |
|----------------------------------|------------------------------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5632 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.145229995 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 50.2299995 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.468051612 ± 0.000125655810790826 |

| Test Step 1.6 (Repeat Count = 1) | |
|----------------------------------|------------------------------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.55769992 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.00125584798 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 25.2000008 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.271430701 ± 0.000125655810790826 |

| Test Step 1.7 (Repeat Count = 1) | |
|----------------------------------|------------------------------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.99850011 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.715390444 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 26 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.278718382 ± 0.000125655810790826 |

| Test Step 1.8 (Repeat Count = 1) | |
|----------------------------------|------------------------------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.12300014 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.587459981 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 35.25 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.357870042 ± 0.000125655810790826 |

| Test Step 1.9 (Repeat Count = 1) | |
|----------------------------------|------------------------------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.66669989 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.532140017 |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 84 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.652007759 ± 0.000125655810790826 |

| Test Step 1.10 (Repeat Count = 1) | |
|-----------------------------------|--------------|
| Name | Input Value |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.66669989 |
| TbarVelFiltSv M str.K Uls f32 | 0.0147850001 |

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FrqDepDmpnInrtCmp_Init

| Name | Input Value |
|--------------------------------|------------------------------------|
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 95.0100021 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.696972251 ± 0.000125655810790826 |

| Test Step 1.11 (Repeat Count = 1) | | |
|-----------------------------------|------------------------------------|--|
| Name | Input Value | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.0258959997 | |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 41.2000008 | |
| Name | Expected Value | |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.404131025 ± 0.000125655810790826 | |

| Test Step 1.12 (Repeat Count = 1) | | |
|-----------------------------------|--|--|
| Name | Input Value | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.69869995 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.632139981 | |
| k_InrtCmp_TBarVelLPFKn_Hz_f32 | 56.3499985 | |
| Name | Expected Value | |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | $0.507428169 \pm 0.000125655810790826$ | |

| Name | Input Value |
|--|-----------------------------------|
| TbarVelFiltSv M str.SV Uls f32 | -5.14230013 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.0147850001 |
| <pre>c_InrtCmp_TBarVelLPFKn_Hz_f32</pre> | 63.25 |
| Name | Expected Value |
| PreDecelGain_Uls_M_f32 | 1 ± 0.0625 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 ± 0.00390625 |
| TbarVelFiltSv M str.K Uls f32 | 0.54833883 ± 0.000125655810790826 |

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DecelGain

| Project | FDD_Inertia |
|-------------|-------------|
| Module | FDD_Inertia |
| Test Object | DecelGain |

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| Total Testcases | 3 |
|-----------------|---|
| Successful | 3 |
| Failed | 0 |
| Not Executed | 0 |



Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp | | |
|------------------------|---|--|--|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml | | |
| Target Environment | TI TMS 570 PLS UDE (Default) | | |
| Kind of Test | Unit Test | | |
| Linker Options | | | |
| Source File(s) | | | |
| File | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c | | |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\\NxtrLib\include -I\$(PROJECTROOT)\\NxtrLib\include -I\$(PROJECTROOT)\\StdDef\include -I\$(ProgramFiles)\\Texas Instruments\\ccsv4\tools\\compiler\\tms470_4.9.5\\include | | |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c | | |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\-I\$(PROJECTROOT)\\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\\StdDef\include -I\$(ProgramFiles)\\Texas Instruments\\ccsv4\\tools\\compiler\\tms470_4.9.5\\include | | |

| Comments/Descripti | on/Specification |
|----------------------|--|
| Name | Text |
| Module 'FDD_Inertia' | **************************Unit Test Description************************************ |
| | Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c Code File(s) Version: 13 Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc Module Design Document Version: 18 Data Dictionary Version: 16 Unit Test Plan Version: 6 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes): 1994 Total RAM Used (Bytes): 328 Special Test Requirements: Test Date: 09-19-2014 Comments: |
| | Note1:Inline Function defined in ""globalmacro.h"" are not unit tested. |
| | Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference. |
| | Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function. |
| | Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function. |
| | Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable "filtCoef_Uls_T_Str.b0_Uls_f32" is calculated as -2.74156205240179 to 0 and "filtCoef_Uls_T_Str.b1_Uls_f32" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16 |
| | Note6:In ""GenFddIcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range. |
| | Note 7:The range of the parameter "VehicleSpeed_Kph_T_f32" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication. |
| | Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DirverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"". |
| | *************************************** |

| Attributes | | |
|-----------------------|--|--|
| Name | Value | |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 | |
| Float Precision | 9 | |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj | |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src | |
| Linker File | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre> | |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl | |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 | |
| Time Unit | Cycles | |
| Timer Enabled | false | |
| Timer Prescale | 0 | |
| Timer Resolution | 1 | |

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DecelGain



| Attributes | |
|-----------------|--|
| Name | Value |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Metrics Test

Performance Metrics (With "None" Instrumentation and "WithPS" Environment) Specification

CPU Cycles:

TS1.1 320.00 Cycles TS1.2 343.00 Cycles

Description

Test Vector Description:

TS1.1 "Shortest Execution Path:
(-VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32)=True
(RawDecelGain_Uls_T_f32>=(D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32)=True"
TS1.2 "Longest Execution Path:
(-VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32)=False
(-VehicleLonAccel_KphpS_T_f32 < k_DmpGainOnfThresh_KphpS_f32)=False
(-VehicleLonAccel_KphpS_T_f32 < k_DmpGainOnfThresh_KphpS_f32)=False
(RawDecelGain_Uls_T_f32>=(D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32)=False
(RawDecelGain_Uls_T_f32<=(D_2MS_SEC_F32 * -k_DmpDecelGainFSlew_UlspS_f32)+ PreDecelGain_Uls_M_f32)=False"

| Test Step 1.1 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -1118 | | |
| PreDecelGain_Uls_M_f32 | 1 | | |
| VehicleLonAccel_KphpS_T_f32 | -10 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1 | | |
| k_DmpDecelGain_Uls_f32 | 2 | | |
| k_DmpGainOffThresh_KphpS_f32 | 0 | | |
| k_DmpGainOnThresh_KphpS_f32 | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 1.00199997 | 1.002 ± 0.000009 | ~ |
| PreDecelGain_Uls_M_f32 | 1.00199997 | 1.002 ± 0.0625 | ✓ |

| Т | | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|------|----------|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Name | Input Value | | |
|---------------------------------------|--------------|------------------|-------|
| CRFMotorVel_MtrRadpS_T_f32 | 500.68 | | |
| PreDecelGain_Uls_M_f32 | 127118.835 | | |
| VehicleLonAccel_KphpS_T_f32 | -3.1 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1700.02 | | |
| k_DmpDecelGain_Uls_f32 | 2.1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 0 | | |
| k_DmpGainOnThresh_KphpS_f32 | 44.45 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Resul |
| DecelGain() | 127118.836 | 127118.835 ± 0.9 | |

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| Name | Actual Value | Expected Value | Result |
|------------------------|--------------|---------------------|----------|
| PreDecelGain Uls M f32 | 127118.836 | 127118.835 ± 0.0625 | ✓ |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Case 2: | : Path Test | |
|---------------|---|--|
| Specification | Performance Metrics (With "None" Instrumentation and "WithPS" Environment) | |
| | CPU Cycles: | |
| | TS2.1 326.00 Cycles TS2.2 344.00 Cycles TS2.3 342.00 Cycles TS2.4 320.00 Cycles | |
| Description | Test Vector Description: | |
| | TS2.1 "(-VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32) = True and (RawDecelGain_Uls_T_f32>= (D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32))=True" TS2.2 "(-VehicleLonAccel_KphpS_T_f32 > k_DmpGainOnThresh_KphpS_f32) = False and (-VehicleLonAccel_KphpS_T_f32 < k_DmpGainOffThresh_KphpS_f32)=True and (RawDecelGain_Uls_T_f32>= (D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32))=False and (RawDecelGain_Uls_T_f32<= (D_2MS_SEC_F32 * -k_DmpDecelGainFSlew_UlspS_f32)+ PreDecelGain_Uls_M_f32)=True" TS2.3 (-VehicleLonAccel_KphpS_T_f32 < k_DmpGainOffThresh_KphpS_f32)=False TS2.4 (RawDecelGain_Uls_T_f32>= (D_2MS_SEC_F32 * MaxDecelGain_UlspS_T_f32)+ PreDecelGain_Uls_M_f32))=True | |

| Test Step 2.1 (Repeat Count = 1) | | | |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 100.02 | | |
| PreDecelGain_Uls_M_f32 | 125487.235 | | |
| VehicleLonAccel_KphpS_T_f32 | -10 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 100.02 | | |
| k_DmpDecelGain_Uls_f32 | 2.1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 11.5 | | |
| k_DmpGainOnThresh_KphpS_f32 | 5.25 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 408 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 416 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 424 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 432 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 440 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 448 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125487.031 | 125487.035 ± 0.9 | - |
| PreDecelGain Uls M f32 | 125487.031 | 125487.035 ± 0.0625 | ✓ |

| Т | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.2 (Repeat Count = 1) | | √ |
|---------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 200.03 | |
| PreDecelGain_Uls_M_f32 | 125589.21 | |
| VehicleLonAccel_KphpS_T_f32 | 10 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 200.05 | |
| k_DmpDecelGain_Uls_f32 | 3.5 | |
| k_DmpGainOffThresh_KphpS_f32 | 22.25 | |
| k_DmpGainOnThresh_KphpS_f32 | 10.12 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | |

DecelGain

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| N | Invest Walter |
|---------------------------------------|---------------|
| Name | Input Value |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032 |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2424 |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 |

| Name | Actual Value | Expected Value | Result |
|------------------------|--------------|----------------------|--------|
| DecelGain() | 125588.813 | 125588.8099 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 125588.813 | 125588.8099 ± 0.0625 | ~ |

| Т | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 2.3 (Repeat Count = 1) | | | |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 500.68 | | |
| PreDecelGain_Uls_M_f32 | 127118.835 | | |
| VehicleLonAccel_KphpS_T_f32 | -3.1 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1700.02 | | |
| k_DmpDecelGain_Uls_f32 | 2.1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 0 | | |
| k_DmpGainOnThresh_KphpS_f32 | 44.45 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127118.836 | 127118.835 ± 0.9 | ~ |
| PreDecelGain Uls M f32 | 127118.836 | 127118.835 ± 0.0625 | ✓ |

| T | | | | V | l |
|-----------------------------|-------|-----------------------------|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | ı |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | ï |

| Test Step 2.4 (Repeat Count = 1) | | ✓ |
|---------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | -1118 | |
| PreDecelGain_Uls_M_f32 | 1 | |
| VehicleLonAccel_KphpS_T_f32 | -10 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1 | |
| k_DmpDecelGain_Uls_f32 | 2 | |
| k_DmpGainOffThresh_KphpS_f32 | 0 | |
| k_DmpGainOnThresh_KphpS_f32 | 0 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 8 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 8 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 8 | |

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DecelGain

| Name | Input Value | | |
|------------------------------------|--------------|------------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 1.00199997 | 1.002 ± 0.000009 | ~ |
| PreDecelGain_Uls_M_f32 | 1.00199997 | 1.002 ± 0.0625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

CPU Cycles:



Test Case 3: Boundary Test

Specification

```
Performance Metrics (With "None" Instrumentation and "WithPS" Environment)
```

328.00 Cycles 334.00 Cycles 326.00 Cycles 345.00 Cycles 334.00 Cycles 344.00 Cycles TS3.1 TS3.2 TS3.2 TS3.3 TS3.4 TS3.5 TS3.6 TS3.7

344.00 Cycles 344.00 Cycles 337.00 Cycles 326.00 Cycles 326.00 Cycles 326.00 Cycles 34.00 Cycles TS3.8 TS3.9 TS3.10 TS3.11 TS3.12 TS3.13 344.00 Cycles 344.00 Cycles 345.00 Cycles 345.00 Cycles 345.00 Cycles TS3.14 TS3.15

TS3.16 TS3.17 TS3.18 TS3.19 TS3.20 TS3.21 TS3.22 342.00 Cycles 345.00 Cycles 345.00 Cycles 325.00 Cycles TS3.23 TS3.24 TS3.25

325.00 Cycles 345.00 Cycles TS3.26 TS3.27 TS3.28 TS3.29 TS3.30 TS3.31 TS3.32 TS3.33

Description

Test Vector Description:

TS3.1 All min TS3.2 All max

TS3.1 All min
TS3.2 All max
TS3.2 All max
TS3.3 VehicleLonAccel_KphpS_T_f32 = min
TS3.4 VehicleLonAccel_KphpS_T_f32 = max
TS3.5 VehicleLonAccel_KphpS_T_f32 = zero
TS3.6 VehicleLonAccel_KphpS_T_f32 = pos
TS3.7 VehicleLonAccel_KphpS_T_f32 = pos
TS3.7 VehicleLonAccel_KphpS_T_f32 = max
TS3.10 CRFMotorVel1_MtrRadpS_T_f32 = min
TS3.9 CRFMotorVel1_MtrRadpS_T_f32 = zero
TS3.11 CRFMotorVel1_MtrRadpS_T_f32 = zero
TS3.11 CRFMotorVel1_MtrRadpS_T_f32 = neg
TS3.12 CRFMotorVel1_MtrRadpS_T_f32 = neg
TS3.13 k_DmpGainOnThresh_KphpS_f32 = min
TS3.14 k_DmpGainOnThresh_KphpS_f32 = max
TS3.15 k_DmpCainOnThresh_KphpS_f32 = max
TS3.16 k_DmpDecelGain_Uls_f32 = min
TS3.17 k_DmpDecelGain_Uls_f32 = min
TS3.18 k_DmpDecelGain_Uls_f32 = pos
TS3.19 k_DmpGainOffThresh_KphpS_f32 = min
TS3.20 k_DmpGainOffThresh_KphpS_f32 = max
TS3.21 k_DmpGainOffThresh_KphpS_f32 = pos
TS3.22 PreDecelGain_Uls_M_f32 = min
TS3.23 PreDecelGain_Uls_M_f32 = max
TS3.24 PreDecelGain_Uls_M_f32 = pos
TS3.25 t_DmpDecelGainSlewX_MtrRadpS_u11p5[
TS3.26 t_DmpDecelGainSlewX_MtrRadpS_u11p5[
TS3.27 t_DmpDecelGainSlewX_MtrRadpS_u11p5[
TS3.28 t_DmpDecel

TS3.25 TS3.26

PreDecelGain_Uls_M_f32 = pos
t_DmpDecelGainSlewX_MtrRadpS_u11p5[6]= min
t_DmpDecelGainSlewX_MtrRadpS_u11p5[6] = max
t_DmpDecelGainSlewX_MtrRadpS_u11p5[6] = pos
t_DmpDecelGainSlewY_UlspS_u13p3[6] = min
t_DmpDecelGainSlewY_UlspS_u13p3[6] = max
t_DmpDecelGainSlewY_UlspS_u13p3[6] = pos
k_DmpDecelGainFlew_UlspS_f32 = min
k_DmpDecelGainFSlew_UlspS_f32 = max
k_DmpDecelGainFSlew_UlspS_f32 = pos TS3.27 TS3.28 TS3.29

TS3.30 TS3.31

TS3.32

TS3.33

| Test Step 3.1 (Repeat Count = 1) | ✓ |
|---------------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -1118 |
| PreDecelGain_Uls_M_f32 | 1 |
| VehicleLonAccel_KphpS_T_f32 | -10 |
| k_DmpDecelGainFSlew_UlspS_f32 | 1 |
| k_DmpDecelGain_Uls_f32 | 1 |
| k_DmpGainOffThresh_KphpS_f32 | 0 |
| k_DmpGainOnThresh_KphpS_f32 | 0 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0 |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 8 |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 8 |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 8 |

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| Name | Input Value | | |
|------------------------------------|--------------|----------------|--------|
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 1 | 1 ± 0.000009 | ~ |
| PreDecelGain_Uls_M_f32 | 1 | 1 ± 0.0625 | • |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.2 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|----------------|---------------------|--------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 1118 | | |
| PreDecelGain_Uls_M_f32 | 4294967295 | | |
| VehicleLonAccel_KphpS_T_f32 | 10 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 4500 | | |
| k_DmpDecelGain_Uls_f32 | 10 | | |
| k_DmpGainOffThresh_KphpS_f32 | 50 | | |
| k_DmpGainOnThresh_KphpS_f32 | 50 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 35776 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 4000 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 4.2949673e+009 | 4294967286 ± 9999 | ~ |
| PreDecelGain_Uls_M_f32 | 4.2949673e+009 | 4294967286 ± 0.0625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.3 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 100.02 | | |
| PreDecelGain_Uls_M_f32 | 125487.235 | | |
| VehicleLonAccel_KphpS_T_f32 | -10 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 100.02 | | |
| k_DmpDecelGain_Uls_f32 | 2.1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 11.5 | | |
| k_DmpGainOnThresh_KphpS_f32 | 5.25 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 408 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 416 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 424 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 432 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 440 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 448 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125487.031 | 125487.035 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 125487.031 | 125487.035 ± 0.0625 | ✓ |



Actual Function

IntplVarXY_u16_u16Xu16Y_Cnt



Count Result

| T | | | | • | |
|-----------------------------|-------|-----------------------------|-------|-------|---|
| Actual Function | Count | Expected Function | Count | Resul | t |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | • | 7 |

| Test Step 3.4 (Repeat Count = 1) | | | V |
|---------------------------------------|--------------|----------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 200.03 | | |
| PreDecelGain_Uls_M_f32 | 125589.21 | | |
| VehicleLonAccel_KphpS_T_f32 | 10 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 200.05 | | |
| k_DmpDecelGain_Uls_f32 | 3.5 | | |
| k_DmpGainOffThresh_KphpS_f32 | 22.25 | | |
| k_DmpGainOnThresh_KphpS_f32 | 10.12 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2424 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125588.813 | 125588.8099 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 125588.813 | 125588.8099 ± 0.0625 | ✓ |

Count Expected Function

IntplVarXY_u16_u16Xu16Y_Cnt

| Name | Input Value | | |
|---------------------------------------|--------------|----------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32 | -100.04 | | |
| PreDecelGain Uls M f32 | 125691.185 | | |
| VehicleLonAccel_KphpS_T_f32 | 0 | | |
| k DmpDecelGainFSlew UlspS f32 | 300.06 | | |
| k DmpDecelGain Uls f32 | 4.2 | | |
| k_DmpGainOffThresh_KphpS_f32 | 33.35 | | |
| k DmpGainOnThresh KphpS f32 | 15.32 | | |
| t DmpDecelGainSlewX MtrRadpS u11p5[0] | 4192 | | |
| t DmpDecelGainSlewX MtrRadpS u11p5[1] | 4224 | | |
| t DmpDecelGainSlewX MtrRadpS u11p5[2] | 4256 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125690.586 | 125690.5849 ± 0.9 | |
| PreDecelGain Uls M f32 | 125690.586 | 125690.5849 ± 0.0625 | • |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |





| Test Step 3.6 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|----------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -200.05 | | |
| PreDecelGain_Uls_M_f32 | 125793.16 | | |
| VehicleLonAccel_KphpS_T_f32 | 5.3 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 400.04 | | |
| k_DmpDecelGain_Uls_f32 | 6.1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 44.45 | | |
| k_DmpGainOnThresh_KphpS_f32 | 20.25 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1208 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1216 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1224 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1232 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1240 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1248 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125792.359 | 125792.3599 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 125792.359 | 125792.3599 ± 0.0625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.7 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 300.02 | | |
| PreDecelGain_Uls_M_f32 | 125895.135 | | |
| VehicleLonAccel_KphpS_T_f32 | -5.4 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 500.02 | | |
| k_DmpDecelGain_Uls_f32 | 5.2 | | |
| k_DmpGainOffThresh_KphpS_f32 | 8.21 | | |
| k_DmpGainOnThresh_KphpS_f32 | 25.12 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1608 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1616 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1624 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1632 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1640 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1648 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125894.133 | 125894.135 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 125894.133 | 125894.135 ± 0.0625 | ✓ |

| T . | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | | |

| Test Step 3.8 (Repeat Count = 1) | ✓ |
|----------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -1118 |
| PreDecelGain_Uls_M_f32 | 125997.11 |
| VehicleLonAccel_KphpS_T_f32 | -2.2 |
| k_DmpDecelGainFSlew_UlspS_f32 | 600.04 |
| k_DmpDecelGain_Uls_f32 | 7.8 |
| k_DmpGainOffThresh_KphpS_f32 | 16.62 |

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DecelGain

| Name | Input Value | | |
|---------------------------------------|--------------|----------------------|----------|
| k_DmpGainOnThresh_KphpS_f32 | 1.25 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2424 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 125995.906 | 125995.9099 ± 0.9 | ~ |
| PreDecelGain Uls M f32 | 125995.906 | 125995.9099 ± 0.0625 | ✓ |

| Т | | | | | |
|-----------------------------|-------|-----------------------------|-------|------|----|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 3.9 (Repeat Count = 1) | | | |
|---------------------------------------|--------------|----------------------|--------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 1118 | | |
| PreDecelGain_Uls_M_f32 | 126099.085 | | |
| VehicleLonAccel_KphpS_T_f32 | -3.3 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 700.03 | | |
| k_DmpDecelGain_Uls_f32 | 8.7 | | |
| k_DmpGainOffThresh_KphpS_f32 | 24.21 | | |
| k_DmpGainOnThresh_KphpS_f32 | 2.58 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126097.688 | 126097.6849 ± 0.9 | ~ |
| PreDecelGain Uls M f32 | 126097.688 | 126097.6849 ± 0.0625 | ✓ |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.10 (Repeat Count = 1) | | V |
|---------------------------------------|-------------|---|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 0 | |
| PreDecelGain_Uls_M_f32 | 126201.06 | |
| VehicleLonAccel_KphpS_T_f32 | -4.1 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 800.04 | |
| k_DmpDecelGain_Uls_f32 | 9.2 | |
| k_DmpGainOffThresh_KphpS_f32 | 11.21 | |
| k_DmpGainOnThresh_KphpS_f32 | 3.21 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 27264 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 27296 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 27328 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 27360 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 27392 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 27424 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 3608 | |

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| Name | Input Value | | |
|------------------------------------|--------------|----------------------|----------|
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 3616 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 3624 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 3632 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 3640 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 3648 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126199.461 | 126199.4599 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 126199.461 | 126199.4599 ± 0.0625 | ✓ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|------|----------|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 3.11 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 100.5 | | |
| PreDecelGain_Uls_M_f32 | 126303.035 | | |
| VehicleLonAccel_KphpS_T_f32 | -5.6 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 900.02 | | |
| k_DmpDecelGain_Uls_f32 | 1.1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 22.41 | | |
| k_DmpGainOnThresh_KphpS_f32 | 4.62 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 14592 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 14624 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 14656 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 14688 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 14720 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 14752 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 288 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 296 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 304 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 312 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 320 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 328 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126301.234 | 126301.235 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 126301.234 | 126301.235 ± 0.0625 | ✓ |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.12 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|--------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -100.2 | | |
| PreDecelGain_Uls_M_f32 | 126405.01 | | |
| VehicleLonAccel_KphpS_T_f32 | -6.1 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1000.01 | | |
| k_DmpDecelGain_Uls_f32 | 1.5 | | |
| k_DmpGainOffThresh_KphpS_f32 | 33.32 | | |
| k_DmpGainOnThresh_KphpS_f32 | 5.64 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 20960 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 20992 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 21024 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 21056 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 21088 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 21120 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 384 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 392 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 400 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 408 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 416 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 424 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126403.008 | 126403.01 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 126403.008 | 126403.01 ± 0.0625 | ✓ |

Actual Function

IntplVarXY_u16_u16Xu16Y_Cnt



Count Result

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - |

| Test Step 3.13 (Repeat Count = 1) | | | ~ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 200.12 | | |
| PreDecelGain_Uls_M_f32 | 126506.985 | | |
| VehicleLonAccel_KphpS_T_f32 | 6.2 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1100.02 | | |
| k_DmpDecelGain_Uls_f32 | 1.9 | | |
| k_DmpGainOffThresh_KphpS_f32 | 44.45 | | |
| k_DmpGainOnThresh_KphpS_f32 | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 25216 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 25248 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 25280 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 25312 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 25344 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 25376 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126504.781 | 126504.785 ± 0.9 | - |
| PreDecelGain_Uls_M_f32 | 126504.781 | 126504.785 ± 0.0625 | • |

Count Expected Function

IntplVarXY_u16_u16Xu16Y_Cnt

| Name | Input Value | | |
|--------------------------------------|--------------|--------------------|----------|
| CRFMotorVel_MtrRadpS_T_f32 | -200.31 | | |
| PreDecelGain_Uls_M_f32 | 126608.96 | | |
| VehicleLonAccel_KphpS_T_f32 | 7.5 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1200.02 | | |
| k_DmpDecelGain_Uls_f32 | 2.5 | | |
| k_DmpGainOffThresh_KphpS_f32 | 8.62 | | |
| k_DmpGainOnThresh_KphpS_f32 | 50 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3264 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3296 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3328 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3360 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3392 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3424 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 680 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 688 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 696 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 704 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 712 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 720 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126606.563 | 126606.56 ± 0.9 | ✓ |
| PreDecelGain Uls M f32 | 126606.563 | 126606.56 ± 0.0625 | ✓ |

| | | | | ~ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |



| Test Step 3.15 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 300.52 | | |
| PreDecelGain_Uls_M_f32 | 126710.935 | | |
| VehicleLonAccel_KphpS_T_f32 | 8.2 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1300.02 | | |
| k_DmpDecelGain_Uls_f32 | 5.6 | | |
| k_DmpGainOffThresh_KphpS_f32 | 16.21 | | |
| k_DmpGainOnThresh_KphpS_f32 | 25.25 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3808 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3840 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3872 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3904 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3936 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1536 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1544 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1552 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1560 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1568 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1576 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126708.336 | 126708.335 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 126708.336 | 126708.335 ± 0.0625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.16 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|--------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -300.63 | | |
| PreDecelGain_Uls_M_f32 | 126812.91 | | |
| VehicleLonAccel_KphpS_T_f32 | 9.3 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1400.01 | | |
| k_DmpDecelGain_Uls_f32 | 1 | | |
| k_DmpGainOffThresh_KphpS_f32 | 24.12 | | |
| k_DmpGainOnThresh_KphpS_f32 | 11.21 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5280 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5312 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5344 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5376 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5408 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5440 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1488 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1496 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1504 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1512 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1520 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126810.109 | 126810.11 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 126810.109 | 126810.11 ± 0.0625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 3.17 (Repeat Count = 1) | | ✓ |
|-----------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 400.75 | |
| PreDecelGain_Uls_M_f32 | 126914.885 | |
| VehicleLonAccel_KphpS_T_f32 | -1.2 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1500.04 | |
| k_DmpDecelGain_Uls_f32 | 10 | |
| k_DmpGainOffThresh_KphpS_f32 | 32.41 | |

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DecelGain

| - | | | |
|---------------------------------------|--------------|----------------------|----------|
| Name | Input Value | | |
| k_DmpGainOnThresh_KphpS_f32 | 22.41 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 11680 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 11712 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 11744 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 11776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 11808 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 11840 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1608 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1616 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1624 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1632 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1640 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1648 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 126911.883 | 126911.8849 ± 0.9 | ~ |
| PreDecelGain Uls M f32 | 126911.883 | 126911.8849 ± 0.0625 | ✓ |

| T | | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|------|----------|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 3.18 (Repeat Count = 1) | | | · · |
|---------------------------------------|--------------|--------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -400.52 | | |
| PreDecelGain_Uls_M_f32 | 127016.86 | | |
| VehicleLonAccel_KphpS_T_f32 | -2.3 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1600.02 | | |
| k_DmpDecelGain_Uls_f32 | 5.25 | | |
| k_DmpGainOffThresh_KphpS_f32 | 40.52 | | |
| k_DmpGainOnThresh_KphpS_f32 | 33.32 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2424 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127013.656 | 127013.66 ± 0.9 | ~ |
| PreDecelGain Uls M f32 | 127013.656 | 127013.66 ± 0.0625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Name | Input Value | |
|---------------------------------------|-------------|--|
| CRFMotorVel_MtrRadpS_T_f32 | 500.68 | |
| PreDecelGain_Uls_M_f32 | 127118.835 | |
| VehicleLonAccel_KphpS_T_f32 | -3.1 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1700.02 | |
| k_DmpDecelGain_Uls_f32 | 2.1 | |
| k_DmpGainOffThresh_KphpS_f32 | 0 | |
| k_DmpGainOnThresh_KphpS_f32 | 44.45 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | |

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| Name | Input Value | | |
|------------------------------------|--------------|---------------------|----------|
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127118.836 | 127118.835 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 127118.836 | 127118.835 ± 0.0625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.20 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|--------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 600.46 | | |
| PreDecelGain_Uls_M_f32 | 127220.81 | | |
| VehicleLonAccel_KphpS_T_f32 | -4.2 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1800.01 | | |
| k_DmpDecelGain_Uls_f32 | 2.2 | | |
| k_DmpGainOffThresh_KphpS_f32 | 50 | | |
| k_DmpGainOnThresh_KphpS_f32 | 8.62 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 3608 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 3616 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 3624 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 3632 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 3640 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 3648 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127217.211 | 127217.21 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 127217.211 | 127217.21 ± 0.0625 | ~ |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.21 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|-------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 700.02 | | |
| PreDecelGain_Uls_M_f32 | 127322.785 | | |
| VehicleLonAccel_KphpS_T_f32 | -5.2 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1900.03 | | |
| k_DmpDecelGain_Uls_f32 | 2.6 | | |
| k_DmpGainOffThresh_KphpS_f32 | 25.45 | | |
| k_DmpGainOnThresh_KphpS_f32 | 16.21 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 288 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 296 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 304 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 312 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 320 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 328 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127318.984 | 127318.9849 ± 0.9 | ✓ |

127318.984

PreDecelGain_Uls_M_f32

127318.9849 ± 0.0625

Actual Function

IntplVarXY_u16_u16Xu16Y_Cnt



Count Result

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - |

| Test Step 3.22 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|----------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 800.03 | | |
| PreDecelGain_Uls_M_f32 | 1 | | |
| VehicleLonAccel_KphpS_T_f32 | -6.5 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2000.06 | | |
| k_DmpDecelGain_Uls_f32 | 2.8 | | |
| k_DmpGainOffThresh_KphpS_f32 | 11.21 | | |
| k_DmpGainOnThresh_KphpS_f32 | 24.12 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 1 | 1 ± 0.000009 | ~ |
| PreDecelGain_Uls_M_f32 | 1 | 1 ± 0.0625 | ✓ |

Count Expected Function

IntplVarXY_u16_u16Xu16Y_Cnt

| Name | Input Value | | |
|--------------------------------------|----------------|---------------------|----------|
| CRFMotorVel_MtrRadpS_T_f32 | 900.08 | | |
| PreDecelGain_Uls_M_f32 | 4294967295 | | |
| VehicleLonAccel_KphpS_T_f32 | -7.6 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2100.02 | | |
| k_DmpDecelGain_Uls_f32 | 3.5 | | |
| k_DmpGainOffThresh_KphpS_f32 | 22.41 | | |
| k_DmpGainOnThresh_KphpS_f32 | 32.41 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 4.2949673e+009 | 4294967291 ± 9999 | → |
| PreDecelGain Uls M f32 | 4.2949673e+009 | 4294967291 ± 0.0625 | ✓ |

| Т | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |





| Test Step 3.24 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|--------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 1000.12 | | |
| PreDecelGain_Uls_M_f32 | 127628.71 | | |
| VehicleLonAccel_KphpS_T_f32 | -8.2 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2200.02 | | |
| k_DmpDecelGain_Uls_f32 | 3.9 | | |
| k_DmpGainOffThresh_KphpS_f32 | 33.32 | | |
| k_DmpGainOnThresh_KphpS_f32 | 40.52 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 27264 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 27296 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 27328 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 27360 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 27392 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 27424 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 680 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 688 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 696 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 704 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 712 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 720 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127624.313 | 127624.31 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 127624.313 | 127624.31 ± 0.0625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.25 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|----------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 1100.26 | | |
| PreDecelGain_Uls_M_f32 | 127730.685 | | |
| VehicleLonAccel_KphpS_T_f32 | -9.2 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2300.04 | | |
| k_DmpDecelGain_Uls_f32 | 3.7 | | |
| k_DmpGainOffThresh_KphpS_f32 | 44.45 | | |
| k_DmpGainOnThresh_KphpS_f32 | 48.62 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1536 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1544 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1552 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1560 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1568 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1576 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127726.086 | 127726.0849 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 127726.086 | 127726.0849 ± 0.0625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 3.26 (Repeat Count = 1) | | ✓ |
|-----------------------------------|-------------|---|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | -500.23 | |
| PreDecelGain_Uls_M_f32 | 127832.66 | |
| VehicleLonAccel_KphpS_T_f32 | 1.1 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2400.08 | |
| k_DmpDecelGain_Uls_f32 | 4.8 | |
| k_DmpGainOffThresh_KphpS_f32 | 8.62 | |

DecelGain

PreDecelGain_Uls_M_f32

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127827.8598 ± 0.0625

| Name | Input Value | | |
|---------------------------------------|--------------|-------------------|--------|
| k_DmpGainOnThresh_KphpS_f32 | 4.21 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 35776 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 35776 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1480 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1488 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1496 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1504 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1512 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1520 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127827.859 | 127827.8598 ± 0.9 | ~ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|-----|----------|
| Actual Function | Count | Expected Function | Count | Res | ult |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

127827.859

| Name | Input Value | | |
|---------------------------------------|--------------|---------------------|----------|
| CRFMotorVel MtrRadpS T f32 | -600.52 | | |
| PreDecelGain Uls M f32 | 127934.635 | | |
| VehicleLonAccel_KphpS_T_f32 | 1.2 | | |
| k DmpDecelGainFSlew UlspS f32 | 2500.02 | | |
| k_DmpDecelGain_Uls_f32 | 5.9 | | |
| k_DmpGainOffThresh_KphpS_f32 | 16.21 | | |
| k_DmpGainOnThresh_KphpS_f32 | 8.85 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3200 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 6400 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9600 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 12800 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 16000 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 19200 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1208 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1216 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1224 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1232 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1240 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1248 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 127929.633 | 127929.635 ± 0.9 | ✓ |
| PreDecelGain Uls M f32 | 127929.633 | 127929.635 ± 0.0625 | ✓ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 3.28 (Repeat Count = 1) | | ✓ |
|---------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | -700.14 | |
| PreDecelGain_Uls_M_f32 | 128036.61 | |
| VehicleLonAccel_KphpS_T_f32 | 1.6 | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2600.07 | |
| k_DmpDecelGain_Uls_f32 | 5.8 | |
| k_DmpGainOffThresh_KphpS_f32 | 24.12 | |
| k_DmpGainOnThresh_KphpS_f32 | 12.61 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 8 | |

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DecelGain

| Name | Input Value | | |
|------------------------------------|--------------|----------------------|----------|
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 8 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 8 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 128031.406 | 128031.4099 ± 0.9 | ✓ |
| PreDecelGain_Uls_M_f32 | 128031.406 | 128031.4099 ± 0.0625 | ✓ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|------|----------|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 3.29 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|----------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -800.52 | | |
| PreDecelGain_Uls_M_f32 | 128138.585 | | |
| VehicleLonAccel_KphpS_T_f32 | 1.8 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2700.03 | | |
| k_DmpDecelGain_Uls_f32 | 6.5 | | |
| k_DmpGainOffThresh_KphpS_f32 | 32.41 | | |
| k_DmpGainOnThresh_KphpS_f32 | 16.21 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 4000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 4000 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 128133.188 | 128133.1849 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 128133.188 | 128133.1849 ± 0.0625 | ✓ |

| T | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Test Step 3.30 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|--------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -900.63 | | |
| PreDecelGain_Uls_M_f32 | 128240.56 | | |
| VehicleLonAccel_KphpS_T_f32 | -2.1 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2800.02 | | |
| k_DmpDecelGain_Uls_f32 | 6.8 | | |
| k_DmpGainOffThresh_KphpS_f32 | 40.52 | | |
| k_DmpGainOnThresh_KphpS_f32 | 20.63 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2000 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2008 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2016 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2024 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2032 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2040 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 128234.961 | 128234.96 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 128234.961 | 128234.96 ± 0.0625 | ✓ |

Actual Function

IntplVarXY_u16_u16Xu16Y_Cnt



Count Result

| T | | | | • |
|-----------------------------|-------|-----------------------------|-------|-------|
| Actual Function | Count | Expected Function | Count | Resul |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |

| Test Step 3.31 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -1000.25 | | |
| PreDecelGain_Uls_M_f32 | 128342.535 | | |
| VehicleLonAccel_KphpS_T_f32 | -2.5 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 1 | | |
| k_DmpDecelGain_Uls_f32 | 6.9 | | |
| k_DmpGainOffThresh_KphpS_f32 | 48.62 | | |
| k_DmpGainOnThresh_KphpS_f32 | 24.14 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 680 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 688 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 696 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 704 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 712 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 720 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 128342.531 | 128342.533 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 128342.531 | 128342.533 ± 0.0625 | ✓ |

Count Expected Function

IntplVarXY_u16_u16Xu16Y_Cnt

| Name | Input Value | | |
|--|--------------|--------------------|--------|
| CRFMotorVel_MtrRadpS_T_f32 | -1100.85 | | |
| PreDecelGain_Uls_M_f32 | 128444.51 | | |
| VehicleLonAccel_KphpS_T_f32 | -2.9 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 4500 | | |
| k_DmpDecelGain_Uls_f32 | 3.8 | | |
| k_DmpGainOffThresh_KphpS_f32 | 4.21 | | |
| <pre>c_DmpGainOnThresh_KphpS_f32</pre> | 28.18 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448 | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1536 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1544 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1552 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1560 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1568 | | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1576 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 128435.508 | 128435.51 ± 0.9 | • |
| PreDecelGain Uls M f32 | 128435.508 | 128435.51 ± 0.0625 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

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| Test Step 3.33 (Repeat Count = 1) | | | ✓ |
|---------------------------------------|--------------|---------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 458.62 | | |
| PreDecelGain_Uls_M_f32 | 128546.485 | | |
| VehicleLonAccel_KphpS_T_f32 | -8.1 | | |
| k_DmpDecelGainFSlew_UlspS_f32 | 2500.02 | | |
| k_DmpDecelGain_Uls_f32 | 6.9 | | |
| k_DmpGainOffThresh_KphpS_f32 | 8.85 | | |
| k_DmpGainOnThresh_KphpS_f32 | 32.25 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720 | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 1208 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 1216 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 1224 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 1232 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 1240 | | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 1248 | | |
| Name | Actual Value | Expected Value | Result |
| DecelGain() | 128541.484 | 128541.485 ± 0.9 | ~ |
| PreDecelGain_Uls_M_f32 | 128541.484 | 128541.485 ± 0.0625 | ✓ |

| Т | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

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FilterCoefCalc

| Project | FDD_Inertia |
|-------------|----------------|
| Module | FDD_Inertia |
| Test Object | FilterCoefCalc |

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Branch (C1) Coverage | 100 % |

Statistics

| Total Testcases | 1 |
|-----------------|---|
| Successful | 1 |
| Failed | 0 |
| Not Executed | 0 |



Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp |
|------------------------|---|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract -I\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -\\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract -\\$(PROJECTROOT)\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -\\$(PROJECTROOT) \NxtrLib\include -\\$(PROJECTROOT)\StdDef\include -\\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

| Comments/Descripti | on/Specification |
|----------------------|--|
| Name | Text |
| Module 'FDD_Inertia' | **************************Unit Test Description************************************ |
| | Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c Code File(s) Version: 13 Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc Module Design Document Version: 18 Data Dictionary Version: 16 Unit Test Plan Version: 6 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes): 1994 Total RAM Used (Bytes): 328 Special Test Requirements: Test Date: 09-19-2014 Comments: |
| | Note1:Inline Function defined in ""globalmacro.h"" are not unit tested. |
| | Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference. |
| | Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function. |
| | Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function. |
| | Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable "filtCoef_Uls_T_Str.b0_Uls_f32" is calculated as -2.74156205240179 to 0 and "filtCoef_Uls_T_Str.b1_Uls_f32" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16 |
| | Note6:In ""GenFddIcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range. |
| | Note 7:The range of the parameter "VehicleSpeed_Kph_T_f32" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication. |
| | Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DirverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"". |
| | *************************************** |

| Attributes | | | |
|-----------------------|--|--|--|
| Name | Value | | |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 | | |
| Float Precision | 9 | | |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj | | |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src | | |
| Linker File | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre> | | |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl | | |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 | | |
| Time Unit | Cycles | | |
| Timer Enabled | false | | |
| Timer Prescale | 0 | | |
| Timer Resolution | 1 | | |

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| Attributes | | | |
|-----------------|--|--|--|
| Name | Value | | |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg | | |
| Workspace File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP | | |



Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and "WithPS" ${\tt Environment}$)

CPU Cycles:

1239.00 Cycles 1283.00 Cycles 1285.00 Cycles 1274.00 Cycles 1274.00 Cycles 1251.00 Cycles TS1.1 TS1.2 TS1.3 TS1.4 TS1.6 TS1.7 1285.00 Cycles 1274.00 Cycles 1239.00 Cycles 1250.00 Cycles 1663.00 Cycles 1272.00 Cycles 1239.00 Cycles 1272.00 Cycles 1272.00 Cycles 1274.00 Cycles 1274.00 Cycles 1274.00 Cycles 1274.00 Cycles 1285.00 Cycles TS1.8 TS1.9 TS1.10 TS1.11 TS1.11 TS1.12 TS1.13 TS1.14 TS1.15 TS1.16 TS1.17 1274.00 Cycles 1274.00 Cycles 1274.00 Cycles 1285.00 Cycles 1285.00 Cycles 1274.00 Cycles 1274.00 Cycles 1285.00 Cycles 1285.00 Cycles 1285.00 Cycles 1285.00 Cycles 1285.00 Cycles 1274.00 Cycles 1274.00 Cycles 1274.00 Cycles TS1.18 TS1.19 TS1.20 TS1.21 TS1.22 TS1.23 TS1.26 TS1.27 TS1.28 TS1.29 TS1.30 TS1.31 TS1.32

Description

Test Vector Description

| TS1.1 All min TS1.2 All max TS1.3 ADDCoef_MtrNmSpRad_T_f32 min TS1.4 ADDCoef_MtrNmSpRad_T_f32 max TS1.5 ADDCoef_MtrNmSpRad_T_f32 max TS1.5 ADDCoef_MtrNmSpRad_T_f32 pos TS1.6 VehicleSpeed2_Kph_T_f32 min TS1.7 VehicleSpeed2_Kph_T_f32 min TS1.7 VehicleSpeed2_Kph_T_f32 max TS1.8 VehicleSpeed2_Kph_T_f32 pos TS1.9 WIRCmdAmpBlnd1_MtrNm_T_f32 min TS1.10 WIRCmdAmpBlnd1_MtrNm_T_f32 max TS1.11 WIRCmdAmpBlnd1_MtrNm_T_f32 pos TS1.12 t_CmnVehSpd_Kph_u9p7[12] min TS1.13 t_CmnVehSpd_Kph_u9p7[12] max TS1.14 t_CmnVehSpd_Kph_u9p7[12] max TS1.15 t2_FDD_FreqTblYM1_Hz_u12p4[12] min TS1.16 t2_FDD_FreqTblYM1_Hz_u12p4[12] max TS1.17 t2_FDD_FreqTblYM1_Hz_u12p4[12] max TS1.18 t2_FDD_FreqTblYM2_Hz_u12p4[12] max TS1.19 t2_FDD_FreqTblYM2_Hz_u12p4[12] max TS1.20 t2_FDD_FreqTblYM2_Hz_u12p4[12] max TS1.21 t_WIRBIndTblX_MtrNm_u8p8[5] min TS1.22 t_WIRBIndTblX_MtrNm_u8p8[5] max TS1.23 t_UmpFiltKpWIRBIndY_Uls_u2p14[5] min TS1.25 t_DmpFiltKpWIRBIndY_Uls_u2p14[5] min | | |
|--|------------------------|--|
| TS1.26 t_DmpFiltKpWIRBIndY_Uls_u2p14[5] pos | | |
| TS1.27 t_InrtCmp_ScaleFactorTbIY_UIs_u9p7[12] mi TS1.28 t_InrtCmp_ScaleFactorTbIY_UIs_u9p7[12] m TS1.29 t InrtCmp_ScaleFactorTbIY_UIs_u9p7[12] pc | ax | |
| TS1.30 k_InrtCmp_MtrInertia_KgmSq_f32 min TS1.31 k_InrtCmp_MtrInertia_KgmSq_f32 max | | |
| TS1.32 k_InrtCmp_MtrInertia_KgmSq_f32 pos | | |
| Test Step 1.1 (Repeat Count = 1) | ✓ | |
| Name | Input Value | |
| ADDCoef_MtrNmSpRad_T_f32 | 0 | |
| VehicleSpeed_Kph_T_f32 | 0 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 0 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | |
| k_InrtCmp_MtrInertia_KgmSq_f32 0.00001 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 16 | |
| 12_FDD_FreqTbIYM_Hz_u12p4[0][2] | 16 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] 16 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 16 | | |
| | 16 | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5] t2_FDD_FreqTbIYM_Hz_u12p4[0][6] | 16 | |
| 12_FDD_FreqTbIYM_Hz_u12p4[0][7] | 16 | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8] | 16 | |
| t2 FDD FreqTblYM Hz u12p4[0][9] | 16 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 16 | |

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| Name | Input Value | | |
|--|----------------|---------------------------------------|-------|
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 16 | | |
| | 0 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[4] t_CmnVehSpd_Kph_u9p7[5] | 0 | | |
| | 0 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[11] t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 0 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[10] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 0 | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 0 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 0 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt filtCoef Uls T Str.b0 Uls f32 | O Actual Value | · · · · · · · · · · · · · · · · · · · | Resul |
| | 0 | 0 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0 | 0 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | | 0 ± 0.000009 | |
| tgt_filtCoef_UIs_T_Str.a0_UIs_f32 | 3.94989252 | 3.949892431 ± 0.000009 | |
| tgt_filtCoef_UIs_T_Str.a1_UIs_f32 | -7.99968433 | -7.999684173 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.05042315 | 4.050423396 ± 0.000009 | |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.2 (Repeat Count = 1) | | √ |
|----------------------------------|------------------------|----------|
| Name | Input Value | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.041306 | |
| VehicleSpeed_Kph_T_f32 | 511.9921875 | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 8.8 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0005 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1600 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1600 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1600 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1600 | |

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| Name | Input Value | | |
|--|--------------------------|--|----------|
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1600 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][9] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1600 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 32640 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 16384 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 16384 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[2] | 16384 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[3] | 16384 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[4] | 16384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 384 | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 2048 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] t_WIRBIndTbIX_MtrNm_u8p8[2] | 2048 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 2046 | | |
| | 2046 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | | Expected Value | Dani'i |
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_UIs_T_Str.b0_UIs_f32 | -2.74156237 | -2.741562052 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330448002 | 0.330448 ± 0.0000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 2.41111422 | 2.411114052 ± 0.000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 0.552588403 | 0.552588458 ± 0.0000009 | ~ |
| tot fileCoof Illo T Ctr of Illo foo | 4.0447060 | 4.044700500 + 0.000000 | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | -4.8417263 10.6056852 | -4.841726592 ± 0.000009 10.60568495 ± 0.00009 | → |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 4 | IntplVarXY u16 u16Xu16Y Cnt | 4 | |

| Test Step 1.3 (Repeat Count = 1) | ✓ |
|----------------------------------|-------------|
| Name | Input Value |
| ADDCoef_MtrNmSpRad_T_f32 | 0 |
| VehicleSpeed_Kph_T_f32 | 100.02 |

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| Name | Input Value | | |
|--|------------------------|---|------|
| WIRCmdAmpBInd_MtrNm_T_f32 | 2.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00002 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[0] t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| | 384 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[4] t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| | 896 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[7] t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[5] | 77 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 282 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 307 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 358 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 384 | | |
| Name | Actual Value | Expected Value | Post |
| | -0.00059381465 | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | 0 | -0.000593815 ± 0.0000000009 0 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | | | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.00059381465 | 0.000593815 ± 0.00000000009 | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.39635515 | 3.39635548 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | -7.95065212 | -7.950651978 ± 0.000009 | |
| tut IIItGuel UIS I Str.az UIS 132 | 4.65299273 | 4.652992542 ± 0.000009 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |



| Test Step 1.4 (Repeat Count = 1) Name | Input Value | | |
|---|------------------------|--------------------------|---|
| ADDCoef_MtrNmSpRad_T_f32 | 0.041306 | | |
| VehicleSpeed_Kph_T_f32 | 200.06 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 1.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00003 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 128 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 192 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 224 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] t DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[1] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[10] | 154 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] t_WIRBIndTblX_MtrNm_u8p8[0] | 538 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 563 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 589 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 614 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 640 | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.170364141 | -0.170364138 ± 0.0000009 | ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,, |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.330448002 | 0.330448 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.16008386 | -0.160083862 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.36400986 | 3.364009947 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.94497013 | -7.944970142 ± 0.000009 | |

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| Name | Actual Value | Expected Value | Result |
|-----------------------------------|--------------|------------------------|----------|
| tgt filtCoef Uls T Str.a2 Uls f32 | 4.69101954 | 4.691019911 ± 0.000009 | ✓ |

| Т | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.5 (Repeat Count = 1) | |
|---|------------------------|
| Name | Input Value |
| ADDCoef_MtrNmSpRad_T_f32 | 0.02 |
| VehicleSpeed_Kph_T_f32 | 300.08 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 0.5 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00004 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 48 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 128 |
| t2 FDD FreqTblYM Hz u12p4[1][5] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 160 |
| t2 FDD FreqTblYM Hz u12p4[1][7] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 240 |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 |
| t CmnVehSpd Kph u9p7[8] | 7808 |
| _ | 7936 |
| t_CmnVehSpd_Kph_u9p7[9] t_CmnVehSpd_Kph_u9p7[10] | 8064 |
| | 8192 |
| t_CmnVehSpd_Kph_u9p7[11] | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 6554 |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[1] | 111 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[3] | 9830 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 38 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 51 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 64 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 77 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 90 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 102 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 115 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 128 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 141 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 154 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 166 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 179 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 794 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 819 |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 845 |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 870 |

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| Name | Input Value | | |
|-----------------------------------|---------------|---------------------------|----------|
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 896 | | |
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0846711174 | -0.084671116 ± 0.00000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.159999996 | 0.16 ± 0.0000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0753288791 | -0.075328884 ± 0.00000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.31349587 | 3.313495926 ± 0.000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.9354167 | -7.935416577 ± 0.000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.75108767 | 4.751087497 ± 0.000009 | ✓ |

| T | | | ✓ | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.6 (Repeat Count = 1) | |
|---------------------------------------|------------------------|
| Name | Input Value |
| ADDCoef_MtrNmSpRad_T_f32 | 0.001 |
| VehicleSpeed_Kph_T_f32 | 0 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 6.5 |
| filtCoef_Uls_T_Str | tgt filtCoef Uls T Str |
| | 0.00005 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 64 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 112 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 256 |
| t_CmnVehSpd_Kph_u9p7[0] | 128 |
| t_CmnVehSpd_Kph_u9p7[1] | 256 |
| t_CmnVehSpd_Kph_u9p7[2] | 384 |
| t_CmnVehSpd_Kph_u9p7[3] | 512 |
| t_CmnVehSpd_Kph_u9p7[4] | 640 |
| t_CmnVehSpd_Kph_u9p7[5] | 768 |
| t_CmnVehSpd_Kph_u9p7[6] | 896 |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 51 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 64 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 77 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 102 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 115 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 128 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 141 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 154 |
| _ (,,,,,,,,,,, | t . |

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Input Value t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] 166 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] 179 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] 192 t_WIRBIndTbIX_MtrNm_u8p8[0] 1050 t_WIRBIndTbIX_MtrNm_u8p8[1] 1075 t_WIRBIndTbIX_MtrNm_u8p8[2] 1101 t_WIRBIndTbIX_MtrNm_u8p8[3] t_WIRBIndTbIX_MtrNm_u8p8[4] 1126 1152

| L_VVIXBING T DIX_IVIETATII_dopo[+] | 1132 | | |
|------------------------------------|----------------|----------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.00400001789 | -0.004000018 ± 0.000000009 | ~ |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.00800000038 | 0.008 ± 0.000000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00399998249 | -0.003999982 ± 0.000000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.76236439 | 3.76236461 ± 0.000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.99272346 | -7.992723375 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.24491215 | 4.244912015 ± 0.000009 | • |

| T | | | • | |
|-----------------------------|-------|-----------------------------|-------|-------|
| Actual Function | Count | Expected Function | Count | Resul |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | |

| Test Step 1.7 (Repeat Count = 1) | |
|---------------------------------------|------------------------|
| Name | Input Value |
| ADDCoef_MtrNmSpRad_T_f32 | 0.002 |
| VehicleSpeed_Kph_T_f32 | 511.9921875 |
| WIRCmdAmpBind_MtrNm_T_f32 | 5.5 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00006 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 256 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 112 |
| t2 FDD FreqTblYM Hz u12p4[1][2] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 256 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 272 |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 |
| t DmpFiltKpWIRBIndY Uls u2p14[3] | 13107 |
| t DmpFiltKpWIRBIndY Uls u2p14[4] | 14746 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 64 |
| t InrtCmp ScaleFactorTblY UIs u9p7[1] | 77 |

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| Name | Input Value | | |
|--|----------------|----------------------------|----------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 205 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1306 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1331 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1357 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1382 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1408 | | |
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0128454715 | -0.012845471 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0160000008 | 0.016 ± 0.00000009 | ✓ |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00315452972 | -0.003154529 ± 0.000000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.1956141 | 3.195613973 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.90979624 | -7.909796293 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.89459038 | 4.894589734 ± 0.000009 | ~ |

| T T | | | V | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.8 (Repeat Count = 1) | |
|----------------------------------|------------------------|
| Name | Input Value |
| ADDCoef_MtrNmSpRad_T_f32 | 0.003 |
| VehicleSpeed_Kph_T_f32 | 255.25 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 3.6 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00007 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 256 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 272 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 336 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 352 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 368 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 384 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 400 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 416 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 432 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 448 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 464 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 480 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 496 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 512 |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 |

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| Name | Input Value | | |
|--|----------------|------------------------------------|------|
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 294 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1562 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1587 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1613 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1638 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1664 | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.022498928 | -0.0224989261685139 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.024000002 | 0.024 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00150107313 | -0.00150107383148608 ± 0.000000009 | ٠, |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.12415075 | 3.12415079635252 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.89191246 | -7.89191237196188 ± 0.000009 | • |
| tgt filtCoef Uls T Str.a2 Uls f32 | 4.98393726 | 4.98393683168561 ± 0.000009 | |

| T . | | | • | | | |
|-----|-----------------------------|-------|-----------------------------|-------|-------|---|
| | Actual Function | Count | Expected Function | Count | Resul | t |
| | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • | |

| Test Step 1.9 (Repeat Count = 1) | |
|----------------------------------|------------------------|
| Name | Input Value |
| ADDCoef MtrNmSpRad T f32 | 0.004 |
| VehicleSpeed_Kph_T_f32 | 16.25 |
| WIRCmdAmpBInd MtrNm T f32 | 0 |
| filtCoef Uls T Str | tgt filtCoef Uls T Str |
| k InrtCmp MtrInertia KgmSq f32 | 0.00008 |
| t2 FDD FreqTblYM Hz u12p4[0][0] | 336 |
| t2 FDD FreqTblYM Hz u12p4[0][1] | 352 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 368 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 384 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 400 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 416 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 432 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 448 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 464 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 480 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 496 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 512 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 656 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 672 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 688 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 704 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 720 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 |

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| Manage | Innut Value | | |
|--|----------------|----------------------------|--------|
| Name | Input Value | | |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 320 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1792 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1818 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1843 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1869 | | |
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0280437507 | -0.028043747 ± 0.00000009 | ~ |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0320000015 | 0.032 ± 0.00000009 | ~ |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.00395625085 | -0.003956253 ± 0.000000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.84204841 | 2.842048638 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.8026042 | -7.802604057 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.35534716 | 5.355347305 ± 0.000009 | ~ |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.10 (Repeat Count = 1) | | |
|-----------------------------------|------------------------|--|
| Name | Input Value | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.005 | |
| VehicleSpeed_Kph_T_f32 | 32.28 | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 8.8 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00009 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 656 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 672 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 688 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 704 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 720 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 736 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 752 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 768 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 784 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 800 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 816 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 832 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1296 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1312 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1328 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1344 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1360 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1376 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1392 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1408 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1424 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1440 | |

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|--|---------------|---------------------------|--------|
| Name | Input Value | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1456 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1472 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 282 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 410 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 435 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 461 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 486 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 512 | | |
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0954187065 | -0.095418708 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.039999991 | 0.04 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0554187112 | 0.055418708 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.28349459 | 1.283494792 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.49632454 | -6.496324749 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.22018147 | 8.220180459 ± 0.000009 | • |
| | | | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.11 (Repeat Count = 1) | | |
|-----------------------------------|------------------------|--|
| Name | Input Value | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.006 | |
| VehicleSpeed_Kph_T_f32 | 48.52 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 5.6 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0001 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1296 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1312 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1328 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1344 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1360 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1376 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1392 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1408 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1424 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1440 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1456 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1472 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1136 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1152 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1168 | |

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| FilterCoetCalc | | (WZ | 201606 |
|--|--------------|--------------------------|--------|
| Name | Input Value | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1184 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1200 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1216 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1232 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1248 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1264 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1280 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1296 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1312 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[2] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[4] | 13107 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 218 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[5] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 269 | | |
| t InrtCmp ScaleFactorTblY UIs u9p7[9] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 307 | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 666 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 691 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 717 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 742 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 768 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.246170521 | -0.246170482 ± 0.0000009 | Resul |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0480000004 | 0.048 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.198170513 | 0.198170482 ± 0.0000009 | |
| | 0.196170513 | 0.976945693 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -5.9533534 | -5.953353668 ± 0.000009 | |
| | 9.06970024 | 9.06970064 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 9.009/0024 | 9.009/0004 ± 0.000009 | |

| Т | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.12 (Repeat Count = 1) | | ~ |
|-----------------------------------|------------------------|---|
| Name | Input Value | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.007 | |
| VehicleSpeed_Kph_T_f32 | 64.95 | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.1 | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00011 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1136 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1152 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1168 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1184 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1200 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1216 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1232 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1248 | |

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| FilterCoefCalc | | (VAZ | Cital |
|--|--------------|--------------------------|-------|
| Name | Input Value | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1264 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1280 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1296 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1312 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 272 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 288 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 304 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 320 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 336 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 352 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 0 | | |
| t CmnVehSpd Kph u9p7[8] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 0 | | |
| t CmnVehSpd Kph u9p7[10] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 243 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[4] | 256 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[5] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 346 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 922 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 947 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 973 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 998 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1024 | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.170547396 | -0.170547388 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0560000017 | 0.056 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.114547402 | 0.114547388 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.81319332 | 1.813193477 ± 0.000009 | |
| | -7.14600277 | -7.14600287 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | | | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

| Test Step 1.13 (Repeat Count = 1) | |
|-----------------------------------|------------------------|
| Name | Input Value |
| ADDCoef_MtrNmSpRad_T_f32 | 0.008 |
| VehicleSpeed_Kph_T_f32 | 80.35 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.2 |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00012 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 176 |

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| Name | Input Value | | |
|--|---------------|---------------------------|------|
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 208 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 272 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][7] | 288 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 304 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 320 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 336 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 352 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 512 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 528 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 544 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 560 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 576 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 592 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 608 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 624 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 672 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 32640 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[0] | 1638 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[1] | 3277 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[2] | 4915 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[3] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 294 | | |
| | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 307 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 346 358 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 1178 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 1203 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 1254 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1280 | · · · · | 1 |
| Name | Actual Value | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0392927453 | -0.039292744 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.064000003 | 0.064 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0247072577 | -0.024707256 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.37325883 | 3.373258677 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.94662905 | -7.946629189 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.68011236 | 4.680112134 ± 0.000009 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |



| Test Step 1.14 (Repeat Count = 1) | | | • |
|--|------------------------|---------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.009 | | |
| VehicleSpeed_Kph_T_f32 | 96.62 | | |
| WIRCmdAmpBind_MtrNm_T_f32 | 1.3 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00013 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 512 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 528 544 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 560 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5] | 576 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 592 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 608 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 624 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 240 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[0] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[3] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[4] t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[5] | 64 77 | | |
| t_InrtCmp_ScaleFactor1blY_Uis_u9p7[5] t_InrtCmp_ScaleFactorTblY_Uis_u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactor1blY_Uis_u9p7[6] t_InrtCmp_ScaleFactorTblY_Uis_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactor1blY_Uis_u9p7[/] t_InrtCmp_ScaleFactorTblY_Uis_u9p7[8] | 115 | | |
| t_InitCmp_ScaleFactorTblY_Uis_u9p7[9] | 128 | | |
| t_InitCmp_ScaleFactorTblY_Uis_u9p7[9] t_InitCmp_ScaleFactorTblY_Uis_u9p7[10] | 141 | | |
| t_InitCmp_ScaleFactorTblY_Uis_u9p7[10] t_InitCmp_ScaleFactorTblY_Uis_u9p7[11] | 154 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1434 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1459 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1485 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1510 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1536 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0544182248 | -0.054418228 ± 0.00000009 | Nesul |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0719999969 | 0.072 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0175817721 | -0.017581772 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.50426316 | 2.504263453 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.6513648 | -7.651364918 ± 0.000009 | |
| -g 50:_0:0_ · _0:00:0_!0 2 | 7.00100-10 | | |

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| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.15 (Repeat Count = 1) | | | |
|---|------------------------|--------------------------|------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.01 | | |
| VehicleSpeed_Kph_T_f32 | 112.41 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.4 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00014 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 16 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] t2_FDD_FreqTbIYM_Hz_u12p4[0][2] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 16 | | |
| t2 FDD FreqTblYM Hz u12p4[0][6] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 16 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 224 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 64 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] t InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 115 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[8] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 166 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1690 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1715 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1741 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1792 | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0412790775 | -0.04127908 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.079999982 | 0.08 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0387209207 | -0.03872092 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.72832537 | 3.728325621 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.99044704 | -7.990446859 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.28122759 | 4.28122752 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.16 (Repeat Count = 1) | | | ✓ |
|--|------------------------|--------------------------|--------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.011 | | |
| VehicleSpeed_Kph_T_f32 | 128.56 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00015 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 272 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[1] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[3] | 11469 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 77 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 115 | | |
| | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 179 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1894 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 1920 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1946 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1997 | | |
| Name | Actual Value | Expected Value | Dogult |
| | | | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.199160993 | -0.199160956 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.087999995 | 0.088 ± 0.00000009 | · · |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.111160994 | 0.111160956 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.34697342 | 1.346973575 ± 0.000009 | Ž |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.59078789 | -6.590788107 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 8.06223869 | 8.062238318 ± 0.000009 | _ |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |



| Test Step 1.17 (Repeat Count = 1) | | | |
|---|------------------------|--------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.012 | | |
| VehicleSpeed_Kph_T_f32 | 144.52 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 1.6 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00016 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 816 832 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] t2_FDD_FreqTbIYM_Hz_u12p4[0][2] | 848 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2] | 864 | | |
| t2_FDD_F1eq1b1fM_F12_012p4[0][3] t2_FDD_F1eq1b1fM_Hz_u12p4[0][4] | 880 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 896 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 912 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 928 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 944 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 960 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 976 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 992 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 704 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 720 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 794 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 819 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 845 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 870 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 896 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.17973122 | -0.179731222 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.0960000008 | 0.096 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0837312266 | 0.083731222 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.64792883 | 1.647929015 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.97387695 | -6.97387697 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.37819529 | 7.378194015 ± 0.000009 | • |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.18 (Repeat Count = 1) | | | ✓ |
|---|------------------------|---------------------------|----------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.013 | | |
| VehicleSpeed_Kph_T_f32 | 160.63 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 1.7 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0003 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 16 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 7552 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 7936 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[10] | | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 1638 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | | |
| t_DmpFiltKpWlRBIndY_Uls_u2p14[1] | | | |
| t DmpFiltKpWlRBIndY Uls u2p14[3] | 4915 6554 | | |
| | | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[4] | 8192 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 115 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 205 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1050 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 1030 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1101 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 1126 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1152 | | |
| Name | Actual Value | Expected Value | Result |
| | | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0631598011 | -0.063159799 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.104000002 | 0.104 ± 0.0000009 | · · |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | -0.0408402011 | -0.040840201 ± 0.0000009 | ~ |
| | 3.47085524 | 3.47085539 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.96247482 | -7.962474705 ± 0.000009 | |

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| Test Step 1.19 (Repeat Count = 1) | | | |
|--|------------------------|--------------------------|------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.014 | | |
| VehicleSpeed_Kph_T_f32 | 176.85 | | |
| WIRCmdAmpBind_MtrNm_T_f32 | 1.8 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00031 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 48 64 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2] t2_FDD_FreqTbIYM_Hz_u12p4[0][3] | 80 | | |
| t2_FDD_Fleq16lfW_Fl2_u12p4[0][3] t2_FDD_Fleq16lfW_Fl2_u12p4[0][4] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1600 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][2] | 1600 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][3] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1600 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][5] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1600 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1600 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 294 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1306 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1331 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1357 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1382 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1408 | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.173795044 | -0.173795005 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.112000003 | 0.112 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0617950335 | 0.061795005 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.61782336 | 2.617823645 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.70810461 | -7.708104611 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.67407131 | 5.674071744 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.20 (Repeat Count = 1) | | | |
|--|------------------------|--------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.015 | | |
| VehicleSpeed_Kph_T_f32 | 192.52 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 1.9 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00032 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 48 64 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] | 80 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2] t2_FDD_FreqTbIYM_Hz_u12p4[0][3] | 96 | | |
| t2_FDD_F1eq1b1fW_F12_012p4[0][3] t2_FDD_F1eq1b1fW_F12_012p4[0][4] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 672 | | |
| t2 FDD FreqTblYM Hz u12p4[1][2] | 688 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][3] | 704 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 720 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 | | |
| t2 FDD FreqTblYM Hz u12p4[1][7] | 768 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 320 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[0] | 1562 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1587 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1613 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1638 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1664 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.155867472 | -0.155867459 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.119999997 | 0.12 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0358674712 | 0.035867459 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.82515574 | 2.825155925 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.79624844 | -7.796248275 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.37859583 | 5.3785958 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.21 (Repeat Count = 1) | | | |
|---|-----------------------------------|---------------------------|---------------------------------------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.016 | | |
| VehicleSpeed_Kph_T_f32 | 208.12 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 filtCoef Uls T Str | | | |
| k InrtCmp MtrInertia KgmSq f32 | tgt_filtCoef_Uls_T_Str 0.00033 | | |
| k_intemp_witherta_kginsq_isz t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 64 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] | 80 | | |
| t2_FDD_F1eq1b1fM_F12_012p4[0][1] t2_FDD_F1eq1b1fM_Hz_u12p4[0][2] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 144 | | |
| t2 FDD FreqTblYM Hz u12p4[0][6] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 192 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 282 | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 0 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 0 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 0 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0883268192 | -0.088326814 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.128000006 | 0.128 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0396731868 | -0.039673186 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.39172339 | 3.3917236 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.94985914 | -7.94985896 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.65841722 | 4.65841744 ± 0.000009 | • • • • • • • • • • • • • • • • • • • |

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| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.22 (Repeat Count = 1) | | | |
|---|------------------------|---------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.017 | | |
| VehicleSpeed_Kph_T_f32 | 224.01 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 2.1 | | |
| filtCoef_UIs_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00034 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 80 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 128 | | |
| t2 FDD FreqTblYM Hz u12p4[0][4] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 32 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 208 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[3] | 13107 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[4] | 14746 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[1] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] t InrtCmp ScaleFactorTblY Uls u9p7[3] | 192 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 2048 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.10374245 | -0.103742449 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.136000007 | 0.136 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0322575532 | -0.032257551 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.30435205 | 3.304351854 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.93359709 | -7.933597302 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.76205063 | 4.762050845 ± 0.000009 | • |

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| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.23 (Repeat Count = 1) | Ironat Walna | | |
|---|-----------------------------------|---------------------------|---------------------------------------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.018 | | |
| VehicleSpeed_Kph_T_f32 | 240.02 3.5 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 filtCoef Uls T Str | | | |
| k InrtCmp MtrInertia KgmSq f32 | tgt_filtCoef_Uls_T_Str 0.00035 | | |
| k_inticinp_withertia_kgm5q_i32 t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 96 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0] | 112 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 144 | | |
| t2 FDD FreqTbIYM Hz u12p4[0][4] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 176 | | |
| t2 FDD FreqTblYM Hz u12p4[0][6] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 272 | | |
| t2 FDD FreqTblYM Hz u12p4[1][0] | 48 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 224 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[8] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 346 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 256 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 512 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 768 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 1024 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1280 | F 1 111 1 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.120654218 | -0.120654218 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.143999994 | 0.144 ± 0.0000009 | • |
| tgt_filtCoef_UIs_T_Str.b2_UIs_f32 | -0.0233457759 | -0.023345782 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.25202346 | 3.25202347 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.922647 | -7.92264714 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.82532883 | 4.82532939 ± 0.000009 | · · · · · · · · · · · · · · · · · · · |

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| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.24 (Repeat Count = 1) | | | |
|--|------------------------|--------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.019 | | |
| VehicleSpeed_Kph_T_f32 | 256.05 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 4.3 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00036 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 336 352 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] t2_FDD_FreqTbIYM_Hz_u12p4[0][2] | 368 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2] | 384 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3] t2_FDD_FreqTbIYM_Hz_u12p4[0][4] | 400 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 416 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 432 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 448 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 464 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 480 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 512 | | |
| t2 FDD FreqTblYM Hz u12p4[1][0] | 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 80 | | |
| t2 FDD FreqTblYM Hz u12p4[1][2] | 96 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][3] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 160 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][7] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 240 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 0 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 346 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 358 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1792 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1818 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1843 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1869 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.266277403 | -0.266277387 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.151999995 | 0.152 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.114277415 | 0.114277387 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.55320787 | 2.55320816 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.67659283 | -7.676592803 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 5.7701993 | 5.770199037 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.25 (Repeat Count = 1) | Ironat Walna | | |
|--|------------------------|---------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.02 | | |
| VehicleSpeed_Kph_T_f32 | 272.06 5.1 | | |
| WIRCmdAmpBlnd_MtrNm_T_f32 filtCoef Uls T Str | tgt_filtCoef_Uls_T_Str | | |
| k InrtCmp MtrInertia KgmSq f32 | 0.00037 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 704 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 720 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 736 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 752 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 768 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 784 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9] | 800 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 816 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 832 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 80 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 128 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][4] | 144 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 160 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 3968 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 4096 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 4224 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 4352 4480 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 4608 | | |
| t_CmnVehSpd_Kph_u9p7[5] t_CmnVehSpd_Kph_u9p7[6] | 4736 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 4864 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 4992 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 5120 | | |
| t CmnVehSpd Kph u9p7[10] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[0] | 16384 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 16384 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 16384 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 16384 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 16384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 410 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 435 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 461 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 486 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 512 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0996317267 | -0.099631729 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.159999996 | 0.16 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0603682697 | -0.060368271 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 3.23617816 | 3.23617818 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.91914797 | -7.919148201 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 4.84467363 | 4.844673619 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.26 (Repeat Count = 1) | | | ✓ | |
|--|---------------------------|--------------------------|----------|--|
| Name | Input Value | | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.021 | 0.021 | | |
| VehicleSpeed_Kph_T_f32 | 288.08 | 288.08 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 6.4 | | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00038 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1296 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1312 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1328 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1344 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1360 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1376 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1392 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1408 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1424 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1440 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1456 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1472 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 96 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 112 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 128 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 144 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 160 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 176 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 192 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 208 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 224 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 240 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 256 | | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 272 | | | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | | | |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 | | | |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 | | | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | | | |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 | | | |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 | | | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | | | |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 | | | |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 13952 | | | |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 | | | |
| t_CmnVehSpd_Kph_u9p7[10] | 14208 | | | |
| t_CmnVehSpd_Kph_u9p7[11] | 4915 | | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | | | |
| t DmpFiltKpWlRBlndY Uls u2p14[2] | 8192 | | | |
| | 9830 | | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 | | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 26 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | | |
| | | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 64 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 77 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 90 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 115 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 128 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 141 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 154 | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 166 | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 666 | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 691 | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 717 | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 742 | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 768 | | | |
| Name | Actual Value | Expected Value | Pagult | |
| | | • | Result | |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.211607069 | -0.211607064 ± 0.0000009 | | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.16799998 | 0.168 ± 0.0000009 | * | |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.0436070785 | 0.043607064 ± 0.0000009 | | |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.26093268 | 2.260932845 ± 0.000009 | | |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.50725317 6.23191343 | -7.507253234 ± 0.000009 | | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.23181343 | 6.231813921 ± 0.000009 | | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | - |





| Test Step 1.27 (Repeat Count = 1) | | | · |
|---|------------------------|---------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.022 | | |
| VehicleSpeed_Kph_T_f32 | 304.09 | | |
| WIRCmdAmpBind_MtrNm_T_f32 | 7.1 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00039 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1152 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1168 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1184 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1200 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1216 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1232 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1248 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1264 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1280 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1296 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1312 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 336 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 352 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 368 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 384 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 400 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 416 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 432 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 448 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 464 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 480 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 16384 | | |
| t_CmnVehSpd_Kph_u9p7[7] | | | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 16640 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 16768 | | |
| t_CmnVehSpd_Kph_u9p7[10] | | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 1638 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] t DmpFiltKpWIRBIndY_Uls_u2p14[2] | | | |
| t DmpFiltKpWIRBIndY Uls u2p14[3] | 4915 6554 | | |
| t DmpFiltKpWlRBIndY Uls u2p14[4] | | | |
| | 8192 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[4] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] t InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 0 | | |
| | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 0 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 922 947 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 947 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 973 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] t_WIRBIndTblX_MtrNm_u8p8[4] | 1024 | | |
| | | Even a set of Males | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.0564835407 | -0.056483543 ± 0.00000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.175999999 | 0.176 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.119516462 | -0.119516457 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.64792883 | 1.647929015 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.97387695 | -6.97387697 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.37819529 | 7.378194015 ± 0.000009 | • |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.28 (Repeat Count = 1) | | | ✓ |
|--|------------------------|-------------------------|--------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.023 | | |
| VehicleSpeed_Kph_T_f32 | 320.07 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 8.2 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0004 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 176 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][1] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 272 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 288 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 304 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 320 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 336 352 | | |
| | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 704 | | |
| t2_FDD_FreqTbiYM_Hz_u12p4[1][3] | 704 | | |
| t2 FDD FreqTblYM Hz u12p4[1][5] | 736 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 750 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | | |
| t CmnVehSpd Kph u9p7[8] | 11392 | | |
| t CmnVehSpd Kph u9p7[9] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[0] | 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[2] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[4] | 9830 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 384 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 384 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1178 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1203 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1280 | | |
| Name | Actual Value | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.44143194 | -0.44143189 ± 0.0000009 | ~ |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.184 | 0.184 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.257431924 | 0.25743189 ± 0.0000009 | _ |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.24206972 | 2.242070137 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.49469471 | -7.49469476 ± 0.000009 | _ |
| | | | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.29 (Repeat Count = 1) | | | |
|---|------------------------|--------------------------|-------------------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.024 | | |
| VehicleSpeed_Kph_T_f32 WIRCmdAmpBlnd_MtrNm_T_f32 | 336.06 4.5 | | |
| filtCoef Uls T Str | tgt_filtCoef_UIs_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00041 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 512 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 528 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 544 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 560 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 576 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 592 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 608 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 624 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 640 656 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][11] | 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1296 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1312 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1328 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1344 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1360 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1376 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1392 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1424 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1440 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1456 1472 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] t_DmpFiltKpWIRBIndY_UIs_u2p14[0] | 6656 4915 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[1] | 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | | |
| t_DmpFiltKpWlRBIndY_Uls_u2p14[3] | 9830 | | |
| t_DmpFiltKpWIRBIndY_UIs_u2p14[4] | 11469 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 256 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1434 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1459 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1485 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1510 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1536 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.64859736 | -0.648597291 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.192000002 | 0.192 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.456597328 | 0.456597291 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.64794874 | 1.647948707 ± 0.000009 | · · · · · · · · · |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.97389889 | -6.973898945 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.30 (Repeat Count = 1) | | | ~ |
|---|------------------------|---------------------------|--------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.025 | | |
| VehicleSpeed_Kph_T_f32 | 352.05 | | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 4.9 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00001 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 816 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 832 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 848 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 864 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 880 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 896 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 912 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 928 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 944 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 960 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 976 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 992 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1152 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1168 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1184 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1200 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1216 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1232 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1248 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1264 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1280 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1296 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1312 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 3968 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 4096 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 4224 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 4352 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 4480 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 4608 4736 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 4864 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 4992 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[10] | | | |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 6554 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 | | |
| t_DmpFiltKpWlRBIndY_Uls_u2p14[1] | 9830 | | |
| t DmpFiltKpWIRBIndY Uls u2p14[3] | 11469 | | |
| t_DmpFiltKpWlRBIndY_Uls_u2p14[4] | 13107 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 51 | | |
| | | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[2] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 179 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1690 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1715 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1741 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1792 | | |
| Name | Actual Value | Expected Value | Boor!4 |
| | | Expected Value | Result |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.12834549 | -0.128345472 ± 0.0000009 | |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.200000003 | 0.2 ± 0.0000009 | · · |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | -0.0716545135 | -0.071654528 ± 0.00000009 | ~ |
| | 1.25517929 | 1.255179464 ± 0.000009 | ~ |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -6.45242405 | -6.45242444 ± 0.000009 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.31 (Repeat Count = 1) | | | |
|--|------------------------|--------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.026 | | |
| VehicleSpeed_Kph_T_f32 | 368.01 | | |
| WIRCmdAmpBind_MtrNm_T_f32 | 7.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_UIs_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0005 1392 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1392 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1424 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][2] | 1440 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][3] | 1440 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1472 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1488 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1504 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8] | 1520 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1536 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1552 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1568 | | |
| t2 FDD FreqTblYM Hz u12p4[1][0] | 176 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 208 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][3] | 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 240 | | |
| t2 FDD FreqTbIYM Hz u12p4[1][5] | 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 272 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 288 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 304 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 320 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 336 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 352 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1894 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1920 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1946 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1997 | | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.44634214 | -0.446342077 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.208000004 | 0.208 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | 0.238342136 | 0.238342077 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 1.7996192 | 1.7996192 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.13275242 | -7.132752506 ± 0.000009 | |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 7.06762838 | 7.067628294 ± 0.000009 | |

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| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |





| Test Step 1.32 (Repeat Count = 1) | | | ~ |
|---|------------------------|---------------------------|-------|
| Name | Input Value | | |
| ADDCoef_MtrNmSpRad_T_f32 | 0.027 | | |
| VehicleSpeed_Kph_T_f32 | 384.02 | | |
| WIRCmdAmpBind_MtrNm_T_f32 | 2.5 | | |
| filtCoef_Uls_T_Str | tgt_filtCoef_Uls_T_Str | | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00003 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 512 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 528 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 544 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 560 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 576 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 592 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 608 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 624 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 496 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 512 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 528 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 544 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 560 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 576 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 592 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][7] | 608 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][8] | 624 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][9] | 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][10] | 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][11] | 672 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 11520 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[8] | | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 3277 | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | | |
| t_DmpFiltKpWlRBIndY_Uls_u2p14[1] | | | |
| t_DmpFiltKpWlRBIndY_Uls_u2p14[3] | 6554 8192 | | |
| | | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] t InrtCmp ScaleFactorTblY Uls u9p7[0] | 9830 179 | | |
| | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 230 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 250 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] t InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 269 | | |
| | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 307 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 794 | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 819 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] t_WIRBIndTblX_MtrNm_u8p8[2] | 845 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 870 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 896 | | |
| | | Property of Males | |
| Name | Actual Value | Expected Value | Resul |
| tgt_filtCoef_Uls_T_Str.b0_Uls_f32 | -0.1716436 | -0.171643583 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b1_Uls_f32 | 0.216000006 | 0.216 ± 0.0000009 | • |
| tgt_filtCoef_Uls_T_Str.b2_Uls_f32 | -0.0443564057 | -0.044356417 ± 0.00000009 | • |
| tgt_filtCoef_Uls_T_Str.a0_Uls_f32 | 2.16740918 | 2.167409451 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a1_Uls_f32 | -7.44288063 | -7.442880571 ± 0.000009 | • |
| tgt_filtCoef_Uls_T_Str.a2_Uls_f32 | 6.38971043 | 6.389709978 ± 0.000009 | • |

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| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |

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ADDCoefCalc

| Project | FDD_Inertia |
|-------------|-------------|
| Module | FDD_Inertia |
| Test Object | ADDCoefCalc |

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Branch (C1) Coverage | 100 % |

Statistics

| Total Testcases | 1 |
|-----------------|---|
| Successful | 1 |
| Failed | 0 |
| Not Executed | 0 |



Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp |
|------------------------|---|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\-I\$(PROJECTROOT)\\FrqDepDmpnInrtCmp\utp\contract\-I\$(PROJECTROOT)\\NxtrLib\\nclude -I\$(PROJECTROOT)\\StdDef\\nclude -I\$(PROJECTROOT)\\StdDef\\nclude \rm I\\$(Projectro I)\\\nclude \rm I\\$(Projectro I)\\\\nclude \rm I\\$(Projectro I)\\\\\nclude \rm I\\$(Projectro I)\\\\\nclude \rm I\\$(Projectro I)\\\\\\nclude \rm I\\$(Projectro I)\\\\\\nclude \rm I\\$(Projectro I)\\\\\\\\\nclude \rm I\\$(Projectro I)\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

| Comments | Description/ | Specification |
|----------|--------------|---------------|
| | | |

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| ame | | |
| | | |

Module 'FDD_Inertia'

Name of Tester: Spoorti Mali
Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c
Code File(s) Version: 13
Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc
Module Design Document Version: 18
Data Dictionary Version: 16
Unit Test Plan Version: 6
Optimization Level: Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30
Total FLASH Used (Bytes): 1994
Total RAM Used (Bytes): 60
Total CALS Used (Bytes): 328
Special Test Requirements:
Test Date: 09-19-2014
Comments:

Comments:

Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.

Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference.

Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ivM





| Attributes | |
|-----------------|--|
| Name | Value |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and "WithPS" ${\tt Environment}$)

CPU Cycles:

TS1.1 1409.00 Cycles 1399.00 Cycles 1399.00 Cycles 1430.00 Cycles 1487.00 Cycles 1387.00 Cycles 1432.00 Cycles 1541.00 Cycles 1375.00 Cycles 1386.00 Cycles 1375.00 Cycles 1375.00 Cycles 1387.00 Cycles 1387.00 Cycles 1387.00 Cycles 1556.00 Cycles 1587.00 Cycles TS1.2 TS1.3 TS1.4 TS1.6 TS1.7 TS1.8 TS1.9 TS1.10 TS1.11 TS1.12 TS1.13 TS1.14 TS1.15 TS1.16 TS1.17 TS1.18 1387.00 Cycles 1387.00 Cycles 1419.00 Cycles 1387.00 Cycles 1419.00 Cycles 1419.00 Cycles 1398.00 Cycles 1387.00 Cycles 1387.00 Cycles 1398.00 Cycles 1398.00 Cycles 1398.00 Cycles 1398.00 Cycles TS1.18 TS1.19 TS1.20 TS1.21 TS1.22 TS1.23 TS1.26 TS1.27 TS1.28 TS1.29 1398.00 Cycles 1601.00 Cycles 1419.00 Cycles 1387.00 Cycles 1387.00 Cycles 1387.00 Cycles 1387.00 Cycles 1398.00 Cycles 1398.00 Cycles TS1.30 TS1.31 TS1.32 TS1.33 TS1.34 TS1.35 TS1.36 TS1.37

Description

Test Vector Description

```
TS1 1 All min
```

TS1.2 All max

TS1.3 BaseAssistCmd_MtrNm_T_f32 min TS1.4 BaseAssistCmd_MtrNm_T_f32 max TS1.5 BaseAssistCmd_MtrNm_T_f32 zero

TS1.6 BaseAssistCmd_MtrNm_T_f32 pos

TS1.6 BaseAssistCmd_MtrNm_I_T32 pos
TS1.7 BaseAssistCmd_MtrNm_T_f32 neg
TS1.8 WIRCmdAmpBInd_MtrNm_T_f32 min
TS1.9 WIRCmdAmpBInd_MtrNm_T_f32 max
TS1.10 WIRCmdAmpBInd_MtrNm_T_f32 pos
TS1.11 VehicleSpeed1_Kph_T_f32 min
TS1.12 VehicleSpeed1_Kph_T_f32 max
TS1.13 VehicleSpeed1_Kph_T_f32 pos
TS1.14 t_DmpADDCoefX_MtrNm_u4p12[10] min
TS1.15 t_DmpADDCoefX_MtrNm_u4p12[10] min
TS1.16 t_DmpADDCoefX_MtrNm_u4p12[10] max

TS1.15 t_DmpADDCoefX_MtrNm_u4p12[10] max
TS1.16 t_DmpADDCoefX_MtrNm_u4p12[10] pos
TS1.17 t2_FDD_ADDRollingTblYM1_MtrNmpRadpS_um1p17[10] min
TS1.18 t2_FDD_ADDRollingTblYM1_MtrNmpRadpS_um1p17[10] max
TS1.19 t2_FDD_ADDRollingTblYM1_MtrNmpRadpS_um1p17[10] pos
TS1.20 t2_FDD_ADDRollingTblYM2_MtrNmpRadpS_um1p17[10] min
TS1.21 t2_FDD_ADDRollingTblYM2_MtrNmpRadpS_um1p17[10] max
TS1.22 t2_FDD_ADDRollingTblYM2_MtrNmpRadpS_um1p17[10] mos
TS1.23 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[10] min
TS1.24 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[10] max
TS1.25 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[10] pos
TS1.26 t_WIRBINGTblY_MtrNmpRadpS_um1p17[10] pos

TS1.26 TS1.27

TS1.28

TS1.29 TS1.30

L PUD_ADDState 101 "ullnilipadaps_t t_WIRBIndTbIX_MtrNm_u8p8[5] min t_WIRBIndTbIX_MtrNm_u8p8[5] max t_WIRBIndTbIX_MtrNm_u8p8[5] pos t_RIAstWIRBIndTbIY_UIs_u2p14[5] min t_RIAstWIRBIndTbIY_UIs_u2p14[5] max t_RIAstWIRBIndTbIY_UIs_u2p14[5] pos TS1.31

TS1.32

TS1 33

TS1.34

TS1.35 TS1 36

t_CmnVehSpd_Kph_u9p7[12] min t_CmnVehSpd_Kph_u9p7[12] max t_CmnVehSpd_Kph_u9p7[12] pos t_FDD_BlendTblY_Uls_u8p8[12] min t_FDD_BlendTblY_Uls_u8p8[12] max t_FDD_BlendTblY_Uls_u8p8[12] pos

| Test Step 1.1 (Repeat Count = 1) | | ✓ |
|---|-------------|---|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | -8.8 | |
| VehicleSpeed_Kph_T_f32 | 0 | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 0 | |

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| Name | Input Value | | |
|---|------------------|-----------------------------|--------|
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 0 | | |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][0] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 0 | | |
| | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 0 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 0 | | |
| t CmnVehSpd Kph u9p7[7] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 0 | | |
| t CmnVehSpd Kph u9p7[9] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 0 | | |
| | | | |
| t_CmnVehSpd_Kph_u9p7[11] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 0 | | |
| | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 0 | | |
| | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 0 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 0 | | |
| | 0 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 0 | | |
| | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] t_WIRBIndTbIX_MtrNm_u8p8[2] t_WIRBIndTbIX_MtrNm_u8p8[3] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] t_WIRBIndTbIX_MtrNm_u8p8[2] t_WIRBIndTbIX_MtrNm_u8p8[3] t_WIRBIndTbIX_MtrNm_u8p8[4] | 0 0 0 0 | Expected Value | Result |
| t_WIRBIndTbIX_MtrNm_u8p8[1] t_WIRBIndTbIX_MtrNm_u8p8[2] t_WIRBIndTbIX_MtrNm_u8p8[3] | 0 0 0 | Expected Value 0 ± 0.000009 | Result |



| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.2 (Repeat Count = 1) | |
|--|----------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 8.8 |
| VehicleSpeed_Kph_T_f32 | 511.9921875 |
| WIRCmdAmpBInd_MtrNm_T_f32 t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 8.8 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 6554 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 6554 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 6554 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 6554 |
| t_CmnVehSpd_Kph_u9p7[0] | 32640 |
| t_CmnVehSpd_Kph_u9p7[1] | 32640 |
| t_CmnVehSpd_Kph_u9p7[2] | 32640 |
| t_CmnVehSpd_Kph_u9p7[3] | 32640 |
| t_CmnVehSpd_Kph_u9p7[4] | 32640 |
| t_CmnVehSpd_Kph_u9p7[5] | 32640 |
| t_CmnVehSpd_Kph_u9p7[6] | 32640 |
| t_CmnVehSpd_Kph_u9p7[7] | 32640 |
| t_CmnVehSpd_Kph_u9p7[8] | 32640 |
| t_CmnVehSpd_Kph_u9p7[9] | 32640 |
| t_CmnVehSpd_Kph_u9p7[10] | 32640 |
| t_CmnVehSpd_Kph_u9p7[11] | 32640 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[0] t_DmpADDCoefX_MtrNm_u4p12[1] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 36045 |
| t DmpADDCoefX MtrNm u4p12[4] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 36045 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 6554 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 6554 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[4] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 256 |
| t_FDD_BlendTbIY_Uls_u8p8[6] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[8] | 256 |

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| Name | Input Value | | |
|---------------------------------|--------------|--------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[9] | 256 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 256 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 256 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 16384 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 16384 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 16384 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 16384 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 16384 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 2048 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 2048 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.0500030518 | 0.050003052 + 0.00000009 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.3 (Repeat Count = 1) | |
|---|-------------|
| Name | Input Value |
| | -8.8 |
| BaseAssistCmd_MtrNm_T_f32 | 12.32 |
| VehicleSpeed_Kph_T_f32 WIRCmdAmpBInd_MtrNm_T_f32 | 5.2 |
| | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 494 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661 827 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3409 |
| t_CmnVehSpd_Kph_u9p7[0] | 128 |
| t_CmnVehSpd_Kph_u9p7[1] | 256 |
| t_CmnVehSpd_Kph_u9p7[2] | 384 |
| t_CmnVehSpd_Kph_u9p7[3] | 512 |
| t_CmnVehSpd_Kph_u9p7[4] | 640 |
| t_CmnVehSpd_Kph_u9p7[5] | 768 |
| t_CmnVehSpd_Kph_u9p7[6] | 896 |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 4506 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 4915 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 5325 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 5734 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 6144 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 6554 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 6963 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 7373 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 7782 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 8192 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 |

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| Name | Input Value | | |
|---|--------------|--------------------------|--------|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 3 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 5 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 8 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 10 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 13 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 31 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 282 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 358 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 384 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.0369348824 | 0.036934882 ± 0.00000009 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.4 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 8.8 |
| VehicleSpeed Kph T f32 | 24 |
| WIRCmdAmpBlnd MtrNm T f32 | 6.5 |
| t2 FDD ADDRollingTbIYM MtrNmpRadpS um1p17[0][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3409 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2583 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3099 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159 |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 |



| ΑD | DC | oef | Ca | c |
|----|----|-----|----|---|
| | | | | |

| Lemmerson Lemm | ADDCOCICAIC | | | |
|--|---|--------------|----------------|-------|
| Lemmeshog Koh, Lipo719 14988 15580 15 | Name | Input Value | | |
| Comwhesper Lego, usp67[10] 15580 15680 | t_CmnVehSpd_Kph_u9p7[9] | | | |
| DempADDCoeft, Minkm_ushr2[1] | t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| DimpADDCoefX, Mirkim_usip120 8002 9004 900 | t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| DmpADDCocK_Mirkm_usp12[2] | t DmpADDCoefX MtrNm u4p12[0] | 8602 | | |
| DmpADDCoeK, Mirkm, usp1223 9820 | t_DmpADDCoefX_MtrNm_u4p12[1] | 9011 | | |
| DempADDCoePK_Mirhm_u4p12[4] 10240 10250 | t_DmpADDCoefX_MtrNm_u4p12[2] | 9421 | | |
| DmpADDCoetX, MirNm_u4p128 10850 | t_DmpADDCoefX_MtrNm_u4p12[3] | 9830 | | |
| DmpADDCoeK MirNim_u4p12 6 11059 11469 | t_DmpADDCoefX_MtrNm_u4p12[4] | 10240 | | |
| DmpADDCoeff, MrNm_u4p12[7] | t_DmpADDCoefX_MtrNm_u4p12[5] | 10650 | | |
| DmpADDCoefK MirNm_u4p12[8] | t_DmpADDCoefX_MtrNm_u4p12[6] | 11059 | | |
| DMPADDCoefX_MtnVm_u4p12[9] 12288 | t_DmpADDCoefX_MtrNm_u4p12[7] | 11469 | | |
| FDD_ADDStaticTbIY_MirnPRadpS_um1p17(0) 704 | t_DmpADDCoefX_MtrNm_u4p12[8] | 11878 | | |
| FDD_ADDStaticTbIY_Mfr/mpRadps_um1p17[1] 924 925 | t_DmpADDCoefX_MtrNm_u4p12[9] | 12288 | | |
| FDD_ADDStaticTbrY_MtrNmpRadpS_um1p17[2] 924 | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704 | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814 | | |
| FDD_ADDStaticTbY_MtrNmpRadpS_um1p17[4] | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 924 | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 1475 | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144 | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1254 | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364 | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475 | | |
| FDD_BlendTblY_Uls_u8p8[0] 5 5 5 5 5 5 5 5 | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585 | | |
| FDD_BlendTblY_Uls_u8p8[1] 8 | t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695 | | |
| FDD_BlendTbiY_Uis_u8p8[2] | t FDD BlendTblY Uls u8p8[0] | 5 | | |
| FDD_BlendTblY_Uls_u8p8[3] 13 | t FDD BlendTblY Uls u8p8[1] | 8 | | |
| FDD_BlendTblY_Uls_u8p8[4] 15 | t_FDD_BlendTblY_Uls_u8p8[2] | 10 | | |
| FDD_BlendTblY_Uls_u8p8[5] 18 20 20 20 20 20 20 20 2 | t_FDD_BlendTblY_Uls_u8p8[3] | 13 | | |
| FDD_BlendTbiY_Uls_u8p8[6] 20 | t_FDD_BlendTblY_Uls_u8p8[4] | 15 | | |
| FDD_BlendTblY_Uls_u8p8[7] 23 | t_FDD_BlendTblY_Uls_u8p8[5] | 18 | | |
| FDD_BlendTblY_Uls_u8p8[8] 26 FDD_BlendTblY_Uls_u8p8[9] 28 FDD_BlendTblY_Uls_u8p8[10] 31 FDD_BlendTblY_Uls_u8p8[11] 33 FID_BlendTblY_Uls_u2p14[0] 3277 FILASTWIRBINdTblY_Uls_u2p14[1] 4915 FRIASTWIRBINdTblY_Uls_u2p14[2] 6554 FRIASTWIRBINdTblY_Uls_u2p14[3] 8192 FILASTWIRBINdTblY_Uls_u2p14[4] 9830 WIRBINdTblX_MtrNm_u8p8[0] 538 WIRBINdTblX_MtrNm_u8p8[1] 563 WIRBINdTblX_MtrNm_u8p8[2] 589 WIRBINdTblX_MtrNm_u8p8[3] 614 WIRBINdTblX_MtrNm_u8p8[4] 640 WIRBINDTBLX_MtrNm_u8p8[4] FEXPECTED Value Resident Content of the conten | t_FDD_BlendTblY_Uls_u8p8[6] | 20 | | |
| FDD_BlendTbIY_UIs_u8p8[9] 28 | t_FDD_BlendTblY_Uls_u8p8[7] | 23 | | |
| FDD_BlendTblY_Uls_u8p8[10] 31 33 33 34 35 35 35 35 35 | t_FDD_BlendTblY_Uls_u8p8[8] | 26 | | |
| FDD_BlendTbIY_UIs_u8p8[11] 33 RIAstWIRBIndTbIY_UIs_u2p14[0] 3277 _RIAstWIRBIndTbIY_UIs_u2p14[1] 4915 _RIAStWIRBIndTbIY_UIs_u2p14[2] 6554 _RIAStWIRBIndTbIY_UIs_u2p14[3] 8192 _RIAStWIRBIndTbIY_UIs_u2p14[4] 9830 _WIRBIndTbIX_MtrNm_u8p8[0] 538 _WIRBIndTbIX_MtrNm_u8p8[1] 563 _WIRBIndTbIX_MtrNm_u8p8[2] 589 _WIRBIndTbIX_MtrNm_u8p8[3] 614 _WIRBIndTbIX_MtrNm_u8p8[4] 640 _WIRBIndTbIX_MtrNm_u8p8[4] Result Value Expected Value Result Result Value | t_FDD_BlendTblY_Uls_u8p8[9] | 28 | | |
| RIAstWIRBIndTbIY_UIs_u2p14[0] 3277 RIAstWIRBIndTbIY_UIs_u2p14[1] 4915 RIAStWIRBIndTbIY_UIs_u2p14[2] 6554 RIAStWIRBIndTbIY_UIs_u2p14[3] 8192 RIAStWIRBIndTbIX_MtrNm_u8p8[0] 538 WIRBIndTbIX_MtrNm_u8p8[1] 563 WIRBIndTbIX_MtrNm_u8p8[2] 589 WIRBIndTbIX_MtrNm_u8p8[3] 614 WIRBIndTbIX_MtrNm_u8p8[4] 640 WIRBINDTBIX_MtrNm_u8p8[4] Restence | t_FDD_BlendTblY_Uls_u8p8[10] | 31 | | |
| RIAstWIRBIndTbIY_UIs_u2p14[1] | t_FDD_BlendTblY_Uls_u8p8[11] | 33 | | |
| RiAstWiRBindTbiY_Uis_u2p14[2] 6554 RiAstWiRBindTbiY_Uis_u2p14[3] 8192 RiAstWiRBindTbiY_Uis_u2p14[4] 9830 WiRBindTbiX_MtrNm_u8p8[0] 538 WiRBindTbiX_MtrNm_u8p8[1] 563 WiRBindTbiX_MtrNm_u8p8[2] 589 WiRBindTbiX_MtrNm_u8p8[3] 614 WiRBindTbiX_MtrNm_u8p8[4] 640 WiRBindTbiX_MtrNm_u8p8[4] Result Value Expected Value Result R | t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 3277 | | |
| RiAstWiRBindTbiY_Uis_u2p14[3] 8192 | t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 4915 | | |
| RIAstWIRBIndTbIY_UIs_u2p14[4] 9830 WIRBIndTbIX_MtrNm_u8p8[0] 538 WIRBIndTbIX_MtrNm_u8p8[1] 563 WIRBIndTbIX_MtrNm_u8p8[2] 589 WIRBIndTbIX_MtrNm_u8p8[3] 614 WIRBIndTbIX_MtrNm_u8p8[4] 640 WIRBIndTbIX_MtrNm_u8p8[4] Actual Value Expected Value Result | t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 6554 | | |
| RIAstWIRBIndTbIY_Uls_u2p14[4] 9830 | t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 8192 | | |
| | t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 9830 | | |
| | t_WIRBIndTbIX_MtrNm_u8p8[0] | 538 | | |
| | t_WIRBIndTbIX_MtrNm_u8p8[1] | 563 | | |
| | t_WIRBIndTbIX_MtrNm_u8p8[2] | 589 | | |
| Name Actual Value Expected Value Rest | t_WIRBIndTbIX_MtrNm_u8p8[3] | 614 | | |
| · · | t_WIRBIndTbIX_MtrNm_u8p8[4] | 640 | | |
| | Name | Actual Value | Expected Value | Resul |
| | ADDCoefCalc() | 0.013426058 | • | |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntolVarXY u16 u16Xu16Y Cnt | 5 | _ |

| Test Step 1.5 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 0 |
| VehicleSpeed_Kph_T_f32 | 36.25 |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 7.3 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 523 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1553 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2583 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3099 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3614 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4129 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4644 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5159 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 924 |

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| N | Inner Males | | |
|---|---------------|--------------------------|--------|
| Name | Input Value | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1034 1144 | | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 1254 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1364 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1475 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1585 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1695 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 12698 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 13107 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 13517 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 13926 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 14336 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 14746 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 15155 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 15565 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 15974 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 16384 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 885 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 986 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1087 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591 1692 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1793 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] t FDD BlendTblY Uls u8p8[0] | 10 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 13 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 18 | | |
| t FDD BlendTblY Uls u8p8[4] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 23 | | |
| t FDD BlendTblY Uls u8p8[6] | 26 | | |
| t FDD BlendTblY Uls u8p8[7] | 28 | | |
| t FDD BlendTblY Uls u8p8[8] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 36 | | |
| t FDD BlendTblY Uls u8p8[11] | 38 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 11469 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 794 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[1] | 819 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 845 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[3] | 870 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 896 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00668188976 | 0.00668189 ± 0.000000009 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntplVarXY u16 u16Xu16Y Cnt | 5 | ~ |

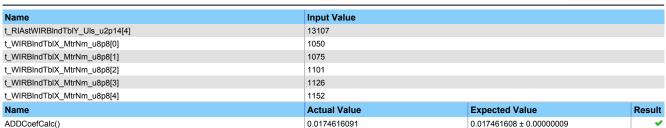




| Test Step 1.6 (Repeat Count = 1) | · · · · · · · · · · · · · · · · · · · |
|---|---------------------------------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 5.25 |
| VehicleSpeed_Kph_T_f32 | 48.12 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 8.1 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 924 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1144 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1364 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1475 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1695 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 885 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 986 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1087 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1188 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1288 1389 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1490 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1591 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1692 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1793 |
| t CmnVehSpd Kph u9p7[0] | 128 |
| t CmnVehSpd Kph u9p7[1] | 256 |
| t_CmnVehSpd_Kph_u9p7[2] | 384 |
| t_CmnVehSpd_Kph_u9p7[3] | 512 |
| t_CmnVehSpd_Kph_u9p7[4] | 640 |
| t_CmnVehSpd_Kph_u9p7[5] | 768 |
| t_CmnVehSpd_Kph_u9p7[6] | 896 |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 16794 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 17203 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 17613 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 18022 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 18432 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 18842 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 19251 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 19661 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 20070 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 20480 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1066 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1212 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1359 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1506 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1653 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1800 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1946 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2093 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 2240 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 2387 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 13 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 15 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 18 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 20 |
| t_FDD_BlendTblY_Uls_u8p8[4] | 23 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 26 |
| t_FDD_BlendTblY_Uls_u8p8[6] | 28 31 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 31 33 |
| t_FDD_BlendTblY_Uls_u8p8[8] | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 36 38 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 41 |
| t FDD RiandThiV Lile u8p8[11] | 71 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 6554 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 6554 8192 |
| | 6554 8192 9830 |

ADDCoefCalc





| Τ | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Input Value |
|-------------|
| |
| -5.45 |
| 60 |
| 5.2 |
| 885 |
| 986 |
| 1087 |
| 1188 |
| 1288 |
| 1389 |
| 1490 |
| 1591 |
| 1692 |
| 1793 |
| 1066 |
| 1212 |
| 1359 |
| 1506 |
| 1653 |
| 1800 |
| 1946 |
| 2093 |
| 2240 |
| 2387 |
| 2560 |
| 3840 |
| 5120 |
| 6400 |
| 7680 |
| 8960 |
| 10240 |
| 11520 |
| 12800 |
| 14080 |
| 15360 |
| 16640 |
| 20890 |
| 21299 |
| 21709 |
| 22118 |
| 22528 |
| 22938 |
| 23347 |
| 23757 |
| 24166 |
| 24576 |
| 1246 |
| 1638 |
| 2030 |
| 2422 |
| 2814 |
| 3206 |
| 3598 |
| 3990 |
| 4382 |
| 4774 |
| |





| N | Instruct Wales | | |
|---------------------------------|----------------|---------------------------|--------|
| Name | Input Value | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 38 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 41 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 44 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 9830 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 11469 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 14746 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1306 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1331 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1357 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1382 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1408 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.0190629773 | 0.0190629773 ± 0.00000009 | - |

| Τ | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.8 (Repeat Count = 1) | | ✓ |
|---|-------------|----------|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | 1.1 | |
| VehicleSpeed Kph T f32 | 72.35 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1066 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1212 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1359 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1506 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1653 | |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][5] | 1800 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1946 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2093 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 2240 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 2387 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1246 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1638 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2030 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2422 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2814 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3206 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3598 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3990 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4382 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4774 | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 | |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 | |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 | |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 | |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 | |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 | |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 25395 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 25805 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |

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| ADDCoefCalc | 2014-09-19, 10.20.53+0530 | Razoncat |
|---|---------------------------|----------|
| Name | Input Value | |
| t DmpADDCoefX MtrNm u4p12[4] | 26624 | |
| t DmpADDCoefX MtrNm u4p12[5] | 27034 | |
| t DmpADDCoefX MtrNm u4p12[6] | 27443 | |
| t DmpADDCoefX MtrNm u4p12[7] | 27853 | |
| t DmpADDCoefX MtrNm u4p12[8] | 28262 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 28672 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1427 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1655 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1884 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2112 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2340 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2568 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2796 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3024 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3252 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3480 | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 18 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 20 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 23 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 26 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 28 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 31 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 33 | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 36 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 38 | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 41 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 44 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 46 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 1638 | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 3277 | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 4915 | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 6554 | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 8192 | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1562 | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1587 | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1613 | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1638 | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1664 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

Actual Value

0.0107031446

Expected Value

0.010703144 ± 0.00000009

| Test Step 1.9 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.2 |
| VehicleSpeed_Kph_T_f32 | 84 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 8.8 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2030 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2422 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3206 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4774 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1427 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1655 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1884 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2112 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2340 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2568 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2796 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3252 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3480 |

Name

ADDCoefCalc()

Result





| Name | Input Value | | |
|---|--------------|--------------------------|-------|
| t_CmnVehSpd_Kph_u9p7[0] | 15488 | | |
| t CmnVehSpd Kph u9p7[1] | 15616 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 | | |
| t CmnVehSpd Kph u9p7[4] | 16000 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 | | |
| t CmnVehSpd Kph u9p7[10] | 16768 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 28262 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 28672 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 29082 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 29491 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 29901 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 30310 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 30720 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 31130 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 31539 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 31949 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1608 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 2032 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2455 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2878 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3302 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3725 | | |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 4148 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4572 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4995 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5419 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 26 | | |
| | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 38 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 41 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 44 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 46 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 49 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1792 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1818 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1843 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1869 | 1 | |
| Name | Actual Value | Expected Value | Resul |
| ADDCoefCalc() | 0.0121170254 | 0.012117026 ± 0.00000009 | - I |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.10 (Repeat Count = 1) | ranga da arang da ar |
|---|--|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.3 |
| VehicleSpeed_Kph_T_f32 | 96.14 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 4.25 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112 |

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| 710000000000 | | (11111110110 |
|---|------------------------|------------------|
| Name | Input Value | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2340 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2568 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2796 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3024 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3252 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3480 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3302 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3725 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 4148 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4572 4995 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5419 | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 25395 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 25805 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 26624 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 27034 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 27443 | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 27853 | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 28262 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 28672 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1789 2130 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2471 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2811 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3152 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3493 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3834 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4175 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4515 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4856 | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 49 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 51 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 54 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 57 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 60 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 63 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 66 | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 68 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 71 | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 74 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 77 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 80 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 4915 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 6554 | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 8192 | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 9830 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 11469 | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 410 | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 435 | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 461 | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 486 512 | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | | d Volus |
| Name ADDCoofColo() | Actual Value Expecte | |
| ADDCoefCalc() | 0.0130879935 0.0130879 | 993 ± 0.00000009 |





| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | - |

| Test Step 1.11 (Repeat Count = 1) | |
|--|----------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.4 |
| VehicleSpeed_Kph_T_f32 WIRCmdAmpBInd_MtrNm_T_f32 | 1.1 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1608 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2032 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2878 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3302 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3725 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 4148 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4572 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4995 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5419 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1789 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2130 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2471 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2811 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3152 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3834 4175 |
| tz_FDD_ADDROIIIngTbIYM_MtrNmpRadpS_umTp17[1][7] t2_FDD_ADDRoIIingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4175 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 4856 |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 |
| t CmnVehSpd Kph u9p7[1] | 5376 |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 28262 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 28672 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 29082 |
| t_DmpADDCoefX_MtrNm_u4p12[3] t DmpADDCoefX_MtrNm_u4p12[4] | 29491 29901 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 30310 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 30720 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 31130 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 31539 |
| t DmpADDCoefX MtrNm u4p12[9] | 31949 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 494 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 65 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 68 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 70 73 |
| t_FDD_BlendTbIY_Uls_u8p8[3] t_FDD_BlendTbIY_Uls_u8p8[4] | 75 |
| t_FDD_BlendTbIY_Uls_u8p8[5] | 78 |
| | 80 |
| t FDD BlendTblY Uls u8p8[6] | |
| t_FDD_BlendTblY_Uls_u8p8[6] t_FDD_BlendTblY_Uls_u8p8[7] | 83 |

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| Name | Input Value | | |
|---------------------------------|---------------|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[9] | 88 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 91 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 93 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 11469 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 13107 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 666 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 691 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 717 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 742 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 768 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00417164806 | 0.004171648 ± 0.000000009 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.12 (Repeat Count = 1) | |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.5 |
| VehicleSpeed_Kph_T_f32 | 511.9921875 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.2 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1789 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2130 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2471 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2811 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3152 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3834 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4175 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4515 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4856 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3302 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3725 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 4148 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4572 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4995 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5419 |
| t_CmnVehSpd_Kph_u9p7[0] | 3968 |
| t_CmnVehSpd_Kph_u9p7[1] | 4096 |
| t_CmnVehSpd_Kph_u9p7[2] | 4224 |
| t_CmnVehSpd_Kph_u9p7[3] | 4352 |
| t_CmnVehSpd_Kph_u9p7[4] | 4480 |
| t_CmnVehSpd_Kph_u9p7[5] | 4608 |
| t_CmnVehSpd_Kph_u9p7[6] | 4736 |
| t_CmnVehSpd_Kph_u9p7[7] | 4864 |
| t_CmnVehSpd_Kph_u9p7[8] | 4992 |
| t_CmnVehSpd_Kph_u9p7[9] | 5120 |
| t_CmnVehSpd_Kph_u9p7[10] | 5248 |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 4506 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 4915 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 5325 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 5734 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 6144 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 6554 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 6963 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 7373 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 7782 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 8192 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 342 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1024 |

ADDCoefCalc

Name

ADDCoefCalc()

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Result

| ABBOOCIOUIO | | (-4 - 10-10 |
|---|-------------|--------------|
| Name | Input Value | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409 | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 93 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 96 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 99 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 101 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 104 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 106 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 109 | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 111 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 114 | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 116 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 119 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 122 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 8192 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 9830 | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 11469 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 13107 | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 14746 | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 922 | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 947 | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 973 | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 998 | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1024 | |
| | | |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

Actual Value

0.0185419321

Expected Value

0.018541932 ± 0.00000009

| Test Step 1.13 (Repeat Count = 1) | → |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.6 |
| VehicleSpeed Kph T f32 | 100.21 |
| WIRCmdAmpBlnd MtrNm T f32 | 1.3 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][0] | 1608 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2032 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2878 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3302 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3725 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 4148 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4572 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4995 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5419 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1789 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2130 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2471 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2811 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3152 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3834 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4175 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4515 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4856 |
| t_CmnVehSpd_Kph_u9p7[0] | 128 |
| t_CmnVehSpd_Kph_u9p7[1] | 256 |
| t_CmnVehSpd_Kph_u9p7[2] | 384 |
| t_CmnVehSpd_Kph_u9p7[3] | 512 |
| t_CmnVehSpd_Kph_u9p7[4] | 640 |
| t_CmnVehSpd_Kph_u9p7[5] | 768 |
| t_CmnVehSpd_Kph_u9p7[6] | 896 |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 |

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| ADDCOelCaic | | | 1010 |
|---|---------------|---------------------------|--------|
| Name | Input Value | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 8602 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 9011 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 9421 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 9830 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 10240 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 10650 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 11059 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 11469 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 11878 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 12288 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | | |
| t FDD ADDStaticTblY MtrNmpRadpS um1p17[9] | 5159 | | |
| t FDD BlendTblY Uls u8p8[0] | 116 | | |
| t FDD BlendTblY Uls u8p8[1] | 118 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 121 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 123 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 126 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 129 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 131 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 134 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 136 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 139 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 141 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 144 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1178 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1203 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1280 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00872414559 | 0.008724146 ± 0.000000009 | - |
| <u> </u> | | | |

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|-----------------------------|-------|-----------------------------|-------|------|
| Actual Function | Count | Expected Function | Count | Resu |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | |

| Test Step 1.14 (Repeat Count = 1) | v v v v v v v v v v v v v v v v v v v |
|---|---------------------------------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.7 |
| VehicleSpeed_Kph_T_f32 | 108 |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 1.4 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1789 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2130 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2471 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2811 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3152 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3834 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4175 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4515 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4856 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 494 |





| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[3] 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[4] 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 1150 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 1150 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[7] 1326 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[8] 1493 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[8] 1493 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[9] 1659 1_CmnVehSpd_Kph_u9p7(0] 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[9] 1650 1_CmnVehSpd_Kph_u9p7(1] 1_CmnVehSpd_Kph_u9p7(1) 1_CmnVehSpd_Kph_u9p7(2) 1_CmnVehSpd_Kph_u9p7(3) 1_CmnVehSpd_Kph_u9p7(3) 1_CmnVehSpd_Kph_u9p7(6) 1_CmnVehSpd_Kph_u9p7(6) 1_CmnVehSpd_Kph_u9p7(7) 1_DupndpDCoetX_MtrNm_u4p12(7) 1_DmpADDCoetX_MtrNm_u4p12(7) 1_DmpADDCoetX_MtrNm_u4p12(7) 1_DmpADDCoetX_MtrNm_u4p12(8) 1_DmpADDC | 0 6 6 3 9 0 0 0 0 0 0 0 40 22 00 80 60 40 |
|--|--|
| t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][4] 827 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][5] 994 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][6] 1160 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][7] 1326 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][8] 1493 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][9] 1659 t_CmnVehSpd_Kph_u9p7[0] 2560 t_CmnVehSpd_Kph_u9p7[1] 3840 t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[4] 7680 t_CmnVehSpd_Kph_u9p7[5] 8960 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[7] 11526 t_CmnVehSpd_Kph_u9p7[8] 12806 t_CmnVehSpd_Kph_u9p7[9] 14086 t_CmnVehSpd_Kph_u9p7[1] 15366 t_CmnVehSpd_Kph_u9p7[1] 16646 t_DmpADDCoetX_MtrNm_u4p12[1] 0 t_DmpADDCoetX_MtrNm_u4p12[1] 0 t_DmpADDCoetX_MtrNm_u4p12[2] 0 t_DmpADDCoetX_MtrNm_u4p12[6] 0 t_DmpADDCoetX_MtrNm_u4p12[9] 0 | 0 6 6 3 9 0 0 0 0 0 0 0 40 22 00 80 60 40 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] 994 t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] 1180 t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] 1326 t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 1483 t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 1659 t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 1659 t_CmnVehSpd_Kph_u9p7[1] 3840 t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[8] 12806 t_CmnVehSpd_Kph_u9p7[8] 14986 t_CmnVehSpd_Kph_u9p7[1] 15366 t_CmnVehSpd_Kph_u9p7[1] 16646 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 </td <td>0 6 6 3 9 9 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</td> | 0 6 6 3 9 9 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
| Italian | 0 6 3 9 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 1326 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 1493 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 1659 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2560 t_CmnVehSpd_Kph_u9p7[1] 3840 t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[4] 7680 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 15360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_Mt | 6 3 9 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 1493 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 1659 t_CmnVehSpd_Kph_u9p7[0] 2560 t_CmnVehSpd_Kph_u9p7[1] 3840 t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[6] 10246 t_CmnVehSpd_Kph_u9p7[7] 11526 t_CmnVehSpd_Kph_u9p7[8] 12806 t_CmnVehSpd_Kph_u9p7[9] 14086 t_CmnVehSpd_Kph_u9p7[10] 15366 t_CmnVehSpd_Kph_u9p7[11] 16646 t_CmnVehSpd_Kph_u9p7[11] 16646 t_CmnVehSpd_Kph_u9p7[11] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 | 3 9 0 0 0 0 0 40 20 00 80 60 44 4 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 1659 t_CmnVehSpd_Kph_u9p7[0] 2560 t_CmnVehSpd_Kph_u9p7[1] 3840 t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[6] 7680 t_CmnVehSpd_Kph_u9p7[6] 1024 t_CmnVehSpd_Kph_u9p7[6] 1024 t_CmnVehSpd_Kph_u9p7[8] 1280 t_CmnVehSpd_Kph_u9p7[9] 1408 t_CmnVehSpd_Kph_u9p7[10] 1536 t_CmnVehSpd_Kph_u9p7[11] 1664 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 | 9 0 0 0 0 0 0 40 20 00 80 60 40 |
| L_CmnVehSpd_Kph_u9p7[0] | 0 0 0 0 0 0 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[1] 3840 t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[6] 7680 t_CmnVehSpd_Kph_u9p7[6] 10240 t_CmnVehSpd_Kph_u9p7[7] 11520 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 13360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_DmpADDCoetX_MtrNm_u4p12[0] 0 t_DmpADDCoetX_MtrNm_u4p12[1] 0 t_DmpADDCoetX_MtrNm_u4p12[3] 0 t_DmpADDCoetX_MtrNm_u4p12[3] 0 t_DmpADDCoetX_MtrNm_u4p12[5] 0 t_DmpADDCoetX_MtrNm_u4p12[6] 0 t_DmpADDCoetX_MtrNm_u4p12[7] 0 t_DmpADDCoetX_MtrNm_u4p12[8] 0 t_DmpADDCoetX_MtrNm_u4p12[9] 0 t_EDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 0 0 0 0 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[2] 5120 t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[4] 7680 t_CmnVehSpd_Kph_u9p7[5] 8960 t_CmnVehSpd_Kph_u9p7[6] 1024 t_CmnVehSpd_Kph_u9p7[7] 1152 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[10] 15360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_CmnVehSpd_Kph_u9p7[11] 0 t_CmnVehSpd_Kph_u9p7[11] 0 t_CmnVehSpd_Kph_u9p7[11] 0 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1384 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 | 0 0 0 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[3] 6400 t_CmnVehSpd_Kph_u9p7[4] 7680 t_CmnVehSpd_Kph_u9p7[6] 10240 t_CmnVehSpd_Kph_u9p7[6] 10240 t_CmnVehSpd_Kph_u9p7[7] 11520 t_CmnVehSpd_Kph_u9p7[7] 11520 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 15360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_DmpADDCoefX_MtrNm_u4p12[0] 0 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_BlendTblY_Uis_u8p8[0] 144 t_FDD_BlendTblY_Uis_u8p8[0] 144 t_FDD_BlendTblY_Uis_u8p8[0] 144 t_FDD_BlendTblY_Uis_u8p8[0] 149 t_FDD_BlendTblY_Uis_u8p8[0] 149 t_FDD_BlendTblY_Uis_u8p8[0] 159 t_FDD_BlendTblY_Uis_u8p8[0] 159 t_FDD_BlendTblY_Uis_u8p8[6] 159 t_FDD_BlendTblY_Uis_u8p8[6] 159 t_FDD_BlendTblY_Uis_u8p8[6] 159 t_FDD_BlendTblY_Uis_u8p8[6] 159 t_FDD_BlendTblY_Uis_u8p8[6] 159 | 0 0 0 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[4] 7680 t_CmnVehSpd_Kph_u9p7[5] 8960 t_CmnVehSpd_Kph_u9p7[6] 10240 t_CmnVehSpd_Kph_u9p7[7] 11520 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 15360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_PDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_M | 0 0 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[5] | 0 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[6] 10240 t_CmnVehSpd_Kph_u9p7[7] 11520 t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 15360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_CmnVehSpd_Kph_u9p7[11] 16640 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_UIs_u8p8[1] 146 t_FDD_BlendTblY_UIs_u8p8[2] 149 t_FDD_BlendTblY_UIs_u8p8[3] 152 t_FDD_BlendTblY_UIs_u8p8[3] 152 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[8] 164 | 40 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[7] | 20 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[8] 12800 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 15360 t_CmnVehSpd_Kph_u9p7[11] 16640 t_DmpADDCoefX_MtrNm_u4p12[0] 0 t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[8] 164 | 00 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[9] | 80 60 40 |
| t_CmnVehSpd_Kph_u9p7[10] | 60 40 44 4 4 |
| t_CmnVehSpd_Kph_u9p7[11] | 4 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[0] t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[8] | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[1] 0 t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[8] 164 | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[2] 0 t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[8] 164 | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[3] 0 t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[6] </td <td>4 4 4</td> | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[4] 0 t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_UIs_u8p8[0] 144 t_FDD_BlendTblY_UIs_u8p8[1] 146 t_FDD_BlendTblY_UIs_u8p8[3] 152 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 169 t_FDD_BlendTblY_UIs_u8p8[6]< | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[5] 0 t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1375 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1575 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[6] 162 t_FDD_BlendTbIY_UIs_u8p8[8] | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[6] 0 t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1254 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[4] 154 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[6] 162 t_FDD_BlendTbIY_UIs_u8p8[6] 162 t_FDD_BlendTbIY_UIs_u8p8[8 | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[7] 0 t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[8] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[8] 0 t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[5] 157 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[7] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | 4 4 4 |
| t_DmpADDCoefX_MtrNm_u4p12[9] 0 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_UIs_u8p8[0] 144 t_FDD_BlendTblY_UIs_u8p8[1] 146 t_FDD_BlendTblY_UIs_u8p8[2] 149 t_FDD_BlendTblY_UIs_u8p8[3] 152 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 162 t_FDD_BlendTblY_UIs_u8p8[6] 164 t_FDD_BlendTblY_UIs_u8p8[9] 167 | 4 4 4 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 704 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_UIs_u8p8[0] 144 t_FDD_BlendTblY_UIs_u8p8[1] 146 t_FDD_BlendTblY_UIs_u8p8[2] 149 t_FDD_BlendTblY_UIs_u8p8[3] 152 t_FDD_BlendTblY_UIs_u8p8[5] 157 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[8] 164 t_FDD_BlendTblY_UIs_u8p8[9] 167 | 4 4 4 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] 814 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_UIs_u8p8[1] 144 t_FDD_BlendTblY_UIs_u8p8[2] 149 t_FDD_BlendTblY_UIs_u8p8[2] 149 t_FDD_BlendTblY_UIs_u8p8[3] 152 t_FDD_BlendTblY_UIs_u8p8[4] 154 t_FDD_BlendTblY_UIs_u8p8[6] 159 t_FDD_BlendTblY_UIs_u8p8[7] 162 t_FDD_BlendTblY_UIs_u8p8[8] 164 t_FDD_BlendTblY_UIs_u8p8[9] 167 | 4 4 4 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] 924 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[6] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | 4 4 4 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] 1034 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[6] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | 4 4 4 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 1144 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[6] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | 4 4 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] 1254 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[4] 154 t_FDD_BlendTbIY_UIs_u8p8[5] 157 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[7] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | 4 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 1364 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[4] 154 t_FDD_BlendTbIY_UIs_u8p8[5] 157 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[7] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] 1475 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[4] 154 t_FDD_BlendTbIY_UIs_u8p8[5] 157 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[7] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | 4 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 1585 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[4] 154 t_FDD_BlendTbIY_UIs_u8p8[5] 157 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[7] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 1695 t_FDD_BlendTbIY_UIs_u8p8[0] 144 t_FDD_BlendTbIY_UIs_u8p8[1] 146 t_FDD_BlendTbIY_UIs_u8p8[2] 149 t_FDD_BlendTbIY_UIs_u8p8[3] 152 t_FDD_BlendTbIY_UIs_u8p8[4] 154 t_FDD_BlendTbIY_UIs_u8p8[5] 157 t_FDD_BlendTbIY_UIs_u8p8[6] 159 t_FDD_BlendTbIY_UIs_u8p8[7] 162 t_FDD_BlendTbIY_UIs_u8p8[8] 164 t_FDD_BlendTbIY_UIs_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| t_FDD_BlendTblY_Uls_u8p8[9] 167 | |
| | |
| | |
| t_FDD_BlendTblY_Uls_u8p8[10] 169 | |
| t_FDD_BlendTblY_Uls_u8p8[11] 172 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] 3277 | 7 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] 4915 | 5 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] 6554 | 4 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] 8192 | 2 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] 9830 | 0 |
| t_WIRBIndTblX_MtrNm_u8p8[0] 1434 | 4 |
| t_WIRBIndTblX_MtrNm_u8p8[1] 1459 | 9 |
| t_WIRBIndTblX_MtrNm_u8p8[2] 1485 | |
| t_WIRBIndTblX_MtrNm_u8p8[3] 1510 | 5 |
| t_WIRBIndTblX_MtrNm_u8p8[4] 1536 | |
| | 0 |
| ADDCoefCalc() 0.025 | 0 |

| Τ | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntplVarXY u16 u16Xu16Y Cnt | 5 | |





| Test Step 1.15 (Repeat Count = 1) | √ |
|---|--------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.8 |
| VehicleSpeed_Kph_T_f32 | 120.14 |
| WIRCmdAmpBind_MtrNm_T_f32 | 1.5 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 661 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1659 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1705 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 |
| t2 FDD ADDRollingTbIYM MtrNmpRadpS um1p17[1][7] | 2728 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409 |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 |
| t_CmnVehSpd_Kph_u9p7[9] t_CmnVehSpd_Kph_u9p7[10] | 7936 8064 |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 36045 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 36045 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 885 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 986 1087 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1793 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 172 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 174 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 176 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 178 |
| t_FDD_BlendTblY_Uls_u8p8[4] | 180 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 183 185 |
| t_FDD_BlendTblY_Uls_u8p8[6] t_FDD_BlendTblY_Uls_u8p8[7] | 185 |
| t_FDD_BlendTblY_Uls_u8p8[8] | 189 |
| t_FDD_BlendTblY_Uls_u8p8[9] | 191 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 193 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 195 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 4915 |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 6554 |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 8192 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 9830 |
| | |

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| Name | Input Value |
|---------------------------------|-------------|
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 11469 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1690 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | |

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| Name | Input Value | | |
|---------------------------------|---------------|---------------------------|----------|
| t FDD BlendTblY Uls u8p8[0] | 218 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 220 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 223 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 225 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 227 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 230 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 232 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 234 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 237 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 239 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 241 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 243 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 11469 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 13107 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1894 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1920 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1946 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1997 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00236540218 | 0.002365402 ± 0.000000009 | ✓ |

| Τ | | | V | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.17 (Repeat Count = 1) | | V |
|---|-------------|---|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | 2 | |
| VehicleSpeed_Kph_T_f32 | 144.25 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 1.7 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 161 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 328 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 494 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 661 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 827 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 994 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1160 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1326 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1493 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1659 | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |

ADDCoefCalc

Name

ADDCoefCalc()

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| 7.22 000,04.0 | |
|---|-------------|
| Name | Input Value |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 6144 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 6554 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 6963 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 7373 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 7782 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 8192 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1246 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1638 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2030 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2422 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2814 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3206 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3598 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3990 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4382 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4774 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 3 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 5 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 8 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 10 |
| t_FDD_BlendTblY_Uls_u8p8[4] | 13 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 15 |
| t_FDD_BlendTblY_Uls_u8p8[6] | 18 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 20 |
| t_FDD_BlendTblY_Uls_u8p8[8] | 23 |
| t_FDD_BlendTblY_Uls_u8p8[9] | 26 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 28 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 31 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 8192 |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 9830 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 11469 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 13107 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 14746 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 922 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 947 |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 973 |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 998 |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1024 |
| | |

| T . | | | ✓ | |
|-----------------------------|-------|-----------------------------|----------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

Actual Value

0.0327785164

Expected Value

0.032778516 ± 0.00000009

| Test Step 1.18 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | -1 |
| VehicleSpeed_Kph_T_f32 | 156.12 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.8 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3409 |

Result





| Name | Input Value | | |
|---|---------------|----------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | | |
| t CmnVehSpd Kph u9p7[4] | 13312 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 | | |
| t CmnVehSpd Kph u9p7[8] | 13824 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 8602 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 9011 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 9421 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 9830 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 10240 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 10650 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 11059 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 11469 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] t DmpADDCoefX MtrNm_u4p12[8] | | | |
| | 11878 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 12288 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 342 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1024 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 5 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 8 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 10 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 13 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 18 | | |
| t_FDD_BlendTbIY_Uls_u8p8[6] | 20 | | |
| t_FDD_BlendTbIY_Uls_u8p8[7] | 23 | | |
| t_FDD_BlendTbIY_Uls_u8p8[8] | 26 | | |
| t_FDD_BlendTbIY_Uls_u8p8[9] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 33 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1178 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1203 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1280 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00810782239 | 0.008107823 ± 0.000000009 | Nesuit |
| / LDD OOG O COLON | 0.00010702233 | 0.000 101 020 ± 0.00000000 | |

| T . | | | ✓ | |
|-----------------------------|-------|-----------------------------|----------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.19 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | -2 |
| VehicleSpeed_Kph_T_f32 | 168 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 1.9 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112 |

ADDCoefCalc



| Name | Input Value | | |
|---|---------------|---------------------------|--------|
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2340 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2568 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2796 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3024 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3252 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2583 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3099 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4129 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 | | |
| t CmnVehSpd Kph u9p7[3] | 15872 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 | | |
| t CmnVehSpd Kph u9p7[5] | 16128 | | |
| | 16256 | | |
| t_CmnVehSpd_Kph_u9p7[6] | | | |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 12698 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 13107 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 13517 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 13926 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 14336 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 14746 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 15155 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 15565 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 15974 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 16384 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 10 | | |
| t FDD BlendTblY Uls u8p8[1] | 13 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 26 | | |
| t FDD BlendTblY Uls u8p8[7] | | | |
| | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 38 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 9830 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1434 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1459 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1485 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1510 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1536 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00480917655 | 0.004809176 ± 0.000000009 | ~ |
| | | | |



| T | | | | ~ |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntplVarXY u16 u16Xu16Y Cnt | 5 | ~ |

| Test Step 1.20 (Repeat Count = 1) | | |
|---|-------------|--|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | -3 | |
| VehicleSpeed_Kph_T_f32 | 180.21 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 2 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 0 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 0 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 0 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 0 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 0 | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 16794 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 17203 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 17613 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 18022 | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 18432 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 18842 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 19251 | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 19661 | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 20070 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 20480 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 924 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1254 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695 | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 13 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 15 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 18 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 20 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 23 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 26 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 28 | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 31 | |
| 22_2.0a rbi r_0io_dopo[r] | 101 | |

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 Name
 Input Value

 t_FDD_BlendTblY_Uls_u8p8[9]
 36

 t_FDD_BlendTblY_Uls_u8p8[10]
 38

 t_FDD_BlendTblY_Uls_u8p8[11]
 41

 t_RIAstWIRBIndTblY_Uls_u2p14[0]
 4915

| ADDCoefCalc() | 0.00464858953 | 0.00464859 ± 0.000000009 | ~ |
|---------------------------------|---------------|--------------------------|--------|
| Name | Actual Value | Expected Value | Result |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1792 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1741 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1715 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1690 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 11469 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 4915 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 41 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 38 | | |
| | 30 | | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.21 (Repeat Count = 1) | |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | -4 |
| VehicleSpeed_Kph_T_f32 | 192 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 2.1 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 342 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 3409 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 6554 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 6554 |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 20890 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 21299 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 21709 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 22118 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 22528 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 22938 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 23347 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 23757 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 24166 |
| t DmpADDCoefX MtrNm u4p12[9] | 24576 |
| t FDD ADDStaticTblY MtrNmpRadpS um1p17[0] | 885 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 986 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1087 |
| , , , , , , , , , , , , , , , , , | |

ADDCoefCalc

ADDCoefCalc()

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| | | • | |
|---|--------------|---|--------|
| Name | Input Value | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1793 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 38 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 41 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 44 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 11469 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 13107 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1894 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1920 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1946 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1997 | | |
| Name | Actual Value | Expected Value | Result |

| T | | | V | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | - |

0.00929849967

0.0092985 ± 0.000000009

| Input Value BaseAssitCmd_MtrNm_T_f32 | - 101 100 ID 10 10 IV | |
|--|---|---------------------------------------|
| BaseAssistCmd_MtrNm_T_i32 | Test Step 1.22 (Repeat Count = 1) | · · · · · · · · · · · · · · · · · · · |
| VehicleSpeed_Kph_T_62 204 WIRCmdAmpBlind_Minn_T_f32 2.2 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[1] 523 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[2] 1553 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[4] 268 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[4] 2583 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[6] 3099 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[7] 4129 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[8] 4644 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[8] 4644 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(0)[8] 4644 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 4644 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 4644 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 464 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 2032 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 2032 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 2876 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 3725 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 4572 12_FDD_ADDRollingTbYM_MirkmpRadpS_um1p17(1)[8] 4572 < | | |
| VIRCmdAmpElind_MirNm_T_f32 | BaseAssistCmd_MtrNm_T_f32 | -5 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 523 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 1038 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 153 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 153 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 153 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 153 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 154 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 154 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 154 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 154 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 155 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 155 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 156 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1) 154 155 156 156 156 156 156 156 156 156 156 156 156 156 156 156 156 156 156 156 156 | VehicleSpeed_Kph_T_f32 | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | WIRCmdAmpBInd_MtrNm_T_f32 | 2.2 |
| 150 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | |
| 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][3] 2068 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][4] 2583 2590_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][5] 3099 2590_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][5] 3614 2590_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][7] 4129 4 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1038 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 2583 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3099 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3614 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4129 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1553 |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] 3099 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][6] 3614 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] 4129 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] 4644 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] 4644 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] 2032 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 5419 1_Cmn/vehSpd_Kph_u9p7[0] 4096 1_Cmn/vehSpd_Kph_u9p7[1] 4096 1_Cmn/vehSpd_Kph_u9p7[2] 4224 1_Cmn/vehSpd_Kph_u9p7[3] 4352 1_Cmn/vehSpd_Kph_u9p7[6] 4608 1_Cmn/vehSpd_Kph_u9p7[6] 4736 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[6] 4736 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[6] 4736 1_Cmn/vehSpd_Kph_u9p7[7] 4864 1_Cmn/vehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2068 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3614 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4129 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4644 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5159 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 1_CmnVehSpd_Kph_u9p7[0] 3968 1_CmnVehSpd_Kph_u9p7[2] 4224 1_CmnVehSpd_Kph_u9p7[3] 4352 1_CmnVehSpd_Kph_u9p7[6] 4608 1_CmnVehSpd_Kph_u9p7[6] 4608 1_CmnVehSpd_Kph_u9p7[6] 4608 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnVehSpd_Kph_u9p7[7] 48 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2583 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3099 |
| 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[0][8] 4644 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][0] 5159 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][6] 3302 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17[1][8] 4995 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3614 |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][9] 5159 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 4952 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][9] 5419 1_CmnVehSpd_Kph_u9p7[0] 3968 1_CmnVehSpd_Kph_u9p7[1] 4096 1_CmnVehSpd_Kph_u9p7[3] 4352 1_CmnVehSpd_Kph_u9p7[4] 4480 1_CmnVehSpd_Kph_u9p7[5] 4608 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 1_CmnV | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4129 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 t_CmnVehSpd_Kph_u9p7[0] 3968 t_CmnVehSpd_Kph_u9p7[1] 4096 t_CmnVehSpd_Kph_u9p7[2] 4224 t_CmnVehSpd_Kph_u9p7[3] 4352 t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4644 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FN_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1][1][1][1][1][1][1][1][1][1][1][1][| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5159 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 t_CmnVehSpd_Kph_u9p7[0] 4096 t_CmnVehSpd_Kph_u9p7[1] 4096 t_CmnVehSpd_Kph_u9p7[2] 4224 t_CmnVehSpd_Kph_u9p7[3] 4352 t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 1_CmnVehSpd_Kph_u9p7[0] 3968 1_CmnVehSpd_Kph_u9p7[1] 4096 1_CmnVehSpd_Kph_u9p7[2] 4224 1_CmnVehSpd_Kph_u9p7[3] 4352 1_CmnVehSpd_Kph_u9p7[4] 4480 1_CmnVehSpd_Kph_u9p7[5] 4608 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032 |
| 12_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][9] 5419 1_CmnVehSpd_Kph_u9p7[0] 3968 1_CmnVehSpd_Kph_u9p7[1] 4096 1_CmnVehSpd_Kph_u9p7[2] 4224 1_CmnVehSpd_Kph_u9p7[3] 4352 1_CmnVehSpd_Kph_u9p7[4] 4480 1_CmnVehSpd_Kph_u9p7[5] 4608 1_CmnVehSpd_Kph_u9p7[6] 4736 1_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 t_CmnVehSpd_Kph_u9p7[0] 3968 t_CmnVehSpd_Kph_u9p7[1] 4096 t_CmnVehSpd_Kph_u9p7[2] 4224 t_CmnVehSpd_Kph_u9p7[3] 4352 t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3302 |
| t2_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][7] | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3725 |
| t2_FDD_ADDRolling TblYM_MtrNmpRadpS_um1p17[1][8] | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 4148 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 t_CmnVehSpd_Kph_u9p7[0] 3968 t_CmnVehSpd_Kph_u9p7[1] 4096 t_CmnVehSpd_Kph_u9p7[2] 4224 t_CmnVehSpd_Kph_u9p7[3] 4352 t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4572 |
| t_CmnVehSpd_Kph_u9p7[0] 3968 t_CmnVehSpd_Kph_u9p7[1] 4096 t_CmnVehSpd_Kph_u9p7[2] 4224 t_CmnVehSpd_Kph_u9p7[3] 4352 t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4995 |
| t_CmnVehSpd_Kph_u9p7[1] 4096 t_CmnVehSpd_Kph_u9p7[2] 4224 t_CmnVehSpd_Kph_u9p7[3] 4352 t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5419 |
| t_CmnVehSpd_Kph_u9p7[2] | t_CmnVehSpd_Kph_u9p7[0] | 3968 |
| t_CmnVehSpd_Kph_u9p7[3] | t_CmnVehSpd_Kph_u9p7[1] | 4096 |
| t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t_CmnVehSpd_Kph_u9p7[2] | 4224 |
| t_CmnVehSpd_Kph_u9p7[4] 4480 t_CmnVehSpd_Kph_u9p7[5] 4608 t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t_CmnVehSpd_Kph_u9p7[3] | 4352 |
| t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t_CmnVehSpd_Kph_u9p7[4] | 4480 |
| t_CmnVehSpd_Kph_u9p7[6] 4736 t_CmnVehSpd_Kph_u9p7[7] 4864 | t_CmnVehSpd_Kph_u9p7[5] | 4608 |
| | t_CmnVehSpd_Kph_u9p7[6] | 4736 |
| Cmn\/chSnd Knh u0n7/91 | t_CmnVehSpd_Kph_u9p7[7] | 4864 |
| t_Gillivenopu_npii_uapr[o] 4992 | t_CmnVehSpd_Kph_u9p7[8] | 4992 |



| ADDCoefCalc | | |
|--------------------------|-------------|--|
| Name | Input Value | |
| t_CmnVehSpd_Kph_u9p7[9] | 5120 | |
| t_CmnVehSpd_Kph_u9p7[10] | 5248 | |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 | |

| Name | Input Value | | |
|---|---------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[9] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 24986 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 25395 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 25805 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 26214 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 26624 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 27034 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 27443 | | |
| t DmpADDCoefX MtrNm u4p12[7] | 27853 | | |
| t DmpADDCoefX MtrNm u4p12[8] | 28262 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 28672 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 494 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 31 | | |
| t FDD BlendTblY Uls u8p8[6] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 38 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 41 | | |
| t FDD BlendTblY Uls u8p8[10] | 44 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 46 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 8192 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 9830 | | |
| t RIAstWIRBIndTblY Uls u2p14[2] | 11469 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 14746 | | |
| t WIRBIndTblX MtrNm u8p8[0] | 1178 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1203 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1280 | | |
| Name | Actual Value | Expected Value | Boov!4 |
| | | Expected Value | Result |
| ADDCoefCalc() | 0.00246831775 | 0.002468318 ± 0.000000009 | |

| ADDCoefCalc() | | 0.00246831775 | 0.002468318 ± 0.000000009 | | |
|-----------------------------|-------|-----------------------------|---------------------------|-------|--------|
| | | | | | |
| Τ | | | | | V |
| Actual Function | Count | Expected Function | | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | | 5 | ~ |

| Test Step 1.23 (Repeat Count = 1) | | V |
|---|-------------|---|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | -6 | |
| VehicleSpeed_Kph_T_f32 | 216.25 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 2.3 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 704 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 814 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 924 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1034 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1144 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1254 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1364 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1475 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1585 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1695 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553 | |

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ADDCoefCalc

| Name | Input Value | | |
|--|---------------|---------------------------|--------|
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2583 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3099 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4129 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| | | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 28262 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 28672 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 29082 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 29491 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 29901 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 30310 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 30720 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 31130 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 31539 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 31949 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 0 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 0 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 26 | | |
| | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] t_FDD_BlendTblY_Uls_u8p8[4] | 31 | | |
| | 33 | | |
| t_FDD_BlendTblY_UIs_u8p8[5] | | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 38 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 41 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 44 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 46 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 49 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1434 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1459 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1485 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1510 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1536 | | |
| Name | Actual Value | Expected Value | Resul |
| ADDCoefCalc() | 0.00100163568 | 0.001001636 ± 0.000000009 | rtooui |

Τ







| Test Step 1.24 (Repeat Count = 1) | → |
|---|----------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | -7 |
| VehicleSpeed_Kph_T_f32 | 228.25 |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 2.4 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 885 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 986 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1087 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1188 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1288 1389 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1490 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1591 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1692 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 1793 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 924 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1144 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1475 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1695 |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 11520 |
| t_CmnVehSpd_Kph_u9p7[7] t_CmnVehSpd_Kph_u9p7[8] | 12800 |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 4506 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 4915 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 5325 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 5734 |
| t_DmpADDCoefX_MtrNm_u4p12[4] t DmpADDCoefX_MtrNm_u4p12[5] | 6144 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 6554 6963 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 7373 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 7782 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 8192 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 6554 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 6554 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 49 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 51 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 54 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 57 |
| t_FDD_BlendTbIY_Uls_u8p8[4] | 60 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 63 |
| t_FDD_BlendTblY_Uls_u8p8[6] | 66 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 68 |
| t_FDD_BlendTblY_Uls_u8p8[8] t_FDD_BlendTblY_Uls_u8p8[9] | 71 74 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 77 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 80 |
| | |
| | 3277 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 3277 4915 |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | |

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ADDCoefCalc

| Name | Input Value |
|---------------------------------|-------------|
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 9830 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1690 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1715 |





| N. | | | |
|---------------------------------|--------------|--------------------------|--------|
| Name | Input Value | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 65 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 68 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 70 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 73 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 75 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 78 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 80 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 83 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 86 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 88 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 91 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 93 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 11469 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1894 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[1] | 1920 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1946 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[4] | 1997 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.0226821322 | 0.022682133 ± 0.00000009 | ~ |

| Т | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.26 (Repeat Count = 1) | | • |
|---|-------------|---|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | 3 | |
| VehicleSpeed_Kph_T_f32 | 252.24 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 2.6 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 2030 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2422 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3206 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 4774 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1066 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1212 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1359 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1506 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1653 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1800 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1946 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2093 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 2240 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 2387 | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 12698 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 13107 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 13517 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 13926 | |

2014-09-19, 16:26:53+0530



| ADDCoefCalc | 2014-09-19, 10.20.33+0330 | Razonat |
|---|---------------------------|---------|
| Name | Input Value | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 14336 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 14746 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 15155 | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 15565 | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 15974 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 16384 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1608 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2032 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2455 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2878 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3302 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3725 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 4148 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4572 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4995 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5419 | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 93 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 96 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 99 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 101 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 104 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 106 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 109 | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 111 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 114 | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 116 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 119 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 122 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 6554 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 8192 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 9830 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 11469 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 13107 | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 0 | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 0 | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 0 | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 0 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

Actual Value

0.0104283169

Expected Value

0.010428317 ± 0.00000009

| Test Step 1.27 (Repeat Count = 1) | √ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 4 |
| VehicleSpeed_Kph_T_f32 | 264 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 2.7 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2340 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2568 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2796 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3252 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3480 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1246 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1638 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2030 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2422 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3206 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3598 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3990 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4382 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4774 |

t_WIRBIndTbIX_MtrNm_u8p8[4]

Name

ADDCoefCalc()

Result



| ADD | Coef | Calc |
|-----|------|------|
| | | |

| Name | | |
|--|----------------|--------------------|
| Comversion Kin, Jen 711 3840 | Input Value | |
| Comverbiged (April, 1967)2 5120 Comverbiged (April, 1967)4 7859 Comverbiged (April, 1967)4 7859 Comverbiged (April, 1967)4 7859 Comverbiged (April, 1967)8 10240 Comverbiged (April, 1967)8 10240 Comverbiged (April, 1967)8 10240 Comverbiged (April, 1967)8 10260 Comverbiged (April, 1967)8 10260 Comverbiged (April, 1967)8 10860 Comverbi | [0] 2560 | |
| Comversiged Kynl. u6p773 | [1] 3840 | |
| Comvising Light | [2] 5120 | |
| Comversional Koph_usp7[5] 8960 Comversional Koph_usp7[5] 10240 Comversional Koph_usp7[5] 10240 Comversional Koph_usp7[6] 10240 Comversional Koph_usp7[7] 11520 Comversional Koph_usp7[7] 11520 Comversional Koph_usp7[7] 10240 Comversional Koph_usp7[7] 10840 Comversional Koph_usp7[7] 10841 CompaDDCoceK, Mirkm_usp12[7] 10842 CompaDDCoceK, Mirkm_usp12[7] 10842 CompaDDCoceK, Mirkm_usp12[8] 10843 | [3] 6400 | |
| Commissed Moh. Lep7f5 19240 1924 | | |
| Comvision Commission Comm | [5] 8960 | |
| L_CmarlesSed_Xpt_usp7[7] | | |
| Comversion Com | | |
| Comversion of No. up9719 14900 | | |
| L_CmmVehSpd_Kpn_upb7[10] 15550 L_CmmVehSpd_Kpn_upb7[11] 16640 L_CmmVehSpd_Kpn_upb7[11] 16640 L_CmmVehSpd_Kpn_upb7[11] 16640 L_CmmVehSpd_Kpn_upb7[11] 16794 L_CmmVehSpd_Kpn_upb7[11] 17203 16794 L_CmmADDCoetK_MinNm_upb1[21] 17613 16022 17613 16022 L_CmmADDCoetK_MinNm_upb1[22] 17613 18022 L_CmmADDCoetK_MinNm_upb1[24] 18432 L_CmmADDCoetK_MinNm_upb1[24] 18432 L_CmmADDCoetK_MinNm_upb1[25] 19841 L_CmmADDCoetK_MinNm_upb1[26] 19951 L_CmmADDCoetK_MinNm_upb1[27] 19661 L_CmmADDCoetK_MinNm_upb1[27] 19661 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20480 L_CmmADDCoetK_MinNm_upb1[28] 20070 L_CmmADDCoetK_MinNm_upb1[28] 20070 1789 1780 178 | | |
| DmpADDCoeK_Minhm_u4p12(0) | | |
| DmpADDCoeK_Minhm_u4p12(0) | | |
| LompADDCock, Mirkim_upip12[1] 17203 1761 | | |
| DmpADDCocK, Mirkm_u4p12[2] 17613 18022 18022 | | |
| LDmpADDCocK, Mirkm_upip12(3) 18022 LDmpADDCocK, Mirkm_upip12(6) 18482 LDmpADDCocK, Mirkm_upip12(6) 19251 LDmpADDCocK, Mirkm_upip12(7) 19961 LDmpADDCocK, Mirkm_upip12(7) 19961 LDmpADDCocK, Mirkm_upip12(8) 20070 LDmpADDCocK, Mirkm_upip12(8) 20070 LDmpADDCocK, Mirkm_upip12(8) 20070 LDmpADDCocK, Mirkm_upip12(8) 20070 LDmpADDCocK, Mirkm_upip12(8) 20480 LDmpADDCocK, Mirkm_upip12(8) 20480 LDmpADDCocK, Mirkm_upip12(8) 20480 LDmpADDCocK, Mirkm_upip12(8) 20480 LDMPADDCocK, Mirkm_upip12(8) 2471 LDMPADCocK, Mirkm_upip12(8) 2471 LDMPADDCocK, Mirkm_upip12(8) 2 | | |
| DmpADDCoefV, MtrNm_u4p12[4] 18432 18842 10mpADDCoefV, MtrNm_u4p12[5] 18842 19mpADDCoefV, MtrNm_u4p12[6] 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 1956 1955 195 | | |
| DmpADDCoePC, MtrNm_u4p12[5] 18842 19251 19251 19261 19251 19261 19251 19261 19251 19261 | | |
| DmpADDCoePC MtrNm_u4p12[6] 19251 19681 | | |
| DmpADDCoeft MirNm_u4p12[7] 19661 10mpADDCoeft MirNm_u4p12[8] 20070 10mpADDCoeft MirNm_u4p12[9] 20480 17mpADDCoeft MirNm_u4p12[9] 20480 17mpADDCoeft MirNm_p8adpS_ump17[0] 1789 17mpADDCoeft MirNm_p8adpS_ump17[0] 1789 17mpADDCoeft MirNm_p8adpS_ump17[0] 2130 17mpADDCoeft MirNm_p8adpS_ump17[2] 2471 17mpADDStaticTbiry MirNmpRadpS_ump17[2] 2471 17mpADDStaticTbiry MirNmpRadpS_ump17[3] 2811 17mpADDStaticTbiry MirNmpRadpS_ump17[8] 3493 17mpADDStaticTbiry MirNmpRadpS_ump17[8] 3493 17mpADDStaticTbiry MirNmpRadpS_ump17[8] 3834 17mpADDStaticTbiry MirNmpRadpS_ump17[7] 4175 41 | | |
| DmpADDCoefX_MrNm_u4p12[8] 20070 10mpADDCoefX_MrNm_u4p12[8] 20480 1.50mpADCoefX_MrNm_u4p12[9] 20490 1.50mpADCoefX_MrNm_u4p12[9] 20490 20480 1.50mpADCoefX_MrNm_u4p12[9] 20490 20480 | | |
| DmpADDCoefX_MrNm_u4p12[9] | | |
| LFDD_ADDStaticTbiY_MtrNmpRadpS_um1p17[0] | | |
| FDD_ADDStaticTbY_MtrNmpRadpS_um1p17[1] | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17(2) | = | |
| LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[3] 2811 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[4] 3152 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[6] 3834 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[7] 4175 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[7] 4175 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[7] 4175 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[8] 4515 LFDD_ADDStaticTbY_MtrNmpRadps_um1p17[9] 4856 LFDD_BDStaticTbY_UtrNmpRadps_um1p17[9] 4856 LFDD_BlendTbY_Uis_u8p8[0] 116 LFDD_BlendTbY_Uis_u8p8[1] 118 LFDD_BlendTbY_Uis_u8p8[2] 121 LFDD_BlendTbY_Uis_u8p8[3] 123 LFDD_BlendTbY_Uis_u8p8[3] 123 LFDD_BlendTbY_Uis_u8p8[5] 129 LFDD_BlendTbY_Uis_u8p8[6] 131 LFDD_BlendTbY_Uis_u8p8[6] 131 LFDD_BlendTbY_Uis_u8p8[6] 139 LFDD_BlendTbY_Uis_u8p8[9] 139 LFDD_BlendTbY_Uis_u8p8[9] 139 LFDD_BlendTbY_Uis_u8p8[9] 139 LFDD_BlendTbY_Uis_u8p8[1] 144 LFDD_BlendTbY_Uis_u8p8[1] 144 LFDD_BlendTbY_Uis_u2p14[1] 144 LRASHWIRBIndTbY_Uis_u2p14[1] 9830 L_RIASHWIRBIndTbY_Uis_u2p14[3] 1469 L_RIASHWIRBIndTbY_Uis_u2p14[3] 14746 L_WIRBIndTbY_Uis_u2p14[3] 14746 L_WIRBIndTbY_Uis_u2p14[4] 14746 L_WIRBIndTbY_MtrNm_u8p8[1] 2048 L_WIRBIndTbX_MtrNm_u8p8[2] 2048 | | |
| LFDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 3152 LFDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 3493 LFDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 3834 LFDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 4175 LFDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 4515 LFDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 4856 LFDD_BlendTblY_UIs_u8p8[1] 116 LFDD_BlendTblY_UIs_u8p8[1] 118 LFDD_BlendTblY_UIs_u8p8[2] 121 LFDD_BlendTblY_UIs_u8p8[3] 123 LFDD_BlendTblY_UIs_u8p8[4] 126 LFDD_BlendTblY_UIs_u8p8[6] 131 LFDD_BlendTblY_UIs_u8p8[6] 131 LFDD_BlendTblY_UIs_u8p8[8] 136 LFDD_BlendTblY_UIs_u8p8[8] 136 LFDD_BlendTblY_UIs_u8p8[8] 136 LFDD_BlendTblY_UIs_u8p8[10] 141 LFDD_BlendTblY_UIs_u8p8[11] 144 LFRASWIRBIndTblY_UIs_u2p14[0] 8192 LRIASWIRBIndTblY_UIs_u2p14[1] 1469 LRIASWIRBIndTblY_UIs_u2p14[2] 11469 LRIASWIRBIndTblY_UIs_u2p14[3] 13107 LRIASWIRBIndTblY_UIs_u2p14[3] 13107 LRIASWIRBIndTblY_UIs_u2p14[4] 14746 | = | |
| T. FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 3493 T. FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] 3834 T. FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] 4175 T. FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] 4515 T. FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 4856 T. FDD_BlendTbIY_UIs_u8p8[0] 116 T. FDD_BlendTbIY_UIs_u8p8[1] 118 T. FDD_BlendTbIY_UIs_u8p8[2] 121 T. FDD_BlendTbIY_UIs_u8p8[3] 123 T. FDD_BlendTbIY_UIs_u8p8[6] 126 T. FDD_BlendTbIY_UIs_u8p8[6] 131 T. FDD_BlendTbIY_UIs_u8p8[6] 131 T. FDD_BlendTbIY_UIs_u8p8[8] 136 T. FDD_BlendTbIY_UIs_u8p8[1] 141 T. FDD_BlendTbIY_UIs_u8p8[10] 141 T. FDD_BlendTbIY_UIs_u8p8[11] 144 T. RIAStWIRBIndTbIY_UIs_u2p14[0] 8192 T. RIAStWIRBIndTbIY_UIs_u2p14[2] 11469 T. RIAStWIRBIndTbIY_UIs_u2p14[2] 11469 T. RIAStWIRBIndTbIY_UIs_u2p14[4] 14746 T. WIRBIndTbIX_MtrNm_u8p8[0] 2048 T. WIRBIndTbIX_MtrNm_u8p8[2] 2048 T. WIRBIndTbIX_MtrNm_u8p8[3] 2048 | | |
| L FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 3834 L FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 4175 L FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 4515 L FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 4856 L FDD_BlendTblY_Uls_u8p8[0] 116 L FDD_BlendTblY_Uls_u8p8[1] 118 L FDD_BlendTblY_Uls_u8p8[2] 121 L FDD_BlendTblY_Uls_u8p8[2] 123 L FDD_BlendTblY_Uls_u8p8[3] 123 L FDD_BlendTblY_Uls_u8p8[6] 129 L FDD_BlendTblY_Uls_u8p8[6] 131 L FDD_BlendTblY_Uls_u8p8[6] 131 L FDD_BlendTblY_Uls_u8p8[8] 136 L FDD_BlendTblY_Uls_u8p8[1] 141 L FDD_BlendTblY_Uls_u8p8[10] 141 L FDD_BlendTblY_Uls_u8p8[11] 144 L FDD_BlendTblY_Uls_u2p14[0] 8192 L RIASHWIRBIndTblY_Uls_u2p14[2] 11469 L RIASHWIRBIndTblY_Uls_u2p14[2] 11469 L RIASHWIRBIndTblY_Uls_u2p14[4] 14746 L WIRBIndTblX_MtrNm_u8p8[0] 2048 L WIRBIndTblX_MtrNm_u8p8[1] 2048 L WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 4175 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 4515 t_FDD_BlendTblY_Uls_u8p8[0] 116 t_FDD_BlendTblY_Uls_u8p8[1] 118 t_FDD_BlendTblY_Uls_u8p8[2] 121 t_FDD_BlendTblY_Uls_u8p8[3] 123 t_FDD_BlendTblY_Uls_u8p8[4] 126 t_FDD_BlendTblY_Uls_u8p8[5] 129 t_FDD_BlendTblY_Uls_u8p8[6] 131 t_FDD_BlendTblY_Uls_u8p8[7] 134 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[1] 141 t_FDD_BlendTblY_Uls_u8p8[1] 144 t_FDD_BlendTblY_Uls_u2p14[0] 8192 t_RlastWiRBIndTblY_Uls_u2p14[1] 9830 t_RlastWiRBIndTblY_Uls_u2p14[2] 11469 t_RlastWiRBIndTblY_Uls_u2p14[3] 13107 t_RlastWiRBIndTblY_Uls_u2p14[4] 14746 t_WiRBIndTblX_MtrNm_u8p8[0] 2048 t_WiRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 4515 t_FDD_BlendTblY_Uls_u8p8[0] 4856 t_FDD_BlendTblY_Uls_u8p8[1] 116 t_FDD_BlendTblY_Uls_u8p8[1] 118 t_FDD_BlendTblY_Uls_u8p8[2] 121 t_FDD_BlendTblY_Uls_u8p8[3] 123 t_FDD_BlendTblY_Uls_u8p8[4] 126 t_FDD_BlendTblY_Uls_u8p8[6] 129 t_FDD_BlendTblY_Uls_u8p8[6] 131 t_FDD_BlendTblY_Uls_u8p8[7] 134 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[1] 141 t_FDD_BlendTblY_Uls_u8p8[1] 144 t_FDD_BlendTblY_Uls_u2p14[0] 8192 t_RlastWiRBIndTblY_Uls_u2p14[2] 11469 t_RlastWiRBIndTblY_Uls_u2p14[3] 13107 t_RlastWiRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | = | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 4856 t_FDD_BlendTblY_Uis_u8p8[0] 116 t_FDD_BlendTblY_Uis_u8p8[1] 118 t_FDD_BlendTblY_Uis_u8p8[2] 121 t_FDD_BlendTblY_Uis_u8p8[3] 123 t_FDD_BlendTblY_Uis_u8p8[4] 126 t_FDD_BlendTblY_Uis_u8p8[6] 129 t_FDD_BlendTblY_Uis_u8p8[6] 131 t_FDD_BlendTblY_Uis_u8p8[7] 134 t_FDD_BlendTblY_Uis_u8p8[8] 136 t_FDD_BlendTblY_Uis_u8p8[9] 139 t_FDD_BlendTblY_Uis_u8p8[10] 141 t_FDD_BlendTblY_Uis_u8p8[11] 144 t_RIAstWiRBIndTblY_Uis_u2p14[0] 8192 t_RIAstWiRBIndTblY_Uis_u2p14[2] 11469 t_RIAstWiRBIndTblY_Uis_u2p14[2] 11469 t_RIAstWiRBIndTblY_Uis_u2p14[3] 13107 t_RIAStWiRBIndTblY_Uis_u2p14[4] 14746 t_WiRBIndTblX_MtrNm_u8p8[0] 2048 t_WiRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] 116 t_FDD_BlendTblY_Uls_u8p8[1] 118 t_FDD_BlendTblY_Uls_u8p8[2] 121 t_FDD_BlendTblY_Uls_u8p8[3] 123 t_FDD_BlendTblY_Uls_u8p8[4] 126 t_FDD_BlendTblY_Uls_u8p8[5] 129 t_FDD_BlendTblY_Uls_u8p8[6] 131 t_FDD_BlendTblY_Uls_u8p8[7] 134 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[10] 141 t_RDB_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAStWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] 118 t_FDD_BlendTblY_Uls_u8p8[2] 121 t_FDD_BlendTblY_Uls_u8p8[3] 123 t_FDD_BlendTblY_Uls_u8p8[4] 126 t_FDD_BlendTblY_Uls_u8p8[5] 129 t_FDD_BlendTblY_Uls_u8p8[6] 131 t_FDD_BlendTblY_Uls_u8p8[7] 134 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAstWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_BlendTbIY_Uls_u8p8[2] 121 t_FDD_BlendTbIY_Uls_u8p8[3] 123 t_FDD_BlendTbIY_Uls_u8p8[4] 126 t_FDD_BlendTbIY_Uls_u8p8[5] 129 t_FDD_BlendTbIY_Uls_u8p8[6] 131 t_FDD_BlendTbIY_Uls_u8p8[7] 134 t_FDD_BlendTbIY_Uls_u8p8[8] 136 t_FDD_BlendTbIY_Uls_u8p8[9] 139 t_FDD_BlendTbIY_Uls_u8p8[10] 141 t_FDD_BlendTbIY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTbIY_Uls_u2p14[1] 9830 t_RIAstWIRBIndTbIY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTbIY_Uls_u2p14[3] 13107 t_RIAstWIRBIndTbIY_Uls_u2p14[4] 14746 t_WIRBINGTbIX_MtrNm_u8p8[1] 2048 t_WIRBINDTbIX_MtrNm_u8p8[2] 2048 t_WIRBINDTDIX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] 123 t_FDD_BlendTblY_Uls_u8p8[4] 126 t_FDD_BlendTblY_Uls_u8p8[5] 129 t_FDD_BlendTblY_Uls_u8p8[6] 131 t_FDD_BlendTblY_Uls_u8p8[7] 134 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[1] 1469 t_RIAstWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAstWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_BlendTblY_UIs_u8p8[4] 126 t_FDD_BlendTblY_UIs_u8p8[5] 129 t_FDD_BlendTblY_UIs_u8p8[6] 131 t_FDD_BlendTblY_UIs_u8p8[7] 134 t_FDD_BlendTblY_UIs_u8p8[8] 136 t_FDD_BlendTblY_UIs_u8p8[9] 139 t_FDD_BlendTblY_UIs_u8p8[10] 141 t_FDD_BlendTblY_UIs_u8p8[11] 144 t_RIAStWIRBIndTblY_UIs_u2p14[0] 8192 t_RIAStWIRBIndTblY_UIs_u2p14[1] 9830 t_RIAStWIRBIndTblY_UIs_u2p14[2] 11469 t_RIAStWIRBIndTblY_UIs_u2p14[3] 13107 t_RIAStWIRBIndTblY_UIs_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t FDD_BlendTblY_Uls_u8p8[5] 129 t FDD_BlendTblY_Uls_u8p8[6] 131 t FDD_BlendTblY_Uls_u8p8[7] 134 t FDD_BlendTblY_Uls_u8p8[8] 136 t FDD_BlendTblY_Uls_u8p8[9] 139 t FDD_BlendTblY_Uls_u8p8[10] 141 t FDD_BlendTblY_Uls_u8p8[11] 144 t RIAStWIRBIndTblY_Uls_u2p14[0] 8192 t RIAStWIRBIndTblY_Uls_u2p14[1] 9830 t RIAStWIRBIndTblY_Uls_u2p14[2] 11469 t RIAStWIRBIndTblY_Uls_u2p14[3] 13107 t RIAStWIRBIndTblY_Uls_u2p14[4] 14746 t WIRBIndTblY_Uls_u2p14[4] 14746 t WIRBIndTblX_MtrNm_u8p8[0] 2048 t WIRBIndTblX_MtrNm_u8p8[2] 2048 t WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t FDD_BlendTblY_Uls_u8p8[6] 131 t_FDD_BlendTblY_Uls_u8p8[7] 134 t_FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAStWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAStWIRBIndTblY_Uls_u2p14[1] 9830 t_RIAStWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAStWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAStWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t FDD_BlendTblY_Uls_u8p8[7] 134 t FDD_BlendTblY_Uls_u8p8[8] 136 t FDD_BlendTblY_Uls_u8p8[9] 139 t FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAStWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAStWIRBIndTblY_Uls_u2p14[1] 9830 t_RIAStWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAStWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAStWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t FDD_BlendTblY_Uls_u8p8[8] 136 t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[1] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAstWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] 139 t_FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[1] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAstWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | p8[7] 134 | |
| t_FDD_BlendTblY_Uls_u8p8[10] 141 t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBindTblY_Uls_u2p14[0] 8192 t_RIAstWIRBindTblY_Uls_u2p14[1] 9830 t_RIAstWIRBindTblY_Uls_u2p14[2] 11469 t_RIAstWIRBindTblY_Uls_u2p14[3] 13107 t_RIAstWIRBindTblY_Uls_u2p14[4] 14746 t_WIRBindTblX_MtrNm_u8p8[0] 2048 t_WIRBindTblX_MtrNm_u8p8[1] 2048 t_WIRBindTblX_MtrNm_u8p8[2] 2048 t_WIRBindTblX_MtrNm_u8p8[3] 2048 | p8[8] 136 | |
| t_FDD_BlendTblY_Uls_u8p8[11] 144 t_RIAstWIRBIndTblY_Uls_u2p14[0] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[1] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[2] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[3] 13107 t_RIAstWIRBIndTblY_Uls_u2p14[4] 14746 t_WIRBIndTblX_MtrNm_u8p8[0] 2048 t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | p8[9] 139 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] 8192 t_RIAstWIRBIndTbIY_UIs_u2p14[1] 9830 t_RIAstWIRBIndTbIY_UIs_u2p14[2] 11469 t_RIAstWIRBIndTbIY_UIs_u2p14[3] 13107 t_RIAstWIRBIndTbIY_UIs_u2p14[4] 14746 t_WIRBIndTbIX_MtrNm_u8p8[0] 2048 t_WIRBIndTbIX_MtrNm_u8p8[1] 2048 t_WIRBIndTbIX_MtrNm_u8p8[2] 2048 t_WIRBIndTbIX_MtrNm_u8p8[3] 2048 t_WIRBIndTbIX_MtrNm_u8p8[3] 2048 | | |
| t_RIAStWIRBIndTbIY_UIs_u2p14[1] 9830 t_RIAStWIRBIndTbIY_UIs_u2p14[2] 11469 t_RIAStWIRBIndTbIY_UIs_u2p14[3] 13107 t_RIAStWIRBIndTbIY_UIs_u2p14[4] 14746 t_WIRBIndTbIX_MtrNm_u8p8[0] 2048 t_WIRBIndTbIX_MtrNm_u8p8[1] 2048 t_WIRBIndTbIX_MtrNm_u8p8[2] 2048 t_WIRBIndTbIX_MtrNm_u8p8[3] 2048 | p8[11] 144 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] 11469 t_RIAstWIRBIndTbIY_UIs_u2p14[3] 13107 t_RIAstWIRBIndTbIY_UIs_u2p14[4] 14746 t_WIRBIndTbIX_MtrNm_u8p8[0] 2048 t_WIRBIndTbIX_MtrNm_u8p8[1] 2048 t_WIRBIndTbIX_MtrNm_u8p8[2] 2048 t_WIRBIndTbIX_MtrNm_u8p8[3] 2048 | u2p14[0] 8192 | |
| E_RIAStWIRBIndTbIY_UIs_u2p14[3] 13107 t_RIAStWIRBIndTbIY_UIs_u2p14[4] 14746 t_WIRBIndTbIX_MtrNm_u8p8[0] 2048 t_WIRBIndTbIX_MtrNm_u8p8[1] 2048 t_WIRBIndTbIX_MtrNm_u8p8[2] 2048 t_WIRBIndTbIX_MtrNm_u8p8[3] 2048 | u2p14[1] 9830 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] 14746 t_WIRBIndTbIX_MtrNm_u8p8[0] 2048 t_WIRBIndTbIX_MtrNm_u8p8[1] 2048 t_WIRBIndTbIX_MtrNm_u8p8[2] 2048 t_WIRBIndTbIX_MtrNm_u8p8[3] 2048 | u2p14[2] 11469 | |
| t_WIRBIndTbiX_MtrNm_u8p8[0] 2048 t_WIRBIndTbiX_MtrNm_u8p8[1] 2048 t_WIRBIndTbiX_MtrNm_u8p8[2] 2048 t_WIRBIndTbiX_MtrNm_u8p8[3] 2048 | u2p14[3] 13107 | |
| t_WIRBIndTblX_MtrNm_u8p8[1] 2048 t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | u2p14[4] 14746 | |
| t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | 3p8[0] 2048 | |
| t_WIRBIndTblX_MtrNm_u8p8[2] 2048 t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] 2048 | | |
| | | |
| | | |
| Name Actual Value Expected Value | | Expected Value Res |
| ADDCoefCalc() 0.0117070675 0.011707067 ± 0.00000009 | | • |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.28 (Repeat Count = 1) | → |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 5 |
| VehicleSpeed_Kph_T_f32 | 276.14 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 2.8 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1608 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2032 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2878 |

ADDCoefCalc

2014-09-19, 16:26:53+0530



Input Value t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 $t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7]$ 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 $t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1]$ 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 $t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3]$ 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 $t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5]$ 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 $t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7]$ 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 $t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9]$ 3480 t_CmnVehSpd_Kph_u9p7[0] 12800 t CmnVehSpd Kph u9p7[1] 12928 $t_CmnVehSpd_Kph_u9p7[2]$ 13056 13184 t CmnVehSpd Kph u9p7[3] t_CmnVehSpd_Kph_u9p7[4] 13312 t_CmnVehSpd_Kph_u9p7[5] 13440 t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[8] 13824 13952 t_CmnVehSpd_Kph_u9p7[9] t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_DmpADDCoefX_MtrNm_u4p12[0] 20890 t_DmpADDCoefX_MtrNm_u4p12[1] 21299 21709 t DmpADDCoefX MtrNm u4p12[2] t_DmpADDCoefX_MtrNm_u4p12[3] 22118 t DmpADDCoefX MtrNm u4p12[4] 22528 t_DmpADDCoefX_MtrNm_u4p12[5] 22938 t_DmpADDCoefX_MtrNm_u4p12[6] 23347 t DmpADDCoefX_MtrNm_u4p12[7] 23757 t_DmpADDCoefX_MtrNm_u4p12[8] 24166 t_DmpADDCoefX_MtrNm_u4p12[9] 24576 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] 1608 t FDD ADDStaticTblY MtrNmpRadpS um1p17[1] 2032 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] 2455 2878 t FDD ADDStaticTblY MtrNmpRadpS um1p17[3] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] 3302 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] 3725 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 4148 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 4572 4995 t FDD ADDStaticTblY MtrNmpRadpS um1p17[8] t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] 5419 t_FDD_BlendTblY_Uls_u8p8[0] 144 t_FDD_BlendTblY_Uls_u8p8[1] 146 t_FDD_BlendTblY_Uls_u8p8[2] 149 t_FDD_BlendTblY_Uls_u8p8[3] 152 t_FDD_BlendTblY_Uls_u8p8[4] 154 t_FDD_BlendTblY_Uls_u8p8[5] 157 t_FDD_BlendTblY_Uls_u8p8[6] 159 t_FDD_BlendTblY_Uls_u8p8[7] 162 t_FDD_BlendTblY_Uls_u8p8[8] 164 t_FDD_BlendTblY_Uls_u8p8[9] 167 t_FDD_BlendTblY_Uls_u8p8[10] 169 t_FDD_BlendTblY_Uls_u8p8[11] 172 t_RIAstWIRBIndTbIY_Uls_u2p14[0] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[1] 8192 $t_RIAstWIRBIndTbIY_Uls_u2p14[2]$ 9830 t_RIAstWIRBIndTbIY_Uls_u2p14[3] 11469 t_RIAstWIRBIndTblY_Uls_u2p14[4] 13107 t WIRBIndTbIX MtrNm u8p8[0] 1178 t_WIRBIndTbIX_MtrNm_u8p8[1] 1203 t WIRBIndTbIX MtrNm u8p8[2] 1229 t_WIRBIndTbIX_MtrNm_u8p8[3] 1254 t_WIRBIndTbIX_MtrNm_u8p8[4] 1280 Name **Actual Value Expected Value** Result ADDCoefCalc() 0.011896921 ± 0.00000009 0.0118969213





| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.29 (Repeat Count = 1) | Immut Value | |
|---|----------------|--|
| Name BaseAssistCmd MtrNm T f32 | Input Value 6 | |
| VehicleSpeed_Kph_T_f32 | 288 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 2.9 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1789 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2130 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2471 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2811 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3152 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3493 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3834 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4175 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4515 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4856 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3302 3725 | |
| tz_FDD_ADDRoilingTbIYM_mitrNmpRadpS_um1p17[1][5] t2 FDD ADDRollingTbIYM MtrNmpRadpS um1p17[1][6] | 4148 | |
| t2_FDD_ADDRoillingTbIYM_MtrNmpRadpS_um1p17[1][0] t2_FDD_ADDRoillingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4572 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4995 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 5419 | |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 | |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 | |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 | |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 | |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 | |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 | |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 | |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 | |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 25395 25805 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] t_DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |
| t DmpADDCoefX MtrNm u4p12[4] | 26624 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 27034 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 27443 | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 27853 | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 28262 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 28672 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1789 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2130 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2471 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2811 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3152 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3493 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3834 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4175 | |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] t FDD ADDStaticTbIY MtrNmpRadpS um1p17[9] | 4515 4856 | |
| t_FDD_ADDStaticTbiY_mtrivmpRadps_um1p17[9] t_FDD_BlendTbiY_Uis_u8p8[0] | 172 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 174 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 176 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 178 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 180 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 183 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 185 | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 187 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 189 | |

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ADDCoefCalc

| Name | Input Value | | |
|---------------------------------|--------------|--------------------------|----------|
| t_FDD_BlendTblY_Uls_u8p8[9] | 191 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 193 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 195 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 0 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 0 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 282 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 358 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 384 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.0136489868 | 0.013648987 ± 0.00000009 | ✓ |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | - |

| Name | Input Value | |
|---|-------------|--|
| BaseAssistCmd_MtrNm_T_f32 | 7 | |
| VehicleSpeed Kph T f32 | 300.25 | |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 3.2 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 | |
| | 328 | |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] 2 FDD ADDRollingTbIYM MtrNmpRadpS um1p17[0][2] | 494 | |
| | 661 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 827 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1789 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2130 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2471 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2811 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3152 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3493 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3834 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4175 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4515 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4856 | |
| CmnVehSpd_Kph_u9p7[0] | 10368 | |
| _CmnVehSpd_Kph_u9p7[1] | 10496 | |
| _CmnVehSpd_Kph_u9p7[2] | 10624 | |
| _CmnVehSpd_Kph_u9p7[3] | 10752 | |
| _CmnVehSpd_Kph_u9p7[4] | 10880 | |
| _CmnVehSpd_Kph_u9p7[5] | 11008 | |
| _CmnVehSpd_Kph_u9p7[6] | 11136 | |
| _CmnVehSpd_Kph_u9p7[7] | 11264 | |
| _CmnVehSpd_Kph_u9p7[8] | 11392 | |
| _CmnVehSpd_Kph_u9p7[9] | 11520 | |
| _CmnVehSpd_Kph_u9p7[10] | 11648 | |
| _CmnVehSpd_Kph_u9p7[11] | 11776 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 28262 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 28672 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 29082 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 29491 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 29901 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 30310 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 30720 | |
| | 31130 | |
| DmpADDCoefX MtrNm u4p12[8] | 31539 | |
| DmpADDCoefX MtrNm u4p12[9] | 31949 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[0] | 161 | |
| :_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 494 | |

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ADDCoefCalc

| Name | Input Value | | |
|---|--------------|--------------------------|--------|
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 218 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 220 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 223 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 225 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 227 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 230 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 232 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 234 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 237 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 239 | | |
| t_FDD_BlendTbIY_Uls_u8p8[10] | 241 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 243 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 16384 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 16384 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 16384 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 16384 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 16384 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 538 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 563 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 589 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 614 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 640 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.0155524611 | 0.015552461 ± 0.00000009 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.31 (Repeat Count = 1) | |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 8 |
| VehicleSpeed Kph T f32 | 312 |
| WIRCmdAmpBlnd MtrNm T f32 | 3.1 |
| t2 FDD ADDRollingTbIYM MtrNmpRadpS um1p17[0][0] | 342 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3409 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 494 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 661 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 827 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1326 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 1493 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 1659 |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 |



| ΑD | DC | oei | fCa | lc |
|----|----|-----|-----|----|
| | | | | |

| 7.12.2.000.000.0 | | • | |
|---|--------------|------------------------|-------|
| Name | Input Value | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 4506 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 4915 | | |
| t DmpADDCoefX MtrNm u4p12[2] | 5325 | | |
| t DmpADDCoefX MtrNm u4p12[3] | 5734 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 6144 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 6554 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 6963 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 7373 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 7782 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 8192 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 342 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1024 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 15 | | |
| t FDD BlendTblY Uls u8p8[1] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 36 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 38 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 41 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 44 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 11469 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 794 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 819 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 845 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 870 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 896 | | |
| Name | Actual Value | Expected Value | Resul |
| ADDCoefCalc() | 0.0253202002 | 0.0253202 ± 0.00000009 | |
| <u>~</u> | | | |

| T. | | | | | |
|-----------------------------|-------|-----------------------------|-------|---------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntolVarXY u16 u16Xu16Y Cnt | | IntolVarXY u16 u16Xu16Y Cnt | E | rtoouit | |

| Test Step 1.32 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 1.5 |
| VehicleSpeed_Kph_T_f32 | 324.14 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 3.2 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 523 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1553 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2583 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3099 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3614 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4129 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4644 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 5159 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |

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ADDCoefCalc

| Name | Input Value | | |
|--|---------------|--------------------------|--------|
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3409 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 0 | | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 8602 | | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 9011 | | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 9421 | | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 9830 | | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 10240 | | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 10650 | | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 11059 | | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 11469 | | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 11878 | | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 12288 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 494 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 1659 | | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 116 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 121 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 123 | | |
| t_FDD_BlendTbIY_Uls_u8p8[3] t_FDD_BlendTbIY_Uls_u8p8[4] | 123 | | |
| | | | |
| t_FDD_BlendTblY_Uls_u8p8[5] t FDD BlendTblY Uls u8p8[6] | 131 | | |
| t FDD BlendTbIY Uls u8p8[7] | 134 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 136 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 139 | | |
| t FDD BlendTblY Uls u8p8[10] | 141 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 144 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 1638 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 3277 | | |
| t RIAstWIRBIndTbIY UIs u2p14[2] | 4915 | | |
| t_RiAstWiRBindTblY_Uls_u2p14[3] | 6554 | | |
| t_RiAstWiRBindTblY_Uls_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1050 | | |
| t WIRBIndTbIX_MtrNm_u8p8[1] | 1035 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1101 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1126 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1152 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00270421011 | 0.00270421 ± 0.000000009 | Kesuit |
| ADDOOCIOGIC() | 0.00210421011 | 0.00210421 ± 0.000000009 | |

| Τ | | | ✓ | |
|-----------------------------|-------|-----------------------------|----------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntplVarXY u16 u16Xu16Y Cnt | 5 | |





| Test Step 1.33 (Repeat Count = 1) | ✓ |
|---|----------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | -1.5 |
| VehicleSpeed_Kph_T_f32 | 336 |
| WIRCmdAmpBlnd_MtrNm_T_f32 | 3.3 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 814 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][2] t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 924 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1144 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1364 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 1475 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1695 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1553 2068 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][5] | 3099 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4129 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159 |
| t_CmnVehSpd_Kph_u9p7[0] | 32640 |
| t_CmnVehSpd_Kph_u9p7[1] | 32640 |
| t_CmnVehSpd_Kph_u9p7[2] | 32640 |
| t_CmnVehSpd_Kph_u9p7[3] t_CmnVehSpd_Kph_u9p7[4] | 32640 32640 |
| t_CmnVehSpd_Kph_u9p7[5] | 32640 |
| t_CmnVehSpd_Kph_u9p7[6] | 32640 |
| t_CmnVehSpd_Kph_u9p7[7] | 32640 |
| t_CmnVehSpd_Kph_u9p7[8] | 32640 |
| t_CmnVehSpd_Kph_u9p7[9] | 32640 |
| t_CmnVehSpd_Kph_u9p7[10] | 32640 |
| t_CmnVehSpd_Kph_u9p7[11] | 32640 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 12698 |
| t_DmpADDCoefX_MtrNm_u4p12[1] t DmpADDCoefX_MtrNm_u4p12[2] | 13107 13517 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 13926 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 14336 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 14746 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 15155 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 15565 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 15974 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 16384 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] t FDD ADDStaticTblY MtrNmpRadpS um1p17[2] | 328 494 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 172 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 174 176 |
| t_FDD_BlendTblY_Uls_u8p8[2] t_FDD_BlendTblY_Uls_u8p8[3] | 178 |
| t_FDD_BlendTblY_Uls_u8p8[4] | 180 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 183 |
| t_FDD_BlendTblY_Uls_u8p8[6] | 185 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 187 |
| t_FDD_BlendTblY_Uls_u8p8[8] | 189 |
| t_FDD_BlendTblY_Uls_u8p8[9] | 191 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 193 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 195 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 3277 4915 |
| t_RiAstWiRBindTblY_Uis_u2p14[2] | 6554 |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 8192 |
| | |





| Name | Innut Value | | |
|---------------------------------|---------------|---------------------------|--------|
| | Input Value | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 9830 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1306 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1331 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1357 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1382 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1408 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00417356379 | 0.004173564 ± 0.000000009 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Took Stop 4.24 (Deposit Count = 4) | |
|---|----------------|
| Test Step 1.34 (Repeat Count = 1) | Invest Visites |
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | 2.9 |
| VehicleSpeed_Kph_T_f32 | 348.14 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 3.4 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 885 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 986 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1087 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1188 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1288 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1389 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1490 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1591 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1692 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1793 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 704 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 814 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] | 924 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] | 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1144 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1475 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1695 |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 16794 |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 17203 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 17613 |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 18022 |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 18432 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 18842 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 19251 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 19661 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 20070 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 20480 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 342 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1024 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 |
| t FDD ADDStaticTblY MtrNmpRadpS um1p17[8] | 3068 |
| t FDD ADDStaticTblY MtrNmpRadpS um1p17[9] | 3409 |
| | 15.55 |





| Name | Input Value | | |
|---------------------------------|---------------|---------------------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[0] | 218 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 220 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 223 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 225 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 227 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 230 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 232 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 234 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 237 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 239 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 241 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 243 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 4915 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 11469 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1562 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1587 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1613 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1638 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1664 | | |
| Name | Actual Value | Expected Value | Result |
| ADDCoefCalc() | 0.00614841701 | 0.006148417 ± 0.000000009 | ~ |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

| Test Step 1.35 (Repeat Count = 1) | | |
|---|-------------|--|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | 3.7 | |
| VehicleSpeed_Kph_T_f32 | 360 | |
| WIRCmdAmpBInd_MtrNm_T_f32 | 3.5 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1066 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1212 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1359 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1506 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1653 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1800 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1946 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2093 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 2240 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 2387 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 885 | |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] | 986 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1087 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1188 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1288 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1389 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1490 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1591 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1692 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1793 | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | |
| t CmnVehSpd Kph u9p7[3] | 512 | |
| t CmnVehSpd Kph u9p7[4] | 640 | |
| t CmnVehSpd Kph u9p7[5] | 768 | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | |
| t DmpADDCoefX MtrNm u4p12[0] | 20890 | |
| t DmpADDCoefX MtrNm u4p12[1] | 21299 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 21709 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 22118 | |

ADDCoefCalc

Name

ADDCoefCalc()

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| Name | Input Value |
|---|-------------|
| t_DmpADDCoefX_MtrNm_u4p12[4] | 22528 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 22938 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 23347 |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 23757 |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 24166 |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 24576 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 |
| t_FDD_BlendTblY_Uls_u8p8[0] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[1] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[4] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[5] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[6] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[8] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[9] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 0 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 0 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 6554 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 8192 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 9830 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 11469 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 13107 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1766 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1792 |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1818 |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1843 |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1869 |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntplVarXY u16 u16Xu16Y Cnt | 5 | ✓ |

Actual Value

0.00399017334

Expected Value

0.003990173 ± 0.000000009

| Test Step 1.36 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| BaseAssistCmd_MtrNm_T_f32 | -3.69 |
| VehicleSpeed_Kph_T_f32 | 372.14 |
| WIRCmdAmpBInd_MtrNm_T_f32 | 3.6 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2030 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2422 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3206 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4774 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1066 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1212 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1359 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1506 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1653 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1800 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1946 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2093 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 2240 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 2387 |

Result

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| ADDCoefCalc | 2014-09-19, 16:26:53+0530 | Razorcat |
|---|---------------------------|----------|
| Name | Input Value | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 25395 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 25805 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 26624 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 27034 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 27443 | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 27853 | |
| t DmpADDCoefX MtrNm u4p12[8] | 28262 | |
| t DmpADDCoefX MtrNm u4p12[9] | 28672 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814 | |
| t FDD ADDStaticTblY MtrNmpRadpS um1p17[2] | 924 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1254 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695 | |
| t FDD BlendTblY Uls u8p8[0] | 256 | |
| t FDD BlendTblY Uls u8p8[1] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 256 | |
| t FDD BlendTblY Uls u8p8[4] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 256 | |
| t FDD BlendTblY Uls u8p8[7] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 256 | |
| t FDD BlendTblY Uls u8p8[10] | 256 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 256 | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 8192 | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 9830 | |
| t RIAstWIRBIndTbIY UIs u2p14[2] | 11469 | |
| | | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 5 | IntplVarXY u16 u16Xu16Y Cpt | 5 | |

Actual Value

0.00827023014

Expected Value

0.00827023 ± 0.000000009

13107

14746

410

435

461

486

512

| Test Step 1.37 (Repeat Count = 1) | | ✓ |
|---|-------------|----------|
| Name | Input Value | |
| BaseAssistCmd_MtrNm_T_f32 | 3.9 | |
| VehicleSpeed_Kph_T_f32 | 384.25 | |
| WIRCmdAmpBind_MtrNm_T_f32 | 3.7 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112 | |

t_RIAstWIRBIndTblY_Uls_u2p14[3]

t_RIAstWIRBIndTbIY_Uls_u2p14[4]

t_WIRBIndTbIX_MtrNm_u8p8[0]

t_WIRBIndTbIX_MtrNm_u8p8[1]

t_WIRBIndTbIX_MtrNm_u8p8[2]

t_WIRBIndTbIX_MtrNm_u8p8[3]

 $t_WIRBIndTbIX_MtrNm_u8p8[4]$

Name

ADDCoefCalc()

Result

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ADDCoefCalc

| 710000000000 | | (-410-10 |
|--|----------------|------------------------|
| Name | Input Value | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2340 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2568 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2796 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3024 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3252 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3480 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1246 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1638 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2030 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2422 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2814 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3206 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3598 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3990 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4382 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4774 | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 12928 | |
| t_CmnVehSpd_Kph_u9p7[1] t_CmnVehSpd_Kph_u9p7[2] | 13056 | |
| t CmnVehSpd Kph u9p7[3] | 13184 | |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 | |
| t CmnVehSpd Kph u9p7[5] | 13440 | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 | |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 | |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 | |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 | |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 | |
| t_DmpADDCoefX_MtrNm_u4p12[0] | 28262 | |
| t_DmpADDCoefX_MtrNm_u4p12[1] | 28672 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 29082 | |
| t_DmpADDCoefX_MtrNm_u4p12[3] | 29491 | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 29901 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 30310 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 30720 | |
| t_DmpADDCoefX_MtrNm_u4p12[7] | 31130 | |
| t_DmpADDCoefX_MtrNm_u4p12[8] | 31539 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 31949 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 885 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 986 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1087 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1793 | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 116 | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 118 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 121 | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 123 | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 126 | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 129 | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 131 | |
| t_FDD_BlendTblY_UIs_u8p8[7] | 134 | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 136 | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 139 141 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 141 | |
| t_FDD_BlendTbIY_Uls_u8p8[11] t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 6554 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[u] t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 8192 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 9830 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 11469 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 13107 | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 666 | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 691 | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 717 | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 742 | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 768 | |
| Name | | pected Value Resul |
| ADDCoefCalc() | | 08456621 ± 0.000000009 |
| v | 0.00 | |

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ADDCoefCalc

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |

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DriverVelCalc

| Project | FDD_Inertia |
|-------------|---------------|
| Module | FDD_Inertia |
| Test Object | DriverVelCalc |

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Branch (C1) Coverage | 100 % |

Statistics

| Total Testcases | 1 |
|-----------------|---|
| Successful | 1 |
| Failed | 0 |
| Not Executed | 0 |



Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp | |
|------------------------|---|--|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml | |
| Target Environment | TI TMS 570 PLS UDE (Default) | |
| Kind of Test | Unit Test | |
| Linker Options | | |
| Source File(s) | | |
| File | \$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c | |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\-I\$(PROJECTROOT)\\FrqDepDmpnInrtCmp\utp\contract\-I\$(PROJECTROOT)\\NxtrLib\\nclude -I\$(PROJECTROOT)\\StdDef\\nclude -I\$(PROJECTROOT)\\StdDef\\nclude \rm I\\$(Projectro I)\\\nclude \rm I\\$(Projectro I)\\\\nclude \rm I\\$(Projectro I)\\\\\nclude \rm I\\$(Projectro I)\\\\\nclude \rm I\\$(Projectro I)\\\\\\nclude \rm I\\$(Projectro I)\\\\\\nclude \rm I\\$(Projectro I)\\\\\\\\\nclude \rm I\\$(Projectro I)\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ | |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c | |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include | |

| Comments | Description/ | Specification |
|----------|--------------|---------------|
| | | |

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| ıme | | | |
| | | | |

Module 'FDD_Inertia'

Name of Tester: Spoorti Mali
Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c
Code File(s) Version: 13
Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc
Module Design Document Version: 18
Data Dictionary Version: 16
Unit Test Plan Version: 6
Optimization Level: Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30
Total FLASH Used (Bytes): 1994
Total RAM Used (Bytes): 60
Total CALS Used (Bytes): 328
Special Test Requirements:
Test Date: 09-19-2014
Comments:

Comments:

Note1:Inline Function defined in ""globalmacro.h"" are not unit tested.

Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference.

Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of iv

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DriverVelCalc

| Attributes | |
|-----------------|--|
| Name | Value |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

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DriverVelCalc





Test Case 1: Boundary Test Specification Performance Metrics (With "None" Instrumentation and "WithPS" Environment) CPU Cycles: 329.00 Cycles 341.00 Cycles 329.00 Cycles 329.00 Cycles 329.00 Cycles 417.00 Cycles 329.00 Cycles 329.00 Cycles 329.00 Cycles 329.00 Cycles TS1.1 TS1.2 TS1.2 TS1.3 TS1.4 TS1.5 TS1.6 TS1.7 TS1.8 TS1.9 TS1.10 TS1.12 TS1.13 341.00 Cycles 417.00 Cycles 341.00 Cycles 341.00 Cycles 397.00 Cycles 329.00 Cycles TS1.14 TS1.15 TS1.15 TS1.16 TS1.17 TS1.18 TS1.19 TS1.20 TS1.21 TS1.22 TS1.23 TS1.24 TS1.25 TS1.26 TS1.27 TS1.28 TS1.29 TS1.30 TS1.31 TS1.33 TS1.33 329.00 Cycles 329.00 Cycles 329.00 Cycles 341.00 Cycles TS1.35 TS1.36 TS1.37 TS1.37 TS1.38 TS1.39 TS1.40 TS1.41 TS1.42 Description Test Vector Description TS1.1 HwTroque_HwNm_T_f32 = min TS1.2 HwTroque_HwNm_T_f32 = max TS1.3 HwTroque_HwNm_T_f32 = zero TS1.4 HwTroque_HwNm_T_f32 = neg TS1.5 HwTroque_HwNm_T_f32 = neg TS1.6 CRFMotorVel_MtrRadpS_T_f32 = min TS1.7 CRFMotorVel_MtrRadpS_T_f32 = min TS1.7 CRFMotorVel_MtrRadpS_T_f32 = zero TS1.9 CRFMotorVel_MtrRadpS_T_f32 = neg TS1.10 CRFMotorVel_MtrRadpS_T_f32 = neg TS1.11 VehicleSpeed_Kph_T_f32 = min TS1.12 VehicleSpeed_Kph_T_f32 = max TS1.13 VehicleSpeed_Kph_T_f32 = pos TS1.14 PrevTbarAng_HwDeg_M_f32 = min TS1.15 PrevTbarAng_HwDeg_M_f32 = zero TS1.17 PrevTbarAng_HwDeg_M_f32 = neg

| | PrevTbarAng_HwDeg_M_f32 = neg |
|--------|--|
| TS1.18 | |
| TS1.19 | k_CmnTbarStiff_NmpDeg_f32 = min |
| TS1.20 | k_CmnTbarStiff_NmpDeg_f32 = max |
| TS1.21 | k_CmnTbarStiff_NmpDeg_f32 = mid |
| TS1.22 | k_CmnSysKinRatio_MtrDegpHwDeg_f32 = min |
| TS1.23 | k_CmnSysKinRatio_MtrDegpHwDeg_f32 = max |
| TS1.24 | k_CmnSysKinRatio_MtrDegpHwDeg_f32 = mid |
| TS1.25 | t_CmnVehSpd_Kph_u9p7[12] = min |
| TS1.26 | t_CmnVehSpd_Kph_u9p7[12] = max |
| TS1.27 | t CmnVehSpd Kph u9p7[12] = mid |
| TS1.28 | t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[12] = min |
| TS1.29 | t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[12] = max |
| TS1.30 | t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[12] = mid |
| TS1.31 | k InrtCmp MtrVel ScaleFactor Uls f32 = min |
| TS1.32 | k InrtCmp MtrVel ScaleFactor Uls f32 = max |
| TS1.33 | k InrtCmp MtrVel ScaleFactor Uls f32 = mid |
| TS1.34 | TbarVelFiltSv_M_str.K = min |
| TS1.35 | TbarVelFiltSv_M_str.K = max |
| TS1.36 | TbarVelFiltSv M str.K = mid |
| TS1.37 | TbarVelFiltSv M str.SV = min |
| TS1.38 | TbarVelFiltSv M str.SV = max |
| TS1.39 | TbarVelFiltSv M str.SV = zero |
| TS1.40 | TbarVelFiltSv M str.SV = pos |
| TS1.41 | TbarVelFiltSv M str.SV = neg |
| TS1.42 | All min |
| TS1.43 | All max |
| | |

| Test Step 1.1 (Repeat Count = 1) | ✓ |
|----------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -1118 |

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DriverVelCalc

TbarVelFiltSv_M_str.SV_Uls_f32

| Name | Input Value | | |
|--|--------------|------------------|--------|
| HwTorque_HwNm_T_f32 | -10 | | |
| PrevTbarAng_HwDeg_M_f32 | -20 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.6667 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.001255848 | | |
| VehicleSpeed_Kph_T_f32 | 0 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 1 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 0.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 0 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -0 | 0 ± 0.000009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -20 | -20 ± 0.00390625 | • |
| | | | |

| Τ | | | | | V |
|-----------------------------|-------|-----------------------------|-------|------|----|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

-6.65832758

-6.658327638 ± 0.00390625

| Test Step 1.2 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | 1118 |
| HwTorque_HwNm_T_f32 | 10 |
| PrevTbarAng_HwDeg_M_f32 | 20 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.6667 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.715390457 |
| VehicleSpeed_Kph_T_f32 | 511.9921875 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 100 |
| k_CmnTbarStiff_NmpDeg_f32 | 10 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 1 |
| t_CmnVehSpd_Kph_u9p7[0] | 32640 |
| t_CmnVehSpd_Kph_u9p7[1] | 32640 |
| t_CmnVehSpd_Kph_u9p7[2] | 32640 |
| t_CmnVehSpd_Kph_u9p7[3] | 32640 |
| t_CmnVehSpd_Kph_u9p7[4] | 32640 |
| t_CmnVehSpd_Kph_u9p7[5] | 32640 |
| t_CmnVehSpd_Kph_u9p7[6] | 32640 |
| t_CmnVehSpd_Kph_u9p7[7] | 32640 |
| t_CmnVehSpd_Kph_u9p7[8] | 32640 |
| t_CmnVehSpd_Kph_u9p7[9] | 32640 |
| t_CmnVehSpd_Kph_u9p7[10] | 32640 |
| t_CmnVehSpd_Kph_u9p7[11] | 32640 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 128 |

DriverVelCalc

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| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 128 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 128 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 128 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 128 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 128 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -10740.3115 | -10740.31169 ± 0.09 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1 | 1 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6794.31201 | -6794.311935 ± 0.00390625 | • |

| T The state of the | | | | V |
|--|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.3 (Repeat Count = 1) | | | ~ |
|--|--------------|---------------------------|-------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 100.5 | | |
| HwTorque_HwNm_T_f32 | -10 | | |
| PrevTbarAng_HwDeg_M_f32 | -8.33 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.2587 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 | | |
| VehicleSpeed_Kph_T_f32 | 100.02 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 10.2 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 1.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 15 | | |
| Name | Actual Value | Expected Value | Resul |
| DriverVelCalc() | 90.4685822 | 90.46858168 ± 0.00009 | |
| PrevTbarAng HwDeg M f32 | -8.33333302 | -8.333333333 ± 0.00390625 | |
| TbarVelFiltSv M str.SV Uls f32 | 0.890704095 | 0.890688873 ± 0.00390625 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | • |

| Test Step 1.4 (Repeat Count = 1) | ✓ |
|----------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -100.6 |
| HwTorque_HwNm_T_f32 | 10 |
| PrevTbarAng_HwDeg_M_f32 | 3.9995 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.3697 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2365 |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|-------------------------|----------|
| VehicleSpeed_Kph_T_f32 | 200.03 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 20.3 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 2.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.8 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 17 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -80.3920822 | -80.39208153 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 4 | 4 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.86838663 | 1.86839095 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.5 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | 200.2 |
| HwTorque_HwNm_T_f32 | 0 |
| PrevTbarAng_HwDeg_M_f32 | 0.01 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.2145 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.35874 |
| VehicleSpeed_Kph_T_f32 | 300.05 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 30.4 |
| k_CmnTbarStiff_NmpDeg_f32 | 3.4 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.7 |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 5 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 6 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 8 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 9 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 10 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 12 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 13 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 14 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 15 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 17 |

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| Name | Input Value | | |
|--|--------------|-------------------------|----------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 19 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 140.161072 | 140.161078 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0 | 0 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0.267630339 | 0.26763027 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.6 (Repeat Count = 1) | | | ✓ |
|--|--------------|---------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -200.1 | | |
| HwTorque_HwNm_T_f32 | -5.5 | | |
| PrevTbarAng_HwDeg_M_f32 | -1.221 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5623 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.47856 | | |
| VehicleSpeed_Kph_T_f32 | 400.06 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 40.5 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 4.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 20 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -119.829559 | -119.8295518 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -1.22222221 | -1.222222222 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.08650517 | 2.086512379 ± 0.00390625 | ✓ |

| Τ | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | |

| Test Step 1.7 (Repeat Count = 1) | | ✓ |
|--------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 300.03 | |
| HwTorque_HwNm_T_f32 | 5.2 | |
| PrevTbarAng_HwDeg_M_f32 | 0.92987 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.8745 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.58963 | |
| VehicleSpeed_Kph_T_f32 | 123.07 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 50.6 | |
| k_CmnTbarStiff_NmpDeg_f32 | 5.6 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5 | |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 22 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 150.29483 | 150.2948274 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.928571403 | 0.928571429 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.02786994 | 2.027880229 ± 0.00390625 | ~ |

| T | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | • | |

| Test Step 1.8 (Repeat Count = 1) | | | ✓ |
|--|--------------|-----------------------|--------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -1118 | | |
| HwTorque_HwNm_T_f32 | 1.6 | | |
| PrevTbarAng_HwDeg_M_f32 | 0.2461 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.369 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.63214 | | |
| VehicleSpeed_Kph_T_f32 | 150.08 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 60.8 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 6.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 23 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -447.362946 | -447.3629225 ± 0.0009 | ~ |

DriverVelCalc

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| Name | Actual Value | Expected Value | Result |
|--------------------------------|--------------|---------------------------|--------|
| PrevTbarAng_HwDeg_M_f32 | 0.246153846 | 0.246153846 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.854439139 | -0.854441186 ± 0.00390625 | ~ |

| au | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.9 (Repeat Count = 1) | | | ✓ |
|--|--------------|---------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 1118 | | |
| HwTorque_HwNm_T_f32 | -1.2 | | |
| PrevTbarAng_HwDeg_M_f32 | -0.15321 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.124 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.014785 | | |
| VehicleSpeed_Kph_T_f32 | 16.25 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 70.1 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 7.8 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 23 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 24 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 335.105377 | 335.1053608 ± 0.0009 | |
| PrevTbarAng_HwDeg_M_f32 | -0.15384616 | -0.153846154 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.08251452 | -3.082514427 ± 0.00390625 | ~ |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |

| Test Step 1.10 (Repeat Count = 1) | | ✓ |
|--------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 0 | |
| HwTorque_HwNm_T_f32 | 2.2 | |
| PrevTbarAng_HwDeg_M_f32 | 0.27 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.5511 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.025896 | |
| VehicleSpeed_Kph_T_f32 | 58.63 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 80.2 | |
| k_CmnTbarStiff_NmpDeg_f32 | 8.1 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2 | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | |

DriverVelCalc

TbarVelFiltSv_M_str.SV_Uls_f32

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-4.412463974 ± 0.00390625

| Name | Input Value | | |
|--|--------------|--------------------------|----------|
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 29 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 30 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 40 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -1.15806818 | -1.15806835 ± 0.000009 | - |
| PrevTbarAng_HwDeg_M_f32 | 0.271604925 | 0.271604938 ± 0.00390625 | ✓ |
| | | | |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

-4.41246414

| Test Step 1.11 (Repeat Count = 1) | | | ✓ |
|--|--------------|---------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel MtrRadpS T f32 | -450 | | |
| HwTorque HwNm T f32 | -2.7 | | |
| PrevTbarAng HwDeg M f32 | -0.292 | | |
| TbarVelFiltSv M str.SV Uls f32 | -5.7412 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.03698 | | |
| VehicleSpeed_Kph_T_f32 | 22.51 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 90.5 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 9.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 39 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 40 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 43 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 44 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 45 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 47 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -47.2626114 | -47.26260964 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.29347828 | -0.293478261 ± 0.00390625 | ✓ |
| TbarVelFiltSv M str.SV Uls f32 | -5.55622387 | -5.556223467 ± 0.00390625 | ~ |



| T | | | | • |
|-----------------------------|-------|-----------------------------|-------|-------|
| Actual Function | Count | Expected Function | Count | Resul |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |

| Name | Input Value | | |
|--|--------------|--------------------------|-------|
| CRFMotorVel MtrRadpS T f32 | 400 | | |
| HwTorque HwNm T f32 | 3.6 | | |
| PrevTbarAng HwDeg M f32 | 2.39 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.2587 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.02547 | | |
| VehicleSpeed_Kph_T_f32 | 33.25 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 11.2 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 1.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 3968 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 4096 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 4224 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 4352 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 4480 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 4608 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 4736 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 4864 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 4992 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 47 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 48 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 52 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 53 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 56 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 57 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 61 | | |
| Name | Actual Value | Expected Value | Resul |
| DriverVelCalc() | 360.101318 | 360.1013205 ± 0.0009 | • |
| PrevTbarAng_HwDeg_M_f32 | 2.3999986 | 2.4 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.35398781 | 1.353990911 ± 0.00390625 | • |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.13 (Repeat Count = 1) | | V |
|--------------------------------------|-------------|---|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | -300.12 | |
| HwTorque_HwNm_T_f32 | -3.1 | |
| PrevTbarAng_HwDeg_M_f32 | -1.239 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.3697 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.02145 | |
| VehicleSpeed_Kph_T_f32 | 0 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 22.3 | |
| k_CmnTbarStiff_NmpDeg_f32 | 2.5 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.8 | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|--------------------------|----------|
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 59 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 62 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 66 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 69 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 71 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 72 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -239.688934 | -239.6889354 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -1.24000001 | -1.24 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.30814433 | 2.308144935 ± 0.00390625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Name | Input Value | | |
|--|--------------|-------------------------|-------|
| CRFMotorVel_MtrRadpS_T_f32 | 699.23 | | |
| HwTorque_HwNm_T_f32 | 4.2 | | |
| PrevTbarAng HwDeg M f32 | 1.191 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.2145 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.03692 | | |
| VehicleSpeed_Kph_T_f32 | 511.9921875 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 33.5 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 3.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.99 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 72 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 73 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 74 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 76 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 78 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 80 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 81 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 82 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 83 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 85 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 86 | | |
| Name | Actual Value | Expected Value | Resul |
| DriverVelCalc() | 693.519104 | 693.5191138 ± 0.0009 | |
| PrevTbarAng_HwDeg_M_f32 | 1.19999993 | 1.2 ± 0.00390625 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.26195955 | 3.26196066 ± 0.00390625 | |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |



| Test Step 1.15 (Repeat Count = 1) | | | ✓ |
|--|--------------|--------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -500.45 | | |
| HwTorque_HwNm_T_f32 | -4.5 | | |
| PrevTbarAng_HwDeg_M_f32 | -0.997 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5623 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.01258 | | |
| VehicleSpeed_Kph_T_f32 | 55.52 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 44.4 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 4.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 86 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 87 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 88 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 89 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 90 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 91 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 92 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 94 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 97 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -297.880035 | -297.8800114 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -1 | -1 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.4860363 | 4.486036266 ± 0.00390625 | ~ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|------|----|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 1.16 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | 600.63 |
| HwTorque_HwNm_T_f32 | -10 |
| PrevTbarAng_HwDeg_M_f32 | -20 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.8745 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.03257 |
| VehicleSpeed_Kph_T_f32 | 17.17 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 55.6 |
| k_CmnTbarStiff_NmpDeg_f32 | 0.5 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5 |
| t_CmnVehSpd_Kph_u9p7[0] | 128 |
| t_CmnVehSpd_Kph_u9p7[1] | 256 |
| t_CmnVehSpd_Kph_u9p7[2] | 384 |
| t_CmnVehSpd_Kph_u9p7[3] | 512 |
| t_CmnVehSpd_Kph_u9p7[4] | 640 |
| t_CmnVehSpd_Kph_u9p7[5] | 768 |
| t_CmnVehSpd_Kph_u9p7[6] | 896 |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 109 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 110 |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|--------------------------|----------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 111 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 113 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 114 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 115 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 116 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 117 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 118 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 119 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 121 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 122 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 305.571442 | 305.5714494 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -20 | -20 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.68316746 | 5.683167535 ± 0.00390625 | ✓ |

| T | | | | | V |
|-----------------------------|-------|-----------------------------|-------|-----|----------|
| Actual Function | Count | Expected Function | Count | Res | ult |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 1.17 (Repeat Count = 1) | | | → |
|--|--------------|---------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -600.84 | | |
| HwTorque_HwNm_T_f32 | 10 | | |
| PrevTbarAng_HwDeg_M_f32 | 20 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.369 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.096321 | | |
| VehicleSpeed_Kph_T_f32 | 27.95 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 66.5 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 0.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 15 | | |
| Name | Actual Value | Expected Value | Resul |
| DriverVelCalc() | -240.374832 | -240.3748238 ± 0.0009 | |
| PrevTbarAng HwDeg M f32 | 20 | 20 ± 0.00390625 | • |
| TbarVelFiltSv M str.SV Uls f32 | -2.1408155 | -2.140815551 ± 0.00390625 | |

| Τ | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | |

| Test Step 1.18 (Repeat Count = 1) | ✓ |
|-----------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | 150.14 |

t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10]

DriverVelCalc

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| Name | Input Value |
|---|-------------|
| HwTorque_HwNm_T_f32 | 0.05 |
| PrevTbarAng_HwDeg_M_f32 | 0 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.124 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.047852 |
| VehicleSpeed_Kph_T_f32 | 37.02 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 77.2 |
| k_CmnTbarStiff_NmpDeg_f32 | 10 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3 |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 |
| t_CmnVehSpd_Kph_u9p7[1] | 12928 |
| t_CmnVehSpd_Kph_u9p7[2] | 13056 |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 |
| t_CmnVehSpd_Kph_u9p7[4] | 13312 |
| t_CmnVehSpd_Kph_u9p7[5] | 13440 |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 |
| t_CmnVehSpd_Kph_u9p7[7] | 13696 |
| t_CmnVehSpd_Kph_u9p7[8] | 13824 |
| t_CmnVehSpd_Kph_u9p7[9] | 13952 |
| t_CmnVehSpd_Kph_u9p7[10] | 14080 |
| t_CmnVehSpd_Kph_u9p7[11] | 14208 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 3 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 4 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 5 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 6 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 8 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 9 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 10 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 12 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 13 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 14 |
| | |

| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 17 | | |
|--|---------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 44.9518433 | 44.95184416 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.00499999989 | 0.005 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.85488033 | -2.854880352 ± 0.00390625 | |

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|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.19 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -150.62 |
| HwTorque_HwNm_T_f32 | -7.5 |
| PrevTbarAng_HwDeg_M_f32 | -0.889 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.5511 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2356 |
| VehicleSpeed_Kph_T_f32 | 11.03 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 88.2 |
| k_CmnTbarStiff_NmpDeg_f32 | 8.5 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2 |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 5 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 6 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 8 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 9 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 10 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 12 |

DriverVelCalc



| Name | Input Value | | |
|--|--------------|---------------------------|----------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 19 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -30.2861042 | -30.28610622 ± 0.00009 | • |
| PrevTbarAng_HwDeg_M_f32 | -0.882352948 | -0.882352941 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.69583821 | -2.695837311 ± 0.00390625 | ~ |

| Τ | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • | |

| Test Step 1.20 (Repeat Count = 1) | | | ✓ |
|--|--------------|---------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 250.24 | | |
| HwTorque_HwNm_T_f32 | 8.2 | | |
| PrevTbarAng_HwDeg_M_f32 | 0.861 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.7412 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.3479 | | |
| VehicleSpeed_Kph_T_f32 | 33.04 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 99.3 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 9.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 20 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 24.7503471 | 24.7503467 ± 0.00009 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | 0.863157868 | 0.863157895 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.36847568 | -3.368470731 ± 0.00390625 | ✓ |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | • |

| Test Step 1.21 (Repeat Count = 1) | | |
|-----------------------------------|-------------|--|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | -250.62 | |
| HwTorque_HwNm_T_f32 | -8.5 | |
| PrevTbarAng_HwDeg_M_f32 | -16.997 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.2587 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2244 | |

DriverVelCalc()

PrevTbarAng_HwDeg_M_f32 TbarVelFiltSv_M_str.SV_Uls_f32 2014-09-19, 16:30:51+0530



| DriverVelCalc | 2014-09-19, 10.30.3110330 | | Razoncat |
|--|---------------------------|----------------|----------|
| Name | Input Value | | |
| VehicleSpeed_Kph_T_f32 | 44.05 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 27.2 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 0.5 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 22 | | |
| Name | Actual Value | Expected Value | Result |

| Т | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

-225.52951

0.639618635

-17

-225.5295319 ± 0.0009

0.63964772 ± 0.00390625

-17 ± 0.00390625

| Test Step 1.22 (Repeat Count = 1) | | |
|---|-------------|--|
| Name | Input Value | |
| CRFMotorVel MtrRadpS T f32 | 350.14 | |
| HwTorque_HwNm_T_f32 | 9.2 | |
| PrevTbarAng HwDeg M f32 | 0.919 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.3697 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.3366 | |
| VehicleSpeed_Kph_T_f32 | 376.06 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 26.8 | |
| k_CmnTbarStiff_NmpDeg_f32 | 10 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 1 | |
| t_CmnVehSpd_Kph_u9p7[0] | 3968 | |
| t_CmnVehSpd_Kph_u9p7[1] | 4096 | |
| t_CmnVehSpd_Kph_u9p7[2] | 4224 | |
| t_CmnVehSpd_Kph_u9p7[3] | 4352 | |
| t_CmnVehSpd_Kph_u9p7[4] | 4480 | |
| t_CmnVehSpd_Kph_u9p7[5] | 4608 | |
| t_CmnVehSpd_Kph_u9p7[6] | 4736 | |
| t_CmnVehSpd_Kph_u9p7[7] | 4864 | |
| t_CmnVehSpd_Kph_u9p7[8] | 4992 | |
| t_CmnVehSpd_Kph_u9p7[9] | 5120 | |
| t_CmnVehSpd_Kph_u9p7[10] | 5248 | |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 9 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 10 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 12 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 13 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 14 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 15 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 17 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 18 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 19 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 20 | |
| 0.0 | | |





| Name | Input Value | | |
|--|--------------|-------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 23 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 350.286285 | 350.2862746 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.91999957 | 0.92 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.74034667 | 1.74035898 ± 0.00390625 | |

| I | | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | |

| Test Step 1.23 (Repeat Count = 1) | | | ✓ |
|--|--------------|--------------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -350.36 | | |
| HwTorque_HwNm_T_f32 | -9.21 | | |
| PrevTbarAng_HwDeg_M_f32 | -1.841 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.2145 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.0147850001 | | |
| VehicleSpeed_Kph_T_f32 | 265.02 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 53.5 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 5.25 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.7 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 23 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 24 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -244.585281 | -244.585297 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -1.75428569 | -1.75428571428571 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.80800867 | 3.80800891 | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 1.24 (Repeat Count = 1) | | ✓ |
|--------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 450.52 | |
| HwTorque_HwNm_T_f32 | 1.5 | |
| PrevTbarAng_HwDeg_M_f32 | 1.154 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5623 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.5599 | |
| VehicleSpeed_Kph_T_f32 | 187.06 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 1 | |
| k_CmnTbarStiff_NmpDeg_f32 | 1.3 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 | |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|--------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t CmnVehSpd Kph u9p7[2] | 5120 | | |
| t CmnVehSpd Kph u9p7[3] | 6400 | | |
| t CmnVehSpd Kph u9p7[4] | 7680 | | |
| t CmnVehSpd Kph u9p7[5] | 8960 | | |
| t CmnVehSpd Kph u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 29 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 30 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 40 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 270.322723 | 270.3227163 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1.15384614 | 1.153846154 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.96478438 | 1.964798999 ± 0.00390625 | |

| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.25 (Repeat Count = 1) | | | ✓ |
|--|--------------|-----------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -450.58 | | |
| HwTorque_HwNm_T_f32 | -1.5 | | |
| PrevTbarAng_HwDeg_M_f32 | -0.551 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.8745 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 | | |
| VehicleSpeed_Kph_T_f32 | 166.08 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 100 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 2.7 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 39 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 40 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 43 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 44 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 45 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 47 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -222.18248 | -222.1824911 ± 0.0009 | ✓ |

DriverVelCalc



| Name | Actual Value | Expected Value | Result |
|--------------------------------|--------------|--------------------------|--------|
| PrevTbarAng_HwDeg_M_f32 | -0.555555522 | -0.55555556 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.84894514 | 4.848943456 ± 0.00390625 | ~ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.26 (Repeat Count = 1) | | | ✓ |
|--|--------------|----------------------------|--------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -689.69 | | |
| HwTorque HwNm T f32 | 2.5 | | |
| PrevTbarAng HwDeg M f32 | 0.805 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.369 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2365 | | |
| VehicleSpeed_Kph_T_f32 | 2.06 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 25.45 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 3.1 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.89 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 47 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 48 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 52 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 53 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 56 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 57 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 61 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -614.096802 | -614.096787907239 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.806451619 | 0.806451613 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.6370784 | -1.637078274 ± 0.00390625 | ~ |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Name | Input Value | |
|--------------------------------------|-------------|--|
| CRFMotorVel_MtrRadpS_T_f32 | -111.41 | |
| HwTorque_HwNm_T_f32 | -2.5 | |
| PrevTbarAng_HwDeg_M_f32 | -0.518 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.124 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.35874 | |
| VehicleSpeed_Kph_T_f32 | 267.07 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 75.5 | |
| k_CmnTbarStiff_NmpDeg_f32 | 4.8 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3 | |
| t_CmnVehSpd_Kph_u9p7[0] | 0 | |
| t_CmnVehSpd_Kph_u9p7[1] | 0 | |
| t_CmnVehSpd_Kph_u9p7[2] | 0 | |
| t_CmnVehSpd_Kph_u9p7[3] | 0 | |

DriverVelCalc

TbarVelFiltSv_M_str.SV_Uls_f32

2014-09-19, 16:30:51+0530



-2.51151124 ± 0.00390625

| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| t_CmnVehSpd_Kph_u9p7[4] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 0 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 59 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 62 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 66 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 69 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 71 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 72 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -35.2845802 | -35.2845812 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.520833313 | -0.520833333 ± 0.00390625 | • |
| | | | |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

-2.51150656

| Test Step 1.28 (Repeat Count = 1) | | | ✓ |
|--|--------------|--------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel MtrRadpS T f32 | 222.62 | | |
| HwTorque HwNm T f32 | 3.5 | | |
| PrevTbarAng HwDeg M f32 | 0.671 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.5511 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.47856 | | |
| VehicleSpeed_Kph_T_f32 | 510.03 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 46.2 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 5.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 32640 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 32640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 72 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 73 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 74 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 76 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 78 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 80 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 81 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 82 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 83 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 85 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 86 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 43.5075684 | 43.50756976 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.673076928 | 0.673076923 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.87615919 | -1.87615943 ± 0.00390625 | |



| T | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Name | Input Value | | |
|--|--------------|---------------------------|-------|
| CRFMotorVel MtrRadpS T f32 | -222.15 | | |
| HwTorque HwNm T f32 | -3.5 | | |
| PrevTbarAng HwDeg M f32 | -0.5134 | | |
| TbarVelFiltSv M str.SV Uls f32 | -5.7412 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.58963 | | |
| VehicleSpeed_Kph_T_f32 | 467.08 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 28.1 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 6.8 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 86 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 87 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 88 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 89 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 90 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 91 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 92 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 94 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 97 | | |
| Name | Actual Value | Expected Value | Resul |
| DriverVelCalc() | -23.2337227 | -23.23372292 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.514705896 | -0.514705882 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.74100852 | -2.74100995 ± 0.00390625 | |

| T | | | ✓ | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.30 (Repeat Count = 1) | | ✓ |
|--------------------------------------|-------------|----------|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | 333.17 | |
| HwTorque_HwNm_T_f32 | 4.5 | |
| PrevTbarAng_HwDeg_M_f32 | 0.614 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.2587 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.63214 | |
| VehicleSpeed_Kph_T_f32 | 166.92 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 85.6 | |
| k_CmnTbarStiff_NmpDeg_f32 | 7.3 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | |

DriverVelCalc



| Name | Input Value | | |
|--|-------------|--------------------------|----------|
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 0 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 0 | | |
| Name | | Expected Value | |
| DriverVelCalc() | 133.268005 | 133.268 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.616438329 | 0.616438356 ± 0.00390625 | ~ |
| TbarVelFiltSv M str.SV Uls f32 | 1.23370099 | 1.233716615 ± 0.00390625 | ✓ |

| T | | | | ✓ |
|-----------------------------|---|-----------------------------|------|----------|
| Actual Function | | Expected Function | Coun | t Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.31 (Repeat Count = 1) | ranger in the second of the se |
|--|--|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -333.62 |
| HwTorque_HwNm_T_f32 | -4.5 |
| PrevTbarAng_HwDeg_M_f32 | -0.917 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.3697 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.014785 |
| VehicleSpeed_Kph_T_f32 | 10.05 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 36.8 |
| k_CmnTbarStiff_NmpDeg_f32 | 4.9 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 128 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 128 |



| Test Step 1.32 (Repeat Count = 1) | | | ✓ |
|--|--------------|--------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 444.52 | | |
| HwTorque_HwNm_T_f32 | 5.5 | | |
| PrevTbarAng_HwDeg_M_f32 | 1.056 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.2145 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 | | |
| VehicleSpeed_Kph_T_f32 | 377.06 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 85.5 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 5.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 59 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 62 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 66 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 69 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 71 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 72 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 402.516144 | 402.5161456 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1.05769229 | 1.057692308 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.91656113 | 2.916562054 ± 0.00390625 | ~ |

| Т | | | | | V |
|-----------------------------|-------|-----------------------------|-------|------|----|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 1.33 (Repeat Count = 1) | ✓ |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -699.63 |
| HwTorque_HwNm_T_f32 | -5.5 |
| PrevTbarAng_HwDeg_M_f32 | -0.89 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5623 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2365 |
| VehicleSpeed_Kph_T_f32 | 38.17 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 29.2 |
| k_CmnTbarStiff_NmpDeg_f32 | 6.1 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0 |
| t_CmnVehSpd_Kph_u9p7[0] | 128 |
| t_CmnVehSpd_Kph_u9p7[1] | 256 |
| t_CmnVehSpd_Kph_u9p7[2] | 384 |
| t_CmnVehSpd_Kph_u9p7[3] | 512 |
| t_CmnVehSpd_Kph_u9p7[4] | 640 |
| t_CmnVehSpd_Kph_u9p7[5] | 768 |
| t_CmnVehSpd_Kph_u9p7[6] | 896 |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 86 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 87 |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 88 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 89 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 90 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 91 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 92 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 94 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 97 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 0.81372714 | 0.813727562 ± 0.0000009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.901639342 | -0.901639344 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.10696244 | 2.106963591 ± 0.00390625 | ~ |

| Т | | | | | V |
|-----------------------------|-------|-----------------------------|-------|-----|----------|
| Actual Function | Count | Expected Function | Count | Res | ult |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

| Test Step 1.34 (Repeat Count = 1) Name | Input Value | |
|--|--|---------------------|
| | The state of the s | |
| CRFMotorVel_MtrRadpS_T_f32 | 555.74 | |
| HwTorque_HwNm_T_f32 | 6.5 | |
| PrevTbarAng_HwDeg_M_f32 | 0.83 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.8745 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.35874 | |
| VehicleSpeed_Kph_T_f32 | 1.18 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 56.5 | |
| k_CmnTbarStiff_NmpDeg_f32 | 7.8 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 1 | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 109 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 110 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 111 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 113 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 114 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 115 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 116 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 117 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 118 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 119 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 121 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 122 | |
| Name | Actual Value Expe | ected Value Resul |
| DriverVelCalc() | · | 054289 ± 0.0009 |
| PrevTbarAng HwDeg M f32 | | 333333 ± 0.00390625 |
| TbarVelFiltSv M str.SV Uls f32 | | 98187 ± 0.00390625 |

| TDarveiFiltSv_M_str.Sv_Uis_t32 | 4.36498117 | 4.36498187 ± 0.00390625 | _ |
|--------------------------------|-------------------------|-------------------------|--------|
| | | | |
| T | | | ~ |
| Actual Function | Count Expected Function | Count | Result |

IntplVarXY_u16_u16Xu16Y_Cnt

| Test Step 1.35 (Repeat Count = 1) | ✓ |
|-----------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -555.81 |

IntplVarXY_u16_u16Xu16Y_Cnt

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-0.78313253 ± 0.00390625

-1.984843167 ± 0.00390625

| DriverVelCalc | | |
|---------------------|--|--|
| Name | | |
| HwTorque HwNm T f32 | | |

| HwTorgue HwMm_T_I32 | Name | Input Value | | |
|---|--|--------------|-----------------------|--------|
| Tbar/vE FillSv_M_str.K_Uls_732 0.47856 VehicleSpeed kph_T_152 276.19 0.47856 VehicleSpeed kph_T_152 2.3 0.47856 K_CmnSyskinRatio_MtrDegpHwDeg_132 12.3 0.5 | HwTorque_HwNm_T_f32 | -6.5 | | |
| ToarVelFillSv_M_str.K_Uls_f32 0.47856 | PrevTbarAng_HwDeg_M_f32 | -0.78 | | |
| VehicleSpeeLyph_T_f32 | TbarVelFiltSv_M_str.SV_Uls_f32 | -2.369 | | |
| k_CmnSyskinRatio_MirDegpHwDeg_f32 8.3 k_CmnTbarStiff_MmpDeg_f32 8.3 k_InriCmp_MirVel_ScaleFactor_Uis_f32 0.5 t_CmnVehSpd_Kph_u9p7[0] 12800 t_CmnVehSpd_Kph_u9p7[1] 12928 t_CmnVehSpd_Kph_u9p7[2] 13056 t_CmnVehSpd_Kph_u9p7[3] 13184 t_CmnVehSpd_Kph_u9p7[4] 13312 t_CmnVehSpd_Kph_u9p7[6] 13440 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[9] 13824 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[0] 14208 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTbY_Uis_u9p7[6] 41 t_InrtCmp_TBarVel_ScaleFactor | TbarVelFiltSv_M_str.K_Uls_f32 | 0.47856 | | |
| k_CmnTbarStiff_NmpDeg_132 | VehicleSpeed_Kph_T_f32 | 276.19 | | |
| k_inrtCmp_MtrVel_ScaleFactor_Uls_132 | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 12.3 | | |
| LCmnVehSpd_Kph_u9p7[0] 12800 LCmnVehSpd_Kph_u9p7[1] 13056 LCmnVehSpd_Kph_u9p7[3] 13184 LCmnVehSpd_Kph_u9p7[3] 13184 LCmnVehSpd_Kph_u9p7[5] 13312 LCmnVehSpd_Kph_u9p7[6] 13440 LCmnVehSpd_Kph_u9p7[6] 13568 LCmnVehSpd_Kph_u9p7[7] 13696 LCmnVehSpd_Kph_u9p7[7] 13696 LCmnVehSpd_Kph_u9p7[8] 13824 LCmnVehSpd_Kph_u9p7[9] 13952 LCmnVehSpd_Kph_u9p7[10] 14080 LCmnVehSpd_Kph_u9p7[10] 14208 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 36 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 39 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 LlmrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 LlmrCmp_TBarVel_Scale | k_CmnTbarStiff_NmpDeg_f32 | 8.3 | | |
| t_CmnVehSpd_Kph_u9p7[1] 12928 t_CmnVehSpd_Kph_u9p7[2] 13056 t_CmnVehSpd_Kph_u9p7[3] 13184 t_CmnVehSpd_Kph_u9p7[3] 13312 t_CmnVehSpd_Kph_u9p7[5] 13440 t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[7] 13896 t_CmnVehSpd_Kph_u9p7[8] 13952 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 47 Name | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5 | | |
| t_CmnVehSpd_Kph_u9p7[2] 13056 t_CmnVehSpd_Kph_u9p7[3] 13184 t_CmnVehSpd_Kph_u9p7[4] 13312 t_CmnVehSpd_Kph_u9p7[5] 13440 t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[6] 13696 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 42 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 47 Name | t_CmnVehSpd_Kph_u9p7[0] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[3] 13184 t_CmnVehSpd_Kph_u9p7[4] 13312 t_CmnVehSpd_Kph_u9p7[5] 13440 t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[7] 13686 t_CmnVehSpd_Kph_u9p7[7] 13896 t_CmnVehSpd_Kph_u9p7[8] 13824 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name | t_CmnVehSpd_Kph_u9p7[1] | 12928 | | |
| t_CmnVehSpd_Kph_u9p7[4] 13312 t_CmnVehSpd_Kph_u9p7[5] 13440 t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[8] 13824 t_CmnVehSpd_Kph_u9p7[8] 13952 t_CmnVehSpd_Kph_u9p7[9] 14080 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name | t_CmnVehSpd_Kph_u9p7[2] | 13056 | | |
| t_CmnVehSpd_Kph_u9p7[5] 13440 t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[8] 13894 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[0] 33 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[1] 34 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[1] 35 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[3] 36 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[3] 36 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[3] 38 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[6] 40 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[7] 41 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[7] 41 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[8] 43 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[8] 43 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[8] 43 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[9] 44 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[1] 45 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[1] 45 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[1] 47 Name Expected Value Expected Value Result | t_CmnVehSpd_Kph_u9p7[3] | 13184 | | |
| t_CmnVehSpd_Kph_u9p7[6] 13568 t_CmnVehSpd_Kph_u9p7[7] 13696 t_CmnVehSpd_Kph_u9p7[8] 13824 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 | t_CmnVehSpd_Kph_u9p7[4] | 13312 | | |
| t_CmnVehSpd_Kph_u9p7[7] | t_CmnVehSpd_Kph_u9p7[5] | 13440 | | |
| t_CmnVehSpd_Kph_u9p7[8] 13824 t_CmnVehSpd_Kph_u9p7[9] 13952 t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 41 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 45 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_IntCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name | t_CmnVehSpd_Kph_u9p7[6] | 13568 | | |
| t_CmnVehSpd_Kph_u9p7[9] | t_CmnVehSpd_Kph_u9p7[7] | 13696 | | |
| t_CmnVehSpd_Kph_u9p7[10] 14080 t_CmnVehSpd_Kph_u9p7[11] 14208 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7[11] 47 Name | t_CmnVehSpd_Kph_u9p7[8] | 13824 | | |
| t_CmnVehSpd_Kph_u9p7[11] | t_CmnVehSpd_Kph_u9p7[9] | 13952 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 33 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_CmnVehSpd_Kph_u9p7[10] | 14080 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 34 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_CmnVehSpd_Kph_u9p7[11] | 14208 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 35 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 36 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 38 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 39 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 40 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 41 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 39 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 43 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | | 40 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 44 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 45 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 43 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 47 Name Actual Value Expected Value Result | | | | |
| Name Actual Value Expected Value Result | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | | | |
| · | t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 47 | | |
| DriverVelCalc() -278.061462 -278.0614576 ± 0.0009 ✓ | Name | Actual Value | Expected Value | Result |
| | DriverVelCalc() | -278.061462 | -278.0614576 ± 0.0009 | ~ |

| Τ | | | | | V |
|-----------------------------|-------|-----------------------------|-------|------|----|
| Actual Function | Count | Expected Function | Count | Resu | lt |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | | ~ |

-0.783132493

-1.98484111

| Test Step 1.36 (Repeat Count = 1) | |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | 666.86 |
| HwTorque_HwNm_T_f32 | 7.5 |
| PrevTbarAng_HwDeg_M_f32 | 0.799 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.124 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.001255848 |
| VehicleSpeed_Kph_T_f32 | 354.2 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 64.4 |
| k_CmnTbarStiff_NmpDeg_f32 | 9.3 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5 |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 |
| t_CmnVehSpd_Kph_u9p7[5] | 16128 |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 |
| t_CmnVehSpd_Kph_u9p7[7] | 16384 |
| t_CmnVehSpd_Kph_u9p7[8] | 16512 |
| t_CmnVehSpd_Kph_u9p7[9] | 16640 |
| t_CmnVehSpd_Kph_u9p7[10] | 16768 |
| t_CmnVehSpd_Kph_u9p7[11] | 16896 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 47 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 48 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 51 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 52 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 53 |

PrevTbarAng_HwDeg_M_f32

TbarVelFiltSv_M_str.SV_Uls_f32

DriverVelCalc



| | 1 | | |
|--|--------------|---------------------------|--------|
| Name | Input Value | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 56 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 57 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 61 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 331.76123 | 331.7612295 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.806451619 | 0.806451613 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.11539769 | -3.115397684 ± 0.00390625 | ~ |

| T | | | | |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - |

| Test Step 1.37 (Repeat Count = 1) | | | ✓ |
|--|--------------|--------------------------|--------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | -666.71 | | |
| HwTorque_HwNm_T_f32 | -7.5 | | |
| PrevTbarAng_HwDeg_M_f32 | -6.249 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.5511 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.715390457 | | |
| VehicleSpeed_Kph_T_f32 | 254.52 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 27.2 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 1.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 10752 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 10880 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 11008 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11264 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 11392 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 11648 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 11776 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 59 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 62 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 66 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 69 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 71 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 72 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -267.125366 | -267.1254046 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -6.24999952 | -6.25 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.6527853 | -1.65298172 ± 0.00390625 | ~ |

| Т | | | | V |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | ✓ |

| Test Step 1.38 (Repeat Count = 1) | |
|-----------------------------------|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | 134.52 |
| HwTorque_HwNm_T_f32 | 8.5 |
| PrevTbarAng_HwDeg_M_f32 | 3.86 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.7412 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.58746 |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| VehicleSpeed_Kph_T_f32 | 154.63 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 75.1 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 2.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 5248 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 5376 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 5504 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 5632 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 5760 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 5888 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 6016 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 6144 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 6272 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 6528 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 6656 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 29 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 30 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 40 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 39.8233643 | 39.8233612 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 3.86363626 | 3.863636364 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.30036688 | -1.300365557 ± 0.00390625 | ~ |

| Τ | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.39 (Repeat Count = 1) | |
|---|-------------|
| Name | Input Value |
| CRFMotorVel_MtrRadpS_T_f32 | -463.91 |
| HwTorque_HwNm_T_f32 | -8.5 |
| PrevTbarAng_HwDeg_M_f32 | -2.35 |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.6667 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.35874 |
| VehicleSpeed_Kph_T_f32 | 55.24 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 20.6 |
| k_CmnTbarStiff_NmpDeg_f32 | 3.6 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2 |
| t_CmnVehSpd_Kph_u9p7[0] | 3968 |
| t_CmnVehSpd_Kph_u9p7[1] | 4096 |
| t_CmnVehSpd_Kph_u9p7[2] | 4224 |
| t_CmnVehSpd_Kph_u9p7[3] | 4352 |
| t_CmnVehSpd_Kph_u9p7[4] | 4480 |
| t_CmnVehSpd_Kph_u9p7[5] | 4608 |
| t_CmnVehSpd_Kph_u9p7[6] | 4736 |
| t_CmnVehSpd_Kph_u9p7[7] | 4864 |
| t_CmnVehSpd_Kph_u9p7[8] | 4992 |
| t_CmnVehSpd_Kph_u9p7[9] | 5120 |
| t_CmnVehSpd_Kph_u9p7[10] | 5248 |
| t_CmnVehSpd_Kph_u9p7[11] | 5376 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 33 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 34 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 35 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 36 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 38 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 39 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 40 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 41 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 43 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 44 |



| Driver\ | /elCalc |
|---------|---------|
|---------|---------|

| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 45 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 47 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -93.6095047 | -93.60949919 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -2.36111116 | -2.361111111 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.26811457 | -6.268088042 ± 0.00390625 | ~ |

| Т | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.40 (Repeat Count = 1) | | | ✓ |
|--|--------------|--------------------------|----------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 263.42 | | |
| HwTorque HwNm T f32 | 9.5 | | |
| PrevTbarAng_HwDeg_M_f32 | 2.25 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.6667 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2874 | | |
| VehicleSpeed_Kph_T_f32 | 444.52 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 21.7 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 4.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 128 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 256 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 384 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 47 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 48 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 52 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 53 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 56 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 57 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 60 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 61 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 27.5082359 | 27.50822923 ± 0.00009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 2.26190495 | 2.261904762 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_UIs_f32 | 6.46143246 | 6.461404706 ± 0.00390625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY u16 u16Xu16Y Cnt | 1 | IntplVarXY u16 u16Xu16Y Cnt | 1 | _ |

| Test Step 1.41 (Repeat Count = 1) | | ✓ |
|--------------------------------------|-------------|---|
| Name | Input Value | |
| CRFMotorVel_MtrRadpS_T_f32 | -522.63 | |
| HwTorque_HwNm_T_f32 | -9.5 | |
| PrevTbarAng_HwDeg_M_f32 | -1.819 | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.025479 | |
| VehicleSpeed_Kph_T_f32 | 333.62 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 45.8 | |
| k_CmnTbarStiff_NmpDeg_f32 | 5.2 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 | |

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DriverVelCalc

| Name | Input Value | | |
|--|--------------|---------------------------|----------|
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 3840 | | |
| t CmnVehSpd Kph u9p7[2] | 5120 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 8960 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 12800 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 14080 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 15360 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 16640 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 23 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 24 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -470.382141 | -470.3821283 ± 0.0009 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -1.82692313 | -1.826923077 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.100936659 | -0.100936038 ± 0.00390625 | ✓ |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Test Step 1.42 (Repeat Count = 1) | | | ✓ |
|--|--------------|----------------------|--------|
| Name | Input Value | | |
| CRFMotorVel_MtrRadpS_T_f32 | 357.25 | | |
| HwTorque_HwNm_T_f32 | 1.563 | | |
| PrevTbarAng_HwDeg_M_f32 | 0.251 | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.6987 | | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.03698 | | |
| VehicleSpeed_Kph_T_f32 | 222.42 | | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 76.9 | | |
| k_CmnTbarStiff_NmpDeg_f32 | 6.2 | | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.8 | | |
| t_CmnVehSpd_Kph_u9p7[0] | 6784 | | |
| t_CmnVehSpd_Kph_u9p7[1] | 6912 | | |
| t_CmnVehSpd_Kph_u9p7[2] | 7040 | | |
| t_CmnVehSpd_Kph_u9p7[3] | 7168 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 7296 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 7424 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 7552 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 7680 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 7808 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 7936 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 8064 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 8192 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 29 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 30 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 40 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | 288.110321 | 288.1102911 ± 0.0009 | ~ |

DriverVelCalc



| Name | Actual Value | Expected Value | Result |
|--------------------------------|--------------|--------------------------|--------|
| PrevTbarAng_HwDeg_M_f32 | 0.252096772 | 0.252096774 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.50824165 | 5.508241429 ± 0.00390625 | ~ |

| Т | | | | V |
|-----------------------------|-------|-----------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| CRFMotorVel MtrRadpS T f32 | -464.25 | | |
| HwTorque HwNm T f32 | -2.645 | | |
| PrevTbarAng HwDeg M f32 | -0.3525 | | |
| TbarVelFiltSv M str.SV Uls f32 | -5.1423 | | |
| TbarVelFiltSv M str.K Uls f32 | 0.024588 | | |
| VehicleSpeed Kph T f32 | 111.52 | | |
| k CmnSysKinRatio MtrDegpHwDeg f32 | 42.5 | | |
| k CmnTbarStiff NmpDeg f32 | 7.5 | | |
| k InrtCmp MtrVel ScaleFactor Uls f32 | 0.7 | | |
| t CmnVehSpd Kph u9p7[0] | 128 | | |
| t CmnVehSpd Kph u9p7[1] | 256 | | |
| t CmnVehSpd Kph u9p7[2] | 384 | | |
| t CmnVehSpd Kph u9p7[3] | 512 | | |
| t_CmnVehSpd_Kph_u9p7[4] | 640 | | |
| t_CmnVehSpd_Kph_u9p7[5] | 768 | | |
| t_CmnVehSpd_Kph_u9p7[6] | 896 | | |
| t_CmnVehSpd_Kph_u9p7[7] | 1024 | | |
| t_CmnVehSpd_Kph_u9p7[8] | 1152 | | |
| t_CmnVehSpd_Kph_u9p7[9] | 1280 | | |
| t_CmnVehSpd_Kph_u9p7[10] | 1408 | | |
| t_CmnVehSpd_Kph_u9p7[11] | 1536 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 34 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 39 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 40 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 43 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 44 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 45 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 47 | | |
| Name | Actual Value | Expected Value | Result |
| DriverVelCalc() | -326.341705 | -326.3417122 ± 0.0009 | - |
| PrevTbarAng_HwDeg_M_f32 | -0.352666676 | -0.352666667 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.01791048 | -5.017910128 ± 0.00390625 | • |

| T | | | | ✓ |
|-----------------------------|-------|-----------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |

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FrqDepDmpnInrtCmp_Per1

 Project
 FDD_Inertia

 Module
 FDD_Inertia

 Test Object
 FrqDepDmpnInrtCmp_Per1

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| Total Testcases | 3 |
|-----------------|---|
| Successful | 3 |
| Failed | 0 |
| Not Executed | 0 |





Module Properties

| Project Root Directory | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp |
|------------------------|--|
| Configuration File | D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | $\label{lem:projection} $$(PROJECTROOT)\FrqDepDmpnInrtCmp\src\Ap_FrqDepDmpnInrtCmp.c$ |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\-1\\$(PROJECTROOT)\\FrqDepDmpnInrtCmp\utp\contract\Ap_FrqDepDmpnInrtCmp -I\\$(PROJECTROOT) \NxtrLib\\include -I\\$(PROJECTROOT)\\StdDef\\include -I\\$(Projempring)\Texas Instruments\\ccsv4\\tooIs\\comp\\indle -I\\$(Projempring)\Texas Instruments\\\ccsv4\\tooIs\\\comp\\indle -I\\$(Projempring)\Texas Instruments\\\\ccsv4\\\tooIs\\\\comp\\indle -I\\$(Projempring)\Texas Instruments\\\\\\ccsv4\\\\\\\\\\\\\\\\\\\\\\\\\\\\ |
| File | \$(PROJECTROOT)\NxtrLib\src\interpolation.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -Dstatic= -DBC_FREQDEPDAMPING_FAULTINJECTIONPOINT=STD_OFF -I\$(PROJECTROOT) \FrqDepDmpnInrtCmp\utp\contract\4p_FrqDepDmpnInrtCmp\utp\contract\4p_FrqDepDmpnInrtCmp -I\$(PROJECTROOT) \NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include |

| Comments/Descripti | on/Specification |
|----------------------|--|
| Name | Text |
| Module 'FDD_Inertia' | **************************Unit Test Description************************************ |
| | Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_FrqDepDmpnInrtCmp.c Code File(s) Version: 13 Module Design Document: Frequency_Dependent_Damping_And_Inertia_Compensation_MDD.doc Module Design Document Version: 18 Data Dictionary Version: 16 Unit Test Plan Version: 6 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes): 1994 Total RAM Used (Bytes): 328 Special Test Requirements: Test Date: 09-19-2014 Comments: |
| | Note1:Inline Function defined in ""globalmacro.h"" are not unit tested. |
| | Note2:""CBD_Sandbox_dbg.map"" file is embedded for reference. |
| | Note3:In ""DriverVelCalc"" function,difference between TbarAngle and PrevTbarAngle cannot be more than 0.013334 since this function is run in 2ms period so Max value for ""PrevTbarAng_HwDeg_M_f32"" variable is given as 1.013334 in All Max Vector and also in All Max Vector of ""FrqDepDmpnInrtCmp_Per1"" function. |
| | Note4:In ""ADDCoefCalc"" function,return value is going out of range due to conversion happening in the function. |
| | Note5:In ""FilterCoefCalc"" function,the Range of the Structure Variable "filtCoef_Uls_T_Str.b0_Uls_f32" is calculated as -2.74156205240179 to 0 and "filtCoef_Uls_T_Str.b1_Uls_f32" is calculated as -0.160083862455113 to 2.41111405240179 and the same is updated in MDD version 16 |
| | Note6:In ""GenFddIcCmd"" function, return value and output variable ""Prev1PreAttnComp_MtrNm_M_f32"" are going out of range.And as there is call to this function in ""FrqDepDmpnInrtCmp_Per1"" so here also output variable ""Prev1PreAttnComp_MtrNm_M_f32"" is going out of range. |
| | Note 7:The range of the parameter "VehicleSpeed_Kph_T_f32" is mentioned in MDD as 0 to 512, but at line number 437, FPM_FloatToFixed_m macro is used for U9P7_T, For All Max vector of parameter ""VehicleSpeed_Kph_T_f32"", the value is going out of range, so its range is considered as "" 0 to 511.9921875"" considering data type u9P7 as per email communication. |
| | Note 8: Six significant tolerance is used in the functions ""ADDCoefCalc"", ""DecelGain"", ""DirverVelCalc"", ""FilterCoefCalc"", ""GenFddlcCmd"" for the return values and in function ""FrqDepDmpnInrtCmp_Per1"" for the variable ""Prev1PreAttnComp_MtrNm_M_f32"". |
| | *************************************** |

| Attributes | | |
|-----------------------|--|--|
| Name | Value | |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 | |
| Float Precision | 9 | |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj | |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src | |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd | |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl | |
| Target Install Path | <pre>\$(ProgramFiles)\pls\UDE 3.2</pre> | |
| Time Unit | Cycles | |
| Timer Enabled | false | |
| Timer Prescale | 0 | |
| Timer Resolution | 1 | |

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| Attributes | |
|--|--|
| Name Value | |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Orkspace File D:\Synergy_Work_Area\CBD_FrqDepDmpnInrtCmp\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP | |



| Usercode | |
|---|--|
| Stub Function Name | Stub Function Body |
| ${\tt Rte_Call_FrqDepDmpnInrtCmp_Perl_CP0_Checkpo.}$ | \$stub void Rte_Call_FrqDepDmpnInrtCmp_Perl_CP0_CheckpointReached() { |
| | /* empty stub code created by TESSY */ } |
| ${\tt Rte_Call_FrqDepDmpnInrtCmp_Perl_CPl_Checkpo}$ | <pre>\$stub void Rte_Call_FrqDepDmpnInrtCmp_Perl_CPl_CheckpointReached() {</pre> |
| | /* empty stub code created by TESSY */ } |

| | 1: Metrics Test | • |
|---------------|---|---|
| Specification | Performance Metrics (With "None" Instrumentation and "WithPS" Environment) | |
| | CPU Cycles: | |
| | TS1.1 5667.00 Cycles TS1.2 5703.00 Cycles | |
| Description | Test Vector Description: | |
| | TS1.1 "Shortest Execution Path: (FDDDefSrvFlg_Cnt_T_lgc == TRUE)=False (FrqDepDmpnInrtCmp_MtrNm_T_f32>=D_MTRTRQCMDHILMT_MTRNM_F32)=True" TS1.2 "Longest Execution Path: (FDDDefSrvFlg_Cnt_T_lgc == TRUE)=True (FrqDepDmpnInrtCmp_MtrNm_T_f32>= D_MTRTRQCMDHILMT_MTRNM_F32)=False (FrqDepDmpnInrtCmp_MtrNm_T_f32>= -D_MTRTRQCMDHILMT_MTRNM_F32)=False" | |

| Test Step 1.1 (Repeat Count = 1) Name | Input Value |
|--|-----------------------------------|
| | • |
| PreDecelGain_Uls_M_f32 | 1 |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.1 |
| Prev1ScIDrvVel_RadpS_M_f32 | 2205.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.3 |
| Prev2SclDrvVel_RadpS_M_f32 | 101.2 |
| PrevTbarAng_HwDeg_M_f32 | -8.32 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| FbarVelFiltSv_M_str.SV_Uls_f32 | 3.5 |
| FbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 |
| c_CmnSysKinRatio_MtrDegpHwDeg_f32 | 10.2 |
| C_CmnTbarStiff_NmpDeg_f32 | 1.2 |
| C_DmpDecelGainFSlew_UlspS_f32 | 100.02 |
| <_DmpDecelGain_Uls_f32 | 2.5 |
| <_DmpGainOffThresh_KphpS_f32 | 16.5 |
| c_DmpGainOnThresh_KphpS_f32 | 30.2 |
| <pre><_InrtCmp_MtrInertia_KgmSq_f32</pre> | 0.00008 |
| <pre><_InrtCmp_MtrVel_ScaleFactor_Uls_f32</pre> | 0.9 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 |
| 2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][9] | 1659 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| 2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][1] | 683 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 |
| 2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][5] | 2046 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 2728 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 3068 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] | 3409 |
| 2 FDD FreqTblYM Hz u12p4[0][0] | 16 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 32 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 48 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 64 |
| 2_FDD_rreqTblYM_Hz_u12p4[0][4] | 80 |
| 2_FDD_FreqTbIYM_Hz_u12p4[0][4] 2 FDD FreqTbIYM Hz u12p4[0][5] | 96 |
| 2_FDD_F1eq1b11M_Hz_u12p4[0][5] 2 FDD FreqTbIYM Hz_u12p4[0][6] | 112 |

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| Name | Input Value | |
|---|-------------|--|
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 128 | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][8] | 144 | |
| 2_FDD_FreqTbIYM_Hz_u12p4[0][9] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 192 | |
| | | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 32 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 48 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][2] | 64 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 208 | |
| _CmnVehSpd_Kph_u9p7[0] | 128 | |
| _CmnVehSpd_Kph_u9p7[1] | 256 | |
| _CmnVehSpd_Kph_u9p7[2] | 384 | |
| _CmnVehSpd_Kph_u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| _CmnVehSpd_Kph_u9p7[5] | 768 | |
| _CmnVehSpd_Kph_u9p7[6] | 896 | |
| _CmnVehSpd_Kph_u9p7[7] | 1024 | |
| _CmnVehSpd_Kph_u9p7[8] | 1152 | |
| CmnVehSpd_Kph_u9p7[9] | 1280 | |
| _CmnVehSpd_Kph_u9p7[10] | 1408 | |
| _CmnVehSpd_Kph_u9p7[11] | 1536 | |
| omnvenepu_rpn_uspr[11] _DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| | 4915 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 408 | |
| DmpDecelGainSlewY_UlspS_u13p3[1] | 416 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 424 | |
| DmpDecelGainSlewY UlspS u13p3[3] | 432 | |
| DmpDecelGainSlewY UlspS u13p3[4] | 440 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 448 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| DmpFiltKpWIRBIndY_UIS_u2p14[0] | 3277 | |
| | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 3099 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 240 | |
| | 320 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | | |
| _FDD_AttenTblY_Uls_u8p8[0] | 49 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 51 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 3 | |
| _FDD_BlendTblY_Uls_u8p8[1] | 5 | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 8 | |

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FrqDepDmpnInrtCmp_Per1

| Namo | Input Value | | |
|---|--------------|---------------------------|--------|
| Name t_FDD_BlendTblY_Uls_u8p8[3] | 10 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 13 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 31 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[6] | 90 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 15 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[2] | 4915 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 282 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 358 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 384 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 8.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 600.2 | | |
| $tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value$ | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -10 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -35.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 100.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 1.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | 0- 1 1 1 | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIl tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_kte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_Hw1orque_Hw tgt_kte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per I VehicleSpeed | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 WIRCmdAmpl | | | |
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 1.11199999 | 1.112 ± 0.0625 | Result |
| Prev1PreAttnComp MtrNm M f32 | 128.764511 | 128.764510970637 ± 0.0009 | |
| Prev1ScIDrvVel RadpS M f32 | 540.226318 | 540.2263355 ± 0.00390625 | |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.10000002 | 1.1 ± 0.00048828125 | • |
| Prev2ScIDrvVel_RadpS_M_f32 | 2205.30005 | 2205.3 ± 0.00390625 | |
| PrevTbarAng_HwDeg_M_f32 | -8.33333302 | -8.333333333 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.22103405 | 2.221033333 ± 0.00390625 | • |
| tat FraDenDmnnInrtCmn Per1 FraDenDmnnInrtCmn MtrNm f32 value | 8 8000019 | 8 8 + 0 00048828125 | |

8.80000019

 $tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value$

8.8 ± 0.00048828125



| T | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Took Ston 4.2 (Bonnet Count = 4) | |
|---|-----------------------------------|
| Test Step 1.2 (Repeat Count = 1) | |
| | Input Value |
| | 125487.235 |
| | 1.1 |
| | 2205.3 |
| | 7.3 |
| Prev2SclDrvVel_RadpS_M_f32 | 101.2 |
| PrevTbarAng_HwDeg_M_f32 | -8.32 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 10.2 |
| k_CmnTbarStiff_NmpDeg_f32 | 1.2 |
| k_DmpDecelGainFSlew_UlspS_f32 | 100.02 |
| k_DmpDecelGain_Uls_f32 | 2.5 |
| k_DmpGainOffThresh_KphpS_f32 | 16.5 |
| k_DmpGainOnThresh_KphpS_f32 | 30.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00008 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 |
| | 661 |
| | 827 |
| | 994 |
| | 1160 |
| | 1326 |
| | 1493 |
| | 1659 |
| | 342 |
| | 683 |
| | 1024 |
| | 1364 |
| | 1705 |
| | 2046 |
| | 2387 |
| | 2728 |
| | 3068 |
| | 3409 |
| | 16 |
| , | 32 |
| | 48 |
| | 64 |
| ' '' | |
| | 80 |
| | 96 |
| | 112 |
| : : | 128 |
| | 144 |
| | 160 |
| | 176 |
| | 192 |
| | 32 |
| | 48 |
| | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 |

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| FrqDepDmpnInrtCmp_Per1 | | TO TO TO |
|--|--------------|----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 | |
| P_FDD_FreqTblYM_Hz_u12p4[1][7] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 208 | |
| CmnVehSpd_Kph_u9p7[0] | 128 | |
| CmnVehSpd_Kph_u9p7[1] | 256 | |
| _CmnVehSpd_Kph_u9p7[2] | 384 | |
| _CmnVehSpd_Kph_u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| CmnVehSpd_Kph_u9p7[5] | 768 | |
| CmnVehSpd_Kph_u9p7[6] | 896 | |
| CmnVehSpd_Kph_u9p7[7] | 1024 | |
| CmnVehSpd_Kph_u9p7[8] | 1152 | |
| _CmnVehSpd_Kph_u9p7[9] | 1280 | |
| CmnVehSpd_Kph_u9p7[10] | 1408 | |
| CmnVehSpd_Kph_u9p7[11] | 1536 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 408 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 416 | |
| DmpDecelGainSlewY_UlspS_u13p3[2] | 424 | |
| DmpDecelGainSlewY_UlspS_u13p3[3] | 432 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 440 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 448 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| DmpFiltKpWIRBIndY Uls u2p14[1] | 3277 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| DmpFiltKpWIRBIndY Uls u2p14[3] | 6554 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[0] | 523 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1038 | |
| | 1553 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2068 2583 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3099 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3614 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 240 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 320 | |
| FDD_AttenTblY_Uls_u8p8[0] | 49 | |
| FDD_AttenTblY_Uls_u8p8[1] | 51 | |
| FDD_BlendTblY_Uls_u8p8[0] | 3 | |
| FDD_BlendTblY_Uls_u8p8[1] | 5 | |
| FDD_BlendTbIY_Uls_u8p8[2] | 8 | |
| FDD_BlendTbIY_Uls_u8p8[3] | 10 | |
| FDD_BlendTblY_Uls_u8p8[4] | 13 | |
| FDD_BlendTbIY_Uls_u8p8[5] | 15 | |
| FDD_BlendTbIY_Uls_u8p8[6] | 18 | |
| FDD_BlendTbIY_Uls_u8p8[7] | 20 | |
| FDD_BlendTblY_Uls_u8p8[8] | 23 | |
| FDD_BlendTblY_Uls_u8p8[9] | 26 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[11] | 31 | |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 13 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 14 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 15 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 1638 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 3277 | | |
| t RIAstWIRBIndTbIY Uls u2p14[2] | 4915 | | |
| t RIAstWIRBIndTbIY Uls u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 282 | | |
| t WIRBIndTblX MtrNm u8p8[1] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 358 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 384 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 8.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 600.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -10 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -21.32 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 100.01 | | |
| tgt FrgDepDmpnInrtCmp Per1 WIRCmdAmpBInd MtrNm f32.value | 1.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | · - · · · | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt Rte Inst Ap FrgDepDmpnInrtCmp.FrgDepDmpnInrtCmp Per1 VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB | | | |
| | | | Basiil |
| Name | Actual Value | Expected Value | Result |

| 20 | | · · · · _ · · · · · · · · · · · · · · · | |
|--|--------------|---|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125487.234 | 125487.235 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 14899641 | 14899642.12 ± 99.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 540.226318 | 540.2263355 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.10000002 | 1.1 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 2205.30005 | 2205.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -8.33333302 | -8.333333333 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.22103405 | 2.221033333 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | 0 | 0 + 0 00048828125 | _ |



FrqDepDmpnInrtCmp_Per1

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

Test Case 2: Path Test

Specification

Performance Metrics (With "None" Instrumentation and "WithPS"

Environment)

CPU Cycles:

TS2.1 5693.00 Cycles TS2.2 5724.00 Cycles TS2.3 6713.00 Cycles

Description

Test Vector Description:

 $\label{eq:total_$

| Test Step 2.1 (Repeat Count = 1) Name | Input Value |
|---|-----------------------------------|
| PreDecelGain Uls M f32 | 125487.235 |
| Prev1PreAttnComp MtrNm M f32 | 1.1 |
| Prev1ScIDrvVel RadpS M f32 | 2205.3 |
| Prev2PreAttnComp MtrNm M f32 | 7.3 |
| Prev2ScIDrvVel RadpS M f32 | 101.2 |
| PrevTbarAng HwDeg M f32 | -8.32 |
| Rte Inst Ap FrqDepDmpnInrtCmp | tgt Rte Inst Ap FrgDepDmpnInrtCmp |
| TbarVelFiltSv M str.SV Uls f32 | 3.5 |
| TbarVelFiltSv M str.K Uls f32 | 0.1258 |
| k CmnSysKinRatio MtrDegpHwDeg f32 | 10.2 |
| k CmnTbarStiff NmpDeg f32 | 1.2 |
| k DmpDecelGainFSlew UlspS f32 | 100.02 |
| k DmpDecelGain Uls f32 | 2.5 |
| k DmpGainOffThresh KphpS f32 | 16.5 |
| k DmpGainOnThresh KphpS f32 | 30.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00008 |
| k InrtCmp MtrVel ScaleFactor Uls f32 | 0.9 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][9] | 3409 |

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| гідоеротрініні стр_гегі | | Macifat |
|---|-------------|---------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 32 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 48 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 64 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][4] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][5] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][6] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][7] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 32 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 48 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 64 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 208 | |
| _CmnVehSpd_Kph_u9p7[0] | 128 | |
| _CmnVehSpd_Kph_u9p7[1] | 256 | |
| _CmnVehSpd_Kph_u9p7[2] | 384 | |
| _CmnVehSpd_Kph_u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| _CmnVehSpd_Kph_u9p7[5] | 768 | |
| _CmnVehSpd_Kph_u9p7[6] | 896 | |
| _CmnVehSpd_Kph_u9p7[7] | 1024 | |
| _CmnVehSpd_Kph_u9p7[8] | 1152 | |
| _CmnVehSpd_Kph_u9p7[9] | 1280 | |
| _CmnVehSpd_Kph_u9p7[10] | 1408 | |
| _CmnVehSpd_Kph_u9p7[11] | 1536 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712 | |
| DmpDecelGainSlewY_UlspS_u13p3[0] | 408 | |
| DmpDecelGainSlewY_UlspS_u13p3[1] | 416 | |
| DmpDecelGainSlewY_UlspS_u13p3[2] | 424 | |
| DmpDecelGainSlewY_UlspS_u13p3[3] | 432 | |
| DmpDecelGainSlewY_UlspS_u13p3[4] | 440 | |
| DmpDecelGainSlewY_UlspS_u13p3[5] | 448 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2068 | |
| _FDD_ADDStaticTbl1_MtlNlllpRadpS_ull11p17[3] _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 | |
| | 3099 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3614 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | |
| | | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | |

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| Name | Input Value | | |
|---|---------------------------------|-------------------------------|-------|
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 240 | | |
| t FDD AttenTblX MtrRadpS u12p4[1] | 320 | | |
| t_FDD_AttenTblY_Uls_u8p8[0] | 49 | | |
| t_FDD_AttenTblY_Uls_u8p8[1] | 51 | | |
| t_FDD_BlendTblY_Uls_u8p8[0] | 3 | | |
| t_FDD_BlendTblY_Uls_u8p8[1] | 5 | | |
| t_FDD_BlendTblY_Uls_u8p8[2] | 8 | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 10 | | |
| t FDD BlendTblY Uls u8p8[4] | 13 | | |
| t FDD BlendTblY Uls u8p8[5] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 28 | | |
| t_FDD_BlendTbIY_Uls_u8p8[11] | 31 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t InrtCmp_ScaleFactorTblY_Uls_u9p7[1] t InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_Intromp_ScaleFactorTblY_Ols_u9p7[4] t Intromp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[7] | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 15 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 282 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 358 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 384 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 8.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 600.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -10 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 10.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 100.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 1.2 | | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmpDmpnInrtCmp_BaseAssistCnppTerpDmpnInrtCmpDmpnDmpnInrtCmp_BaseAssistCnppTe$ | nc tgt_FrqDepDmpnInrtCmp_Per1_E | BaseAssistCmd_MtrNm_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_CRFMoto$ | _I tgt_FrqDepDmpnInrtCmp_Per1_C | CRFMotorVel_MtrRadpS_f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 FreqDepDmp | Sr tgt_FrqDepDmpnInrtCmp_Per1_F | reqDepDmpSrlComSvcDft_Cnt_lgc | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpn$ | In tgt_FrqDepDmpnInrtCmp_Per1_F | rqDepDmpnInrtCmp_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hv | wt tgt_FrqDepDmpnInrtCmp_Per1_F | lwTorque_HwNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAc | cc tgt_FrqDepDmpnInrtCmp_Per1_V | /ehicleLonAccel_KphpS_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed | _I tgt_FrqDepDmpnInrtCmp_Per1_V | /ehicleSpeed_Kph_f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 WIRCmdAmp | B tat FraDepDmpnInrtCmp Per1 V | VIRCmdAmpBlnd_MtrNm_f32 | |
| tgt_tte_inst_Ap_1 rqbepbinpinintcinp.i rqbepbinpinintcinp_Fer1_vvircondAmp | 3_ 1 1 1 1 1 1 | | |
| уд_тке_пьс_хр_, түрөрөтірініпкотір, түрөрөтірініпкотір_, ет_үүпкотірдатір Name | Actual Value | Expected Value | Resul |

| tgt_rte_inst_xp_1 rqbepbinpinintcinp.i rqbepbinpinintcinp_r en_wirtcindxinpbi | tgt_r rqbepbinpinintomp_r er r_wirtomaxin | ipbina_iviti1vin_i3z | |
|---|---|---------------------------|--------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125487.031 | 125487.035 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 14899619 | 14899618.37 ± 99.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 540.226318 | 540.2263355 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.10000002 | 1.1 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 2205.30005 | 2205.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -8.33333302 | -8.333333333 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.22103405 | 2.221033333 ± 0.00390625 | ~ |

FrqDepDmpnInrtCmp_Per1

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| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|----------|
| tat FraDepDmpnInrtCmp Per1 FraDepDmpnInrtCmp MtrNm f32.value | 8.80000019 | 8.8 ± 0.00048828125 | ✓ |

| T | | | | ~ |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Fest Step 2.2 (Repeat Count = 1) | Input Value |
|--|-----------------------------------|
| | · |
| PreDecelGain_Uls_M_f32 | 125589.21 |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.1 |
| Prev1ScIDrvVel_RadpS_M_f32 | -445.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.8 |
| Prev2ScIDrvVel_RadpS_M_f32 | -220.3 |
| PrevTbarAng_HwDeg_M_f32 | 4.339 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| barVelFiltSv_M_str.SV_Uls_f32 | -2.5 |
| barVelFiltSv_M_str.K_Uls_f32 | 0.2365 |
| _CmnSysKinRatio_MtrDegpHwDeg_f32 | 20.3 |
| _CmnTbarStiff_NmpDeg_f32 | 2.3 |
| _DmpDecelGainFSlew_UlspS_f32 | 200.03 |
| _DmpDecelGain_Uls_f32 | 3.6 |
| _DmpGainOffThresh_KphpS_f32 | 20.2 |
| _DmpGainOnThresh_KphpS_f32 | 35.3 |
| _InrtCmp_MtrInertia_KgmSq_f32 | 0.00009 |
| _InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.8 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 342 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 683 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][7] | 2728 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][8] | 3068 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3409 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2583 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3099 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4129 |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644 |
| P FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][9] | 5159 |
| P | 32 |
| P_FDD_FreqTblYM_Hz_u12p4[0][1] | 48 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 64 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 80 |
| r_bbr.req1611M_riz_d12p4[0][4] ?_FDD_FreqTblYM_Hz_u12p4[0][4] | 96 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][5] | 112 |
| 2 FDD FreqTbIYM Hz u12p4[0][6] | 128 |
| P_FDD_FreqTbIYM_Hz_u12p4[0][7] | 144 |
| 2_FDD_FreqTbIYM_Hz_u12p4[0][7] 2_FDD_FreqTbIYM_Hz_u12p4[0][8] | 160 |
| | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 176 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 192 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 208 |
| 2_FDD_FreqTbIYM_Hz_u12p4[1][0] 2_FDD_FreqTbIYM_Hz_u12p4[1][1] | 48 64 |

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| Name | Input Value | |
|--|-------------|--|
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 224 | |
| _CmnVehSpd_Kph_u9p7[0] | 2560 | |
| _CmnVehSpd_Kph_u9p7[1] | 3840 | |
| _CmnVehSpd_Kph_u9p7[2] | 5120 | |
| _CmnVehSpd_Kph_u9p7[3] | 6400 | |
| CmnVehSpd_Kph_u9p7[4] | 7680 | |
| _CmnVehSpd_Kph_u9p7[5] | 8960 | |
| _CmnVehSpd_Kph_u9p7[6] | 10240 | |
| _CmnVehSpd_Kph_u9p7[7] | 11520 | |
| _CmnVehSpd_Kph_u9p7[8] | 12800 | |
| _CmnVehSpd_Kph_u9p7[9] | 14080 | |
| _CmnVehSpd_Kph_u9p7[10] | 15360 | |
| _CmnVehSpd_Kph_u9p7[11] | 16640 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 8602 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 9011 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 9421 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 9830 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 10240 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 10650 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 11059 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 11469 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 11878 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 12288 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | |
| DmpDecelGainSlewX MtrRadpS u11p5[1] | 3904 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 | |
| | 4032 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1480 | |
| mpDecelGainSlewY_UlspS_u13p3[1] | 1488 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1496 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1504 | |
| DmpDecelGainSlewY UlspS u13p3[4] | 1512 | |
| DmpDecelGainSlewY UlspS u13p3[5] | 1520 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 | |
| | | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 924 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1144 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1254 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1364 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1475 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695 | |
| _FDD_AttenTbIX_MtrRadpS_u12p4[0] | 352 | |
| _FDD_AttenTbIX_MtrRadpS_u12p4[1] | 400 | |
| _FDD_AttenTbIY_Uls_u8p8[0] | 65 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 68 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 5 | |
| FDD_BlendTblY_Uls_u8p8[1] | 8 | |
| FDD_BlendTblY_Uls_u8p8[2] | 10 | |
| FDD_BlendTblY_Uls_u8p8[3] | 13 | |
| _FDD_BlendTblY_Uls_u8p8[4] | 15 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 18 | |
| _FDD_BlendTblY_Uls_u8p8[6] | 20 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 23 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 26 | |
| :_FDD_BlendTblY_Uls_u8p8[9] | 28 | |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_FDD_BlendTblY_Uls_u8p8[10] | 31 | | |
| | 33 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 166 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 23 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 28 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 29 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 538 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 563 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 589 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 614 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 640 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -8.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -600.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 10 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 20.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 200.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 2.3 | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCm | | tCmd_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | <u> </u> | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 VehicleSpeed | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 WIRCmdAmpE | | | |
| | <u> </u> | | Result |
| Name | Actual Value | Expected Value | Result |

| igt the met springerpri | ps. tgt_i rqs opsptop | or i_rrin tomar importa_interni_ioz | |
|--|-----------------------|-------------------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125588.813 | 125588.8099 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -321190.063 | -321190.1416 ± 0.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -480.309448 | -480.3094401 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.10000002 | -1.1 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -445.299988 | -445.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 4.347826 | 4.347826087 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.865101695 | -0.865065217 ± 0.00390625 | ✓ |
| tgt FrgDepDmpnInrtCmp Per1 FrgDepDmpnInrtCmp MtrNm f32.value | 0 | 0 ± 0.00048828125 | ✓ |



| T | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.3 (Repeat Count = 1) | |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 125997.11 |
| Prev1PreAttnComp_MtrNm_M_f32 | -3.3 |
| Prev1SclDrvVel_RadpS_M_f32 | -4021.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.3 |
| Prev2SclDrvVel_RadpS_M_f32 | -363.2 |
| PrevTbarAng_HwDeg_M_f32 | 0.159 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.6 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.63214 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 60.05 |
| k_CmnTbarStiff_NmpDeg_f32 | 6.2 |
| k_DmpDecelGainFSlew_UlspS_f32 | 400.05 |
| k_DmpDecelGain_Uls_f32 | 6.5 |
| k_DmpGainOffThresh_KphpS_f32 | 44.5 |
| k_DmpGainOnThresh_KphpS_f32 | 20.6 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00008 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 1066 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1212 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1359 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1506 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1653 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1800 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1946 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2093 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 2240 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1246 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1638 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2030 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2422 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3206 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3598 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3990 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4382 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4774 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 208 |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][8] | 224 |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][9] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 256 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 272 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 336 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 352 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 368 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 384 |

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| гідрервтрттстр_Регі | | - COLOR CONTRACTOR OF THE COLO |
|---|-------------|--|
| Name | Input Value | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 400 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 416 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 432 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 448 | |
| 2 FDD FregTblYM Hz u12p4[1][8] | 464 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][9] | 480 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][10] | 496 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][11] | 512 | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | |
| :_CmnVehSpd_Kph_u9p7[1] | 12928 | |
| :_CmnVehSpd_Kph_u9p7[2] | 13056 | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | |
| | 13312 | |
| | 13440 | |
| t_CmnVehSpd_Kph_u9p7[5] | | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | |
| _CmnVehSpd_Kph_u9p7[7] | 13696 | |
| CmnVehSpd_Kph_u9p7[8] | 13824 | |
| CmnVehSpd_Kph_u9p7[9] | 13952 | |
| CmnVehSpd_Kph_u9p7[10] | 14080 | |
| CmnVehSpd_Kph_u9p7[11] | 14208 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| :_DmpADDCoefX_MtrNm_u4p12[1] | 25395 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 25805 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 26624 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 27034 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 27443 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 27853 | |
| u4p12[8] | 28262 | |
| :_DmpADDCoefX_MtrNm_u4p12[9] | 28672 | |
| | 32320 | |
| mpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | |
| bmpbecelGainGlewX_mtrkaape_u11p5[2] _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | |
| | 32448 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2424 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1427 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1655 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1884 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2112 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2340 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2568 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2796 | |
| | | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3024 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3252 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3480 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 656 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 720 | |
| _FDD_AttenTblY_Uls_u8p8[0] | 172 | |
| _FDD_AttenTbIY_Uls_u8p8[1] | 174 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 18 | |
| _FDD_BlendTblY_Uls_u8p8[1] | 20 | |
| _FDD_BlendTblY_Uls_u8p8[2] | 23 | |
| _FDD_BlendTblY_Uls_u8p8[3] | 26 | |
| _FDD_BlendTblY_Uls_u8p8[4] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 31 | |
| | | |
| _FDD_BlendTblY_Uls_u8p8[6] | 33 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 36 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 38 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 41 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 44 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 46 | |

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| Name | Input Value |
|--|---|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 154 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 166 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 179 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 192 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 205 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 218 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 230 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 243 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 256 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 269 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 282 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 294 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 77 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 78 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 79 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 81 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 82 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 83 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 84 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 86 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 87 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 88 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 90 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 91 |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 1638 |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 3277 |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 4915 |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 6554 |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1562 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1587 |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1613 |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1638 |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1664 |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -6.3 |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -1118 |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 1.02 |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -20.01 |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 110.07 |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 6.3 |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCmc | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32 |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel I | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32 |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrdepDmpS$ | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_F$ | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32 |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwInrtCmp_HwInrt$ | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32 |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAccessions and the property of the proper$ | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32 |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_Vehicle$ | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB$ | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32 |
| Name | Actual Value Expected Value Resu |

| 2C | h .3hh | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125996.313 | 125996.3099 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -9984653 | -9984653.482 ± 9.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -447.704346 | -447.704346 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.29999995 | -3.3 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -4021.30005 | -4021.3 ± 0.00390625 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | 0.164516136 | 0.164516129 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.684389591 | -0.684393097 ± 0.00390625 | ~ |
| tat FraDenDmonInrtCmp Per1 FraDenDmonInrtCmp MtrNm f32 value | -8 80000019 | -8.8 + 0.00048828125 | ✓ |



FrqDepDmpnInrtCmp_Per1

| T | | | | V |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | - |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | - |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | - |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ✓ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

Test Case 3: Boundary Test Performance Metrics (With "None" Instrumentation and "WithPS" Specification Environment) CPU Cycles: 5484.00 Cycles 5549.00 Cycles 5698.00 Cycles 5724.00 Cycles 5698.00 Cycles 5572.00 Cycles 5708.00 Cycles TS3 1 TS3.2 TS3.3 TS3.4 TS3.5 TS3.6 TS3.7 5708.00 Cycles 6713.00 Cycles 6713.00 Cycles 5630.00 Cycles 5508.00 Cycles 5560.00 Cycles 5560.00 Cycles 5562.00 Cycles 5534.00 Cycles 5458.00 Cycles 5458.00 Cycles 5517.00 Cycles 5517.00 Cycles 5517.00 Cycles 5549.00 Cycles 5549.00 Cycles 5549.00 Cycles 5549.00 Cycles 5549.00 Cycles 5516.00 Cycles TS3.8 TS3.9 TS3.10 TS3.11 TS3.12 TS3.12 TS3.13 TS3.14 TS3.15 TS3.16 TS3.17 TS3.18 TS3.19 TS3.20 TS3.21 TS3.22 TS3.23 5529.00 Cycles 5516.00 Cycles 5539.00 Cycles 5539.00 Cycles 5519.00 Cycles 5619.00 Cycles 5572.00 Cycles 5561.00 Cycles TS3.24 TS3.25 TS3.26 TS3.27 TS3.28 TS3.29 TS3.30 Description Test Vector Description: TS3.1 All min TS3.2 TS3.3 HwTorque_HwNm_f32 = min TS3.4 HwTorque_HwNm_f32 = max TS3.5 HwTorque_HwNm_f32 = zero TS3.6 HwTorque_HwNm_f32 = neg TS3.7 HwTorque HwNm f32 = pos TS3.8 CRFMotorVel_MtrRadpS_f32 = min TS3.9 CRFMotorVel_MtrRadpS_f32 = max TS3.10 CRFMotorVel_MtrRadpS_f32 = zero TS3.11 CRFMotorVel_MtrRadpS_f32 = neg TS3.12 CRFMotorVel_MtrRadpS_f32 = pos TS3.13 BaseAssistCmd_MtrNm_f32 = min TS3.14 BaseAssistCmd_MtrNm_f32 = max TS3.15 BaseAssistCmd_MtrNm_f32 = zero IS3.15 BaseAssistCmd_MtrNm_f32 = zero TS3.16 BaseAssistCmd_MtrNm_f32 = neg TS3.17 BaseAssistCmd_MtrNm_f32 = pos TS3.18 VehicleSpeed_Kph_f32 = min TS3.19 VehicleSpeed_Kph_f32 = max TS3.20 VehicleSpeed_Kph_f32 = pos TS3.21 WIRCmdAmpBInd_MtrNm_f32 = min TS3.22 WIRCmdAmpBInd_MtrNm_f32 = max TS3.23 WIRCmdAmpBInd_MtrNm_f32 = pos TS3.24 FreqDepDmpSrIComSvcDff_Cnt_lgc = min TS3.25 FreqDepDmpSrIComSvcDff_Cnt_lgc = max

| Test Step 3.1 (Repeat Count = 1) | ✓ |
|----------------------------------|-------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 1 |

TS3.25

FreqDepDmpSrlComSvcDft_Cnt_lgc = max

TS3.26 VehicleLonAccel KphpS f32 = min TS3.27 VehicleLonAccel KphpS f32 = max TS3.28 VehicleLonAccel KphpS f32 = zero TS3.29 VehicleLonAccel_KphpS_f32 = neg TS3.30 VehicleLonAccel_KphpS_f32 = pos

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| FrqDepDmpnInrtCmp_Per1 | | |
|--|-----------------------------------|--|
| Name | Input Value | |
| Prev1PreAttnComp_MtrNm_M_f32 | -8.8 | |
| Prev1ScIDrvVel_RadpS_M_f32 | -12917.3 | |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.8 | |
| Prev2SclDrvVel_RadpS_M_f32 | -12917.3 | |
| PrevTbarAng_HwDeg_M_f32 | -20 | |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -6.6667 | |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.001255848 | |
| <pre><_CmnSysKinRatio_MtrDegpHwDeg_f32</pre> | 0.5 | |
| <pre><_CmnTbarStiff_NmpDeg_f32 <_DmpDecelGainFSlew_UlspS_f32</pre> | 1 | |
| CDMpDecelGain_Uls_f32 | 1 | |
| <pre>c_DmpGainOffThresh_KphpS_f32</pre> | 0 | |
| <pre>c_DmpGainOnThresh_KphpS_f32</pre> | 0 | |
| CInrtCmp_MtrInertia_KgmSq_f32 | 0.00001 | |
| <pre><_inrtCmp_MtrVel_ScaleFactor_Uls_f32</pre> | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 0 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 0 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 16 16 | |
| :2_FDD_FreqTbIYM_Hz_u12p4[0][3] :2_FDD_FreqTbIYM_Hz_u12p4[0][4] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][4] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][6] | 16 | |
| 2 FDD FreqTbIYM Hz u12p4[0][7] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 16 | |
| _CmnVehSpd_Kph_u9p7[0] | 0 | |
| _CmnVehSpd_Kph_u9p7[1] | 0 | |
| _CmnVehSpd_Kph_u9p7[2] | 0 | |
| _CmnVehSpd_Kph_u9p7[3] | 0 | |
| CmnVehSpd_Kph_u9p7[4] | 0 | |
| CmnVehSpd_Kph_u9p7[5] | 0 | |
| _CmnVehSpd_Kph_u9p7[6] | 0 | |
| CmnVehSpd_Kph_u9p7[7] | 0 | |
| | 0 | |
| :_CmnVehSpd_Kph_u9p7[8] | | |
| :_CmnVehSpd_Kph_u9p7[9] | 0 | |
| | 0 0 0 | |

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|--|-------------|
| Name | Input Value |
| _DmpADDCoefX_MtrNm_u4p12[1] | 0 |
| DmpADDCoefX_MtrNm_u4p12[2] | 0 |
| DmpADDCoefX_MtrNm_u4p12[3] | 0 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 0 |
| DmpADDCoefX_MtrNm_u4p12[5] | 0 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 0 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 0 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 0 |
| DmpADDCoefX_MtrNm_u4p12[9] | 0 |
| | 0 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 0 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 0 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 0 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 0 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 0 |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 8 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 8 |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 8 |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 8 |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 8 |
| DmpDecelGainSlewY_UlspS_u13p3[5] | 8 |
| DmpFiltKpWIRBIndY_Uls_u2p14[0] | 0 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 0 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 0 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 0 |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 0 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 0 |
| | 0 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 0 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 0 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 0 |
| _FDD_AttenTblY_Uls_u8p8[0] | 0 |
| _FDD_AttenTblY_Uls_u8p8[1] | 0 |
| _FDD_BlendTblY_Uls_u8p8[0] | 0 |
| _FDD_BlendTblY_Uls_u8p8[1] | 0 |
| _FDD_BlendTblY_Uls_u8p8[2] | 0 |
| _FDD_BlendTblY_Uls_u8p8[3] | 0 |
| _FDD_BlendTblY_Uls_u8p8[4] | 0 |
| FDD_BlendTblY_Uls_u8p8[5] | 0 |
| FDD_BlendTblY_Uls_u8p8[6] | 0 |
| _FDD_BlendTblY_Uls_u8p8[7] | 0 |
| FDD_BlendTblY_Uls_u8p8[8] | 0 |
| FDD_BlendTblY_Uls_u8p8[9] | 0 |
| FDD_BlendTblY_Uls_u8p8[10] | 0 |
| FDD_BlendTblY_Uls_u8p8[11] | 0 |
| InrtCmp ScaleFactorTblY Uls u9p7[0] | 0 |
| InrtCmp ScaleFactorTblY Uls u9p7[1] | 0 |
| InttCmp ScaleFactorTblY Uls u9p7[2] | 0 |
| InttCmp_ScaleFactorTblY_Uls_u9p7[3] | 0 |
| | 0 |
| InrtCmp_ScaleFactorTblY_UIs_u9p7[4] | |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 0 |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 0 |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 0 |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 0 |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 0 |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 0 |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 0 |
| _InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 0 |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 0 |
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| Name | Input Value |
|--|---|
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 0 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 0 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 0 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 0 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 0 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 0 |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 0 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 0 |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 0 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 0 |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 0 |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 0 |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 0 |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -8.8 |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -1118 |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -10 |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -50 |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 0 |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 0 |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_BaseAssistCmc$ | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_I | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32 |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSinrtCmp_Per1_FreqDepDmpSinrtCmp_Per1_FreqDepDmpSinrtCmp_Per1_FreqDepDmpNinrtCmp_$ | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmp$ | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32 |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 HwTorque Hw1 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcce | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_InstAp_FrqDepDmpnInrtCmp_P$ | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBi | , |
| Name | Actual Value Expected Value Result |

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|--|---|--|
| Name | Input Value | |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0005 | |
| <pre><_InrtCmp_MtrVel_ScaleFactor_Uls_f32</pre> | 1 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 6554 | |
| 2_FDD_ADDROIllingTblYM_MtrNmpRadpS_um1p17[1][4] | 6554 | |
| | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | | |
| P_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 6554 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 6554 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1600 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1600 | |
| 2_FDD_freqTblYM_Hz_u12p4[1][10] | 1600 | |
| | | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1600 | |
| _CmnVehSpd_Kph_u9p7[0] | 32640 | |
| _CmnVehSpd_Kph_u9p7[1] | 32640 | |
| _CmnVehSpd_Kph_u9p7[2] | 32640 | |
| _CmnVehSpd_Kph_u9p7[3] | 32640 | |
| _CmnVehSpd_Kph_u9p7[4] | 32640 | |
| _CmnVehSpd_Kph_u9p7[5] | 32640 | |
| _CmnVehSpd_Kph_u9p7[6] | 32640 | |
| _CmnVehSpd_Kph_u9p7[7] | 32640 | |
| _CmnVehSpd_Kph_u9p7[8] | 32640 | |
| _CmnVehSpd_Kph_u9p7[9] | 32640 | |
| _CmnVehSpd_Kph_u9p7[10] | 32640 | |
| _CmnVehSpd_Kph_u9p7[11] | 32640 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 36045 | |
| DmpADDCoefX_MtrNm_u4p12[5] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 36045 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 36045 | |
| | 35776 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 35776 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 35776 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 35776 | |
| | 35776 | |

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|--|-------------|
| Name | Input Value |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 35776 |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 4000 |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 4000 |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 4000 |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 4000 |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 4000 |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 4000 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 16384 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 16384 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 16384 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 16384 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 16384 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 6554 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 6554 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 6554 |
| t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 6554 |
| :_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 6554 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 6554 |
| t_FDD_AttenTblX_MtrRadpS_u12p4[0] | 17600 |
| t_FDD_AttenTblX_MtrRadpS_u12p4[1] | 17600 |
| t_FDD_AttenTblY_Uls_u8p8[0] | 256 |
| t_FDD_AttenTblY_Uls_u8p8[1] | 256 |
| t_FDD_BlendTbIY_Uls_u8p8[0] | 256 |
| t_FDD_BlendTbIY_Uls_u8p8[1] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[2] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[3] | 256 |
| _FDD_BlendTblY_Uls_u8p8[4] | 256 |
| t_FDD_BlendTbIY_Uls_u8p8[5] | 256 |
| t_FDD_BlendTbIY_Uls_u8p8[6] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[7] | 256 |
| t_FDD_BlendTbIY_Uls_u8p8[8] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[9] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[10] | 256 |
| t_FDD_BlendTblY_Uls_u8p8[11] | 256 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 384 |
| _InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 384 |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 384 |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 128 |
| _InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 128 |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 128 |
| :_RIAstWIRBIndTbIY_Uls_u2p14[0] | 16384 |
| :_RIAstWIRBIndTbIY_Uls_u2p14[1] | 16384 |
| :_RIAstWIRBIndTbIY_Uls_u2p14[2] | 16384 |
| :_RIAstWIRBIndTbIY_Uls_u2p14[3] | 16384 |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 16384 |
| :_WIRBIndTbIX_MtrNm_u8p8[0] | 2048 |
| | 2049 |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 2048 |
| | 2048 |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | |
| t_WirBindTbiX_MtrNm_u8p8[1] t_WirBindTbiX_MtrNm_u8p8[2] t_WirBindTbiX_MtrNm_u8p8[3] t_WirBindTbiX_MtrNm_u8p8[4] | 2048 |



| Name | Input Value | | |
|--|--|-----------------------|---------|
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 1118 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 10 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 50 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 511.9921875 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 8.8 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCmc | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistC | Cmd_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_I | tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVe | el_MtrRadpS_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrrqDepDmpSrrqDepDmpSrrqDepDmpSrrqDepDmpNnrtCmp_Per1_FreqDepDmpNnrtC$ | tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDm | pSrlComSvcDft_Cnt_lgc | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmp | nInrtCmp_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hwf | tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_I | HwNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcce | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonA | ccel_KphpS_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_InrtCmp_Inrt$ | tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpee | d_Kph_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBl | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAm | pBInd_MtrNm_f32 | |
| Name | Actual Value | Even ata d Value | Daguile |

| Name | Actual Value | Expected Value | Result |
|--|------------------|--------------------------------|----------|
| PreDecelGain_Uls_M_f32 | 4.2949673e+009 | 4294967286 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -2.45381431e+011 | -245381471607.646 ± 999999.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 1112.98718 | 1112.9872366867 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.80000019 | 8.8 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 12917.2998 | 12917.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1 | 1 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.8721137 | -2.87210173650089 ± 0.00390625 | ~ |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value | 0 | 0 ± 0.00048828125 | ✓ |

| T | | | | V |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.3 (Repeat Count = 1) | ✓ |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 125487.235 |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.1 |
| Prev1SclDrvVel_RadpS_M_f32 | 2205.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.3 |
| Prev2SclDrvVel_RadpS_M_f32 | 101.2 |
| PrevTbarAng_HwDeg_M_f32 | -8.32 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 10.2 |
| k_CmnTbarStiff_NmpDeg_f32 | 1.2 |
| k_DmpDecelGainFSlew_UlspS_f32 | 100.02 |
| k_DmpDecelGain_Uls_f32 | 2.5 |
| k_DmpGainOffThresh_KphpS_f32 | 16.5 |
| k_DmpGainOnThresh_KphpS_f32 | 30.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00008 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |

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| Name | Input Value | |
|--|---------------------|--|
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 | |
| 2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][3] | 1364 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068 | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3409 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][0] | 16 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 32 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 48 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 64 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][4] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][5] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][6] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][7] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 32 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 48 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 64 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 208 | |
| _CmnVehSpd_Kph_u9p7[0] | 128 | |
| _CmnVehSpd_Kph_u9p7[1] | 256 384 | |
| _CmnVehSpd_Kph_u9p7[2] _CmnVehSpd_Kph_u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| _CmnVehSpd_Kph_u9p7[5] | 768 | |
| CmnVehSpd Kph u9p7[6] | 896 | |
| _CmnVehSpd_Kph_u9p7[7] | 1024 | |
| _CmnVehSpd_Kph_u9p7[8] | 1152 | |
| _CmnVehSpd_Kph_u9p7[9] | 1280 | |
| CmnVehSpd_Kph_u9p7[10] | 1408 | |
| _CmnVehSpd_Kph_u9p7[11] | 1536 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3552 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3584 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3616 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3648 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3680 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3712 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 408 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 416 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 424 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 432 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 440 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 448 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| | | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| | 6554 8192 523 | |

FrqDepDmpnInrtCmp_Per1

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Input Value t FDD ADDStaticTblY MtrNmpRadpS um1p17[2] 1553 2068 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] t FDD ADDStaticTblY_MtrNmpRadpS_um1p17[4] 2583 t_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] 3099 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] 3614 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] 4129 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] 4644 t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] 5159 t_FDD_AttenTblX_MtrRadpS_u12p4[0] 240 t_FDD_AttenTblX_MtrRadpS_u12p4[1] 320 t_FDD_AttenTblY_Uls_u8p8[0] 49 t_FDD_AttenTblY_Uls_u8p8[1] 51 t_FDD_BlendTblY_Uls_u8p8[0] 3 t_FDD_BlendTblY_Uls_u8p8[1] 5 t_FDD_BlendTblY_Uls_u8p8[2] 8 t_FDD_BlendTblY_Uls_u8p8[3] 10 t_FDD_BlendTblY_Uls_u8p8[4] 13 t_FDD_BlendTblY_Uls_u8p8[5] 15 t_FDD_BlendTblY_Uls_u8p8[6] 18 t_FDD_BlendTblY_Uls_u8p8[7] 20 t_FDD_BlendTblY_Uls_u8p8[8] 23 t_FDD_BlendTblY_Uls_u8p8[9] 26 t_FDD_BlendTblY_Uls_u8p8[10] 28 t_FDD_BlendTblY_Uls_u8p8[11] 31 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] 13 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] 26 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] 38 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] 51 t InrtCmp ScaleFactorTblY Uls u9p7[4] 64 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] 77 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] 90 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] 102 t InrtCmp ScaleFactorTblY Uls u9p7[8] 115 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] 128 t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] 141 154 $t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11]$ t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 3 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] 5 t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 6 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 8 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 9 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 10 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 12 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 13 t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] 14 15 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] t_RIAstWIRBIndTbIY_Uls_u2p14[0] 1638 t_RIAstWIRBIndTblY_Uls_u2p14[1] 3277 t_RIAstWIRBIndTblY_Uls_u2p14[2] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[3] 6554 t_RIAstWIRBIndTbIY_Uls_u2p14[4] 8192 t_WIRBIndTbIX_MtrNm_u8p8[0] 282 t_WIRBIndTbIX_MtrNm_u8p8[1] 307 $t_WIRBIndTbIX_MtrNm_u8p8[2]$ 333 t_WIRBIndTbIX_MtrNm_u8p8[3] 358 t_WIRBIndTbIX_MtrNm_u8p8[4] 384 8.1 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value $tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value$ 600.2 $tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value \\$ -10 $tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value$ $tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value$ 10.02 tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value 100.01 tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value 1.2 $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_BaseAssistCmc \\ tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd \\ tgt_FrqDepDmpnInrtCmp_Per1_BaseAssis$ tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_Il tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32 tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 FreqDepDmpsI tgt FrqDepDmpnInrtCmp Per1 FreqDepDmpSrlComSvcDft Cnt Igc tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 FrqDepDmpnIn tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32 $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_HwTorque_Hwt| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32$ tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcce tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32 tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_l tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32 $\label{total_tot$

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| FrqDepDmpnInrtCmp_Per | 1 |
|-----------------------|---|
|-----------------------|---|

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------------|----------|
| PreDecelGain_Uls_M_f32 | 125487.031 | 125487.035 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 14899619 | 14899618.37 ± 99.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 540.226318 | 540.2263355 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.10000002 | 1.1 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 2205.30005 | 2205.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -8.33333302 | -8.333333333 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.22103405 | 2.2210333333 ± 0.00390625 | ~ |
| tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value | 8.80000019 | 8.8 ± 0.00048828125 | ~ |

| Т | | | | V |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | ✓ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | ✓ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ✓ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.4 (Repeat Count = 1) | Innut Value |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 125589.21 |
| Prev1PreAttnComp_MtrNm_M_f32 | -1.1 |
| Prev1SclDrvVel_RadpS_M_f32 | -445.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.8 |
| Prev2SclDrvVel_RadpS_M_f32 | -220.3 |
| PrevTbarAng_HwDeg_M_f32 | 4.339 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.2365 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 20.3 |
| k_CmnTbarStiff_NmpDeg_f32 | 2.3 |
| k_DmpDecelGainFSlew_UlspS_f32 | 200.03 |
| k_DmpDecelGain_Uls_f32 | 3.6 |
| k_DmpGainOffThresh_KphpS_f32 | 20.2 |
| k_DmpGainOnThresh_KphpS_f32 | 35.3 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00009 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.8 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2728 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3068 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][9] | 3409 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] | 2583 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3099 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 3614 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] | 4129 |
| t2 FDD ADDRollingTbIYM MtrNmpRadpS_um1p17[1][8] | 4644 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159 |
| t2 FDD FreqTblYM Hz u12p4[0][0] | 32 |
| t2 FDD FreqTbIYM Hz u12p4[0][1] | 48 |
| : = ::::: | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][4] | 96 |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][5] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 128 |

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|--|---------------|--|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][7] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 48 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 64 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 112 | |
| 2 FDD FreqTblYM Hz u12p4[1][5] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 144 | |
| | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 176 | |
| | 192 | |
| P_FDD_FreqTblYM_Hz_u12p4[1][10] | 208 | |
| P_FDD_FreqTblYM_Hz_u12p4[1][11] | 224 | |
| CmnVehSpd_Kph_u9p7[0] | 2560 | |
| CmnVehSpd Kph u9p7[1] | 3840 | |
| CmnVehSpd Kph u9p7[2] | 5120 | |
| | 6400 | |
| _CmnVehSpd_Kph_u9p7[3] CmnVehSpd_Kph_u9p7[4] | 7680 | |
| | | |
| CmnVehSpd_Kph_u9p7[5] | 8960 10240 | |
| CmnVehSpd_Kph_u9p7[6] | | |
| CmnVehSpd_Kph_u9p7[7] | 11520 | |
| CmnVehSpd_Kph_u9p7[8] | 12800 | |
| _CmnVehSpd_Kph_u9p7[9] | 14080 | |
| _CmnVehSpd_Kph_u9p7[10] | 15360 | |
| _CmnVehSpd_Kph_u9p7[11] | 16640 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 8602 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 9011 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 9421 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 9830 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 10240 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 10650 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 11059 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 11469 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 11878 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 12288 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | |
| DmpDecelGainSlewX MtrRadpS u11p5[3] | 3968 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 | |
| DmpDecelGainSlewX MtrRadpS u11p5[5] | 4032 | |
| DmpDecelGainSlewY_UlspS_u13p3[0] | 1480 | |
| DmpDecelGainSlewY_UlspS_u13p3[1] | 1488 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1496 | |
| DmpDecelGainSlewY UlspS u13p3[3] | 1504 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1512 | |
| DmpDecelGainSlewY UlspS u13p3[4] | 1520 | |
| _DmpFiltKpWIRBIndY_UIs_u2p14[0] | 3277 | |
| | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 924 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1254 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 352 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 400 | |
| TDD_AttenTblY_Uls_u8p8[0] | 65 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 68 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 5 | |
| _FDD_BlendTblY_Uls_u8p8[1] | 8 | |
| | ~ | |

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FrqDepDmpnInrtCmp_Per1

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|---|--------------------------------------|---------------------------|--------|
| Name | Input Value | | |
| t_FDD_BlendTblY_Uls_u8p8[3] | 13 | | |
| t_FDD_BlendTblY_Uls_u8p8[4] | 15 | | |
| t_FDD_BlendTblY_Uls_u8p8[5] | 18 | | |
| t_FDD_BlendTblY_Uls_u8p8[6] | 20 | | |
| t_FDD_BlendTblY_Uls_u8p8[7] | 23 | | |
| t_FDD_BlendTblY_Uls_u8p8[8] | 26 | | |
| t_FDD_BlendTblY_Uls_u8p8[9] | 28 | | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 31 | | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 33 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTbIY_UIs_u9p7[7] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[9] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[10] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_UIs_u9p7[11] | 166 | | |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[0] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 19 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] t InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 22 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[6] | 23 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 28 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 29 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTbIY_Uis_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uis_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 9830 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 538 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 563 | | |
| t WIRBIndTbIX MtrNm u8p8[2] | 589 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 614 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 640 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -8.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -600.3 | | |
| tgt FrqDepDmpnInrtCmp Per1 FreqDepDmpSrlComSvcDft Cnt Igc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 10 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 20.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 200.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 2.3 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | tgt_FrqDepDmpnInrtCmp_Per1_BaseAssi | stCmd_MtrNm_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_CRFMotorVel_Inst_RepPer1_CRFMotorVel_$ | 1 tgt_FrqDepDmpnInrtCmp_Per1_CRFMoto | orVel_MtrRadpS_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | 1 - 1 - 1 - 1 | | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_F$ | tgt_FrqDepDmpnInrtCmp_Per1_FrqDepD | mpnInrtCmp_MtrNm_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_HwTorqu$ | tgt_FrqDepDmpnInrtCmp_Per1_HwTorqu | e_HwNm_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcception (Compared to the Compared to th$ | tgt_FrqDepDmpnInrtCmp_Per1_VehicleLo | onAccel_KphpS_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCm$ | tgt_FrqDepDmpnInrtCmp_Per1_VehicleS | peed_Kph_f32 | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpError Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmpnInrtCmp_Perror Ap_FrqDepDmp$ | tgt_FrqDepDmpnInrtCmp_Per1_WIRCmd | AmpBlnd_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125588.813 | 125588.8099 ± 0.0625 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | -321190.063 | -321190.1416 ± 0.9 | |
| Prev1SclDrvVel_RadpS_M_f32 | -480.309448 | -480.3094401 ± 0.00390625 | |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.10000002 | -1.1 ± 0.00048828125 | |
| Prev2SclDrvVel_RadpS_M_f32 | -445.299988 | -445.3 ± 0.00390625 | • |
| PrevTbarAng_HwDeg_M_f32 | 4.347826 | 4.347826087 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.865101695 | -0.865065217 ± 0.00390625 | • |
| tat FraDenDmnnInrtCmn Per1 FraDenDmnnInrtCmn MtrNm f32 value | 0 | 0 + 0 00048828125 | - |

0

 $tgt_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32.value$

0 ± 0.00048828125

FrqDepDmpnInrtCmp_Per1

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T
Actual Function Count Expected Function Count Result

Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached

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|--|-------------|-----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 224 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 240 | |
| _CmnVehSpd_Kph_u9p7[0] | 6784 | |
| _CmnVehSpd_Kph_u9p7[1] | 6912 | |
| _CmnVehSpd_Kph_u9p7[2] | 7040 | |
| _CmnVehSpd_Kph_u9p7[3] | 7168 | |
| CmnVehSpd_Kph_u9p7[4] | 7296 | |
| | 7424 | |
| _CmnVehSpd_Kph_u9p7[6] | 7552 | |
| :_CmnVehSpd_Kph_u9p7[7] | 7680 | |
| | 7808 | |
| CmnVehSpd_Kph_u9p7[9] | 7936 | |
| _CmnVehSpd_Kph_u9p7[10] | 8064 | |
| | 8192 | |
| _CmnVehSpd_Kph_u9p7[11] | | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 12698 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 13107 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 13517 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 13926 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 14336 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 14746 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 15155 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 15565 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 15974 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 16384 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 | |
| | 2408 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | |
| mpDecelGainSlewY_UlspS_u13p3[2] | 2424 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2448 | |
| | 4915 | |
| :_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 | |
| :_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 885 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 986 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1087 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1793 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 448 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 480 | |
| FDD_AttenTblY_Uls_u8p8[0] | 93 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 96 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 10 | |
| | 13 | |
| FDD_BlendTblY_Uls_u8p8[1] | | |
| FDD_BlendTblY_Uls_u8p8[2] | 15 | |
| FDD_BlendTblY_Uls_u8p8[3] | 18 | |
| _FDD_BlendTblY_Uls_u8p8[4] | 20 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 23 | |
| _FDD_BlendTblY_Uls_u8p8[6] | 26 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 31 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 33 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 36 | |
| | 38 | |

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| Name | Input Value | | |
|---|--------------|------------------|------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 90 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[5] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 154 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[10] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 179 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 32 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[2] | 33 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 40 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 42 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 44 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 45 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 4915 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[4] | 11469 | | |
| t WIRBIndTbiX MtrNm u8p8[0] | 794 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 819 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 845 | | |
| t WIRBIndTbIX MtrNm u8p8[3] | 870 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 896 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 7.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 500.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 0 | | |
| tgt FrqDepDmpnInrtCmp Per1 VehicleLonAccel KphpS f32.value | 30.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 300.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32.value | 3.2 | | |
| tgt_riqDepDriprimitCimp_rei1_wikCinidAnipBind_witNin_i52.value tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCm | | istCmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnI | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed | | | |
| | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpt | | | |
| Name Proposition IIIo M #22 | Actual Value | Expected Value | Resu |
| ProPossiCoin IIIs M f22 | | | |

| 3 | | 3- 1 - 1 - 1 - 1 | | |
|----------------------|--|------------------|--------------------------|----------|
| Name | | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M | 1_f32 | 125690.984 | 125690.985 ± 0.0625 | ~ |
| Prev1PreAttnComp_I | MtrNm_M_f32 | 232822.953 | 232822.9685 ± 0.9 | • |
| Prev1SclDrvVel_Rad | pS_M_f32 | 350.610321 | 350.6103097 ± 0.00390625 | ~ |
| Prev2PreAttnComp_f | MtrNm_M_f32 | 2.20000005 | 2.2 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_Rad | pS_M_f32 | 292.600006 | 292.6 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg | g_M_f32 | 0 | 0 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str. | SV_Uls_f32 | 1.78252006 | 1.78252 ± 0.00390625 | ~ |
| tat FraDepDmpnInrt(| Cmp Per1 FrgDepDmpplprtCmp MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | ~ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.6 (Repeat Count = 1) | |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 125793.16 |
| Prev1PreAttnComp_MtrNm_M_f32 | -2.2 |
| Prev1ScIDrvVel_RadpS_M_f32 | -160.3 |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.2 |
| Prev2ScIDrvVel_RadpS_M_f32 | -301.2 |
| PrevTbarAng_HwDeg_M_f32 | -1.1549 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.47856 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 40.4 |
| k_CmnTbarStiff_NmpDeg_f32 | 4.5 |
| k_DmpDecelGainFSlew_UlspS_f32 | 200.05 |
| k_DmpDecelGain_Uls_f32 | 3.2 |
| k_DmpGainOffThresh_KphpS_f32 | 22.3 |
| k_DmpGainOnThresh_KphpS_f32 | 45.6 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00003 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 924 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1144 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1475 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1695 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 885 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 986 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1087 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1188 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1288 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1389 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1490 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1591 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1692 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1793 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 128 |

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|--|-------------|-----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 224 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 240 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 256 | |
| _CmnVehSpd_Kph_u9p7[0] | 128 | |
| _CmnVehSpd_Kph_u9p7[1] | 256 | |
| _CmnVehSpd_Kph_u9p7[2] | 384 | |
| CmnVehSpd Kph u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| CmnVehSpd_Kph_u9p7[5] | 768 | |
| _CmnVehSpd_Kph_u9p7[6] | 896 | |
| _CmnVehSpd_Kph_u9p7[7] | 1024 | |
| _CmnVehSpd_Kph_u9p7[8] | 1152 | |
| _CmnVehSpd_Kph_u9p7[9] | 1280 | |
| CmnVehSpd Kph u9p7[10] | 1408 | |
| CmnVehSpd_Kph_u9p7[10] | 1536 | |
| _ | | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 16794 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 17203 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 17613 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 18022 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 18432 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 18842 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 19251 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 19661 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 20070 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 20480 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1208 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1216 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1224 | |
| | 1232 | |
| | 1240 | |
| | 1248 | |
| : DmpFiltKpWIRBIndY Uls u2p14[0] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 | |
| | | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1066 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1212 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1359 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1506 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1653 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1800 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1946 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 2093 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 2240 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 2387 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 512 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 560 | |
| _FDD_AttenTblY_Uls_u8p8[0] | 116 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 118 | |
| FDD_BlendTblY_Uls_u8p8[0] | 13 | |
| FDD_BlendTblY_Uls_u8p8[1] | 15 | |
| FDD_BlendTblY_Uls_u8p8[2] | 18 | |
| FDD_BlendTblY_Uls_u8p8[3] | 20 | |
| _FDD_BlendTblY_Uls_u8p8[4] | 23 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 26 | |
| | | |
| _FDD_BlendTblY_Uls_u8p8[6] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 31 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 33 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 36 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 38 | |
| _FDD_BlendTblY_Uls_u8p8[11] | 41 | |

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| Name | Input Value | | |
|--|----------------|----------------------|------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 51 | | |
| _InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 77 | | |
| i_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 | | |
| InrtCmp ScaleFactorTblY Uls u9p7[4] | 102 | | |
| InrtCmp ScaleFactorTblY Uls u9p7[5] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 128 | | |
| _InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 141 | | |
| | 154 | | |
| | 166 | | |
| | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 192 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 46 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[1] | 47 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 50 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 51 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 52 | | |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 55 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 56 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 58 | | |
| _inrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 59 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 60 | | |
| RIAstWIRBIndTblY Uls u2p14[0] | 6554 | | |
| _RIAstWIRBIndTbIY_UIs_u2p14[0] | 8192 | | |
| _RIAstWIRBIndTbIY_UIs_u2p14[2] | 9830 | | |
| :_RIAstWIRBIndTbIY_UIs_u2p14[3] | 11469 | | |
| _RIAstWIRBIndTbIY_Uls_u2p14[4] | 13107 | | |
| KIAStWIRDINGTBIT_DIS_02p14[4] | 1050 | | |
| WIRBINGTBIX_MITNIT_dopo[0] | 1075 | | |
| _WIRBINDTDIX_MINIT_U6p6[1] _WIRBINDTDIX_MtrNm_u8p8[2] | 1101 | | |
| | 1126 | | |
| _WIRBIndTbIX_MtrNm_u8p8[3] | | | |
| WIRBINGTBIX_MtrNm_u8p8[4] | 1152 -7.1 | | |
| gt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -7.1 -500.5 | | |
| gt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 1 | | |
| gt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | -5.2 | | |
| gt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | | | |
| gt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 40.02 | | |
| gt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 400.06 | | |
| gt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 4.1 | -i-to | |
| gt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssist | | | |
| gt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotor\ | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDr | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDm | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_ | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLon | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpe | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAi | | | |
| Name | Actual Value | Expected Value | Resu |
| PreDecelGain Uls M f32 | 125792.758 | 125792.7599 ± 0.0625 | |

| 3 | 3-11-1-1 | | |
|--|--------------|---------------------------|--------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125792.758 | 125792.7599 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 512151.25 | 512151.2172 ± 0.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -300.610382 | -300.610367 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.20000005 | -2.2 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | -160.300003 | -160.3 ± 0.00390625 | • |
| PrevTbarAng_HwDeg_M_f32 | -1.15555549 | -1.155555556 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.939015687 | -0.939021333 ± 0.00390625 | • |
| tot FroDenDmonInrtCmp Per1 FroDenDmonInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | - |



| Т | | | | ✓ |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Name | T4 04 0.7 (D4 04 1) | |
|--|-----------------------------------|-----------------------------------|
| PreDecidion III. M. 132 125896.135 | Test Step 3.7 (Repeat Count = 1) | V |
| Piew I Pew I SaDrive Eaple M 122 2825 3 | | |
| Prev15citovVer, Radps M. 02 Prev25citovVer, Radps M. 02 Pr | PreDecelGain_Uls_M_f32 | 125895.135 |
| Piece/PeckalthComp, MithMI, M. 192 5.2 Prev/Pos/Ang, J. Haylas, M. 132 1.009 | Prev1PreAttnComp_MtrNm_M_f32 | |
| PrevZsiDnVerIL Padigs_M_132 157.2 | Prev1SclDrvVel_RadpS_M_f32 | |
| Pier/Tarkong Mrc Deg M, 192 1.009 1.00 | Prev2PreAttnComp_MtrNm_M_f32 | 5.2 |
| Re_Inst_Ap_FrqDepDmpnIntCmp | Prev2SclDrvVel_RadpS_M_f32 | 157.2 |
| ThankerHistor_M.str. K. U. Is. 132 | PrevTbarAng_HwDeg_M_f32 | 1.009 |
| ToarVerFittSy_M_str.K_Uls_[32] | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k, CmnSysKinRatio_MtrDegpHwDeg_/32 50.03 k, CmnThatSitf_NimpDeg_/32 52 k, DmpDeelGan_Us_/32 30.006 k, DmpDeelGan_Us_/32 4.2 k, DmpGainOnThresh_KphpS_f12 33.2 k, DmpGainOnThresh_KphpS_f12 15.2 k, InntCmp_Mtrineria_KgmSq_f12 0.00004 k, InntCmp_Mtrineria_KgmSq_f22 0.00004 k, InntCmp_Mtrineria_KgmSq_f22 0.5 k, InntCmp_Mtrineria_KgmSq_f22 0.5 k, InntCmp_Mtrineria_KgmSq_f22 0.5 12, FDD_ADDRollingTbYM_MtrinmRadps_umtp17(0)[1] 988 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[2] 1087 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[3] 1188 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[6] 1288 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[6] 1389 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[7] 1591 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[7] 1591 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(0)[8] 1692 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(1)[6] 1668 12, FDD_ADDRollingTbYM_MtrimmRadps_umtp17(1)[6] 1916 12, FD | TbarVelFiltSv_M_str.SV_Uls_f32 | 1.5 |
| K_CmrDarSilf_NmpDeg_[32] 5.2 k_DmpDeedGainFSilev_UilpsG_132 300.06 K_DmpDeedGain_UilpsG_2 4.2 k_DmpCainCrimresh_Kiphg_132 33.2 k_DmpCainCrimresh_Kiphg_162 15.2 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.5 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.00004 k_IntCmp_Mtrineth_Kiphg_162 0.000004 k_IntCmp_Mtrineth_Kiphg_162 0.00000000000000000000000000000000000 | TbarVelFiltSv_M_str.K_Uls_f32 | 0.58963 |
| K_DmpDecelGain_Fislew_UispS_G2 300.06 K_DmpDecelGain_Uis_G2 4.2 K_DmpGainOfThresh_KphpS_G2 33.2 K_DmpGainOfThresh_KphpS_G2 15.2 K_InfCmp_Mitherial_KgmSq_G12 0.00004 K_InfCmp_Mitherial_KgmSq_G12 0.5 K_InfCmp_Mitherial_KgmSq_G12 0.5 K_InfCmp_Mitherial_KgmSq_G12 0.5 K_InfCmp_Mitherial_KgmSq_G12 0.5 K_InfCmp_Mitherial_KgmSq_G12 0.5 K_InfCmp_Mitherial_KgmSq_G12 0.5 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(0)[1] 986 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(0)[2] 1188 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(0)[3] 1188 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(0)[7] 1389 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(0)[7] 1591 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(0)[8] 1992 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(1)[9] 1793 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(1)[1] 1212 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(1)[8] 1359 L2_FDD_ADDRollingTbVM_MithmpRadpS_um1p17(1)[8] 1480 L2_FDD_ADDRollingTbVM_Mithmp | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 50.03 |
| K_DmpDecelGain_Uls_G32 4.2 k_DmpGainOrThresh_KphpS_G32 3.2 k_DmpGainOrThresh_KphpS_G32 15.2 k_IntrCmp_Mtrleefia_KgmSq_G32 0.00004 k_IntrCmp_Mtrleefia_KgmSq_G32 0.5 L_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[0] 885 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[2] 1087 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[3] 1188 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[4] 1288 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1490 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1490 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1490 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1490 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1991 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1992 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(0)[6] 1992 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(1)[6] 1992 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(1)[6] 1992 12_FDD_ADDRollingTbYYM_MtrlmpRadpS_um1p17(1)[6] 1959 12_FDD_ADRollingTbYYM_MtrlmpRadpS_um1p17(1)[6] 1950 12_FDD_ | k_CmnTbarStiff_NmpDeg_f32 | 5.2 |
| k_DmpGainOffTreeh_KphpS_132 33.2 k_DmpGainOffTreeh_KphpS_132 15.2 k_InnCmp_Mirvel_ScaleFactor_Us_132 0.00004 k_InnCmp_Mirvel_ScaleFactor_Us_132 0.5 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[0] 885 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[1] 986 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[2] 1087 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[4] 1288 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[4] 1288 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[6] 1389 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[6] 1490 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[8] 1692 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(0)[8] 1793 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1793 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1869 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1869 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1869 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1869 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1869 12_FDD_ADDRollingTbYM_MirvimpRadpS_um1p17(1)[9] 1860 | k_DmpDecelGainFSlew_UlspS_f32 | 300.06 |
| K_DmpGainOnThreeh_KphpS_32 15.2 K_IntCmp_Mitrheria_KgmSq_132 0.000004 LR_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][0] 85 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][1] 986 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][2] 1087 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][3] 1188 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][4] 1288 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][6] 1389 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][6] 1490 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][7] 1591 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][8] 1992 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][9] 1793 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][9] 1793 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 1212 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 1212 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 1863 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 1863 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 1946 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 2903 L2_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1]< | k_DmpDecelGain_Uls_f32 | 4.2 |
| k_IntCmp_Mtrivet_ScaleFactor_Uls_f32 0.00004 k_IntCmp_Mtrivet_ScaleFactor_Uls_f32 0.5 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][0] 885 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][2] 1087 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][3] 1188 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][6] 1288 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][6] 1389 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][7] 1591 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][8] 1692 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[0][8] 1692 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][0] 1066 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][0] 1066 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][1] 1212 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][4] 1553 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][4] 1653 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][6] 1946 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][6] 1946 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][6] 1946 12_FDD_ADDRollingTb1/M_MtrivmpRadpS_umtp17[1][6] 1946 12_FDD_ADDRol | k_DmpGainOffThresh_KphpS_f32 | 33.2 |
| k_IntCmp_MitVel_ScaleFactor_Uls_132 0.5 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[0] 885 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[2] 1087 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[3] 1188 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[4] 1288 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[6] 1389 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[6] 1490 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[6] 1591 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[8] 1692 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(0)[8] 1692 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[9] 1793 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[1] 1212 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[1] 1212 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[2] 1359 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[3] 1506 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[4] 1653 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[5] 1846 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[6] 1946 12_FDD_ADDRollingTbt/M_MithmpRadps_umtp17(1)[6] 293 12_FDD_FreqTbt/M_Hz_u12p | k_DmpGainOnThresh_KphpS_f32 | 15.2 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][0] 885 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][1] 986 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][2] 1087 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][3] 1188 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][4] 1288 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][6] 1490 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][7] 1591 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][8] 1692 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][8] 1692 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[0][9] 1793 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][0] 1666 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][1] 1212 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][1] 1212 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][1] 1596 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][1] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 1800 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 1846 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 1846 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 1846 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 288 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 298 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 288 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 288 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 288 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][6] 288 12_FDD_FreqTbYM_Hz_u12p4[0][0] 188 12_FDD_FreqTbYM_Hz_u12p4[0][0] 189 12_ | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00004 |
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| 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0)[2] 1188 1288 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0)[3] 1389 1 | | 986 |
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| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][4] 1288 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][5] 1389 1490 1490 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][6] 1490 | | 1188 |
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| 1490 | | |
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| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 1793 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1066 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1212 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1359 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 12_FDD_FreqTblYM_Hz_u12p4[0][0] 80 12_FDD_FreqTblYM_Hz_u12p4[0][1] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][6] 208 | | |
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| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1212 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1359 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1506 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 208 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1359 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1506 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1506 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 12_FDD_ADRollingTblYM_Hz_u12p4[0][0] 80 12_FDD_FreqTblYM_Hz_u12p4[0][1] 96 12_FDD_FreqTblYM_Hz_u12p4[0][2] 112 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 12_FDD_FreqTblYM_Hz_u12p4[0][0] 80 12_FDD_FreqTblYM_Hz_u12p4[0][1] 96 12_FDD_FreqTblYM_Hz_u12p4[0][2] 112 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] 96 12_FDD_FreqTblYM_Hz_u12p4[0][2] 112 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| 12 FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 208 | | |
| | | |
| tz_FDD_Freq1blYM_Hz_u12p4[0][9] 224 | | |
| 10 FDD F TINAL II 40 WOVEN | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 144 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 144 |

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| · · · · · · · · · · · · · · · · · · · | |
|---|-------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 160 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 176 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 192 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 208 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 224 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 240 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 256 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 272 |
| CmnVehSpd_Kph_u9p7[0] | 2560 |
| _CmnVehSpd_Kph_u9p7[1] | 3840 |
| CmnVehSpd Kph u9p7[2] | 5120 |
| CmnVehSpd Kph u9p7[3] | 6400 |
| _CmnVehSpd_Kph_u9p7[4] | 7680 |
| _CmnVehSpd_Kph_u9p7[5] | 8960 |
| _CmnVehSpd_Kph_u9p7[6] | 10240 |
| _CmnVehSpd_Kph_u9p7[7] | 11520 |
| _CmnVehSpd_Kph_u9p7[8] | 12800 |
| _CmnVehSpd_Kph_u9p7[9] | 14080 |
| _CmnVehSpd_Kph_u9p7[10] | 15360 |
| CmnVehSpd Kph u9p7[11] | 16640 |
| _DmpADDCoefX_MtrNm_u4p12[0] | 20890 |
| DmpADDCoefX_MtrNm_u4p12[1] | 21299 |
| | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 21709 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 22118 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 22528 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 22938 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 23347 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 23757 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 24166 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 24576 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280 |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1608 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1616 |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1624 |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1632 |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1640 |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1648 |
| DmpFiltKpWIRBIndY Uls u2p14[0] | 8192 |
| DmpFiltKpWIRBIndY Uls u2p14[1] | 9830 |
| _DmpFiltKpWIRBIndY_UIs_u2p14[2] | 11469 |
| DmpFiltKpWIRBIndY UIs u2p14[3] | 13107 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1246 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1638 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2030 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2422 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2814 |
| | 3206 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] FDD ADDStaticTblY MtrNmpRadpS um1p17[6] | 3598 |
| | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3990 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4382 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4774 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 512 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 560 |
| _FDD_AttenTblY_Uls_u8p8[0] | 144 |
| _FDD_AttenTblY_Uls_u8p8[1] | 146 |
| _FDD_BlendTblY_Uls_u8p8[0] | 15 |
| _FDD_BlendTblY_Uls_u8p8[1] | 18 |
| _FDD_BlendTblY_Uls_u8p8[2] | 20 |
| _FDD_BlendTblY_Uls_u8p8[3] | 23 |
| _FDD_BlendTblY_Uls_u8p8[4] | 26 |
| _FDD_BlendTblY_Uls_u8p8[5] | 28 |
| FDD_BlendTblY_Uls_u8p8[6] | 31 |
| _FDD_BlendTblY_Uls_u8p8[7] | 33 |
| _FDD_BlendTblY_Uls_u8p8[8] | 36 |
| _FDD_BlendTblY_Uls_u8p8[9] | 38 |
| _FDD_BlendTblY_Uls_u8p8[10] | 41 |
| | 44 |

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| Name | Input Value | | |
|--|--------------|----------------------|-------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 128 | | |
| t InrtCmp ScaleFactorTblY UIs u9p7[6] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 192 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[11] | 205 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 61 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[1] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 65 | | |
| t_inrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 69 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 70 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 72 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 73 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 74 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 76 | | |
| t_RIAstWIRBIndTblY_Uis_u2p14[0] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 9830 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[2] | 11469 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[4] | 14746 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[0] | 1306 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1331 | | |
| t WIRBIndTbiX MtrNm u8p8[2] | 1357 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1382 | | |
| t WIRBIndTbiX MtrNm u8p8[4] | 1408 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 6.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 400.6 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 5.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -10.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 500.08 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 5.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistC | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorV | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDm | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmp | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_I | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonA | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpee | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAm | | | |
| Name | Actual Value | Expected Value | Resul |
| | | • | Resul |
| PreDecelGain_Uls_M_f32 | 125894.531 | 125894.5349 ± 0.0625 | |

| 3 | 3-11-1-1 | | |
|--|--------------|--------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125894.531 | 125894.5349 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 16663430 | 16663430.49 ± 99.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 202.182922 | 202.1828953 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 3.29999995 | 3.3 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 2625.30005 | 2625.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1.01923084 | 1.019230769 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.63177729 | 3.631739231 ± 0.00390625 | ~ |
| tot FroDenDmonInrtCmp Per1 FroDenDmonInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | V |



| Т | | | | ✓ |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Proceeding U.S. M. 152 12997-11 | Took Stan 2.0 (Panast Count - 4) | م |
|--|---|-----------------------------------|
| PROBLEMENT JULY JULY JULY JULY JULY JULY JULY JULY | Test Step 3.8 (Repeat Count = 1) | <u> </u> |
| Prest Stankvor (Bars) M. 182 Prest DaubStankvor (Bars) M. 182 | | |
| Provision Notice Septiment | PreDecelGain_Uls_M_f32 | |
| Privazishancomp, Minhm, M. 192 23 28 28 28 28 28 28 2 | Prev1PreAttnComp_MtrNm_M_f32 | |
| Prov.250P./vvl. Ravips M. 132 Prov. Park Park Pubo M. 152 Ris _ Inst _ Ap _ Frq Dep Drepnint Cmp | Prev1SclDrvVel_RadpS_M_f32 | |
| PrevTbarAng-MoDey_M_22 Bits_Inst_A_F_InSt_DeptroprindrCmp By _Rit_Inst_A_F_InQepDroprindrCmp By _Rit_IndepDroprindrCmp By _Rit_IndepDroprindrCm | Prev2PreAttnComp_MtrNm_M_f32 | -2.3 |
| Ric_Inst_AD_FriqDepDmpnintCmp | Prev2SclDrvVel_RadpS_M_f32 | -363.2 |
| ThanVerlisty, M. Jat. X. U.B. 12 0.83214 k. CrmiSysKinRatio, MirDeghtwOeg, IS2 0.05324 k. CrmiSysKinRatio, MirDeghtwOeg, IS2 0.055 k. CrmiSysKinRatio, MirDeghtwOeg, IS2 0.055 k. CrmiSysKinRatio, MirDeghtwOeg, IS2 0.055 k. DropDecelGam, Ula, IS2 0.55 k. DropDecelGam, Ula, IS2 0.55 k. DropDecelGam, Ula, IS2 0.55 k. DropGendorThresh, KjripS, IS2 0.65 k. DropGendorThresh, KjripS, IS2 0.060 k. IntriCom, Mirthersh, KjripS, IS2 0.00008 k. IntriCom, Mirthersh, KjripS, IS2 0.00008 k. IntriCom, Mirthersh, KjripS, IS2 0.00008 k. IntriCom, Mirthersh, KjripS, IS2 0.04 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[0] 1.066 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.212 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.212 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.213 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.214 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.215 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.216 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.217 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.218 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.219 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.219 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.219 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.2200 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.2300 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.246 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.2600 2. FDD, FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.2700 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.2700 2. FDD, ADDRollingThrVM, MirthershadS, urm1p170[1] 1.2700 2. FDD, FDD, FDD, TDD, MirthershadS, urm1p170[1] 1.2700 1.2700 1.2700 1.2700 1.2700 1.2700 1.2700 1.2700 1.2700 1.2700 1. | PrevTbarAng_HwDeg_M_f32 | 0.159 |
| TRAVERISS, M., str. K., Us. D2 L. Com Trastiff: Ningbeg Moeg. 52 L. Com Moeg. Moeg. 52 L. Moeg. 52 L. Moeg. Moeg. 52 L. Moeg. 52 L. Moeg. 52 L. Moeg. 53 L. Moeg. 53 L. Moeg. 53 L. Moeg. 53 L. Moeg. 54 L | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k, Com/Systinitatio, Michaephrobeg, 132 60.5 k, Com/Dacellarin, Michael, 2018, 52 6.2 k, Dimplecedianin/Siew, Ulays, 52 400.05 k, Dimplecedianin/Siew, Ulays, 52 44.5 k, Dimplecedianin/Siew, Ulays, 52 44.5 k, Dimplecedianin/Siew, 196, 52 20.6 k, Indram, Mirvel, ScaleFactor, Uls, 52 0.00008 k, Indram, Mirvel, ScaleFactor, Uls, 52 0.4 L, EDB, ADDRalling/ShVM, MirrimpRadps, unip17(0)[0] 1066 L, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[1] 12/2 L, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[2] 1359 12, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[3] 1506 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[4] 1853 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[6] 1853 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[7] 2093 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[7] 2093 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(0)[8] 2340 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(1)[9] 2387 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(1)[9] 246 2, FDD, ADDRalling/ShVM, MirrimpRadps, unip17(1)[9] <td>TbarVelFiltSv_M_str.SV_Uls_f32</td> <td>-6.6</td> | TbarVelFiltSv_M_str.SV_Uls_f32 | -6.6 |
| 8. Com/DataSidf NimpDeg_122 8.2 8. DmpDecelSain/Siles UlspS_102 400.05 8. DmpDecelSain/Siles UlspS_102 6.5 8. DmpGeain/Offfreen, KipbpS_102 44.5 8. Indrom_Mitterial, KiphS_102 20.6 8. Indrom_Mitterial, KiphS_102 0.00008 8. Indrom_Mitterial, KiphS_102 0.4 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1066 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1026 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1359 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1359 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1663 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1663 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1946 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 1946 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 293 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 294 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(DIQ) 294 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(TIQ) 293 12. FDD_ADDRollingTbVM, MitteringRadpS_ump17(TIQ) 293 <t< td=""><td>TbarVelFiltSv_M_str.K_Uls_f32</td><td>0.63214</td></t<> | TbarVelFiltSv_M_str.K_Uls_f32 | 0.63214 |
| k_DmpDecelGain_UBL_GI2 40.005 k_DmpGainOfThreels, Kphps_GI2 4.5 k_DmpGainOfThreels, Kphps_GI2 44.5 k_DmpGainOfThreels, Kphps_GI2 20.6 k_IndCmp_Mithreelin_KgmSq_GI2 0.00008 k_IndCmp_Mithreelin_KgmSq_GI2 0.4 k_IndCmp_Mithreelin_KgmSq_GI2 0.4 k_EPD_ADROBingTbVM_MithrepRadpS_umlp17(0)[1] 1212 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[1] 1212 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[3] 1596 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[4] 1653 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[6] 1800 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[7] 293 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[8] 240 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[8] 2387 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[8] 2387 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[1] 1838 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[1] 1838 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[1] 1838 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp17(0)[1] 2426 12_FDD_ADDRollingTbVM_MithrepRadpS_umlp | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 60.05 |
| k_ DmpGainOffTriest, KphpS_132 | k_CmnTbarStiff_NmpDeg_f32 | 6.2 |
| LongolaiofTirrest Kphps 322 | k_DmpDecelGainFSlew_UlspS_f32 | 400.05 |
| k, DmpCainOnTreeth, KypinSc, 132 0.00008 k, InthComp, Mrinertia, KypinSc, 132 0.00008 k, InthComp, Mrinertia, KypinSc, 132 0.04 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(1) 1066 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(2) 1359 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(3) 1506 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(4) 1653 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(5) 1800 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(7) 293 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(7) 293 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(8) 2240 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(0)(9) 2387 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(1)(1) 1638 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(1)(1) 1638 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(1)(1) 1638 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(1)(1) 293 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(1)(1) 294 12, FDD, ADDRolling TbVM, MinhingRadpSc, unit p17(1)(1) 390 12, FDD, FreqTbVM, MinhingRadpS | k_DmpDecelGain_Uls_f32 | 6.5 |
| IntrCmp_Mtritectia_Kgmsq_rs2 | k_DmpGainOffThresh_KphpS_f32 | 44.5 |
| LintChop, MirVel, ScaleFactor, Uis_122 | k_DmpGainOnThresh_KphpS_f32 | 20.6 |
| P. FDD ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 1066 | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00008 |
| 12 FDD _ ADDRollingTbYM_MrNmpRadpS_um1p17(0)[2] 1359 1560 | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 |
| 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][2] 1359 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][4] 1653 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][5] 1800 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][6] 1800 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][7] 2033 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][7] 2033 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][8] 2240 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][8] 2240 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[0][9] 2387 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][9] 1638 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][1] 1638 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][1] 2030 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][4] 2814 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][6] 3060 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][6] 4382 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][6] 4382 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][6] 4382 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17[1][6] 4382 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 112 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 100 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 208 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 209 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 209 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 209 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 209 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 308 2 FDD_Fe | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1066 |
| 12 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 23 1556 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 43 1653 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 54 1653 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 55 1800 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 75 1800 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 77 2093 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 76 2464 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 76 2464 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0) 76 2464 2 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(1) 76 2464 2 FDD_FeqTbYM_Hz_u12p4(0) 1 112 2 FDD_FeqTbYM_Hz_u12p4(0) 1 112 2 FDD_FeqTbYM_Hz_u12p4(0) 3 1444 2 FDD_FeqTbYM_Hz | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][1] | 1212 |
| 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][3] 1506 1507_ADDRollingTbYM_MmmpRadpS_um1p17[0][4] 1653 1500_ADDRollingTbYM_MmmpRadpS_um1p17[0][6] 1840 1840_ADDRollingTbYM_MmmpRadpS_um1p17[0][6] 1946 | | 1359 |
| P. FDD_ADDRollingTbiYM_MrnmpRadpS_um1p17[0][4] 1653 1800 | | 1506 |
| 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(0)[5] 1800 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(0)[6] 1946 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(0)[8] 2933 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(0)[8] 2240 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(0)[8] 2387 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[9] 2387 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[1] 1638 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[1] 2030 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[1] 2030 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 2422 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 3206 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 3598 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 3598 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 3890 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 4382 12_FDD_ADDRollingTbYM_MirthmpRadpS_untp17(1)[8] 4382 12_FDD_FreqTbYM_Hz_u12p4[0][0] 96 12_FDD_FreqTbYM_Hz_u12p4[0][1] 112 12_FDD_FreqTbYM_Hz_u12p4[0][1] 112 12_FDD_FreqTbYM_Hz_u12p4[0][1] 112 12_FDD_FreqTbYM_Hz_u12p4[0][1] 100 12_FDD_FreqTbYM_Hz_u12p4[0][1] 100 12_FDD_FreqTbYM_Hz_u12p4[0][1] 100 12_FDD_FreqTbYM_Hz_u12p4[0][1] 100 12_FDD_FreqTbYM_Hz_u12p4[0][1] 200 12_FDD_FreqTbYM_Hz_u12p4[0][1] 20 | | |
| 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0] 6 1946 | | |
| 12_FDD_ADRRollingTbiYM_MtrNmpRadpS_um1p17(0) 7 2093 2240 2250 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 8 2387 | | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0] 89 2387 22 FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1] 10 1246 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17{1 0} 1246 1638 | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] 1638 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] 2030 | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] 2030 2422 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2422 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2814 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3506 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3598 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3990 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4382 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4774 12_FDD_FORD_IND_MTRNMPRADPS_um1p17[1][9] 4774 12_FDD_Ford_TblYM_Hz_u12p4[0][1] 112 12_FDD_Ford_TblYM_Hz_u12p4[0][1] 112 12_FDD_Ford_TblYM_Hz_u12p4[0][2] 128 12_FDD_Ford_TblYM_Hz_u12p4[0][3] 144 12_FDD_Ford_TblYM_Hz_u12p4[0][4] 160 12_FDD_Ford_TblYM_Hz_u12p4[0][6] 192 12_FDD_Ford_TblYM_Hz_u12p4[0][6] 192 12_FDD_Ford_TblYM_Hz_u12p4[0][6] 192 12_FDD_Ford_TblYM_Hz_u12p4[0][7] 208 12_FDD_Ford_TblYM_Hz_u12p4[0][8] 224 12_FDD_Ford_TblYM_Hz_u12p4[0][9] 240 12_FDD_Ford_TblYM_Hz_u12p4[0][1] 272 12_FDD_Ford_TblYM_Hz_u12p4[0][1] 272 12_FDD_Ford_TblYM_Hz_u12p4[0][1] 362 12_FDD_Ford_TblYM_Hz_u12p4[1][0] 368 12_FDD_Ford_TblYM_Hz_u12p4[1][1] 352 12_FDD_Ford_TblYM_Hz_u12p4[1][1] 352 12_FDD_Ford_TblYM_Hz_u12p4[1][2] 368 12_FDD_Ford_TblYM_Hz_u12p4[1][2 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2814 22_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3206 22_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3598 22_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3990 22_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3990 22_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4382 22_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4774 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 5 3206 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 6 3598 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 7 3990 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 8 4382 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 9 4774 12_FDD_FreqTblYM_Hz_u12p4[0] 0 96 12_FDD_FreqTblYM_Hz_u12p4[0] 2 128 12_FDD_FreqTblYM_Hz_u12p4[0] 3 144 12_FDD_FreqTblYM_Hz_u12p4[0] 3 144 12_FDD_FreqTblYM_Hz_u12p4[0] 5 176 12_FDD_FreqTblYM_Hz_u12p4[0] 6 192 12_FDD_FreqTblYM_Hz_u12p4[0] 6 192 12_FDD_FreqTblYM_Hz_u12p4[0] 6 224 12_FDD_FreqTblYM_Hz_u12p4[0] 8 224 12_FDD_FreqTblYM_Hz_u12p4[0] 9 240 12_FDD_FreqTblYM_Hz_u12p4[0] 9 272 12_FDD_FreqTblYM_Hz_u12p4[0] 11 272 12_FDD_FreqTblYM_Hz_u12p4[1] 0 336 12_FDD_FreqTblYM_Hz_u12p4[1] 11 352 12_FDD_FreqTblYM_Hz_u12p4[1] 12 368 12_FDD_FreqTblYM_Hz_u12p4[1] 11 352 12_FDD_FreqTblYM_Hz_u12p4[1] 11 352 12_FDD_FreqTblYM_Hz_u12p4[1] 11 368 12_FDD_FreqTblYM_Hz_u12p4[1] 12 368 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3598 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3990 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4382 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4774 12_FDD_FreqTblYM_Hz_u12p4[0][0] 96 12_FDD_FreqTblYM_Hz_u12p4[0][1] 112 12_FDD_FreqTblYM_Hz_u12p4[0][2] 128 12_FDD_FreqTblYM_Hz_u12p4[0][3] 144 12_FDD_FreqTblYM_Hz_u12p4[0][4] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][8] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 256 12_FDD_FreqTblYM_Hz_u12p4[0][1] 272 12_FDD_FreqTblYM_Hz_u12p4[1][1] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][1] 368 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3990 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4382 12_FDD_FreqTblYM_Hz_u12p4[0][0] 4774 12_FDD_FreqTblYM_Hz_u12p4[0][1] 112 12_FDD_FreqTblYM_Hz_u12p4[0][2] 128 12_FDD_FreqTblYM_Hz_u12p4[0][3] 144 12_FDD_FreqTblYM_Hz_u12p4[0][4] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][1] 368 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4382 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4774 12_FDD_FreqTblYM_Hz_u12p4[0][0] 96 12_FDD_FreqTblYM_Hz_u12p4[0][1] 112 12_FDD_FreqTblYM_Hz_u12p4[0][2] 128 12_FDD_FreqTblYM_Hz_u12p4[0][3] 144 12_FDD_FreqTblYM_Hz_u12p4[0][4] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][8] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 272 12_FDD_FreqTblYM_Hz_u12p4[0][1] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0] 96 t2_FDD_FreqTbIYM_Hz_u12p4[0][1] 112 t2_FDD_FreqTbIYM_Hz_u12p4[0][2] 128 t2_FDD_FreqTbIYM_Hz_u12p4[0][3] 144 t2_FDD_FreqTbIYM_Hz_u12p4[0][4] 160 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 176 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 192 t2_FDD_FreqTbIYM_Hz_u12p4[0][7] 208 t2_FDD_FreqTbIYM_Hz_u12p4[0][8] 224 t2_FDD_FreqTbIYM_Hz_u12p4[0][9] 240 t2_FDD_FreqTbIYM_Hz_u12p4[0][10] 256 t2_FDD_FreqTbIYM_Hz_u12p4[0][11] 272 t2_FDD_FreqTbIYM_Hz_u12p4[1][0] 336 t2_FDD_FreqTbIYM_Hz_u12p4[1][1] 352 t2_FDD_FreqTbIYM_Hz_u12p4[1][1] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] 112 12_FDD_FreqTblYM_Hz_u12p4[0][2] 128 12_FDD_FreqTblYM_Hz_u12p4[0][3] 144 12_FDD_FreqTblYM_Hz_u12p4[0][4] 160 12_FDD_FreqTblYM_Hz_u12p4[0][5] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][1] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] 128 12_FDD_FreqTblYM_Hz_u12p4[0][3] 144 12_FDD_FreqTblYM_Hz_u12p4[0][4] 160 12_FDD_FreqTblYM_Hz_u12p4[0][5] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][1] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 160 12_FDD_FreqTblYM_Hz_u12p4[0][5] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][8] 224 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][9] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][11] 272 12_FDD_FreqTblYM_Hz_u12p4[1][0] 336 12_FDD_FreqTblYM_Hz_u12p4[1][1] 352 12_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 272 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 336 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 352 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 336 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 352 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 352 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 368 | | |
| | | |
| 40 FDD FronthiVM H= v40n4(4)(0) | | |
| IZ_FDU_FTEQTIBITM_FIZ_U1ZP4[1][3] 384 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 384 |

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|---|-------------|--|
| Name | Input Value | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 400 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][5] | 416 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 432 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 448 | |
| 2 FDD FregTblYM Hz u12p4[1][8] | 464 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][9] | 480 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][10] | 496 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][11] | 512 | |
| t_CmnVehSpd_Kph_u9p7[0] | 12800 | |
| :_CmnVehSpd_Kph_u9p7[1] | 12928 | |
| :_CmnVehSpd_Kph_u9p7[2] | 13056 | |
| t_CmnVehSpd_Kph_u9p7[3] | 13184 | |
| | 13312 | |
| | 13440 | |
| t_CmnVehSpd_Kph_u9p7[5] | | |
| t_CmnVehSpd_Kph_u9p7[6] | 13568 | |
| _CmnVehSpd_Kph_u9p7[7] | 13696 | |
| CmnVehSpd_Kph_u9p7[8] | 13824 | |
| CmnVehSpd_Kph_u9p7[9] | 13952 | |
| CmnVehSpd_Kph_u9p7[10] | 14080 | |
| CmnVehSpd_Kph_u9p7[11] | 14208 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| :_DmpADDCoefX_MtrNm_u4p12[1] | 25395 | |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 25805 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 26624 | |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 27034 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 27443 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 27853 | |
| u4p12[8] | 28262 | |
| :_DmpADDCoefX_MtrNm_u4p12[9] | 28672 | |
| | 32320 | |
| mpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | |
| bmpbecelGainGlewX_mtrkaape_u11p5[2] _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | |
| | 32448 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[2] | 2424 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1427 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1655 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1884 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2112 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2340 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2568 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2796 | |
| | | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3024 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3252 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3480 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 656 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 720 | |
| _FDD_AttenTblY_Uls_u8p8[0] | 172 | |
| _FDD_AttenTbIY_Uls_u8p8[1] | 174 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 18 | |
| _FDD_BlendTblY_Uls_u8p8[1] | 20 | |
| _FDD_BlendTblY_Uls_u8p8[2] | 23 | |
| _FDD_BlendTblY_Uls_u8p8[3] | 26 | |
| _FDD_BlendTblY_Uls_u8p8[4] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 31 | |
| | | |
| _FDD_BlendTblY_Uls_u8p8[6] | 33 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 36 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 38 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 41 | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 44 | |
| t_FDD_BlendTblY_Uls_u8p8[11] | 46 | |

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| Name | Input Value | | |
|---|--------------|-----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 294 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 77 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[1] | 78 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 79 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 81 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 82 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 83 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[6] | 84 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 86 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 87 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 88 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 90 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 91 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[0] | 1638 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[4] | 8192 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1562 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1587 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1613 | | |
| t WIRBIndTbIX MtrNm u8p8[3] | 1638 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1664 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -6.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -1118 | | |
| tgt FrqDepDmpnInrtCmp Per1 FreqDepDmpSrlComSvcDft Cnt Igc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 1.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -20.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 110.07 | | |
| tgt FrqDepDmpnInrtCmp Per1 WIRCmdAmpBlnd MtrNm f32.value | 6.3 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | 1 1 | stCmd MtrNm f32 | |
| tat Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmp | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnI | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hv | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAc | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmp | | | |
| Name | Actual Value | Expected Value | Result |
| Tunio Tunio | Actual Value | Expedied value | Result |

| 20 | h = . .3 | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 125996.313 | 125996.3099 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -9984653 | -9984653.482 ± 9.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -447.704346 | -447.704346 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.29999995 | -3.3 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -4021.30005 | -4021.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.164516136 | 0.164516129 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.684389591 | -0.684393097 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | -8 80000019 | -8.8 + 0.00048828125 | ✓ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Took Stan 2.0 (Panast Count = 4) | ه. |
|---|-----------------------------------|
| Test Step 3.9 (Repeat Count = 1) | · |
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 126099.085 |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.4 |
| Prev1SclDrvVel_RadpS_M_f32 | 1234.2 |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.3 |
| Prev2ScIDrvVel_RadpS_M_f32 | 4678.2 |
| PrevTbarAng_HwDeg_M_f32 | -0.129 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.2 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.014785 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 70.5 |
| k_CmnTbarStiff_NmpDeg_f32 | 7.5 |
| k_DmpDecelGainFSlew_UlspS_f32 | 500.02 |
| k_DmpDecelGain_Uls_f32 | 5.6 |
| k_DmpGainOffThresh_KphpS_f32 | 8.6 |
| k_DmpGainOnThresh_KphpS_f32 | 25.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00009 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][1] | 1638 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 2030 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][3] | 2422 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2814 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 3206 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 3598 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 3990 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4382 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 4774 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1427 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1655 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1884 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2112 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2340 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2568 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2796 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 3024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3252 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 3480 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 336 |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] | 352 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 368 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 384 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 400 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 416 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 432 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 448 |
| 12_FDD_FreqTblYM_Hz_u12p4[0][8] | 464 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 480 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 496 |
| | 512 |
| 12_FDD_FreqTblYM_Hz_u12p4[0][11] | 656 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][1] | 672 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 688 704 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 104 |

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|--|----------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 720 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 |
| CmnVehSpd_Kph_u9p7[0] | 15488 |
| CmnVehSpd_Kph_u9p7[1] | 15616 |
| CmnVehSpd_Kph_u9p7[2] | 15744 |
| CmnVehSpd Kph u9p7[3] | 15872 |
| CmnVehSpd_Kph_u9p7[4] | 16000 |
| CmnVehSpd_Kph_u9p7[5] | 16128 |
| CmnVehSpd_Kph_u9p7[6] | 16256 |
| CmnVehSpd_Kph_u9p7[7] | 16384 |
| CmnVehSpd_Kph_u9p7[8] | 16512 |
| | 16640 |
| CmnVehSpd_Kph_u9p7[9] | 16768 |
| CmnVehSpd_Kph_u9p7[10] | |
| CmnVehSpd_Kph_u9p7[11] | 16896 |
| DmpADDCoefX_MtrNm_u4p12[0] | 28262 |
| DmpADDCoefX_MtrNm_u4p12[1] | 28672 |
| DmpADDCoefX_MtrNm_u4p12[2] | 29082 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 29491 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 29901 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 30310 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 30720 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 31130 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 31539 |
| DmpADDCoefX_MtrNm_u4p12[9] | 31949 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752 |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 384 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 392 |
| DmpDecelGainSlewY UlspS u13p3[2] | 400 |
| _ , _ , _ , _ , _ , _ , _ , _ , _ , _ , | 408 |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 416 |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 424 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1608 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2032 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2455 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2878 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3302 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3725 |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[6] | 4148 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4572 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4995 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5419 |
| | 768 |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 800 |
| FDD_AttenTblY_Uls_u8p8[0] | 218 |
| FDD_AttenTblY_Uls_u8p8[1] | 220 |
| FDD_BlendTbIY_Uls_u8p8[0] | 20 |
| FDD_BlendTbIY_Uls_u8p8[1] | 23 |
| FDD_BlendTbIY_Uls_u8p8[2] | 26 |
| FDD_BlendTbIY_Uls_u8p8[3] | 28 |
| FDD_BlendTblY_Uls_u8p8[4] | 31 |
| FDD_BlendTblY_Uls_u8p8[5] | 33 |
| FDD_BlendTblY_Uls_u8p8[6] | 36 |
| | 38 |
| FUD Blend I DIY UIS U8D8171 | |
| | 41 |
| _FDD_BlendTbIY_Uls_u8p8[8] | 41 |
| _FDD_BiendTblY_Uis_u8p8[7] _FDD_BiendTblY_Uis_u8p8[8] _FDD_BiendTblY_Uis_u8p8[9] _FDD_BiendTblY_Uis_u8p8[10] | 41 44 46 |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 320 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 92 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 97 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 99 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 100 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 101 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 102 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 104 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 105 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 106 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 3277 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 4915 | | |
| t RIAstWIRBIndTbIY Uls u2p14[2] | 6554 | | |
| t RIAstWIRBIndTbIY Uls u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 1766 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 1792 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1818 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1843 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1869 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 4.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 1118 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -1.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -30.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 120.08 | | |
| tgt FrqDepDmpnInrtCmp Per1 WIRCmdAmpBInd MtrNm f32.value | 7.1 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCme | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB | | | |
| | | | Basil |
| Name | Actual Value | Expected Value | Result |

| 20 | h = . .3 | · · · · _ · · · · · · · · · · · · · · · | |
|--|--------------|---|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126098.086 | 126098.085 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -3128609.5 | -3128609.352 ± 9.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 340.747711 | 340.7476731 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.4000001 | 4.4 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 1234.19995 | 1234.2 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.137333333 | -0.137333333 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 6.04672861 | 6.046728833 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | -8 80000019 | -8 8 + 0 00048828125 | ✓ |



| T ✓ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| T4 04-9 0.40 (D-9-4 0-994 - 4) | |
|---|-----------------------------------|
| Test Step 3.10 (Repeat Count = 1) | V |
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 126201.06 |
| Prev1PreAttnComp_MtrNm_M_f32 | -4.4 |
| Prev1SclDrvVel_RadpS_M_f32 | -270.2 |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.7 |
| Prev2SclDrvVel_RadpS_M_f32 | -15.3 |
| PrevTbarAng_HwDeg_M_f32 | 0.279 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.025896 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 80.02 |
| k_CmnTbarStiff_NmpDeg_f32 | 8.8 |
| k_DmpDecelGainFSlew_UlspS_f32 | 600.06 |
| k_DmpDecelGain_Uls_f32 | 7.2 |
| k_DmpGainOffThresh_KphpS_f32 | 16.2 |
| k_DmpGainOnThresh_KphpS_f32 | 30.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0001 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1427 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1655 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1884 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2112 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 2340 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][5] | 2568 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 2796 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][7] | 3024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3252 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 3480 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1608 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2032 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2878 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3302 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3725 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] | 4148 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4572 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] | 4995 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5419 |
| t2 FDD FreqTblYM Hz u12p4[0][0] | 656 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 672 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 688 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 704 |
| 12 FDD FreqTbIYM Hz u12p4[0][4] | 720 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 736 |
| | 752 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 768 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 784 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 800 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 816 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 832 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1296 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1312 |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2] | 1328 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1344 |

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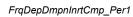
| -гідреропірпіпістір_Регі | |
|--|-------------|
| Name | Input Value |
| t2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1360 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1376 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1392 |
| 12_FDD_FreqTblYM_Hz_u12p4[1][7] | 1408 |
| 12 FDD FregTblYM Hz u12p4[1][8] | 1424 |
| 12_FDD_FreqTblYM_Hz_u12p4[1][9] | 1440 |
| 12_FDD_FreqTblYM_Hz_u12p4[1][10] | 1456 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1472 |
| t_CmnVehSpd_Kph_u9p7[0] | 10368 |
| t_CmnVehSpd_Kph_u9p7[1] | 10496 |
| t_CmnVehSpd_Kph_u9p7[2] | 10624 |
| t CmnVehSpd Kph u9p7[3] | 10752 |
| cmnVehSpd_Kph_u9p7[4] | 10880 |
| :_CmnVehSpd_Kph_u9p7[5] | 11008 |
| t_CmnVehSpd_Kph_u9p7[6] | 11136 |
| :_CmnVehSpd_Kph_u9p7[7] | 11264 |
| :_CmnVehSpd_Kph_u9p7[8] | 11392 |
| :_CmnVehSpd_Kph_u9p7[9] | 11520 |
| : CmnVehSpd Kph u9p7[10] | 11648 |
| : CmnVehSpd Kph u9p7[11] | 11776 |
| | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 24986 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 25395 |
| _DmpADDCoefX_MtrNm_u4p12[2] | 25805 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 26214 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 26624 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 27034 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 27443 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 27853 |
| :_DmpADDCoefX_MtrNm_u4p12[8] | 28262 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 28672 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 27264 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 27296 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 27328 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 27360 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 27392 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 27424 |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 3608 |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 3616 |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 3624 |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 3632 |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 3640 |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 3648 |
| DmpFiltKpWIRBIndY Uls u2p14[0] | 4915 |
| | 6554 |
| | 8192 |
| DmpFiltKpWIRBIndY Uls u2p14[3] | 9830 |
| | 11469 |
| | 1789 |
| FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 2130 |
| FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 2471 |
| _FDD_ADDStaticTbIY_MitNinpRadpS_uni1p17[2] | 2811 |
| _FDD_ADDStaticTbH_MtlnMpRadpS_um1p17[3] _FDD_ADDStaticTbHY_MtrNmpRadpS_um1p17[4] | 3152 |
| | 3493 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3834 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4175 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4515 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4856 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 784 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 880 |
| _FDD_AttenTblY_Uls_u8p8[0] | 63 |
| _FDD_AttenTblY_Uls_u8p8[1] | 66 |
| _FDD_BlendTblY_Uls_u8p8[0] | 49 |
| _FDD_BlendTblY_Uls_u8p8[1] | 51 |
| _FDD_BlendTblY_Uls_u8p8[2] | 54 |
| _FDD_BlendTblY_Uls_u8p8[3] | 57 |
| _FDD_BlendTblY_Uls_u8p8[4] | 60 |
| _FDD_BlendTblY_Uls_u8p8[5] | 63 |
| _FDD_BlendTblY_Uls_u8p8[6] | 66 |
| _FDD_BlendTblY_Uls_u8p8[7] | 68 |
| _FDD_BlendTblY_Uls_u8p8[8] | 71 |
| _FDD_BlendTblY_Uls_u8p8[9] | 74 |
| _FDD_BlendTblY_Uls_u8p8[10] | 77 |
| | 80 |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 282 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 13 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 14 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 15 | | |
| t RIAstWIRBIndTbIY UIs u2p14[0] | 4915 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 6554 | | |
| t RIAstWIRBIndTbIY Uls u2p14[2] | 8192 | | |
| t RIAstWIRBIndTbIY Uls u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 11469 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 410 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 435 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 461 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 486 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 512 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -4.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 2.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -40.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 130.09 | | |
| tgt FrqDepDmpnInrtCmp Per1 WIRCmdAmpBInd MtrNm f32.value | 7.1 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwYorque_Hw tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt_Rte_inst_Ap_FrqDepDmpnintCmp.FrqDepDmpnintCmp_Fer1_venicleConAct tgt_Rte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VenicleSpeed | | | |
| | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | | <u> </u> | ۱ |
| Name | Actual Value | Expected Value | Result |

| 20 | | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126199.859 | 126199.8599 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -377091.875 | -377091.8717 ± 0.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -0.866061449 | -0.866061495 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -4.4000001 | -4.4 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -270.200012 | -270.2 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.284090906 | 0.284090909 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -5.29165506 | -5.291654909 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |





| T ✓ | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.11 (Repeat Count = 1) Name | Input Value |
|---|-----------------------------------|
| | · · |
| PreDecelGain_Uls_M_f32 | 126303.035 |
| Prev1PreAttnComp_MtrNm_M_f32 | 5.5 |
| Prev1ScIDrvVel_RadpS_M_f32 | 6789 |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.7 |
| Prev2SclDrvVel_RadpS_M_f32 | 5322.2 |
| PrevTbarAng_HwDeg_M_f32 | -0.269 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| FbarVelFiltSv_M_str.SV_Uls_f32 | 5.2 |
| FbarVelFiltSv_M_str.K_Uls_f32 | 0.03698 |
| c_CmnSysKinRatio_MtrDegpHwDeg_f32 | 90.02 |
| C_CmnTbarStiff_NmpDeg_f32 | 9.6 |
| C_DmpDecelGainFSlew_UlspS_f32 | 700.02 |
| c_DmpDecelGain_Uls_f32 | 8.5 |
| c_DmpGainOffThresh_KphpS_f32 | 24.1 |
| <pre>C_DmpGainOnThresh_KphpS_f32</pre> | 35.3 |
| _InrtCmp_MtrInertia_KgmSq_f32 | 0.00008 |
| _InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1608 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2032 |
| 2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][2] | 2455 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 2878 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 3302 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 3725 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 4148 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 4572 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 4995 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][9] | 5419 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 1789 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 2130 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 2471 |
| | 2811 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 3152 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 3493 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3834 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4175 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4515 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 4856 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1296 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1312 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1328 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1344 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1360 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1376 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1392 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1408 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1424 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1440 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1456 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1472 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][0] | 1136 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1152 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][2] | 1168 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1184 |

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| гідреропірпіпістір_гегі | TOTAL CITY |
|--|-------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1200 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1216 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1232 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1248 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1264 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1280 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1296 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1312 |
| _CmnVehSpd_Kph_u9p7[0] | 5248 |
| _CmnVehSpd_Kph_u9p7[1] | 5376 |
| CmnVehSpd Kph u9p7[2] | 5504 |
| CmnVehSpd Kph u9p7[3] | 5632 |
| _CmnVehSpd_Kph_u9p7[4] | 5760 |
| | 5888 |
| _CmnVehSpd_Kph_u9p7[5] | 6016 |
| _CmnVehSpd_Kph_u9p7[6] | 6144 |
| _CmnVehSpd_Kph_u9p7[7] | 6272 |
| _CmnVehSpd_Kph_u9p7[8] | |
| _CmnVehSpd_Kph_u9p7[9] | 6400 |
| _CmnVehSpd_Kph_u9p7[10] | 6528 |
| _CmnVehSpd_Kph_u9p7[11] | 6656 |
| _DmpADDCoefX_MtrNm_u4p12[0] | 28262 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 28672 |
| _DmpADDCoefX_MtrNm_u4p12[2] | 29082 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 29491 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 29901 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 30310 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 30720 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 31130 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 31539 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 31949 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 14592 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 14624 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 14656 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 14688 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 14720 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 14752 |
| DmpDecelGainSlewY_UlspS_u13p3[0] | 288 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 296 |
| DmpDecelGainSlewY UlspS u13p3[2] | 304 |
| DmpDecelGainSlewY UlspS u13p3[3] | 312 |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 320 |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 328 |
| DmpFiltKpWIRBIndY Uls u2p14[0] | 6554 |
| DmpFiltKpWIRBIndY_UIs_u2p14[0] | 8192 |
| – – | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 494 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 827 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 994 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1659 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 944 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 960 |
| FDD_AttenTblY_Uls_u8p8[0] | 78 |
| FDD_AttenTblY_Uls_u8p8[1] | 80 |
| FDD_BlendTblY_Uls_u8p8[0] | 65 |
| FDD_BlendTblY_Uls_u8p8[1] | 68 |
| FDD_BlendTblY_Uls_u8p8[2] | 70 |
| FDD_BlendTblY_Uls_u8p8[3] | 73 |
| FDD_BlendTblY_Uls_u8p8[4] | 75 |
| FDD_BlendTblY_Uls_u8p8[5] | 78 |
| | |
| _FDD_BlendTblY_Uls_u8p8[6] | 80 |
| _FDD_BlendTblY_Uls_u8p8[7] | 83 |
| FDD_BlendTblY_Uls_u8p8[8] | 86 |
| FDD_BlendTblY_Uls_u8p8[9] | 88 |
| _FDD_BlendTblY_Uls_u8p8[10] | 91 |
| | 93 |

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| Name | Input Value | | |
|--|--------------------------------|----------------------|-------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 218 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[5] | 230 | | |
| t_InrtCmp_ScaleFactorTbIY_UIs_u9p7[6] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 282 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[10] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 307 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 17 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[2] | 18 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 23 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 24 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 26 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 28 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 29 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 6554 | | |
| t RIAstWIRBIndTbIY_UIs u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 11469 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 13107 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 666 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 691 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 717 | | |
| t WIRBIndTbiX_MtrNm u8p8[3] | 742 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 768 | | |
| | 3.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -350.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value | | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -2.6 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 11.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 140.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 1.1 | A | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIi | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | | |
| $tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDepDmpnInrtCmp_Inst_Ap_FrqDep$ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | tgt_FrqDepDmpnInrtCmp_Per1_WIR | CmdAmpBInd_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| ProDocal Cain, Lllo, M, #22 | 126201 622 | 126201 625 + 0.0625 | |

| 2 | | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126301.633 | 126301.635 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 1181610.88 | 1181610.552 ± 9.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -33.2495117 | -33.24951101 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | 5.5 | 5.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 6789 | 6789 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.270833313 | -0.270833333 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.9738059 | 4.973805667 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | ✓ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Proceedian U.S. M. 52 | T4 04 0 40 (D4 04) | , a |
|--|---|-----------------------------------|
| Prescribedaning Minks M. 192 PerstStehtming Minks Mink | Test Step 3.12 (Repeat Count = 1) | <u> </u> |
| President Comp. Minkin M. 192 President Comp. Minkin Min | | |
| Pervisite/Annum J. Minn M. 192 | PreDecelGain_Uls_M_f32 | |
| PREZEDENTAGEMENT MINERS MESS MESS MESS MESS MESS MESS MESS M | Prev1PreAttnComp_MtrNm_M_f32 | |
| Proze28ch John St. 192 Proz. | Prev1SclDrvVel_RadpS_M_f32 | |
| Pictor Tarking Hobelg M. 12 | Prev2PreAttnComp_MtrNm_M_f32 | -8.3 |
| Rit_Inst_A_FingPepDemoninCrop | Prev2SclDrvVel_RadpS_M_f32 | -42.2 |
| TRANSFERS, M. B. 122 1.17 TRANSFERS, M. B. 122 1.17 | PrevTbarAng_HwDeg_M_f32 | 2.459 |
| Transverifies, M. et K. U. B. 12 L. Comstandin Kinegophiolog. [32] L. Lome, Mitherial. Spanis, [32] L. Indicom, Mitherial. Spanis, [32] L. Lome, Mitherial. Spanis, [32] L. Lome, And Comstandin Kinegophiolog. [32] L. Comstandin Kinegophiolog. [32] L. Comstandin Kinegophiolog. [33] L. Comstandin Kinegophiolog. [34] L. Com | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| | TbarVelFiltSv_M_str.SV_Uls_f32 | -4.2 |
| S. Com/Decidian/Psiew_Uisp\$_102 15 | TbarVelFiltSv_M_str.K_Uls_f32 | 0.02547 |
| | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 11.12 |
| k, DmpGainOffThreeth, KphpS, 152 k, DmpGainOffThreeth, KphpS, 152 k, DmpGainOffThreeth, KphpS, 152 k, IntrCmp, Mirthorita, Mir | k_CmnTbarStiff_NmpDeg_f32 | 1.5 |
| k_ DmpGainOfThreah_Kphps_J32 | k_DmpDecelGainFSlew_UlspS_f32 | 800.01 |
| L DmpCainOnTreeh, KphSc, 132 | k_DmpDecelGain_Uls_f32 | 9.5 |
| Intricmp_Mtrinertia_Kgmsq_G2 | k_DmpGainOffThresh_KphpS_f32 | 32.3 |
| LintCrop, MitVel, ScaleFactor, Uls_122 0.9 | k_DmpGainOnThresh_KphpS_f32 | 40.2 |
| 2 FDD_ADDRollingTbYM_MitNmpRadpS_um1p17(0) 1789 2130 2150 2267 2271 | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00009 |
| 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][1] 2130 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][2] 2471 13 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][3] 2811 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][6] 3152 13 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][6] 3834 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][6] 3834 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][7] 4175 14 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][8] 4515 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][9] 4856 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[0][9] 4856 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 4856 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 2455 13 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 2455 14 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 2455 15 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 3978 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 3978 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 3978 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 4148 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 4148 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 4199 12 FDD_ADDRollingTbiYM_MitrNmpRadpS_um1p17[1][9] 5419 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1192 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1192 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1192 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1194 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 120 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1280 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1280 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 126 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1280 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1280 12 FDD_FerqTbrMM_Hz_u12p4[0][1] 1296 12 FDD_FerqT | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.9 |
| 12 FDD _ ADDRollingTbYM_MrivmpRadpS_um1p17(0)[1) 2130 2471 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2271 2270 _ ADDRollingTbYM_MrivmpRadpS_um1p17(0)[3] 3152 | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][0] | 1789 |
| 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[2] 2471 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[3] 2811 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[5] 3493 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[6] 3493 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[6] 3493 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[7] 4175 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[8] 4515 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[8] 4515 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[8] 4516 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[0] 4856 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[0] 4856 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[0] 2032 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[1] 2032 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[3] 2578 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[4] 3302 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 3725 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 4148 2 FDD_FOR_FORMM_Hz_u12440[0] 1152 2 FDD_FOR_FORMM_Hz_u12440[0] 1168 2 FDD_FOR_FORMM_Hz_u12440[0] 1168 2 FDD_FOR_FORMM_Hz_u1240[0][0] 1266 2 FDD_FreqTbYM_Hz_u1240[0][0] 1268 2 FDD_FreqTbYM_Hz_u1240[0][0] 1268 2 FDD_FreqTbYM_Hz_u1240[0][0] 1268 2 FDD_FreqTbYM_Hz_u1240[0][0] 1268 2 FDD_FreqTbYM_Hz_u1240[0][0] 1269 2 FDD_FreqTbYM_Hz_u1240[0][1] 1262 2 FDD_FreqTbYM_Hz_u1240[0][1] 1262 2 FDD_FreqTbYM_Hz_u1240[0][1] 1262 2 FDD_FreqTbYM_Hz_u1240[0][1] 1262 2 FDD_FreqTbYM_Hz_u1240[0][1] 1269 2 FDD_FreqTbYM_Hz_u1240[0][1] 1269 | | 2130 |
| 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][3] 2811 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][4] 3152 13 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][6] 3834 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][6] 3834 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][7] 4175 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][8] 4516 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][9] 4556 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][0] 4668 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][0] 4668 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][1] 2032 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][2] 2456 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][2] 2456 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][3] 2878 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][6] 3725 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[1][6] 4148 12 FDD_FeqTbYM_MmmpRadpS_um1p17[1][6] 4148 12 FDD_FeqTbYM_MmmpRadpS_um1p17[1][6] 4148 12 FDD_FeqTbYM_MmmpRadpS_um1p17[1][6] 4169 12 FDD_FeqTbYM_MmmpRadpS_um1p17 | | |
| 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0)[4] 3152 3493 | | |
| 2_FDD_ADDRollingTbYM_MtrVmpRadpS_um1p17(0)[5] 3493 3 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3834 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4175 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 8 | | |
| 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0) 9 4856 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 0 1608 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 1 2032 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 2 2455 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 3 2878 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 3 3302 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 5 3725 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 6 4148 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 7 4572 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 8 4995 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 8 4995 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1) 8 4995 12_FDD_FreqTb\YM_Hz_u12p4[0] 0 1136 12_FDD_FreqTb\YM_Hz_u12p4[0] 1 1152 12_FDD_FreqTb\YM_Hz_u12p4[0] 2 1168 12_FDD_FreqTb\YM_Hz_u12p4[0] 3 1184 12_FDD_FreqTb\YM_Hz_u12p4[0] 3 1184 12_FDD_FreqTb\YM_Hz_u12p4[0] 6 1232 12_FDD_FreqTb\YM_Hz_u12p4[0] 6 1232 12_FDD_FreqTb\YM_Hz_u12p4[0] 6 1232 12_FDD_FreqTb\YM_Hz_u12p4[0] 8 1264 12_FDD_FreqTb\YM_Hz_u12p4[0] 9 1280 12_FDD_FreqTb\YM_Hz_u12p4[0] 9 1296 12_FDD_FreqTb\YM_Hz_u12p4[0] 10 1296 12_FDD_FreqTb\YM_Hz_u12p4[0] 11 1312 12_FDD_FreqTb | | |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17{1 0 } 2032 2052 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 2032 2455 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 2 2455 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 3 2878 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 4 3302 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 5 3725 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 6 4148 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 7 4572 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 8 4995 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1] 9 5419 12_FDD_FreqTblYM_Hz_u12p4[0] 0 1136 12_FDD_FreqTblYM_Hz_u12p4[0] 1 1152 12_FDD_FreqTblYM_Hz_u12p4[0] 2 1168 12_FDD_FreqTblYM_Hz_u12p4[0] 3 1184 12_FDD_FreqTblYM_Hz_u12p4[0] 6 1200 12_FDD_FreqTblYM_Hz_u12p4[0] 6 1232 12_FDD_FreqTblYM_Hz_u12p4[0] 6 1232 12_FDD_FreqTblYM_Hz_u12p4[0] 6 1248 12_FDD_FreqTblYM_Hz_u12p4[0] 8 1264 12_FDD_FreqTblYM_Hz_u12p4[0] 9 1280 12_FDD_FreqTblYM_Hz_u12p4[0] 9 1280 12_FDD_FreqTblYM_Hz_u12p4[0] 9 1280 12_FDD_FreqTblYM_Hz_u12p4[0] 1 1312 12_FDD_FreqTblYM_Hz_u12p4[0] 1 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_FORD_FORD_FORD_FORD_FORD_FORD_FORD_ | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_ADDRollingTbIYM_Hz_u12p4[0][0] 1136 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 1152 12_FDD_FreqTbIYM_Hz_u12p4[0][2] 1168 12_FDD_FreqTbIYM_Hz_u12p4[0][3] 1184 12_FDD_FreqTbIYM_Hz_u12p4[0][4] 1200 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 1236 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTbIYM_Hz_u12p4[0][7] 1248 12_FDD_FreqTbIYM_Hz_u12p4[0][8] 1260 12_FDD_FreqTbIYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTbIYM_Hz_u12p4[1][0] 176 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 192 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 192 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 192 12_FDD_FreqTbIYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 5 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 6 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 7 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 8 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 8 4995 12_FDD_FreqTblYM_Hz_u12p4[0] 0 1136 12_FDD_FreqTblYM_Hz_u12p4[0] 1 1152 12_FDD_FreqTblYM_Hz_u12p4[0] 2 1168 12_FDD_FreqTblYM_Hz_u12p4[0] 3 1184 12_FDD_FreqTblYM_Hz_u12p4[0] 3 1200 12_FDD_FreqTblYM_Hz_u12p4[0] 5 1216 12_FDD_FreqTblYM_Hz_u12p4[0] 6 1232 12_FDD_FreqTblYM_Hz_u12p4[0] 6 1232 12_FDD_FreqTblYM_Hz_u12p4[0] 8 1264 12_FDD_FreqTblYM_Hz_u12p4[0] 8 1264 12_FDD_FreqTblYM_Hz_u12p4[0] 9 1280 12_FDD_FreqTblYM_Hz_u12p4[0] 11 1312 12_FDD_FreqTblYM_Hz_u12p4[0] 11 1312 12_FDD_FreqTblYM_Hz_u12p4[0] 11 1312 12_FDD_FreqTblYM_Hz_u12p4[1] 1 192 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_FreqTblYM_Hz_u12p4[1] 1 192 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_FreqTblYM_Hz_u12p4[1] 1 192 12_FDD_FreqTblYM_Hz_u12p4[1] 1 192 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_FreqTblYM_Hz_u12p4[1] 1 192 12_FDD_FreqTblYM_Hz_u12p4[1] 2 208 12_FDD_Fr | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ForeqTblYM_Hz_u12p4[0][0] 1136 12_FDD_ForeqTblYM_Hz_u12p4[0][1] 1152 12_FDD_FreqTblYM_Hz_u12p4[0][2] 1168 12_FDD_FreqTblYM_Hz_u12p4[0][3] 1184 12_FDD_FreqTblYM_Hz_u12p4[0][4] 1200 12_FDD_FreqTblYM_Hz_u12p4[0][5] 1216 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][7] 1248 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][1] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_FreqTblYM_Hz_u12p4[0][0] 1136 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1152 12_FDD_FreqTblYM_Hz_u12p4[0][2] 1168 12_FDD_FreqTblYM_Hz_u12p4[0][3] 1184 12_FDD_FreqTblYM_Hz_u12p4[0][4] 1200 12_FDD_FreqTblYM_Hz_u12p4[0][5] 1216 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][7] 1248 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_FreqTblYM_Hz_u12p4[0][0] 1136 12_FDD_FreqTblYM_Hz_u12p4[0][2] 1168 12_FDD_FreqTblYM_Hz_u12p4[0][3] 1184 12_FDD_FreqTblYM_Hz_u12p4[0][4] 1200 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1216 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1248 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_FreqTb YM_Hz_u12p4[0][0] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 1200 12_FDD_FreqTblYM_Hz_u12p4[0][5] 1216 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][7] 1248 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 1216 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][7] 1248 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1232 12_FDD_FreqTblYM_Hz_u12p4[0][7] 1248 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][8] 1264 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][9] 1280 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][10] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 12_FDD_FreqTblYM_Hz_u12p4[1][0] 176 12_FDD_FreqTblYM_Hz_u12p4[1][1] 192 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 1312 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 176 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 192 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 192 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | | |
| | | |
| t2 FDD FrenThIYM Hz u12n4[1][3] 224 | | |
| re_i pp_indiprim_ine_unebultifol | t2_FDD_FreqTbIYM_Hz_u12p4[1][3] | 224 |

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| Namo | Innut Value | |
|--|-------------|--|
| Name 2 FDD FreqTbIYM Hz u12p4[1][4] | Input Value | |
| .z_FDD_FreqTbIYM_Hz_u12p4[1][4] :2_FDD_FreqTbIYM_Hz_u12p4[1][5] | 240 256 | |
| z_FDD_FreqTbiYM_Hz_u12p4[1][6] | 272 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 288 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 304 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 320 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 336 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 352 | |
| _CmnVehSpd_Kph_u9p7[0] | 3968 | |
| _CmnVehSpd_Kph_u9p7[1] | 4096 | |
| _CmnVehSpd_Kph_u9p7[2] | 4224 | |
| _CmnVehSpd_Kph_u9p7[3] | 4352 | |
| _CmnVehSpd_Kph_u9p7[4] | 4480 | |
| _CmnVehSpd_Kph_u9p7[5] | 4608 | |
| _CmnVehSpd_Kph_u9p7[6] | 4736 | |
| _CmnVehSpd_Kph_u9p7[7] | 4864 | |
| _CmnVehSpd_Kph_u9p7[8] | 4992 | |
| _CmnVehSpd_Kph_u9p7[9] | 5120 | |
| _CmnVehSpd_Kph_u9p7[10] | 5248 | |
| _CmnVehSpd_Kph_u9p7[11] | 5376 | |
| | | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 20960 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 20992 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 21024 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 21056 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 21088 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 21120 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 384 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 392 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 400 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 408 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 416 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 424 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 342 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1024 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1008 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1040 | |
| FDD_AttenTblY_Uls_u8p8[0] | 106 | |
| FDD_AttenTblY_Uls_u8p8[1] | 109 | |
| FDD_BlendTblY_Uls_u8p8[0] | 93 | |
| FDD_BlendTblY_Uls_u8p8[1] | 96 | |
| FDD_BlendTblY_Uls_u8p8[2] | 99 | |
| FDD_BlendTblY_Uls_u8p8[3] | 101 | |
| FDD_BlendTblY_Uls_u8p8[4] | 104 | |
| FDD_BlendTblY_Uls_u8p8[5] | 106 | |
| _FDD_BlendTblY_Uls_u8p8[6] | 109 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 111 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 114 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 116 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 119 | |
| o.o_aopo[10] | 1.10 | |

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| Name | Input Value | | |
|---|-----------------------------------|-------------------------|------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 294 | | |
| _InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 307 | | |
| _InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 346 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 32 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[2] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 40 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 42 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 44 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 45 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 8192 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 11469 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 14746 | | |
| t WirbindTbiX MtrNm u8p8[0] | 922 | | |
| | 947 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 973 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 998 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1024 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -3.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 350.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 3.7 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 22.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 150.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 2.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssist | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotor\ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDm | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLon | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpe | ed_I tgt_FrqDepDmpnInrtCmp_Per1_\ | /ehicleSpeed_Kph_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAi | mpBl tgt_FrqDepDmpnInrtCmp_Per1_\ | VIRCmdAmpBlnd_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| PreDecelGain Uls M f32 | 126403 406 | 126403 41 + 0 0625 | |

| 3 | 3- 1 1 1 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- | | |
|--|---|--------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126403.406 | 126403.41 ± 0.0625 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | -343428.688 | -343428.7798 ± 0.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 314.997375 | 314.9973886 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -5.5 | -5.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -37.0299988 | -37.03 ± 0.00390625 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | 2.4666667 | 2.466666667 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.99539185 | -3.995391 ± 0.00390625 | • |
| tot ErgDenDmonlortCmp Per1 ErgDenDmonlortCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |

FrqDepDmpnInrtCmp_Per1

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T
Actual Function Count Expected Function Count Result

Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached

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|--|-------------|---------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 560 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 576 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 592 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 608 | |
| 2 FDD FregTblYM Hz u12p4[1][8] | 624 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 640 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 656 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 672 | |
| | 128 | |
| _CmnVehSpd_Kph_u9p7[1] | 256 | |
| _CmnVehSpd_Kph_u9p7[2] | 384 | |
| _CmnVehSpd_Kph_u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| | 768 | |
| _CmnVehSpd_Kph_u9p7[5] | | |
| _CmnVehSpd_Kph_u9p7[6] | 896 | |
| _CmnVehSpd_Kph_u9p7[7] | 1024 | |
| _CmnVehSpd_Kph_u9p7[8] | 1152 | |
| _CmnVehSpd_Kph_u9p7[9] | 1280 | |
| _CmnVehSpd_Kph_u9p7[10] | 1408 | |
| _CmnVehSpd_Kph_u9p7[11] | 1536 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 8602 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 9011 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 9421 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 9830 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 10240 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 10650 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 11059 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 11469 | |
| DmpADDCoefX_MtrNm_u4p12[8] | 11878 | |
| DmpADDCoefX_MtrNm_u4p12[9] | 12288 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 25216 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 25248 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 25280 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 25312 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 25344 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 25376 | |
| _DmpDecelGainGlewY_UlspS_u13p3[0] | 448 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 456 | |
| DmpDecelGainSlewY UlspS u13p3[2] | 464 | |
| | 472 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 480 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 523 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1088 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1120 | |
| FDD_AttenTblY_Uls_u8p8[0] | 129 | |
| FDD_AttenTblY_Uls_u8p8[1] | 131 | |
| | 116 | |
| FDD_BlendTblY_Uls_u8p8[0] EDD_BlendTblY_Uls_u8p8[1] | | |
| FDD_BlendTblY_Uls_u8p8[1] | 118 | |
| _FDD_BlendTblY_Uls_u8p8[2] | 121 | |
| _FDD_BlendTbIY_Uls_u8p8[3] | 123 | |
| _FDD_BlendTbIY_Uls_u8p8[4] | 126 | |
| _FDD_BlendTbIY_Uls_u8p8[5] | 129 | |
| FDD_BlendTblY_Uls_u8p8[6] | 131 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 134 | |
| FDD_BlendTblY_Uls_u8p8[8] | 136 | |
| FDD_BlendTblY_Uls_u8p8[9] | 139 | |
| | 141 | |
| | 1 | |

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| Name | Input Value | | |
|--|--------------|----------------------|------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 218 | | |
| : InrtCmp ScaleFactorTblY Uls u9p7[1] | 230 | | |
| InrtCmp ScaleFactorTblY Uls u9p7[2] | 243 | | |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 256 | | |
| InrtCmp ScaleFactorTblY Uls u9p7[4] | 269 | | |
| InrtCmp ScaleFactorTblY Uls u9p7[5] | 282 | | |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 294 | | |
| _InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 307 | | |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 320 | | |
| inrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 333 | | |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 346 | | |
| InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 358 | | |
| InrtCmp TBarVel ScaleFactorTblY UIs u9p7[0] | 46 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[1] | 47 | | |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 50 | | |
| _InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 51 | | |
| _InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 52 | | |
| _inrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 55 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 56 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 58 | | |
| _inrtCmp_TBarVel_ScaleFactorTbiY_Uis_u9p7[10] | 59 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 60 | | |
| RIAstWIRBIndTbIY UIs u2p14[0] | 1638 | | |
| _RIAstWIRBIndTbIY_UIs_u2p14[1] | 3277 | | |
| _RIAstWIRBIndTbIY_Uls_u2p14[2] | 4915 | | |
| _RIAstWIRBIndTbIY_UIs_u2p14[3] | 6554 | | |
| _RIAstWIRBIndTbIY_UIs_u2p14[4] | 8192 | | |
| _WIRBIndTbIX_MtrNm_u8p8[0] | 1178 | | |
| WIRBIndTblX MtrNm u8p8[1] | 1203 | | |
| _WIRBIndTbIX_MtrNm_u8p8[2] | 1229 | | |
| _WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| _WIRBIndTbIX_MtrNm_u8p8[4] | 1280 | | |
| gt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -8.8 | | |
| gt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -400.2 | | |
| gt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| gt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -3.8 | | |
| gt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 33.05 | | |
| gt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 160.01 | | |
| gt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 3.3 | | |
| gt_rigbeponipililitenip_retr_vvirenibAnipblilid_vvitivili_idz.value gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistC | | Cmd MtrNm f32 | |
| gt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVe | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmp | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpn | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_F | | | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonA | | _ | |
| gt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpee | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAm | | | |
| устке_пьс_хр_п прерыприштетр. прерыприштетр_г е г_wittemaxiii Name | Actual Value | Expected Value | Post |
| | | • | Resu |
| PreDecelGain_Uls_M_f32 | 126505.188 | 126505.1849 ± 0.0625 | |

| | 7 32 4 7 7 7 2 | | |
|--|----------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126505.188 | 126505.1849 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 1010980 | 1010980.109 ± 9.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -319.417603 | -319.4175991 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.5999999 | 6.6 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 26.0200005 | 26.02 ± 0.00390625 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | -1.51999998 | -1.52 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.10051537 | 4.100515 ± 0.00390625 | ✓ |
| tot FrgDenDmpnInrtCmp Per1 FrgDenDmpnInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.14 (Repeat Count = 1) Name Input Value | |
|--|--|
| Name Input Value | |
| | |
| PreDecelGain_Uls_M_f32 126608.96 | |
| Prev1PreAttnComp_MtrNm_M_f32 -6.6 | |
| Prev1SclDrvVel_RadpS_M_f32 -33.05 | |
| Prev2PreAttnComp_MtrNm_M_f32 -7.5 | |
| Prev2SclDrvVel_RadpS_M_f32 -922.3 | |
| PrevTbarAng_HwDeg_M_f32 1.16 | |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | |
| TbarVelFiltSv_M_str.SV_Uls_f32 -3.5 | |
| TbarVelFiltSv_M_str.K_Uls_f32 0.03692 | |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 33.15 | |
| k_CmnTbarStiff_NmpDeg_f32 3.5 | |
| k_DmpDecelGainFSlew_UlspS_f32 1000.05 | |
| k_DmpDecelGain_Uls_f32 1.5 | |
| k_DmpGainOffThresh_KphpS_f32 48.2 | |
| k_DmpGainOnThresh_KphpS_f32 47.6 | |
| k_InrtCmp_MtrInertia_KgmSq_f32 0.00011 | |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 0.99 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] 1789 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] 2130 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 2471 | |
| 12 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][3] 2811 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3152 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3493 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3834 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4175 | |
| 12 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][8] 4515 | |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][9] 4856 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 161 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 328 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 494 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 661 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 827 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 994 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1160 | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 1326 | |
| | |
| | |
| | |
| ' ' ' | |
| | |
| | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] 544 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] 560 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] 576 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 592 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] 608 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] 656 | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 672 | |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][0] 64 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 80 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 96 | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 112 | |

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|--|-------------------|------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 176 | |
| 2 FDD FregTblYM Hz u12p4[1][8] | 192 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][9] | 208 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][10] | 224 | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][11] | 240 | |
| t_CmnVehSpd_Kph_u9p7[0] | 2560 | |
| | 3840 | |
| t_CmnVehSpd_Kph_u9p7[2] | 5120 | |
| t_CmnVehSpd_Kph_u9p7[3] | 6400 | |
| :_CmnVehSpd_Kph_u9p7[4] | 7680 | |
| | 8960 | |
| t_CmnVehSpd_Kph_u9p7[5] | | |
| t_CmnVehSpd_Kph_u9p7[6] | 10240 | |
| t_CmnVehSpd_Kph_u9p7[7] | 11520 | |
| :_CmnVehSpd_Kph_u9p7[8] | 12800 | |
| _CmnVehSpd_Kph_u9p7[9] | 14080 | |
| CmnVehSpd_Kph_u9p7[10] | 15360 | |
| CmnVehSpd_Kph_u9p7[11] | 16640 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| :_DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| t_DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| t_DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3264 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3296 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3328 | |
| | 3360 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3392 | |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3424 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 680 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 688 | |
| t DmpDecelGainSlewY UlspS u13p3[2] | 696 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 704 | |
| t DmpDecelGainSlewY UlspS u13p3[4] | 712 | |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 720 | |
| t DmpFiltKpWIRBIndY Uls u2p14[0] | 3277 | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | |
| | | |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 704 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 814 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 924 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1254 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364 | |
| :_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475 | |
| :_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 1585 | |
| :_FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1695 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1152 | |
| :_FDD_AttenTblX_MtrRadpS_u12p4[1] | 1200 | |
| _FDD_AttenTblY_Uls_u8p8[0] | 157 | |
| FDD_AttenTblY_Uls_u8p8[1] | 161 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 144 | |
| FDD_BlendTblY_Uls_u8p8[1] | 146 | |
| r_FDD_BlendTblY_Uls_u8p8[2] | 149 | |
| EFDD_BlendTblY_Uls_u8p8[3] | 152 | |
| :_FDD_BlendTblY_Uls_u8p8[4] | 154 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 157 | |
| FDD_BlendTblY_Uls_u8p8[6] | 159 | |
| DD_Diona i Di i _olo_dopo[o] | 162 | |
| FDD RlandThIV Lile (185917) | 102 | |
| | 164 | |
| t_FDD_BlendTblY_Uls_u8p8[7] t_FDD_BlendTblY_Uls_u8p8[8] | 164 | |
| | 164 167 169 | |

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| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 61 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 65 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 69 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 70 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 72 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 73 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 74 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 76 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830 | | |
| t WIRBIndTblX MtrNm u8p8[0] | 1434 | | |
| t WIRBIndTblX MtrNm u8p8[1] | 1459 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1485 | | |
| t WIRBIndTbiX MtrNm u8p8[3] | 1510 | | |
| t WIRBIndTbiX MtrNm u8p8[4] | 1536 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 8.8 | | |
| tgt FrqDepDmpnInrtCmp Per1 CRFMotorVel MtrRadpS f32.value | 300.6 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 4.1 | | |
| tgt FrgDepDmpnInrtCmp Per1 VehicleLonAccel KphpS f32.value | -11.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 170.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 4.4 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistC | | 1 BaseAssistCmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVe | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmp | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_F | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonA | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpee | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAm | | 1_WIRCmdAmpBInd_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Result |
| PreDecelGain Uls M f32 | 126606 961 | 126606 9599 + 0 0625 | Result |
| | | | |

| 3 | 3- 1 1 1 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- 1- | | |
|--|---|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126606.961 | 126606.9599 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 1334381.63 | 1334381.785 ± 9.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 296.508514 | 296.5085113 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.5999999 | -6.6 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | -33.0499992 | -33.05 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1.17142856 | 1.171428571 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -3.15980816 | -3.159808571 ± 0.00390625 | • |
| tot FroDenDmnnInrtCmn Per1 FroDenDmnnInrtCmn MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | V |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Name | Input Value |
|---|-----------------------------------|
| | |
| PreDecelGain_Uls_M_f32 | 126710.935 |
| Prev1PreAttnComp_MtrNm_M_f32 | 7.7 |
| Prev1ScIDrvVel_RadpS_M_f32 | 18.03 |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.5 |
| Prev2ScIDrvVel_RadpS_M_f32 | 28.5 |
| PrevTbarAng_HwDeg_M_f32 | -0.92 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| barVelFiltSv_M_str.SV_Uls_f32 | 5.2 |
| FbarVelFiltSv_M_str.K_Uls_f32 | 0.01258 |
| c_CmnSysKinRatio_MtrDegpHwDeg_f32 | 44.51 |
| C_CmnTbarStiff_NmpDeg_f32 | 4.5 |
| C_DmpDecelGainFSlew_UlspS_f32 | 1100.02 |
| _DmpDecelGain_Uls_f32 | 1.9 |
| C_DmpGainOffThresh_KphpS_f32 | 4.2 |
| _DmpGainOnThresh_KphpS_f32 | 30.2 |
| _InrtCmp_MtrInertia_KgmSq_f32 | 0.00012 |
| CInrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 494 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 661 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 827 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 994 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1160 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1326 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1493 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1659 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 342 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 683 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1024 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1364 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1705 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 2046 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 2387 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 2728 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 3068 |
| 2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[1][9] | 3409 |
| 2 FDD FreqTblYM Hz u12p4[0][0] | 1392 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1408 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1424 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1440 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1456 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1472 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1488 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1504 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1520 |
| 2 FDD FreqTblYM Hz u12p4[0][9] | 1536 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1552 |
| 2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1568 |
| 2_FDD_reqTblYM_Hz_u12p4[0][11] | 80 |
| 2_FDD_FIEQT6ITM_HZ_012p4[1][0] 2_FDD_FreqTbIYM_Hz_u12p4[1][1] | 96 |
| 2_FDD_FreqTbIYM_Hz_u12p4[1][1] 2_FDD_FreqTbIYM_Hz_u12p4[1][2] | 112 |
| z_FDD_FreqTbIYM_Hz_u12p4[1][2] :2_FDD_FreqTbIYM_Hz_u12p4[1][3] | 128 |

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|--|-------------|-----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 224 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 240 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 256 | |
| _CmnVehSpd_Kph_u9p7[0] | 6784 | |
| _CmnVehSpd_Kph_u9p7[1] | 6912 | |
| _CmnVehSpd_Kph_u9p7[2] | 7040 | |
| CmnVehSpd_Kph_u9p7[3] | 7168 | |
| _CmnVehSpd_Kph_u9p7[4] | 7296 | |
| _CmnVehSpd_Kph_u9p7[5] | 7424 | |
| _CmnVehSpd_Kph_u9p7[6] | 7552 | |
| _CmnVehSpd_Kph_u9p7[7] | 7680 | |
| _CmnVehSpd_Kph_u9p7[8] | 7808 | |
| | 7936 | |
| _CmnVehSpd_Kph_u9p7[9] | 8064 | |
| _CmnVehSpd_Kph_u9p7[10] | | |
| CmnVehSpd_Kph_u9p7[11] | 8192 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 8602 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 9011 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 9421 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 9830 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 10240 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 10650 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 11059 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 11469 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 11878 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 12288 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3776 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3808 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3840 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3872 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 3904 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 3936 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1536 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1544 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1552 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1560 | |
| | 1568 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1576 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 885 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 986 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1087 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1490 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1591 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1793 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1232 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1280 | |
| FDD_AttenTblY_Uls_u8p8[0] | 183 | |
| FDD_AttenTblY_Uls_u8p8[1] | 185 | |
| FDD_BlendTbIY_Uls_u8p8[0] | 172 | |
| FDD_BlendTblY_Uls_u8p8[1] | 174 | |
| FDD_BlendTblY_Uls_u8p8[2] | 176 | |
| | | |
| FDD_BlendTblY_Uls_u8p8[3] | 178 | |
| FDD_BlendTblY_Uls_u8p8[4] | 180 | |
| FDD_BlendTblY_Uls_u8p8[5] | 183 | |
| _FDD_BlendTbIY_Uls_u8p8[6] | 185 | |
| FDD_BlendTblY_Uls_u8p8[7] | 187 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 189 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 191 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 193 | |
| | 195 | |

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| 7.45.5p.5p | | | |
|--|---------------|---------------------|-------|
| Name | Input Value | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 115 | | |
| t_InrtCmp_ScaleFactorTbIY_Uls_u9p7[8] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 166 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 77 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 78 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 79 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 81 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 82 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 83 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 84 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 86 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 87 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 88 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 90 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 91 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 4915 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 8192 | | |
| t_RiAstWIRBindTbiY_Uis_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 11469 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1690 | | |
| t WIRBIndTblX MtrNm u8p8[1] | 1715 | | |
| t WIRBIndTblX MtrNm u8p8[2] | 1741 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1766 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1792 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -300.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -4.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -22.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 180.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 6.6 | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCm | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | 1 - 1 - 1 - 1 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 FrqDepDmpnIr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_vehicleLonAcc | | _ | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Fe11_ve1ideSpeed_ tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Pe11_WIRCmdAmpE | | | |
| | | <u> </u> | Desir |
| Name | Actual Value | Expected Value | Resul |
| PreDecelGain_Uls_M_f32 | 126710.938 | 126710.935 ± 0.0625 | ' |

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------------|----------|
| PreDecelGain_Uls_M_f32 | 126710.938 | 126710.935 ± 0.0625 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | 26591.9277 | 26591.92825 ± 0.09 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -177.270554 | -177.2705444 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.69999981 | 7.7 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 18.0300007 | 18.03 ± 0.00390625 | • |
| PrevTbarAng_HwDeg_M_f32 | -0.933333278 | -0.933333333 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 5.05071735 | 5.050717333 ± 0.00390625 | • |
| tot FrgDenDmpnInrtCmp Per1 FrgDenDmpnInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Input Value |
|---|
| PreDecelGain_Uls_M_f32 126812.91 Prev1PreAttnComp_MtrNm_M_f32 -7.7 Prev1SclDrvVel_RadpS_M_f32 -28.5 Prev2PreAttnComp_MtrNm_M_f32 -6.5 Prev2SclDrvVel_RadpS_M_f32 -297.3 PrevTbarAng_HwDeg_M_f32 1.145 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFittSv_M_str.SV_Uls_f32 -4.2 TbarVelFittSv_M_str.K_Uls_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmrTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGain_FSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| Prev1PreAttnComp_MtrNm_M_f32 -7.7 Prev1ScIDrvVel_RadpS_M_f32 -28.5 Prev2PreAttnComp_MtrNm_M_f32 -6.5 Prev2ScIDrvVel_RadpS_M_f32 -297.3 PrevTbarAng_HwDeg_M_f32 1.145 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFittSv_M_str.SV_UIs_f32 -4.2 TbarVelFittSv_M_str.K_UIs_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGain_FSlew_UIspS_f32 1200.05 k_DmpDecelGain_UIs_f32 2.5 |
| Prev1ScIDrvVel_RadpS_M_f32 -28.5 Prev2PreAttnComp_MtrNm_M_f32 -6.5 Prev2ScIDrvVel_RadpS_M_f32 -297.3 PrevTbarAng_HwDeg_M_f32 1.145 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFittSv_M_str.SV_UIs_f32 -4.2 TbarVelFittSv_M_str.K_UIs_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGain_FSlew_UIspS_f32 1200.05 k_DmpDecelGain_UIs_f32 2.5 |
| Prev2PreAttnComp_MtrNm_M_f32 -6.5 Prev2SclDrvVel_RadpS_M_f32 -297.3 PrevTbarAng_HwDeg_M_f32 1.145 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFittSv_M_str.SV_UIs_f32 -4.2 TbarVelFittSv_M_str.K_UIs_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGain_FSlew_UIspS_f32 1200.05 k_DmpDecelGain_UIs_f32 2.5 |
| Prev2ScIDrvVel_RadpS_M_f32 -297.3 PrevTbarAng_HwDeg_M_f32 1.145 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFitlSv_M_str.SV_UIs_f32 -4.2 TbarVelFitlSv_M_str.K_UIs_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGain_FSlew_UIspS_f32 1200.05 k_DmpDecelGain_UIs_f32 2.5 |
| PrevTbarAng_HwDeg_M_f32 1.145 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFitlSv_M_str.SV_Uls_f32 -4.2 TbarVelFitlSv_M_str.K_Uls_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGainFSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFitlSv_M_str.SV_Uls_f32 -4.2 TbarVelFitlSv_M_str.K_Uls_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGainFSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| TbarVelFiltSv_M_str.SV_Uls_f32 -4.2 TbarVelFiltSv_M_str.K_Uls_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGainFSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 0.03257 k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGainFSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 55.12 k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGainFSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| k_CmnTbarStiff_NmpDeg_f32 5.5 k_DmpDecelGainFSlew_UlspS_f32 1200.05 k_DmpDecelGain_Uls_f32 2.5 |
| k_DmpDecelGainFSlew_UlspS_f32 |
| k_DmpDecelGain_Uls_f32 2.5 |
| |
| k DmnGainOffThresh KnhnS f32 |
| |
| k_DmpGainOnThresh_KphpS_f32 35.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 0.00013 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 0.5 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] 342 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 1024 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][3] 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 2046 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 2387 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][7] 2728 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][8] 3068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 3409 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 494 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 661 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 827 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 1326 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 1493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 1659 |
| t2 FDD FreqTblYM Hz u12p4[0][0] 496 |
| |
| |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] 528 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] 544 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] 560 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] 576 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 592 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] 608 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] 656 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 672 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 112 |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][2] 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 144 |

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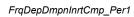
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|--|-------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 160 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 176 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 192 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 208 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 224 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 240 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 256 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 272 |
| _CmnVehSpd_Kph_u9p7[0] | 128 |
| _CmnVehSpd_Kph_u9p7[1] | 256 |
| : CmnVehSpd Kph u9p7[2] | 384 |
| : CmnVehSpd Kph u9p7[3] | 512 |
| _CmnVehSpd_Kph_u9p7[4] | 640 |
| | 768 |
| _CmnVehSpd_Kph_u9p7[5] | |
| :_CmnVehSpd_Kph_u9p7[6] | 896 |
| _CmnVehSpd_Kph_u9p7[7] | 1024 |
| _CmnVehSpd_Kph_u9p7[8] | 1152 |
| _CmnVehSpd_Kph_u9p7[9] | 1280 |
| _CmnVehSpd_Kph_u9p7[10] | 1408 |
| _CmnVehSpd_Kph_u9p7[11] | 1536 |
| _DmpADDCoefX_MtrNm_u4p12[0] | 12698 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 13107 |
| _DmpADDCoefX_MtrNm_u4p12[2] | 13517 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 13926 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 14336 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 14746 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 15155 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 15565 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 15974 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 16384 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5280 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5312 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5344 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5376 |
| | 5408 |
| | 5440 |
| | 1480 |
| :_DmpDecelGainSlewY_UlspS_u13p3[1] | 1488 |
| :_DmpDecelGainSlewY_UlspS_u13p3[2] | 1496 |
| DmpDecelGainSlewY UlspS u13p3[3] | 1504 |
| | 1512 |
| :_DmpDecelGainSlewY_UlspS_u13p3[4] | |
| :_DmpDecelGainSlewY_UlspS_u13p3[5] | 1520 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1066 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1212 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 1359 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1506 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[4] | 1653 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] | 1800 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1946 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2093 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 2240 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 2387 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1296 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 1360 |
| _FDD_AttenTblY_Uls_u8p8[0] | 230 |
| _FDD_AttenTblY_Uls_u8p8[1] | 232 |
| _FDD_BlendTblY_Uls_u8p8[0] | 218 |
| _FDD_BlendTblY_Uls_u8p8[1] | 220 |
| _FDD_BlendTblY_Uls_u8p8[2] | 223 |
| | 225 |
| _FDD_BlendTblY_Uls_u8p8[3] | |
| _FDD_BlendTblY_Uls_u8p8[4] | 227 |
| _FDD_BlendTblY_Uls_u8p8[5] | 230 |
| _FDD_BlendTblY_Uls_u8p8[6] | 232 |
| _FDD_BlendTblY_Uls_u8p8[7] | 234 |
| _FDD_BlendTblY_Uls_u8p8[8] | 237 |
| _FDD_BlendTblY_Uls_u8p8[9] | 239 |
| _FDD_BlendTblY_Uls_u8p8[10] | 241 |
| | |

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| 7.1q5-q5-mp.m.r.cmp_, -c. 1 | | | |
|---|--------------|--------------------|-------|
| Name | Input Value | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 179 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 92 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 97 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 99 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 100 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 101 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 102 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 104 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 105 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 106 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 11469 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 13107 | | |
| t_WIRBIndTbiX_MtrNm_u8p8[0] | 1894 | | |
| t WIRBIndTblX MtrNm u8p8[1] | 1920 | | |
| t WIRBIndTblX MtrNm u8p8[2] | 1946 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1997 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -5.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 200.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 6.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -33.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 190.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 7.7 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | 1 - 1 - 1 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 FrqDepDmpnIr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwYorque_Hw tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_ | | - · - | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | | | |
| Name | Actual Value | Expected Value | Resul |
| PreDecelGain_Uls_M_f32 | 126812.906 | 126812.91 ± 0.0625 | ' |

| 3 | 3-11-1-1 | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 126812.906 | 126812.91 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 267220.719 | 267220.7121 ± 0.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 96.8688278 | 96.86883293 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -7.69999981 | -7.7 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -28.5 | -28.5 ± 0.00390625 | • |
| PrevTbarAng_HwDeg_M_f32 | 1.14545453 | 1.145454545 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -4.05580378 | -4.055803727 ± 0.00390625 | • |
| tot FroDenDmonInrtCmp Per1 FroDenDmonInrtCmp MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | - |





| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.17 (Repeat Count = 1) | |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 126914.885 |
| Prev1PreAttnComp_MtrNm_M_f32 | 1.5 |
| Prev1ScIDrvVel RadpS M f32 | 24.6 |
| Prev2PreAttnComp MtrNm M f32 | 6.5 |
| Prev2ScIDrvVel_RadpS_M_f32 | 382.2 |
| PrevTbarAng_HwDeg_M_f32 | -0.979 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.3 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.096321 |
| k CmnSysKinRatio MtrDegpHwDeg f32 | 66.13 |
| k CmnTbarStiff NmpDeg f32 | 6.5 |
| k DmpDecelGainFSlew UlspS f32 | 1300.06 |
| k_DmpDecelGain_Uls_f32 | 5.6 |
| k_DmpGainOffThresh_KphpS_f32 | 12.2 |
| k_DmpGainOnThresh_KphpS_f32 | 40.1 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00014 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[0][0] | 342 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][1] | 683 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1024 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1705 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 2046 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][6] | 2387 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS_um1p17[0][7] | 2728 |
| | 3068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 3409 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 161 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 328 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 494 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 661 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 827 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 994 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1160 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1326 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1493 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1659 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 1136 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 1152 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 1168 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 1184 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 1200 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 1216 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 1232 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 1248 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 1264 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 1280 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 1296 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 1312 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 656 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 672 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 688 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 704 |

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|--|-------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 720 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 |
| _CmnVehSpd_Kph_u9p7[0] | 2560 |
| _CmnVehSpd_Kph_u9p7[1] | 3840 |
| CmnVehSpd Kph u9p7[2] | 5120 |
| CmnVehSpd Kph u9p7[3] | 6400 |
| _CmnVehSpd_Kph_u9p7[4] | 7680 |
| | 8960 |
| _CmnVehSpd_Kph_u9p7[5] | |
| _CmnVehSpd_Kph_u9p7[6] | 10240 |
| _CmnVehSpd_Kph_u9p7[7] | 11520 |
| _CmnVehSpd_Kph_u9p7[8] | 12800 |
| _CmnVehSpd_Kph_u9p7[9] | 14080 |
| _CmnVehSpd_Kph_u9p7[10] | 15360 |
| CmnVehSpd_Kph_u9p7[11] | 16640 |
| _DmpADDCoefX_MtrNm_u4p12[0] | 16794 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 17203 |
| _DmpADDCoefX_MtrNm_u4p12[2] | 17613 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 18022 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 18432 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 18842 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 19251 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 19661 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 20070 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 20480 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 11680 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 11712 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 11744 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 11776 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 11808 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 11840 |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1608 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1616 |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1624 |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1632 |
| | 1640 |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1648 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1246 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 1638 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2030 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2422 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2814 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3206 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3598 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 3990 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4382 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4774 |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1344 |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1440 |
| FDD_AttenTblY_Uls_u8p8[0] | 71 |
| FDD_AttenTblY_Uls_u8p8[1] | 74 |
| FDD_BlendTblY_Uls_u8p8[0] | 3 |
| FDD_BlendTblY_Uls_u8p8[1] | 5 |
| FDD_BlendTblY_Uls_u8p8[2] | 8 |
| | 10 |
| _FDD_BlendTblY_Uls_u8p8[3] | |
| _FDD_BlendTblY_Uls_u8p8[4] | 13 |
| _FDD_BlendTblY_Uls_u8p8[5] | 15 |
| _FDD_BlendTblY_Uls_u8p8[6] | 18 |
| _FDD_BlendTblY_Uls_u8p8[7] | 20 |
| _FDD_BlendTblY_Uls_u8p8[8] | 23 |
| _FDD_BlendTblY_Uls_u8p8[9] | 26 |
| _FDD_BlendTblY_Uls_u8p8[10] | 28 |
| | |

2014-09-19, 16:35:39+0530



| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 192 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 13 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 14 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 15 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 8192 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 9830 | | |
| t RIAstWIRBIndTbIY Uls u2p14[2] | 11469 | | |
| t RIAstWIRBIndTbIY Uls u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 14746 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 922 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 947 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 973 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 998 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1024 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 5.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -200.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -6.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -44.06 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 210.03 | | |
| tgt FrgDepDmpnInrtCmp Per1 WIRCmdAmpBInd MtrNm f32.value | 1.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCmo | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB | | - · - | |
| устко-постр. Просрония полира просрония полира с гатум колидания Name | Actual Value | | Resul |
| ITAING | Actual Value | Expected Value | Result |

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------------|----------|
| PreDecelGain_Uls_M_f32 | 126912.281 | 126912.2849 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -756922.563 | -756922.4402 ± 0.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -79.67099 | -79.67099743 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.5 | 1.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 24.6000004 | 24.6 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.984615386 | -0.984615385 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.61537886 | 3.615379969 ± 0.00390625 | ~ |
| tot FraDenDmpnInrtCmp Per1 FraDenDmpnInrtCmp MtrNm f32 value | -8 80000019 | -8.8 + 0.00048828125 | ✓ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Name Input Value Proberolision Us, M. N. 32 15 (Proberolision Company Minton, M. 122 15 (Proberolision Company Minton, M. 122 15 (Proberolision Company Minton, M. 122 16 (Proberolision Company Minton, M. 122 45 (Proberolision Company Minton, M. 122 45 (Proberolision Company Minton, M. 122 25 (Proberolision Company Minton, M. 122 0.989 Probesticion Company Minton, M. 122 0.97852 N. 122 0.97852 N. 122 N. 122 0.97852 N. 122 | T+ 04 0.40 (D+ 0+ 4) | |
|--|---|-----------------------------------|
| Piebber Barton Markin M. 192 1.5 | Test Step 3.18 (Repeat Count = 1) | * |
| Piew IPW-SID-MVR Badds M. 192 | | |
| Piers Stankovie, Radjo M, 152 Piers Zhenkovie, Radjo M, 152 Re, Inst. Ap. Fologopropint Cmp Ust. Re, Inst. | | |
| PiewZPeAthComp Mith/m M, M, 32 | · | |
| PrevZBEROVEI RadpS M 522 0.989 Rie_Inst.Ap_FropeDimprintCmp | | |
| PevDatAng, 1-Modg, M. (32) Rel. inst. Ap. FrqDepDmpnintCmp tg. Rie. Inst. Ap. FrqDepDmpnintCmp ts. Rie. Inst. Ap. Rie. In | Prev2PreAttnComp_MtrNm_M_f32 | |
| Re Line J. A. FreQepCimpenintCmp | Prev2ScIDrvVel_RadpS_M_f32 | -25.6 |
| Thank/ellisty M, str. WJ, Us. 52 15 15 15 15 15 15 15 | PrevTbarAng_HwDeg_M_f32 | 0.989 |
| Than/VerFistor, M. Str. K. Uis., 132 0.047852 77.14 1.00 | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| K. Cmm2bysKinRatio_Mincept+Woeg_G2 7.5 K. Cmm2bes(Bill NimpDeq_G32 7.5 K. DmpDeceGain/Fisew_UispS_G2 1400.05 K. DmpDeceGain/Uis_G2 2.1 K. DmpCanoffThreesi, KphpS_G32 16.5 K. DmpCanoffThreesi, KphpS_G32 45.2 K. Infromp_Mirveisa, KpmSS_G32 0.00015 K. Infromp_Mirveisa, KpmSS_G32 0.3 K. Infromp_Mirveisa, KpmSS_G32 0.3 K. Infromp_Mirveisa, KpmSS_G32 0.3 K. Infromp_Mirveisa, KpmSS_G32 0.3 Z. FDD_ADDRailing TbrMA_MinhmpRadeS_um1p170[0] 523 2. FDD_ADDRailing TbrMA_MinhmpRadeS_um1p170[0] 1038 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 2583 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 2583 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 2583 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 364 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 364 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 364 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 342 2. FDD_ADDRailing TbrMM_MinhmpRadeS_um1p170[0] 342 2. FDD_ADD | TbarVelFiltSv_M_str.SV_Uls_f32 | 1.5 |
| K, Cmmbesidif NimpDeg, 132 1,50 K, Dmpbesidsain Fisse, UlspS, 152 1,400.05 K, Dmpbesidsain, Us. 152 2,1 K, Dmpbesidsain, Us. 153 16.5 K, Dmpbesidsain, Us. 153 45.2 K, Innton, Mitriesti, KphpS, 132 45.2 K, Innton, Mitriesti, KphpS, 132 0.00015 K, Innton, D, Mitriesti, KphpS, 132 0.3 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[0] 523 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[1] 1038 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[1] 1553 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[1] 2583 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[1] 2583 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[1] 4129 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p170[1] 4129 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p171[1] 844 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p171[1] 842 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p171[1] 842 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p171[1] 83 L, EDD, ADDRalling TbYM, MitrimpRadpS, um1p171[1] 1024 L, EDD, ADDRalling TbYM, M | TbarVelFiltSv_M_str.K_Uls_f32 | 0.047852 |
| | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 77.14 |
| Laboration Lab | k_CmnTbarStiff_NmpDeg_f32 | 7.5 |
| k. DmpGainOffThresh. KehpS. f22 16.5 k. DmpGainOffTresh. KehpS. f22 45.2 k. Infromp. Mirriers L. KehpS. f22 0.00015 k. Infromp. Mirriers L. KehpS. f22 0.3 2k. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][0] 523 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][1] 1038 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][2] 1553 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][3] 2068 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][4] 2583 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][6] 3614 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][7] 4129 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][8] 4644 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[0][8] 4644 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[1][1] 883 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[1][1] 883 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[1][1] 883 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[1][1] 1384 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[1][1] 1706 12. FDD. ADDRollingTbYM_MirrimPadapS. um1p17[1][1] 1706 12. FDD. ADDRollingTbYM_M | k_DmpDecelGainFSlew_UlspS_f32 | 1400.05 |
| K_DmpGainOnThresh_KphpS_132 45.2 k_IntrCmp_Mitroetla_KgmSq_152 0.00015 k_IntrCmp_Mitroetla_ScaleFacto_Uls_152 0.3 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[0] 523 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[1] 1038 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[2] 1553 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[4] 2583 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[6] 3894 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[6] 3814 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[6] 3814 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[6] 4129 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[6] 4844 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(0)[6] 342 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(1)[0] 342 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(1)[1] 683 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(1)[4] 1705 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(1)[4] 1705 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(1)[6] 2387 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17(1)[6] 2387 12_FDD_ADDRollingTbiYM_MitrompRadpS_um1p17 | k_DmpDecelGain_Uls_f32 | 2.1 |
| K_IntCmp_Mtrion_Mtrong_Mtrion_MtrinnpRadpS_um1p17(0)[0] 0.3 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[0] 523 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[2] 1553 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[2] 1553 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[3] 2668 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[4] 2583 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[6] 3614 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[6] 3614 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[7] 4129 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(0)[9] 5159 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 5159 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 342 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 342 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 342 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 1384 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 1384 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 246 12_FDD_ADDRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 349 12_FDD_ADRollingTb1YM_MtrinnpRadpS_um1p17(1)[9] 368 | k_DmpGainOffThresh_KphpS_f32 | 16.5 |
| k_InnCom_Mrtvel_ScaleFactor_Uis_132 0.3 12_FDD_ADRollingTbYM_MthmpRadps_um1p17(0)[1) 1038 12_FDD_ADRollingTbYM_MthmpRadps_um1p17(0)[2) 1553 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(0)[3) 2068 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(0)[4] 2883 12_FDD_ADRollingTbYM_MthmpRadps_um1p17(0)[5] 3099 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(0)[7] 4129 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(0)[7] 4129 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(0)[8] 4844 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(0)[8] 4844 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[9] 5159 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 683 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 683 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 1024 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 204 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 204 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 2387 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 2387 12_FDD_ADDRollingTbYM_MthmpRadps_um1p17(1)[1] 309 12_FDD_FerdTbYM_MthmpRadps_um1p17(1)[1] 32 | k_DmpGainOnThresh_KphpS_f32 | 45.2 |
| 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 0 1038 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 1 1038 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 3 2068 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 3 2683 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 4 2683 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 5 3099 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 6 3614 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 6 3614 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 6 3614 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 6 3614 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 9 5159 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 9 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 9 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 10 683 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 11 683 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 3 1364 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 4 1705 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 5 2046 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 6 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 6 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 7 2728 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 8 3068 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 8 3068 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17() 9 3409 12_FDD_FreqTbYM_Mtr_ut2p4() 0 16 12_FDD_FreqTbYM_Htr_ut2p4() 0 48 12_FDD_FreqTbYM_Htr_ut2p4() 11 32 12_FDD_FreqTbYM_Htr_ut2p4() 11 30 | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00015 |
| 2_FDD_ADDRollingTbYM_MirhmpRadpS_um1p17(0)[1] 1038 102 102 1038 102 102 1038 102 102 1038 102 1038 102 1038 102 1038 102 1038 102 1038 102 1038 102 1038 | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.3 |
| 2_FDD_ADDRollingTbIYM_MtrimpRadps_umtp17(0)[1] 1553 | | 523 |
| 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[3] 2583 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1038 |
| 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[3] 2583 | | 1553 |
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| 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17[0][6] 3614 4129 | | 3099 |
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| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 342 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 683 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1024 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1364 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1705 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][1] 32 12_FDD_FreqTblYM_Hz_u12p4[0][2] 48 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][6] 128 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][1] 160 12_FDD_FreqTblYM_Hz_u12p4[0][1] 160 <t< td=""><td></td><td></td></t<> | | |
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| 1705 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2046 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][0] 16 12_FDD_FreqTblYM_Hz_u12p4[0][1] 32 12_FDD_FreqTblYM_Hz_u12p4[0][2] 48 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 16 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 32 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 48 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 64 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][0] 16 12_FDD_FreqTblYM_Hz_u12p4[0][1] 32 12_FDD_FreqTblYM_Hz_u12p4[0][2] 48 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 16 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 32 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 48 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 64 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][0] 16 12_FDD_FreqTblYM_Hz_u12p4[0][1] 32 12_FDD_FreqTblYM_Hz_u12p4[0][2] 48 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] 16 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 32 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 48 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 64 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] 32 12_FDD_FreqTblYM_Hz_u12p4[0][2] 48 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] 48 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | , ,, | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] 64 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 80 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 96 12_FDD_FreqTblYM_Hz_u12p4[0][6] 112 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] 128 12_FDD_FreqTblYM_Hz_u12p4[0][8] 144 12_FDD_FreqTblYM_Hz_u12p4[0][9] 160 12_FDD_FreqTblYM_Hz_u12p4[0][10] 176 12_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | , ,, | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 192 | | |
| | | |
| 42 FDD FreeThIVM H=4294(4)(0) 470 | | |
| | t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 192 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][2] 208 | , ,, | |
| 12_FDD_FreqTbIYM_Hz_u12p4[1][3] 224 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 224 |

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| Name | Input Value | |
|--|-------------|--|
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 240 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 256 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 272 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 288 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 304 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 320 | |
| P_FDD_FreqTblYM_Hz_u12p4[1][10] | 336 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 352 | |
| CmnVehSpd_Kph_u9p7[0] | 12800 | |
| CmnVehSpd_Kph_u9p7[1] | 12928 | |
| CmnVehSpd_Kph_u9p7[2] | 13056 | |
| CmnVehSpd Kph u9p7[3] | 13184 | |
| CmnVehSpd_Kph_u9p7[4] | 13312 | |
| CmnVehSpd_Kph_u9p7[5] | 13440 | |
| CmnVehSpd_Kph_u9p7[6] | 13568 | |
| CmnVehSpd_Kph_u9p7[7] | 13696 | |
| CmnVehSpd_Kph_u9p7[8] | 13824 | |
| | 13952 | |
| CmnVehSpd_Kph_u9p7[9] | 14080 | |
| CmnVehSpd_Kph_u9p7[10] | | |
| CmnVehSpd_Kph_u9p7[11] | 14208 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 20890 | |
| DmpADDCoefX_MtrNm_u4p12[1] | 21299 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 21709 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 22118 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 22528 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 22938 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 23347 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 23757 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 24166 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 24576 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4000 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 | |
| | 2424 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 342 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1024 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 | |
| FDD_ADDStaticTbH_MithNinpRadpS_um1p17[7] FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068 | |
| | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1520 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1536 | |
| FDD_AttenTblY_Uls_u8p8[0] | 86 | |
| FDD_AttenTblY_Uls_u8p8[1] | 88 | |
| FDD_BlendTbIY_Uls_u8p8[0] | 5 | |
| FDD_BlendTbIY_Uls_u8p8[1] | 8 | |
| FDD_BlendTbIY_Uls_u8p8[2] | 10 | |
| FDD_BlendTbIY_Uls_u8p8[3] | 13 | |
| FDD_BlendTblY_Uls_u8p8[4] | 15 | |
| FDD_BlendTblY_Uls_u8p8[5] | 18 | |
| FDD_BlendTblY_Uls_u8p8[6] | 20 | |
| FDD_BlendTblY_Uls_u8p8[7] | 23 | |
| | 26 | |
| FDD_BlendTblY_Uls_u8p8[8] | | |
| _FDD_BlendTblY_Uls_u8p8[9] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 31 | |
| _FDD_BlendTblY_Uls_u8p8[11] | 33 | |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 205 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 23 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 24 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 26 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 27 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 28 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 29 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 1638 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 3277 | | |
| t RIAstWIRBIndTbIY UIs u2p14[2] | 4915 | | |
| t RIAstWIRBIndTbIY Uls u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 1178 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 1203 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1280 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 2.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 100.8 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 7.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 11.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 0 | | |
| tgt FrqDepDmpnInrtCmp Per1 WIRCmdAmpBInd MtrNm f32.value | 3.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwYorque_Hw tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt_Rte_inst_Ap_FrqDepDmpnintCmp.FrqDepDmpnintCmp_Fer1_venicleConAct tgt_Rte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VenicleSpeed | | | |
| | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | | <u> </u> | |
| Name | Actual Value | Expected Value | Result |

| 20 | | · · · · _ · · · · · · · · · · · · · · · | |
|--|--------------|---|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127014.063 | 127014.0599 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -12284.4609 | -12284.45952 ± 0.09 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 30.5068626 | 30.50686197 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.5 | -1.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -16.2000008 | -16.2 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1 | 1 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.69140744 | 1.691408 ± 0.00390625 | ~ |
| tot FroDenDmonInrtCmp Per1 FroDenDmonInrtCmp MtrNm f32 value | -8 80000019 | -8 8 + 0 00048828125 | ✓ |

FrqDepDmpnInrtCmp_Per1

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T
Actual Function Count Expected Function Count Result

Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached

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| lame | |
|---|----------------|
| | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 560 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 576 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 592 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 608 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 624 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 640 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 656 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 672 |
| CmnVehSpd_Kph_u9p7[0] | 15488 |
| CmnVehSpd_Kph_u9p7[1] | 15616 |
| _CmnVehSpd_Kph_u9p7[2] | 15744 |
| CmnVehSpd Kph u9p7[3] | 15872 |
| _CmnVehSpd_Kph_u9p7[4] | 16000 |
| _CmnVehSpd_Kph_u9p7[5] | 16128 |
| _CmnVehSpd_Kph_u9p7[6] | 16256 |
| _CmnVehSpd_Kph_u9p7[7] | 16384 |
| _CmnVehSpd_Kph_u9p7[8] | 16512 |
| _CmnVehSpd_Kph_u9p7[9] | 16640 |
| CmnVehSpd Kph u9p7[10] | 16768 |
| CmnVehSpd_Kph_u9p7[11] | 16896 |
| | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 24986 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 25395 |
| _DmpADDCoefX_MtrNm_u4p12[2] | 25805 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 26214 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 26624 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 27034 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 27443 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 27853 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 28262 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 28672 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 384 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 392 |
| DmpDecelGainSlewY UlspS u13p3[2] | 400 |
| DmpDecelGainSlewY_UlspS_u13p3[3] | 408 |
| DmpDecelGainSlewY_UlspS_u13p3[4] | 416 |
| DmpDecelGainSlewY_UlspS_u13p3[5] | 424 |
| DmpFiltKpWIRBIndY Uls u2p14[0] | 3277 |
| DmpFiltKpWIRBIndY Uls u2p14[1] | 4915 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 |
| DmpFiltKpWIRBIndY Uls u2p14[3] | 8192 |
| _DmpFiltKpWIRBIndY_Uis_u2p14[4] | 9830 |
| | 523 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 1038 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1553 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2068 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 2583 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3099 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3614 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4129 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4644 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5159 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1552 |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 1600 |
| _FDD_AttenTblY_Uls_u8p8[0] | 114 |
| _FDD_AttenTblY_Uls_u8p8[1] | 116 |
| _FDD_BlendTblY_Uls_u8p8[0] | 10 |
| _FDD_BlendTblY_Uls_u8p8[1] | 13 |
| FDD_BlendTblY_Uls_u8p8[2] | 15 |
| FDD_BlendTblY_Uls_u8p8[3] | 18 |
| _FDD_BlendTblY_Uls_u8p8[4] | 20 |
| _FDD_BlendTblY_Uls_u8p8[5] | 23 |
| _FDD_BlendTbIY_Uls_u8p8[6] | 26 |
| 22_2.5ha fort_olo_dopo[0] | 28 |
| | 20 |
| FDD_BlendTblY_Uls_u8p8[7] | 31 |
| FDD_BlendTblY_Uls_u8p8[7] FDD_BlendTblY_Uls_u8p8[8] | 31 |
| FDD_BlendTblY_Uls_u8p8[7] | 31 33 36 |

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| Name | Input Value | | |
|--|---------------------------------------|---------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 294 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 32 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 33 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 36 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 40 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 41 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 42 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 44 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 45 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 9830 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1434 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1459 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1485 | | |
| t WIRBIndTbIX MtrNm u8p8[3] | 1510 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1536 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -2.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -100.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -7.6 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 12.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 511.9921875 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 4.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | tgt FrqDepDmpnInrtCmp Per1 BaseAssisi | tCmd MtrNm f32 | |
| tat Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB | | | |
| Name | Actual Value | Expected Value | Result |
| PreDecelGain Uls M f32 | 127115 836 | 127115 835 + 0 0625 | result |

| 20 | | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127115.836 | 127115.835 ± 0.0625 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | -388429.438 | -388429.5001 ± 0.9 | • |
| Prev1SclDrvVel_RadpS_M_f32 | -20.7490158 | -20.74901587 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | 2.5 | 2.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | 100.800003 | 100.8 ± 0.00390625 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | -0.894117653 | -0.894117647 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.23690033 | -1.236898824 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmp Per1 FraDenDmonInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Proceeding U.S. M. 152 | T4 04-9 0 00 (D-9-4 0-994 - 4) | , a |
|--|--------------------------------------|-----------------------------------|
| Processor July Min M. 192 Prof. Staffword, RaspS, M. 192 Prof. Prof. Staffword, RaspS, M. 192 Prof. Prof. Prof. M | Test Step 3.20 (Repeat Count = 1) | V |
| Prest Standword Banks M. 182 Prest Banks | | |
| Provision Minimum Mini | PreDecelGain_Uls_M_f32 | |
| Privazidanticomp, Minhm, M. 192 9.5 | Prev1PreAttnComp_MtrNm_M_f32 | |
| Prov.250P./vvl. Radys, M. 132 Prov. Parkapa, M. brobe, M. 132 | Prev1ScIDrvVel_RadpS_M_f32 | |
| PrevTbarkOp_MoDe_M_R2 Rip.led_A_F_R0DepOmprintCrop TotaricelTask_M_str SY_Us_ R2 10. Add 79 Commystantatio_Mindepointog_gl22 99.12 Commystantatio_Mindepointog_gl22 99.12 Commystantatio_Mindepointog_gl22 10. Depoints_Mindepointog_gl22 10. Depoints_Mindepoints_gl22 1 | Prev2PreAttnComp_MtrNm_M_f32 | -3.5 |
| Re_Inst_A_FingDepDimpointCrop | Prev2SclDrvVel_RadpS_M_f32 | -59.2 |
| ThanVerlisty, M., str. X. U.B., 12 12 13 14 17 17 17 17 17 17 17 17 17 | PrevTbarAng_HwDeg_M_f32 | 0.909 |
| TRAVERISS, M., att. K., Us. D2 L. Com Tharistiff, Whitpberg, 192 L. Com | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k. Com/Spacific Michaelpholog. 132 99.12 k. Com/Dacidian, Jul., G2 1600.03 k. DmpDacedCainFSiew, UlapS, G2 1600.03 k. DmpDacedCainFSiew, UlapS, G2 2.8 k. DmpGacedTiment, Kyphs, G2 2.2 k. DmpGacedTiment, Kyphs, G2 33.5 k. Indram, Mirchent, Kyphs, G2 0.0003 k. Indram, Mirchent, Kyphs, G2 0.01 k. Indram, Mirchent, Kyphs, G2 0.0003 k. Indram, Mirchent, Kyphs, G2 0.1 k. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[1] 328 k. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[2] 494 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[3] 661 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[6] 827 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[7] 1326 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[7] 1326 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[7] 1326 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[8] 1493 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[9] 1659 l. FDD, ADDRalingThVM, MirchimpRadpS, unip17(P)[1] 326 l. FDD, ADDRalingThVM, MirchimpRadpS, unip1 | TbarVelFiltSv_M_str.SV_Uls_f32 | 1.2 |
| k_ CmmTackidf_NmpQeg_122 9.5 k_ DmpDecelGainFSiew_UispS_102 1000.03 k_ DmpDecelGainFSiew_UispS_102 2.6 k_ DmpGainOffThrean_Kphps_132 2.2 k_ Indrom_Mitterial_KpinS_102 33.5 k_ Indrom_Mitterial_KpinS_102 0.0003 k_ Indrom_Mitterial_KpinS_102 0.1 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 161 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 328 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 494 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 194 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 1160 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 1403 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 1403 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(DIQ) 165 12_ FDD_ADDRollingTbVM_MitteringPadoS_ump17(TIQ) 328 12_ FDD_ADDRollingTbVM_Mitte | TbarVelFiltSv_M_str.K_Uls_f32 | 0.3479 |
| K. DmpDecelGain Usi Giz | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 99.12 |
| k_DmpGainOffTriest_KphpS_152 | k_CmnTbarStiff_NmpDeg_f32 | 9.5 |
| LongolaiofiThrees Kyphs 532 22.3 3.5 1 | k_DmpDecelGainFSlew_UlspS_f32 | 1600.03 |
| k, InntComp, Mirrineria, KgmSq, 132 k, InntComp, Mirrineria, Mirrineria, 132 k, InntComp, Mirrineria, 133 k, InntComp, 134 | k_DmpDecelGain_Uls_f32 | 2.6 |
| IntrCmp_Mtritectia_Kgmsq_rs2 | k_DmpGainOffThresh_KphpS_f32 | 22.3 |
| k_IntrCmp_MtrVel_ScaleFactor_Uis_f32 | k_DmpGainOnThresh_KphpS_f32 | 33.5 |
| 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 0 161 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 1 328 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 3 661 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 4 827 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 5 994 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 6 1160 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 6 1160 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 8 1493 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 9 1559 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(0) 9 1659 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 0 161 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 0 161 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 1 328 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 1 328 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 3 661 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 3 661 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 4 327 2 FDD_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 6 1160 2 FDD_FDM_ADDRollingTbiYM_MirhmpRadpS_um1p17(1) 6 1160 2 FDD_FRETDHYM_Hz_u12p40(1) 1 149 2 FDD_FreqTbiYM_Hz_u12p40(1) 1 149 2 FDD_FreqTbiYM_Hz_u12p40(1) 1 149 2 FDD_FreqTbiYM_Hz_u12p40(1) 1 140 2 FDD_FreqTbiYM_Hz_u12p40(1) 1 140 2 FDD_FreqTbiYM_Hz_u12p40(1) 1 160 2 FDD_FreqTbi | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0003 |
| 12 FDD_ADDRollingTbYM_MirNimpRades_um1p17(0)[1] 328 | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 |
| 12 FDD _ ADDRollingTbYM_MirhmpRadpS_um1p17(0)[2] | | 161 |
| 12 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][2] 494 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][4] 827 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][5] 994 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][6] 1160 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][7] 1326 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][7] 1326 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][8] 1493 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][9] 1659 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[0][9] 1659 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 328 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 328 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 349 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 494 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 827 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 827 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 994 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 1160 2 FDD_ADDRollingTbiYM_MirnnpRadpS_um1p17[1][1] 160 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 64 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 64 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 162 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 163 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 164 2 FDD_FeqTbiYM_Hz_u12p4[0][1] 165 2 FDD_FeqTbiYM_Hz_u12p4[| | 328 |
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| t2_FDD_FreqTbIYM_Hz_u12p4[0][0] 48 t2_FDD_FreqTbIYM_Hz_u12p4[0][1] 64 t2_FDD_FreqTbIYM_Hz_u12p4[0][2] 80 t2_FDD_FreqTbIYM_Hz_u12p4[0][3] 96 t2_FDD_FreqTbIYM_Hz_u12p4[0][4] 112 t2_FDD_FreqTbIYM_Hz_u12p4[0][5] 128 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 144 t2_FDD_FreqTbIYM_Hz_u12p4[0][7] 160 t2_FDD_FreqTbIYM_Hz_u12p4[0][8] 176 t2_FDD_FreqTbIYM_Hz_u12p4[0][9] 192 t2_FDD_FreqTbIYM_Hz_u12p4[0][10] 208 t2_FDD_FreqTbIYM_Hz_u12p4[0][11] 224 t2_FDD_FreqTbIYM_Hz_u12p4[1][0] 656 t2_FDD_FreqTbIYM_Hz_u12p4[1][1] 672 t2_FDD_FreqTbIYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] 64 12_FDD_FreqTblYM_Hz_u12p4[0][2] 80 12_FDD_FreqTblYM_Hz_u12p4[0][3] 96 12_FDD_FreqTblYM_Hz_u12p4[0][4] 112 12_FDD_FreqTblYM_Hz_u12p4[0][5] 128 12_FDD_FreqTblYM_Hz_u12p4[0][6] 144 12_FDD_FreqTblYM_Hz_u12p4[0][7] 160 12_FDD_FreqTblYM_Hz_u12p4[0][8] 176 12_FDD_FreqTblYM_Hz_u12p4[0][9] 192 12_FDD_FreqTblYM_Hz_u12p4[0][10] 208 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[1][0] 656 12_FDD_FreqTblYM_Hz_u12p4[1][1] 672 12_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTbIYM_Hz_u12p4[0][2] 80 12_FDD_FreqTbIYM_Hz_u12p4[0][3] 96 12_FDD_FreqTbIYM_Hz_u12p4[0][4] 112 12_FDD_FreqTbIYM_Hz_u12p4[0][5] 128 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 144 12_FDD_FreqTbIYM_Hz_u12p4[0][7] 160 12_FDD_FreqTbIYM_Hz_u12p4[0][8] 176 12_FDD_FreqTbIYM_Hz_u12p4[0][9] 192 12_FDD_FreqTbIYM_Hz_u12p4[0][10] 208 12_FDD_FreqTbIYM_Hz_u12p4[0][11] 224 12_FDD_FreqTbIYM_Hz_u12p4[1][0] 656 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 672 12_FDD_FreqTbIYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] 96 12_FDD_FreqTblYM_Hz_u12p4[0][4] 112 12_FDD_FreqTblYM_Hz_u12p4[0][5] 128 12_FDD_FreqTblYM_Hz_u12p4[0][6] 144 12_FDD_FreqTblYM_Hz_u12p4[0][7] 160 12_FDD_FreqTblYM_Hz_u12p4[0][8] 176 12_FDD_FreqTblYM_Hz_u12p4[0][9] 192 12_FDD_FreqTblYM_Hz_u12p4[0][10] 208 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[1][0] 656 12_FDD_FreqTblYM_Hz_u12p4[1][1] 672 12_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 208 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 224 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 656 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 672 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] 160 12_FDD_FreqTblYM_Hz_u12p4[0][8] 176 12_FDD_FreqTblYM_Hz_u12p4[0][9] 192 12_FDD_FreqTblYM_Hz_u12p4[0][10] 208 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[1][0] 656 12_FDD_FreqTblYM_Hz_u12p4[1][1] 672 12_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][8] 176 12_FDD_FreqTblYM_Hz_u12p4[0][9] 192 12_FDD_FreqTblYM_Hz_u12p4[0][10] 208 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[1][0] 656 12_FDD_FreqTblYM_Hz_u12p4[1][1] 672 12_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][9] 192 12_FDD_FreqTblYM_Hz_u12p4[0][10] 208 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[1][0] 656 12_FDD_FreqTblYM_Hz_u12p4[1][1] 672 12_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][10] 208 12_FDD_FreqTblYM_Hz_u12p4[0][11] 224 12_FDD_FreqTblYM_Hz_u12p4[1][0] 656 12_FDD_FreqTblYM_Hz_u12p4[1][1] 672 12_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 224 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 656 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 672 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 656 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 672 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 672 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| | | |
| 40 EDD FT-IV/M H404/4/101 | | |
| 1Z_FDU_Freq DIYM_HZ_U1ZP4[1][3] /04 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 704 |

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| FrqDepDmpnInrtCmp_Per1 | IMAC | TOTAL |
|--|-------------|-------|
| Name | Input Value | |
| 2 FDD FregTblYM Hz u12p4[1][4] | 720 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 784 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 | |
| 2_FDD_freqTblYM_Hz_u12p4[1][11] | 832 | |
| CmnVehSpd Kph u9p7[0] | 10368 | |
| CmnVehSpd Kph u9p7[1] | 10496 | |
| | 10624 | |
| _CmnVehSpd_Kph_u9p7[2] | | |
| CmnVehSpd_Kph_u9p7[3] | 10752 | |
| CmnVehSpd_Kph_u9p7[4] | 10880 | |
| _CmnVehSpd_Kph_u9p7[5] | 11008 | |
| CmnVehSpd_Kph_u9p7[6] | 11136 | |
| CmnVehSpd_Kph_u9p7[7] | 11264 | |
| CmnVehSpd_Kph_u9p7[8] | 11392 | |
| CmnVehSpd_Kph_u9p7[9] | 11520 | |
| CmnVehSpd_Kph_u9p7[10] | 11648 | |
| CmnVehSpd_Kph_u9p7[11] | 11776 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 28262 | |
| DmpADDCoefX_MtrNm_u4p12[1] | 28672 | |
| DmpADDCoefX_MtrNm_u4p12[2] | 29082 | |
| DmpADDCoefX_MtrNm_u4p12[3] | 29491 | |
| DmpADDCoefX_MtrNm_u4p12[4] | 29901 | |
| DmpADDCoefX_MtrNm_u4p12[5] | 30310 | |
| DmpADDCoefX_MtrNm_u4p12[6] | 30720 | |
| DmpADDCoefX_MtrNm_u4p12[7] | 31130 | |
| DmpADDCoefX_MtrNm_u4p12[8] | 31539 | |
| DmpADDCoefX_MtrNm_u4p12[9] | 31949 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824 | |
| DmpDecelGainSiewx_MitrRadpS_u11p5[1] DmpDecelGainSiewX_MtrRadpS_u11p5[2] | 5856 | |
| | | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 3608 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 3616 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 3624 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 3632 | |
| DmpDecelGainSlewY_UlspS_u13p3[4] | 3640 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 3648 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | |
| DmpFiltKpWIRBIndY Uls u2p14[3] | 9830 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[0] | 704 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 814 | |
| | 924 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1034 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1144 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1254 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1364 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1475 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1585 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1695 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1616 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1680 | |
| FDD_AttenTblY_Uls_u8p8[0] | 136 | |
| FDD_AttenTblY_Uls_u8p8[1] | 139 | |
| FDD_BlendTblY_Uls_u8p8[0] | 13 | |
| FDD_BlendTblY_Uls_u8p8[1] | 15 | |
| FDD_BlendTblY_Uls_u8p8[2] | 18 | |
| FDD_BlendTbIY_UIs_u8p8[3] | 20 | |
| FDD_BlendTblY_Uls_u8p8[4] | 23 | |
| | | |
| FDD_BlendTblY_Uls_u8p8[5] | 26 | |
| FDD_BlendTblY_Uls_u8p8[6] | 28 | |
| FDD_BlendTbIY_Uls_u8p8[7] | 31 | |
| FDD_BlendTbIY_Uls_u8p8[8] | 33 | |
| FDD_BlendTbIY_Uls_u8p8[9] | 36 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 38 | |
| | 41 | |

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| Informy ScaleFactorTbY_Uis_u9p7[0] 199 | |
|---|------|
| IntCmp_ScaleFactorTbY_UIs_u9p7[1] | |
| | |
| LintCmp ScaleFactorTbY_UIs_u\$p7[3] 218 230 LintCmp ScaleFactorTbY_UIs_u\$p7[4] 230 243 | |
| intCmScaleFactorTbY_UIs_usp7[4] | |
| intCmp_ ScaleFactorTblY_Uis_u9p7[5] | |
| intCmp_ScaleFactorTblY_Uis_u9p7[6] 256 intCmp_ScaleFactorTblY_Uis_u9p7[7] 269 intCmp_ScaleFactorTblY_Uis_u9p7[8] 282 intCmp_ScaleFactorTblY_Uis_u9p7[8] 294 intCmp_ScaleFactorTblY_Uis_u9p7[8] 294 intCmp_ScaleFactorTblY_Uis_u9p7[10] 307 intCmp_ScaleFactorTblY_Uis_u9p7[11] 320 intCmp_ScaleFactorTblY_Uis_u9p7[11] 320 intCmp_ScaleFactorTblY_Uis_u9p7[11] 47 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[1] 47 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[2] 49 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[2] 49 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[3] 50 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[3] 51 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[6] 51 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[6] 54 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[6] 54 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[6] 54 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[8] 56 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[8] 56 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[8] 56 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[8] 58 intCmp_TSarVel_ScaleFactorTblY_Uis_u9p7[10] 59 intC | |
| LintCmp_ScaleFactorTblY_Uls_u9p7[7] | |
| LintCmp_ScaleFactorTblY_Uls_u9p7[8] 282 LintCmp_ScaleFactorTblY_Uls_u9p7[9] 294 LintCmp_ScaleFactorTblY_Uls_u9p7[10] 307 | |
| [nrtCmp_ScaleFactorTbty_Uls_u9p7[9] | |
| IntrCmp_ScaleFactorTblY_Uls_u9p7[10] 307 | |
| | |
| LinrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(0) 46 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(1) 47 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(2) 49 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(3) 50 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(4) 51 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(5) 52 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(6) 54 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(7) 55 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(8) 56 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(9) 58 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(10) 59 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(11) 60 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(11) 60 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(11) 60 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(11) 60 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(11) 60 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(11) 60 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(10) 4915 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(10) 4915 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9p7(10) 4915 t_inrtCmp_TBarVel_ScaleFactorTblY_UIs_u9 | |
| IntrCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_Uls_u9p7[2] | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] 50 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] 51 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 52 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 54 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 55 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 55 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 58 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 58 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 59 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 60 t_RIAstWIRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblX_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[1] 1741 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 150.5 | |
| t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[4] 51 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[5] 52 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[6] 54 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[7] 55 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[9] 58 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[10] 59 t_InrtCmp_TBarVel_ScaleFactorTbIY_UIs_u9p7[11] 60 t_RIAstWIRBIndTbIY_UIs_u2p14[0] 4915 t_RIAstWIRBIndTbIY_UIs_u2p14[1] 6554 t_RIAstWIRBIndTbIY_UIs_u2p14[2] 8192 t_RIAstWIRBIndTbIY_UIs_u2p14[3] 9830 t_RIAstWIRBIndTbIX_MtrNm_u8p8[0] 11469 t_WIRBIndTbIX_MtrNm_u8p8[1] 1715 t_WIRBIndTbIX_MtrNm_u8p8[1] 1715 t_WIRBIndTbIX_MtrNm_u8p8[2] 1741 t_WIRBIndTbIX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] 52 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 54 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 55 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 58 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] 59 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] 60 t_RIAstWiRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWiRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWiRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWiRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWiRBIndTblY_Uls_u2p14[4] 11469 t_WiRBIndTblX_MtrNm_u8p8[0] 1690 t_WiRBIndTblX_MtrNm_u8p8[1] 175 t_WiRBIndTblX_MtrNm_u8p8[1] 175 t_WiRBIndTblX_MtrNm_u8p8[3] 1766 t_WiRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 150.5 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] 54 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 55 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 58 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 59 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 60 t_RIAstWIRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblX_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[1] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 150.5 | |
| t InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] 55 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 58 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 59 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 60 t_RIAstWIRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblX_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 150.5 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] 56 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] 58 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 59 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 60 t_RIAstWIRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblX_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 150.5 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] 59 t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 60 t_RIAstWIRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblY_Uls_u2p14[4] 1175 t_WIRBIndTblX_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] 60 t_RIAstWIRBIndTblY_Uls_u2p14[0] 4915 t_RIAstWIRBIndTblY_Uls_u2p14[1] 6554 t_RIAstWIRBIndTblY_Uls_u2p14[2] 8192 t_RIAstWIRBIndTblY_Uls_u2p14[3] 9830 t_RIAstWIRBIndTblY_Uls_u2p14[4] 11469 t_WIRBIndTblY_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] 6554 t_RIAstWIRBIndTbIY_UIs_u2p14[2] 8192 t_RIAstWIRBIndTbIY_UIs_u2p14[3] 9830 t_RIAstWIRBIndTbIY_UIs_u2p14[4] 11469 t_WIRBIndTbIX_MtrNm_u8p8[0] 1690 t_WIRBIndTbIX_MtrNm_u8p8[1] 1715 t_WIRBIndTbIX_MtrNm_u8p8[2] 1741 t_WIRBIndTbIX_MtrNm_u8p8[3] 1766 t_WIRBIndTbIX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 15 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] 8192 t_RIAstWIRBIndTbIY_UIs_u2p14[3] 9830 t_RIAstWIRBIndTbIY_UIs_u2p14[4] 11469 t_WIRBIndTbIX_MtrNm_u8p8[0] 1690 t_WIRBIndTbIX_MtrNm_u8p8[1] 1715 t_WIRBIndTbIX_MtrNm_u8p8[2] 1741 t_WIRBIndTbIX_MtrNm_u8p8[3] 1766 t_WIRBIndTbIX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 15 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] 9830 t_RIAstWIRBIndTbIY_UIs_u2p14[4] 11469 t_WIRBIndTbIX_MtrNm_u8p8[0] 1690 t_WIRBIndTbIX_MtrNm_u8p8[1] 1715 t_WIRBIndTbIX_MtrNm_u8p8[2] 1741 t_WIRBIndTbIX_MtrNm_u8p8[3] 1766 t_WIRBIndTbIX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] 11469 t_WIRBIndTbIX_MtrNm_u8p8[0] 1690 t_WIRBIndTbIX_MtrNm_u8p8[1] 1715 t_WIRBIndTbIX_MtrNm_u8p8[2] 1741 t_WIRBIndTbIX_MtrNm_u8p8[3] 1766 t_WIRBIndTbIX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_WIRBIndTblX_MtrNm_u8p8[0] 1690 t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_WIRBIndTblX_MtrNm_u8p8[1] 1715 t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_WIRBIndTblX_MtrNm_u8p8[2] 1741 t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_WIRBIndTblX_MtrNm_u8p8[3] 1766 t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| t_WIRBIndTblX_MtrNm_u8p8[4] 1792 tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value 1.5 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value 150.5 | |
| | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDtt_Cnt_lgc.value 0 | |
| | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value 8.7 | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value 13.05 | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value 250.02 | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value 5.2 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCmc tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel I tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSi tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSiComSvcDft_Cnt_Igc | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_HwTorque_Hwl tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_VehicleLonAccet_tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccet_KphpS_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32 | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBl tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBlnd_MtrNm_f32 | |
| Name Actual Value Expected Value | Resu |
| PreDecelGain_Uls_M_f32 127217.609 127217.609 127217.6099 ± 0.0625 | |

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------------|----------|
| PreDecelGain_Uls_M_f32 | 127217.609 | 127217.6099 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -34957.4961 | -34957.49739 ± 0.09 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 16.6422844 | 16.64228823 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -2.5 | -2.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | -69.5999985 | -69.6 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.915789425 | 0.915789474 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.96354413 | 1.963548947 ± 0.00390625 | ~ |
| tot FroDenDmonInrtCmp Per1 FroDenDmonInrtCmp MtrNm f32 value | -8 80000019 | -8 8 + 0 00048828125 | ✓ |



| Т | | | | V |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | • |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ✓ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Proceedings Marker M. M22 1732-1765 | T+ 04 0.04 (D+ 0+ -4) | |
|--|---|-----------------------------------|
| Prebre Pred | Test Step 3.21 (Repeat Count = 1) | • · · · · · · · |
| Piest PeristanConing, Minhim, M. 152 | | |
| Prev156-Dr.Vve Facips M, 152 42 | PreDecelGain_Uls_M_f32 | |
| PROVERED AND CONTROL MATERNAL MATERIAN CONTROL OF THE PROVINCIA CONTROL | Prev1PreAttnComp_MtrNm_M_f32 | |
| PrevSab/Only English (1922) Rib_Inst_Ap_FrqDepDmpInntCmp Inst_Ap_FrqDepDmpInntCmp Inst_Rib_Inst_Ap_FrqDepDmpInntCmp Inst_Rib_Inst_Ap_FrqDepDmp Inst_Rib | Prev1SclDrvVel_RadpS_M_f32 | |
| Piev/Tabarko_HwoGe_M_152 | Prev2PreAttnComp_MtrNm_M_f32 | -2.4 |
| Rit _ Inst _ A_ FriQoeDmpnintCrop | Prev2SclDrvVel_RadpS_M_f32 | -366.2 |
| TravVeRINSV_MEX_UIS_02 | PrevTbarAng_HwDeg_M_f32 | -6.771 |
| Travide Tilby, M. str. K. Us. 122 k. CmmSpashir Nanobeg, 132 k. DmpGainor Threath, Kaphs, 132 k. Indrom, Mirherath, 132 k. Indrom, Mirherath, 132 k. Indrom, Mirherath, 132 k. Indrom, Mirherath, 132 k. Indrom, 132 k. Indrom, 133 k | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k, Com/SykinFatio, Mitchegol 122 27.02 k, Com/Dacsidif, NimpDec_[122 1.3 k, DmpDecelGain Uis_132 2.1 k, DmpDecelGain, Uis_132 2.1 k, DmpGainOfThreah, Kiphps, 132 16.2 k, DmpGainOfThreah, Kiphps, 132 44 k, IntrCmp, Mirvel, ScaleFactor, Uis_132 0.00031 k, IntrCmp, Mirvel, ScaleFactor, Uis_132 0.9 2, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(0)(1) 683 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(0)(1) 683 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(0)(1) 1024 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(0)(1) 1706 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(0)(1) 278 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(1)(1) 278 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(1)(1) 342 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(1)(1) 342 12, FDD, ADDRoilingTbVM, MirvinmPadaps, umtp17(1)(1) 342 | TbarVelFiltSv_M_str.SV_Uls_f32 | -1.5 |
| k_ Comtractifit_MingDeg_128 k_ DimpGeelGainFisiew_Ulaps_522 k_ DimpGeelGainFisiew_Ulaps_5232 k_ DimpGeelGainFisiew_Ulaps_63322 k_ DimpGeelGainFisiew | TbarVelFiltSv_M_str.K_Uls_f32 | 0.2244 |
| K DmpDecelGain FSiew_UispS_B2 1700.05 K DmpDearolThresh_KphpS_J32 16.2 K DmpGainOfThresh_KphpS_J32 44.2 K DmpCainOfThresh_KphpS_J32 44.2 K Indrom_Mitrelian KgmSq_J32 0.00031 K Indrom_Mitrelian KgmSq_J32 0.9 L FDD_ADDRollingTbVM_Mitrelian KgmSq_J32 0.9 L FDD_ADDRollingTbVM_Mitreli | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 27.02 |
| k, DmpGainOffTriesh, KphpS, 132 k, DmpGainOffTriesh, KphpS, 132 k, DmpGainOffTriesh, KphpS, 132 k, Indrom, Mitriesta, KphpS, 132 k, Indrom, Mitriesta, KpmSa, 132 k, Indrom, Mitriesta, Mit | k_CmnTbarStiff_NmpDeg_f32 | 1.3 |
| K DmpGainOrIThresh_Kphps_f32 16.2 k_ DmpGainOrIThresh_Kphps_f32 44.2 k_ Infromp_Mirvels_Kgmsq_f32 0.00031 k_ Infromp_Mirvels_CaleFactor_Uis_f32 0.9 2_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[0] 342 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[1] 683 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[2] 1024 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[6] 1034 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[6] 2046 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[6] 2387 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[7] 2728 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[8] 3088 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(0)[8] 3499 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[9] 349 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[1] 683 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[1] 1024 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[1] 1024 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[1] 1034 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[1] 2046 1_FDD_ADDRollingTbiYM_MirkimpRadps_um1p17(1)[1] 2046 | k_DmpDecelGainFSlew_UlspS_f32 | 1700.05 |
| k_ DmpGainOnThrean_KrphpS_f32 | k_DmpDecelGain_Uls_f32 | 2.1 |
| NinCmp_Mtvlnetia_KgmSq_f32 | k_DmpGainOffThresh_KphpS_f32 | 16.2 |
| k_IntCmp_MtVel_ScaleFactor_Uls_132 | k_DmpGainOnThresh_KphpS_f32 | 44.2 |
| 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 0 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 1 683 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 3 1364 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 4 1705 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 5 2046 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 5 2046 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 6 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 6 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 8 3068 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 3409 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 342 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 1384 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 1384 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 2387 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 246 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 9 3409 12_FDD_FeqTbYM_Hz_u12p4(0) 1 36 12_FDD_Feq | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00031 |
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| 12 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0) 2 1024 1024 1025 1024 1025 1 | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][1] | 683 |
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| 2_FDD_ADDRollingTblYM_MirNmpRadpS_um1p17(0)[7] 2728 2728 2_FDD_ADDRollingTblYM_MirNmpRadpS_um1p17(0)[8] 3068 | | |
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| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 3] 1364 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 4] 1705 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 5] 2046 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 6] 2387 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 6] 2728 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 8] 3068 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 9] 3409 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 9] 3409 12_FDD_FreqTb\YM_Hz_u12p4[0][0] 64 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 80 12_FDD_FreqTb\YM_Hz_u12p4[0][3] 112 12_FDD_FreqTb\YM_Hz_u12p4[0][3] 112 12_FDD_FreqTb\YM_Hz_u12p4[0][4] 128 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 144 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 160 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 160 12_FDD_FreqTb\YM_Hz_u12p4[0][7] 176 12_FDD_FreqTb\YM_Hz_u12p4[0][9] 208 12_FDD_FreqTb\YM_Hz_u12p4[0][9] 208 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 224 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 224 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 240 12_FDD_FreqTb\YM_Hz_u12p4[1][0] 166 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1705 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2046 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][0] 64 12_FDD_FreqTblYM_Hz_u12p4[0][1] 80 12_FDD_FreqTblYM_Hz_u12p4[0][2] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 160 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 160 12_FDD_FreqTblYM_Hz_u12p4[0][1] 240 12_FDD_FreqTb | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2046 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 12_FDD_FreqTblYM_Hz_u12p4[0][0] 64 12_FDD_FreqTblYM_Hz_u12p4[0][1] 80 12_FDD_FreqTblYM_Hz_u12p4[0][2] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][6] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2387 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 64 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 208 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 224 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 240 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2728 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 64 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 80 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 96 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 112 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 128 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 144 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 208 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 224 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 240 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3068 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][0] 64 12_FDD_FreqTblYM_Hz_u12p4[0][1] 80 12_FDD_FreqTblYM_Hz_u12p4[0][2] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3409 12_FDD_FreqTblYM_Hz_u12p4[0][0] 64 12_FDD_FreqTblYM_Hz_u12p4[0][1] 80 12_FDD_FreqTblYM_Hz_u12p4[0][2] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0] 64 t2_FDD_FreqTbIYM_Hz_u12p4[0][1] 80 t2_FDD_FreqTbIYM_Hz_u12p4[0][2] 96 t2_FDD_FreqTbIYM_Hz_u12p4[0][3] 112 t2_FDD_FreqTbIYM_Hz_u12p4[0][4] 128 t2_FDD_FreqTbIYM_Hz_u12p4[0][5] 144 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 160 t2_FDD_FreqTbIYM_Hz_u12p4[0][7] 176 t2_FDD_FreqTbIYM_Hz_u12p4[0][8] 192 t2_FDD_FreqTbIYM_Hz_u12p4[0][9] 208 t2_FDD_FreqTbIYM_Hz_u12p4[0][10] 224 t2_FDD_FreqTbIYM_Hz_u12p4[0][11] 240 t2_FDD_FreqTbIYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] 80 12_FDD_FreqTblYM_Hz_u12p4[0][2] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 128 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 160 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 176 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 208 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 224 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 240 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 192 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 192 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 208 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 224 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 240 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][9] 208 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][10] 224 12_FDD_FreqTblYM_Hz_u12p4[0][11] 240 12_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 240 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 16 | | |
| | | |
| | | |
| | t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 32 |
| | t2_FDD_FreqTblYM_Hz_u12p4[1][2] | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 64 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 64 |

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|---|-------------|----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 80 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 192 | |
| _CmnVehSpd_Kph_u9p7[0] | 5248 | |
| _CmnVehSpd_Kph_u9p7[1] | 5376 | |
| CmnVehSpd_Kph_u9p7[2] | 5504 | |
| CmnVehSpd_Kph_u9p7[3] | 5632 | |
| _CmnVehSpd_Kph_u9p7[4] | 5760 | |
| CmnVehSpd_Kph_u9p7[5] | 5888 | |
| _CmnVehSpd_Kph_u9p7[6] | 6016 | |
| _CmnVehSpd_Kph_u9p7[7] | 6144 | |
| _CmnVehSpd_Kph_u9p7[8] | 6272 | |
| _CmnVehSpd_Kph_u9p7[9] | 6400 | |
| CmnVehSpd Kph u9p7[10] | 6528 | |
| | 6656 | |
| _CmnVehSpd_Kph_u9p7[11] | | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 288 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 296 | |
| DmpDecelGainSlewY UlspS u13p3[2] | 304 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 312 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 320 | |
| DmpDecelGainSlewY_UlspS_u13p3[5] | 328 | |
| DmpFiltKpWIRBIndY Uls u2p14[0] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 | |
| | | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 885 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 986 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 1087 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1188 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1288 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 1389 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 1490 | |
| FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[7] | 1591 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1692 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 1793 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1728 | |
| _FDD_AttenTbIX_MtrRadpS_u12p4[1] | 1760 | |
| FDD_AttenTblY_Uls_u8p8[0] | 166 | |
| FDD_AttenTblY_Uls_u8p8[1] | 166 | |
| _FDD_BlendTblY_Uls_u8p8[0] | 15 | |
| _FDD_BlendTblY_Uls_u8p8[1] | 18 | |
| FDD_BlendTblY_Uls_u8p8[2] | 20 | |
| FDD_BlendTblY_Uls_u8p8[3] | 23 | |
| FDD_BlendTblY_Uls_u8p8[4] | 26 | |
| FDD_BlendTblY_Uls_u8p8[5] | 28 | |
| FDD_BlendTblY_Uls_u8p8[6] | 31 | |
| | 33 | |
| _FDD_BlendTblY_Uls_u8p8[7] FDD_BlendTblY_Uls_u8p8[8] | | |
| _FDD_BlendTblY_Uls_u8p8[8] | 36 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 38 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 41 | |
| _FDD_BlendTblY_Uls_u8p8[11] | | |

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| Name | Input Value | | |
|--|--------------|---------------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 282 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 61 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[1] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 65 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 69 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 70 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 72 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 73 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 74 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 76 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 6554 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[3] | 11469 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[4] | 13107 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 1894 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 1920 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1946 | | |
| t WIRBIndTbIX MtrNm u8p8[3] | 1971 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1997 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -1.6 | | |
| tgt FrgDepDmpnInrtCmp Per1 CRFMotorVel MtrRadpS f32.value | -150.6 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -8.8 | | |
| tgt FrgDepDmpnInrtCmp Per1 VehicleLonAccel KphpS f32.value | 14.06 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 220.02 | | |
| tgt_FrqDepDmpnInrtCmp_Fer1_WIRCmdAmpBlnd_MtrNm_f32.value | 0 | | |
| tgt_riqDepDmpnlintCmp_rei1_virkCmdxmpbilid_witNni_i32.vaide tgt_Rte_Inst_Ap_FrqDepDmpnlnrtCmp.FrqDepDmpnlnrtCmp_Per1_BaseAssistC | | 1 RaseAssistCmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCtgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVe | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDm | | | |
| tgt_Rte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnInrtCmp_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_I | | | |
| | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpae | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpee | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAm | | 1_WIRCmdAmpBInd_MtrNm_f32 | = - |
| Name | Actual Value | Expected Value | Result |
| PreDecelGain Uls M f32 | 127319 383 | 127319 3849 + 0 0625 | |

| | h .ahhh | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127319.383 | 127319.3849 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 527959.5 | 527959.4157 ± 0.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -135.810211 | -135.810175 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -3.5 | -3.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -49.2000008 | -49.2 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -6.76923132 | -6.769230769 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -0.96496433 | -0.964892308 ± 0.00390625 | ~ |
| tot FroDenDmonInrtCmp Per1 FroDenDmonInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| T · | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | ~ |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Proceeding My 12 | T+ 04 2 00 (B+ 0+ -4) | a de la companya de |
|--|---|---|
| Prescribedation (July Miner, M. 52) Prest Station (July Miner, M | Test Step 3.22 (Repeat Count = 1) | Y |
| Prest Standword Rays M. 192 Prest Prest M. 192 Prest Standword Rays M. 192 Prest Prest Prest M. 192 Prest P | | · |
| Pervisitation Ministry 182 Pervisitation Ministry 183 Pervisitation Ministr | PreDecelGain_Uls_M_f32 | |
| Price/Pres/Stort/Ample Michael M. 182 24 | Prev1PreAttnComp_MtrNm_M_f32 | |
| Prev25abr/Lehroly M2 Re_Inst_Ap_FrqDepDmpninrCmp Re_Inst_Ap_FrqDepDmpninrCmpninrCmp Re_Inst_Ap_FrqDepDmpninrCmp Re_Inst_Ap_FrqDepDmpninrCmp Re_Inst_Ap_FrqDepDmpninrCmpninrCmpninrCmpninrCmp Re_Inst_Ap_FrqDepDmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpninrCmpn | Prev1SclDrvVel_RadpS_M_f32 | 22.3 |
| Pictro Tarking Hobeg M. 12 | Prev2PreAttnComp_MtrNm_M_f32 | 2.4 |
| NR_Inst_A_FingDepDemoninCrop | Prev2SclDrvVel_RadpS_M_f32 | 115.2 |
| TranVerFills, M., str. X. U.s. G2 0.3366 k. CmnTsycKinRatio. MirDegotHvDeg_132 c. DmpGotGonTreber, KyploS_132 c. DmpGotGonTreber, KyploS_132 c. DmpGotGonTreber, KyploS_132 c. DmpGotGonTreber, KyploS_132 c. Indrom_Mirther SpaleS_132 c. Indrom_Mirther SpaleS_133 c. Indrom_ | PrevTbarAng_HwDeg_M_f32 | 3.403 |
| Transverifies, M. et K. U. B. D. 2 L. Coms Transfer, Mynope, 192 L. More, Mynope, 192 L. Indian, Mynope, 192 L. Indian, Mynope, 192 L. Indian, Mynope, 193 L. Indian, Mynope, 193 L. Lord, Mynope, L. Scale actor Lul, 192 L. P. D. ADDRailing Transfer, Mynope, 193 L. P. D. ADDRailing Transfer, 193 L. P. D. P. P. P. P. P. | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| K. Com/Spácific Horn Michael De State 20 | TbarVelFiltSv_M_str.SV_Uls_f32 | 2.6 |
| Combresial Number 12 | TbarVelFiltSv_M_str.K_Uls_f32 | 0.3366 |
| | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 26.03 |
| k_ DmpGainOffThreets, KphpS_172 2.2 k_ DmpGainOffThreets, KphpS_172 8.5 k_ IntrCmp_Mitterias, KphpS_172 1 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[0] 523 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[1] 1038 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[1] 1038 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[4] 2883 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[6] 3014 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[7] 4129 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[7] 4129 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[8] 4044 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(0)[9] 5159 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(1)[1] 1038 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(1)[1] 1038 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(1)[1] 268 12, FDD_ADDRollingTbMM_MitterpRadpS_um1p17(1)[1] 268 12, FDD_ADDRol | k_CmnTbarStiff_NmpDeg_f32 | 2.7 |
| k_ DmpGainOfThreab_Kpbs_J32 k_ DmpGainOfThreab_Kpbs_J32 k_ DmpGainOfThreab_Kpbs_J32 k_ DmpGainOfThreab_Kpbs_J32 k_ IntrCmp_MtVel_ScaleFactor_Uis_J32 k_ IntrCmp_MtVel_ScaleFactor_Uis_J32 k_ IntrCmp_MtVel_ScaleFactor_Uis_J32 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[0) 523 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1038 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1038 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1038 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1038 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1039 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1039 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1039 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1039 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1040 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(0)[1) 1050 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(1)[1) 1038 1050 l_ FDD_ADDRollingTbVM_MthmpRadps_umtp17(1)[1] 1038 1050 1050 1050 1050 1050 1050 1050 105 | k_DmpDecelGainFSlew_UlspS_f32 | 1800.06 |
| k, ImmComp. Mritherial. KymSrq_ris2 | k_DmpDecelGain_Uls_f32 | 2.2 |
| Intricmp_Mirrierita_Kgmsq_rs2 | k_DmpGainOffThresh_KphpS_f32 | 20.3 |
| LintChop_MitVel_ScaleFactor_Uis_52 | k_DmpGainOnThresh_KphpS_f32 | 8.5 |
| 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 523 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 1038 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 1553 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 2688 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 2688 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 3099 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 4129 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 5159 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 523 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 523 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 1038 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 1038 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 2683 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 3099 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 3014 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 4129 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 4129 2 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1) 4129 2 FDD_FORTPMM_MtrNmpRadpS_um1p17(1) 4000 | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00032 |
| 12 FDD_ADDRollingTbYM_MrNmpRadpS_um1p17(0)[1] 1038 1553 1 | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 1 |
| 12 FDD _ADDRollingTbYM_MrkmpRadpS_um1p17(0)[1] 1038 | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][0] | 523 |
| 12 FDD_ADDRollingTbYM_MrNnpRadpS_um1p17(0)[3] 2068 2 FDD_ADDRollingTbYM_MrNnpRadpS_um1p17(0)[4] 2883 2 FDD_ADDRollingTbYM_MrNnpRadpS_um1p17(0)[6] 3099 3091 | | 1038 |
| 12 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][3] 268 22 FDD_ADDRollingTbYM_MmmpRadpS_um1p17[0][4] 2583 2590 | | 1553 |
| 2 FDD_ADDRollingTbYM_MrhmpRadpS_um1p17[0][4] 2583 2590 25 | | |
| 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[5] 3099 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[6] 3614 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[7] 4129 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[8] 4644 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[8] 4644 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[9] 5159 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[9] 523 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[1] 1038 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[1] 1553 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[1] 1553 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[1] 2688 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[8] 2688 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[8] 3099 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[8] 3614 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_FreqTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_FreqTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_FreqTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 12 FDD_FreqTbYM_MtrNmpRadpS_um1p17(p)[9] 3614 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3814 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4129 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 4129 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5159 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 523 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1038 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1038 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1553 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 258 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 258 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3614 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3614 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4129 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4644 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_FreqTblYM_Hz_u12p4[0][1] 96 12_FDD_FreqTblYM_Hz_u12p4[0][1] 128 12_FDD_FreqTblYM_Hz_u12p4[0][1] 128 12_FDD_FreqTblYM_Hz_u12p4[0][6] 144 12_FDD_FreqTblYM_Hz_u12p4[0][6] 146 12_FDD_FreqTblYM_Hz_u12p4[0][6] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 129 12_FDD_FreqTblYM_Hz_u12p4[0][6] 129 12_FDD_FreqTblYM_Hz_u12p4[0][6] 224 12_FDD_FreqTblYM_Hz_u12p4[0][6] 240 12_FDD_FreqTblYM_Hz_u12p4[0][6] 360 12_FDD_FreqTblYM | | |
| 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][7] 4129 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][8] 4644 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[0][9] 5159 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][0] 523 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][1] 1038 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][2] 1553 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][4] 2583 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][4] 2583 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][6] 3614 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][7] 4129 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][7] 4129 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][8] 4644 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_FreqTbiYM_Hz_u12p4[0][0] 80 12_FDD_FreqTbiYM_Hz_u12p4[0][1] 12 12_FDD_FreqTbiYM_Hz_u12p4[0][2] 12 12_FDD_FreqTbiYM_Hz_u12p4[0][3] 128 12_FDD_FreqTbiYM_Hz_u12p4[0][3] 128 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 160 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 176 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 176 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 208 12_F | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0] 8 5159 | | |
| 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 9 5159 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 0 523 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 1 1038 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 2 1553 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 3 2068 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 4 2583 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 6 3614 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 7 4129 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 7 4129 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 8 4644 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 9 5159 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17() 9 5159 12_FDD_ADDRollingTblYM_Hz_u12p4(0) 0 80 12_FDD_FreqTblYM_Hz_u12p4(0) 1 96 12_FDD_FreqTblYM_Hz_u12p4(0) 3 128 12_FDD_FreqTblYM_Hz_u12p4(0) 3 128 12_FDD_FreqTblYM_Hz_u12p4(0) 5 160 12_FDD_FreqTblYM_Hz_u12p4(0) 6 176 12_FDD_FreqTblYM_Hz_u12p4(0) 6 176 12_FDD_FreqTblYM_Hz_u12p4(0) 6 240 12_FDD_FreqTblYM_Hz_u12p4(0) 6 32 12_FDD_FreqTblYM_H | | |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 0 523 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 1 1038 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 2 1553 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 3 2068 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 4 2583 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 6 3099 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 6 3614 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 6 3614 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 6 4644 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1] 8 4644 12_FDD_FacqTb\YM_Hz_u12p4[0] 0 80 12_FDD_FreqTb\YM_Hz_u12p4[0] 0 112 12_FDD_FreqTb\YM_Hz_u12p4[0] 1 96 12_FDD_FreqTb\YM_Hz_u12p4[0] 3 128 12_FDD_FreqTb\YM_Hz_u12p4[0] 4 144 12_FDD_FreqTb\YM_Hz_u12p4[0] 6 176 12_FDD_FreqTb\YM_Hz_u12p4[0] 6 176 12_FDD_FreqTb\YM_Hz_u12p4[0] 8 224 12_FDD_FreqTb\YM_Hz_u12p4[0] 8 224 12_FDD_FreqTb\YM_Hz_u12p4[0] 9 224 12_FDD_FreqTb\YM_Hz_u12p4[0] 10 240 12_FDD_FreqTb\YM_Hz_u12p4[0] 11 256 12_FDD_FreqTb\YM_Hz_u12p4[0] 11 48 12_FDD_FreqTb\YM_Hz_u12p4[0] 12 48 12_FDD_FreqTb\YM_ | | |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][1] 1038 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][2] 1553 2068 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][3] 2068 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][4] 2583 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][5] 3099 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 3614 4129 4 | | |
| 1553 1553 1553 1553 1553 1553 1553 1553 1553 1555 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2068 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2583 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3099 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3614 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4129 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4644 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_FORD_FORD_FORD_FORD_FORD_FORD_FORD_ | | |
| 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][4] 2583 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][5] 3099 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][6] 3614 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][7] 4129 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][8] 4644 46 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 5 3099 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 6 3614 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 7 4129 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 8 4644 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1] 9 5159 12_FDD_FreqTblYM_Hz_u12p4[0] 0 80 12_FDD_FreqTblYM_Hz_u12p4[0] 2 112 12_FDD_FreqTblYM_Hz_u12p4[0] 3 128 12_FDD_FreqTblYM_Hz_u12p4[0] 3 144 12_FDD_FreqTblYM_Hz_u12p4[0] 5 160 12_FDD_FreqTblYM_Hz_u12p4[0] 6 176 12_FDD_FreqTblYM_Hz_u12p4[0] 6 176 12_FDD_FreqTblYM_Hz_u12p4[0] 8 208 12_FDD_FreqTblYM_Hz_u12p4[0] 8 224 12_FDD_FreqTblYM_Hz_u12p4[0] 9 224 12_FDD_FreqTblYM_Hz_u12p4[0] 9 226 12_FDD_FreqTblYM_Hz_u12p4[0] 11 256 12_FDD_FreqTblYM_Hz_u12p4[1] 0 32 12_FDD_FreqTblYM_Hz_u12p4[1] 11 48 12_FDD_FreqTblYM_Hz_u12p4[1] 11 48 12_FDD_FreqTblYM_Hz_u12p4[1] 2 64 | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] 3614 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] 4129 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4644 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 80 12_FDD_FreqTbIYM_Hz_u12p4[0][2] 112 12_FDD_FreqTbIYM_Hz_u12p4[0][3] 128 12_FDD_FreqTbIYM_Hz_u12p4[0][4] 144 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 160 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 176 12_FDD_FreqTbIYM_Hz_u12p4[0][7] 192 12_FDD_FreqTbIYM_Hz_u12p4[0][8] 208 12_FDD_FreqTbIYM_Hz_u12p4[0][9] 224 12_FDD_FreqTbIYM_Hz_u12p4[0][10] 240 12_FDD_FreqTbIYM_Hz_u12p4[0][11] 256 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 32 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 48 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 48 12_FDD_FreqTbIYM_Hz_u12p4[1][1] 48 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadps_um1p17[1][7] | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4644 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_FreqTblYM_Hz_u12p4[0][0] 80 12_FDD_FreqTblYM_Hz_u12p4[0][1] 96 12_FDD_FreqTblYM_Hz_u12p4[0][3] 112 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][11] 256 12_FDD_FreqTblYM_Hz_u12p4[1][0] 32 12_FDD_FreqTblYM_Hz_u12p4[1][1] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5159 12_FDD_FreqTblYM_Hz_u12p4[0][0] 80 12_FDD_FreqTblYM_Hz_u12p4[0][2] 112 12_FDD_FreqTblYM_Hz_u12p4[0][3] 128 12_FDD_FreqTblYM_Hz_u12p4[0][4] 144 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][6] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][10] 256 12_FDD_FreqTblYM_Hz_u12p4[0][10] 32 12_FDD_FreqTblYM_Hz_u12p4[1][0] 48 12_FDD_FreqTblYM_Hz_u12p4[1][1] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_FreqTb YM_Hz_u12p4[0][0] 80 12_FDD_FreqTb YM_Hz_u12p4[0][1] 96 12_FDD_FreqTb YM_Hz_u12p4[0][2] 112 12_FDD_FreqTb YM_Hz_u12p4[0][3] 128 12_FDD_FreqTb YM_Hz_u12p4[0][4] 144 12_FDD_FreqTb YM_Hz_u12p4[0][6] 160 12_FDD_FreqTb YM_Hz_u12p4[0][6] 176 12_FDD_FreqTb YM_Hz_u12p4[0][7] 192 12_FDD_FreqTb YM_Hz_u12p4[0][8] 208 12_FDD_FreqTb YM_Hz_u12p4[0][8] 208 12_FDD_FreqTb YM_Hz_u12p4[0][9] 224 12_FDD_FreqTb YM_Hz_u12p4[0][10] 240 12_FDD_FreqTb YM_Hz_u12p4[0][11] 256 12_FDD_FreqTb YM_Hz_u12p4[1][0] 32 12_FDD_FreqTb YM_Hz_u12p4[1][0] 48 12_FDD_FreqTb YM_Hz_u12p4[1][1] 48 12_FDD_FreqTb YM_Hz_u12p4[1][2] 64 | | |
| 12 FDD FreqTb YM Hz u12p4[0][1] 96 12 FDD FreqTb YM Hz u12p4[0][2] 112 12 FDD FreqTb YM Hz u12p4[0][3] 128 12 FDD FreqTb YM Hz u12p4[0][4] 144 12 FDD FreqTb YM Hz u12p4[0][6] 176 12 FDD FreqTb YM Hz u12p4[0][7] 192 12 FDD FreqTb YM Hz u12p4[0][8] 208 12 FDD FreqTb YM Hz u12p4[0][9] 224 12 FDD FreqTb YM Hz u12p4[0][10] 240 12 FDD FreqTb YM Hz u12p4[0][11] 256 12 FDD FreqTb YM Hz u12p4[1][0] 32 12 FDD FreqTb YM Hz u12p4[1][0] 32 12 FDD FreqTb YM Hz u12p4[1][1] 48 12 FDD FreqTb YM Hz u12p4[1][2] 64 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 160 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][11] 256 12_FDD_FreqTblYM_Hz_u12p4[1][0] 32 12_FDD_FreqTblYM_Hz_u12p4[1][0] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][6] 176 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][11] 256 12_FDD_FreqTblYM_Hz_u12p4[1][0] 32 12_FDD_FreqTblYM_Hz_u12p4[1][1] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] 192 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][11] 256 12_FDD_FreqTblYM_Hz_u12p4[1][0] 32 12_FDD_FreqTblYM_Hz_u12p4[1][1] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][8] 208 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][11] 256 12_FDD_FreqTblYM_Hz_u12p4[1][0] 32 12_FDD_FreqTblYM_Hz_u12p4[1][1] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][9] 224 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 240 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 256 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 32 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 48 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][10] 240 12_FDD_FreqTblYM_Hz_u12p4[0][11] 256 12_FDD_FreqTblYM_Hz_u12p4[1][0] 32 12_FDD_FreqTblYM_Hz_u12p4[1][1] 48 12_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 256 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 32 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 48 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 32 t2_FDD_FreqTblYM_Hz_u12p4[1][1] 48 t2_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 48 64 64 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 64 | | |
| , , , , , | t2_FDD_FreqTblYM_Hz_u12p4[1][1] | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 80 | t2_FDD_FreqTblYM_Hz_u12p4[1][2] | |
| | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 80 |

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| Name | Input Value | |
|--|-------------|--|
| Name 2 FDD FreqTbIYM Hz u12p4[1][4] | 96 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 208 | |
| CmnVehSpd_Kph_u9p7[0] | 3968 | |
| CmnVehSpd_Kph_u9p7[1] | 4096 | |
| _CmnVehSpd_Kph_u9p7[2] | 4224 | |
| _CmnVehSpd_Kph_u9p7[3] | 4352 | |
| _CmnVehSpd_Kph_u9p7[4] | 4480 | |
| _CmnVehSpd_Kph_u9p7[5] | 4608 | |
| _CmnVehSpd_Kph_u9p7[6] | 4736 | |
| _CmnVehSpd_Kph_u9p7[7] | 4864 | |
| CmnVehSpd_Kph_u9p7[8] | 4992 | |
| CmnVehSpd_Kph_u9p7[9] | 5120 | |
| CmnVehSpd_Kph_u9p7[10] | 5248 | |
| CmnVehSpd_Kph_u9p7[11] | 5376 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 8602 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 9011 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 9421 | |
| DmpADDCoefX_MtrNm_u4p12[3] | 9830 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 10240 | |
| DmpADDCoefX_MtrNm_u4p12[4] | 10650 | |
| DmpADDCoefX_MtrNm_u4p12[6] | 11059 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 11469 | |
| _DmpADDCcetX_MtrNm_u4p12[8] | 11878 | |
| | | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 12288 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 384 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 392 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 400 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 408 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 416 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 424 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[3] | 661 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1776 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 1840 | |
| _FDD_AttenTblY_Uls_u8p8[0] | 189 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 191 | |
| FDD_BlendTblY_Uls_u8p8[0] | 18 | |
| FDD_BlendTblY_Uls_u8p8[1] | 20 | |
| FDD_BlendTblY_Uls_u8p8[2] | 23 | |
| FDD_BlendTblY_Uls_u8p8[3] | 26 | |
| FDD_BlendTblY_Uls_u8p8[4] | 28 | |
| _FDD_BlendTblY_Uls_u8p8[5] | 31 | |
| _FDD_BlendTblY_Uls_u8p8[6] | 33 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 36 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 38 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 41 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 44 | |
| o.o _ ao po[10] | * * | |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 307 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 77 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 78 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 79 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 81 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 82 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 83 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 84 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 86 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 87 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 88 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 90 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 91 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 8192 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 9830 | | |
| t RIAstWIRBIndTblY Uls u2p14[2] | 11469 | | |
| t RIAstWIRBIndTblY Uls u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 14746 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 1178 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 1203 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1229 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1254 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1280 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 1.1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 250.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 9.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 15.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 230.03 | | |
| tgt FrqDepDmpnInrtCmp Per1 WIRCmdAmpBlnd MtrNm f32.value | 8.8 | | |
| tgt_FtqDepDmprimitCmp_Ferr_wikCmbAmpbilid_intitAm_i32.value tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCm | | | |
| tgt Rte Inst Ap FrgDepDmpnInrtCmp.FrgDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Fe11_FrqDepDmpnI tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Fe11_Hw10ique_Hw tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt_Rte_inst_Ap_FrqDepDmpnintCmp.FrqDepDmpnintCmp_Fer1_venicleConAct tgt_Rte_inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VenicleSpeed | | | |
| | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | | | |
| Name | Actual Value | Expected Value | Result |

| 9 | | | |
|--|--------------|--------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 8783.39941 | 8783.39988 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -3935.75269 | -3935.753195 ± 0.009 | • |
| Prev1SclDrvVel_RadpS_M_f32 | 250.816666 | 250.8166781 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 4.5 | 4.5 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 22.2999992 | 22.3 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 3.40740728 | 3.407407407 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 2.46656632 | 2.466606667 ± 0.00390625 | ~ |
| tat FraDepDmpnInrtCmp Per1 FraDepDmpnInrtCmp MtrNm f32 value | -8 80000019 | -8.8 + 0.00048828125 | ✓ |



| T | | | | |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | - |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | - |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.23 (Repeat Count = 1) | · · |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 45678 |
| Prev1PreAttnComp_MtrNm_M_f32 | 4.5 |
| Prev1ScIDrvVel RadpS M f32 | -48.5 |
| Prev2PreAttnComp_MtrNm_M_f32 | -1.1 |
| Prev2ScIDrvVel_RadpS_M_f32 | -380.2 |
| PrevTbarAng_HwDeg_M_f32 | -3.06 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.4488 |
| k CmnSysKinRatio MtrDegpHwDeg f32 | 53.25 |
| k CmnTbarStiff NmpDeg f32 | 3.1 |
| k DmpDecelGainFSlew UlspS f32 | 1900.08 |
| k_DmpDecelGain_Uls_f32 | 2.6 |
| k_DmpGainOffThresh_KphpS_f32 | 22.5 |
| k_DmpGainOnThresh_KphpS_f32 | 16.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00033 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.7 |
| | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 814 |
| | 924 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1144 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 1475 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1695 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 523 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 1038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1553 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 2068 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 2583 |
| t2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] | 3099 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 3614 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 4129 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 4644 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 5159 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 128 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 144 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 160 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 176 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 192 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 208 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 224 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 240 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 256 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 272 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 48 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 96 |

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| FrqDepDmpnInrtCmp_Per1 | | TAZOI(AL |
|---|-------------|----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 112 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 128 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 224 | |
| CmnVehSpd_Kph_u9p7[0] | 128 | |
| CmnVehSpd_Kph_u9p7[1] | 256 | |
| CmnVehSpd_Kph_u9p7[2] | 384 512 | |
| CmnVehSpd_Kph_u9p7[3] | | |
| CmnVehSpd_Kph_u9p7[4] | 640 768 | |
| CmnVehSpd_Kph_u9p7[5] | 896 | |
| CmnVehSpd_Kph_u9p7[6] | 1024 | |
| CmnVehSpd_Kph_u9p7[7] CmnVehSpd_Kph_u9p7[8] | 1152 | |
| CmnVehSpd_Kph_u9p7[9] | 1280 | |
| CmnVehSpd_Kph_u9p7[10] | 1408 | |
| CmnVehSpd_Kph_u9p7[11] | 1536 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 12698 | |
| DmpADDCoefX_MtrNm_u4p12[1] | 13107 | |
| DmpADDCoefX_MtrNm_u4p12[1] DmpADDCoefX_MtrNm_u4p12[2] | 13517 | |
| DmpADDCoefX_MtrNm_u4p12[3] | 13926 | |
| DmpADDCoefX_MtrNm_u4p12[4] | 14336 | |
| DmpADDCoefX_MtrNm_u4p12[5] | 14746 | |
| DmpADDCoefX MtrNm u4p12[6] | 15155 | |
| DmpADDCoefX_MtrNm_u4p12[7] | 15565 | |
| DmpADDCoefX_MtrNm_u4p12[8] | 15974 | |
| DmpADDCoefX_MtrNm_u4p12[9] | 16384 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752 | |
| DmpDecelGainSlewY_UlspS_u13p3[0] | 448 | |
| DmpDecelGainSlewY UlspS u13p3[1] | 456 | |
| DmpDecelGainSlewY_UlspS_u13p3[2] | 464 | |
| DmpDecelGainSlewY_UlspS_u13p3[3] | 472 | |
| DmpDecelGainSlewY UlspS u13p3[4] | 480 | |
| DmpDecelGainSlewY_UlspS_u13p3[5] | 488 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | |
| DmpFiltKpWIRBIndY Uls u2p14[1] | 4915 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | |
| DmpFiltKpWIRBIndY Uls u2p14[3] | 8192 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[0] | 342 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 683 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[2] | 1024 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 1364 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 1705 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 2046 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 2387 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 2728 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 3068 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 3409 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1760 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 1920 | |
| FDD_AttenTblY_Uls_u8p8[0] | 237 | |
| FDD_AttenTblY_Uls_u8p8[1] | 239 | |
| FDD_BlendTblY_Uls_u8p8[0] | 20 | |
| FDD_BlendTblY_Uls_u8p8[1] | 23 | |
| FDD_BlendTblY_Uls_u8p8[2] | 26 | |
| FDD_BlendTblY_Uls_u8p8[3] | 28 | |
| FDD_BlendTblY_Uls_u8p8[4] | 31 | |
| FDD_BlendTblY_Uls_u8p8[5] | 33 | |
| FDD_BlendTblY_Uls_u8p8[6] | 36 | |
| FDD_BlendTblY_Uls_u8p8[7] | 38 | |
| FDD_BlendTblY_Uls_u8p8[8] | 41 | |
| FDD_BlendTblY_Uls_u8p8[9] | 44 | |
| FDD_BlendTblY_Uls_u8p8[10] | 46 | |
| FDD_BlendTblY_Uls_u8p8[11] | 49 | |

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| Name | Input Value | | |
|--|--------------|--|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 346 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 92 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[1] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 97 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 99 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 100 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 101 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 102 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 104 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 105 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 106 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 1638 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 3277 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[2] | 4915 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 | | |
| t WIRBIndTblX MtrNm u8p8[0] | 1434 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 1459 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1485 | | |
| t WIRBIndTbIX MtrNm u8p8[3] | 1510 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 1536 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -1.1 | | |
| tgt FrqDepDmpnInrtCmp Per1 CRFMotorVel MtrRadpS f32.value | -250.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -9.5 | | |
| tgt FrqDepDmpnInrtCmp Per1 VehicleLonAccel KphpS f32.value | 35.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 240.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 5.5 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistC | | r1 BaseAssistCmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorV | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDm | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmp | | : : : : : = = = : | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_I | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLon/ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpe | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAm | | r1_WIRCmdAmpBlnd_MtrNm_f32 | |
| Name | Actual Value | | Result |
| PreDecelGain Uls M f32 | 45674 1992 | Expected Value 45674 19984 + 0.0625 | Result |
| | | | |

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------------|----------|
| PreDecelGain_Uls_M_f32 | 45674.1992 | 45674.19984 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 57899.4453 | 57899.44082 ± 0.09 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -176.861588 | -176.8615543 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -4.5 | -4.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -48.5 | -48.5 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -3.06451631 | -3.064516129 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.39147186 | -2.391419355 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmp Per1 FraDenDmonInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| Τ | | | | V |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.24 (Repeat Count = 1) | |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 127628.71 |
| Prev1PreAttnComp_MtrNm_M_f32 | 6.5 |
| Prev1SclDrvVel_RadpS_M_f32 | 163.6 |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.1 |
| Prev2ScIDrvVel_RadpS_M_f32 | 175.3 |
| PrevTbarAng_HwDeg_M_f32 | 1.154 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.2 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.5599 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 27.06 |
| k_CmnTbarStiff_NmpDeg_f32 | 1.3 |
| k_DmpDecelGainFSlew_UlspS_f32 | 200.09 |
| k_DmpDecelGain_Uls_f32 | 2.8 |
| k_DmpGainOffThresh_KphpS_f32 | 22.2 |
| k_DmpGainOnThresh_KphpS_f32 | 24.6 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00034 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.6 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 885 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 986 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1087 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1188 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1288 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1389 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][6] | 1490 |
| t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][7] | 1591 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 1692 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 1793 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 704 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 814 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 924 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1034 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1144 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1254 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1364 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1475 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1585 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1695 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 336 |
| t2 FDD FregTblYM Hz u12p4[0][1] | 352 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 368 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 384 |
| t2 FDD FreqTblYM Hz u12p4[0][4] | 400 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 416 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 432 |
| t2 FDD FreqTblYM Hz u12p4[0][7] | 448 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 464 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 480 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 496 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 512 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 64 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 112 |
| (E_1 DD_1 TOQ TOTTNI_TIZ_U TEPT[1][0] | 116 |

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| гідоеротріптістр_гегі | |
|--|-------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 128 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 144 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 160 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 176 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 192 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 208 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 224 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 240 |
| _CmnVehSpd_Kph_u9p7[0] | 2560 |
| _CmnVehSpd_Kph_u9p7[1] | 3840 |
| _CmnVehSpd_Kph_u9p7[2] | 5120 |
| CmnVehSpd Kph u9p7[3] | 6400 |
| _CmnVehSpd_Kph_u9p7[4] | 7680 |
| _CmnVehSpd_Kph_u9p7[5] | 8960 |
| _CmnVehSpd_Kph_u9p7[6] | 10240 |
| _CmnVehSpd_Kph_u9p7[7] | 11520 |
| _CmnVehSpd_Kph_u9p7[8] | 12800 |
| | 14080 |
| _CmnVehSpd_Kph_u9p7[9] | 15360 |
| _CmnVehSpd_Kph_u9p7[10] | |
| _CmnVehSpd_Kph_u9p7[11] | 16640 |
| _DmpADDCoefX_MtrNm_u4p12[0] | 16794 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 17203 |
| _DmpADDCoefX_MtrNm_u4p12[2] | 17613 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 18022 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 18432 |
| _DmpADDCoefX_MtrNm_u4p12[5] | 18842 |
| _DmpADDCoefX_MtrNm_u4p12[6] | 19251 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 19661 |
| _DmpADDCoefX_MtrNm_u4p12[8] | 20070 |
| _DmpADDCoefX_MtrNm_u4p12[9] | 20480 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 27264 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 27296 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 27328 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 27360 |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 27392 |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 27424 |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 680 |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 688 |
| DmpDecelGainSlewY UlspS u13p3[2] | 696 |
| DmpDecelGainSlewY UlspS u13p3[3] | 704 |
| | 712 |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 720 |
| _DmpFiltKpWIRBIndY_UIs_u2p14[0] | 8192 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 9830 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 11469 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 13107 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 14746 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[1] | 328 |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1760 |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 2000 |
| _FDD_AttenTblY_Uls_u8p8[0] | 49 |
| _FDD_AttenTblY_Uls_u8p8[1] | 51 |
| | 49 |
| FDD_BlendTblY_Uls_u8p8[0] | |
| FDD_BlendTblY_Uls_u8p8[1] | 51 |
| _FDD_BlendTblY_Uls_u8p8[2] | 54 |
| _FDD_BlendTbIY_Uls_u8p8[3] | 57 |
| _FDD_BlendTblY_Uls_u8p8[4] | 60 |
| _FDD_BlendTblY_Uls_u8p8[5] | 63 |
| _FDD_BlendTblY_Uls_u8p8[6] | 66 |
| _FDD_BlendTblY_Uls_u8p8[7] | 68 |
| FDD_BlendTblY_Uls_u8p8[8] | 71 |
| FDD_BlendTblY_Uls_u8p8[9] | 74 |
| | |
| _FDD_BlendTblY_Uls_u8p8[10] | 77 |

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| Name | Input Value | | |
|---|--|---------------|--|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 256 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 320 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 333 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 346 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 358 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 1 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 3 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 4 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 5 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 6 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 8 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 9 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 10 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 12 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 13 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 14 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 15 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[0] | 3277 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 9830 | | |
| t_WIRBIndTblX_MtrNm_u8p8[0] | 1690 | | |
| t_WIRBIndTblX_MtrNm_u8p8[1] | 1715 | | |
| t_WIRBIndTblX_MtrNm_u8p8[2] | 1741 | | |
| t_WIRBIndTblX_MtrNm_u8p8[3] | 1766 | | |
| t WIRBIndTblX MtrNm u8p8[4] | 1792 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 2.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 450.25 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 1.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -35.06 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 260.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 6.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCmc | tgt_FrqDepDmpnInrtCmp Per1 BaseAssist0 | Cmd_MtrNm_f32 | |
| tat Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel I | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSr | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hwf | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcce | | | |
| | | | |
| tgt Rte inst Ap FrquepumpnintCmp.FrquepumpnintCmp Per1 VenicleSpeed i | i igi i iqbepbilipilililicilib Feli velilciesbet | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_I tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBI | | | |

| 2 | | | |
|--|--------------|--------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127628.313 | 127628.3098 ± 0.0625 | • |
| Prev1PreAttnComp_MtrNm_M_f32 | -25875.293 | -25875.2916 ± 0.09 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 270.225586 | 270.2255612 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | 6.5 | 6.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 163.600006 | 163.6 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 1.15384614 | 1.153846154 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.36523604 | 1.365250769 ± 0.00390625 | ~ |
| tat FraDepDmpnInrtCmp Per1 FraDepDmpnInrtCmp MtrNm f32 value | -8 80000019 | -8.8 + 0.00048828125 | ✓ |



| T | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 3.25 (Repeat Count = 1) | |
|---|-----------------------------------|
| Name | Input Value |
| PreDecelGain_Uls_M_f32 | 127730.685 |
| Prev1PreAttnComp_MtrNm_M_f32 | -6.5 |
| Prev1SclDrvVel_RadpS_M_f32 | -90.23 |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.1 |
| Prev2SclDrvVel_RadpS_M_f32 | -120.1 |
| PrevTbarAng_HwDeg_M_f32 | -0.554 |
| Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 4.5 |
| TbarVelFiltSv_M_str.K_Uls_f32 | 0.1258 |
| k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 26.02 |
| k_CmnTbarStiff_NmpDeg_f32 | 2.7 |
| k_DmpDecelGainFSlew_UlspS_f32 | 300.06 |
| k_DmpDecelGain_Uls_f32 | 3.5 |
| k_DmpGainOffThresh_KphpS_f32 | 33.2 |
| k_DmpGainOnThresh_KphpS_f32 | 32.2 |
| k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00035 |
| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.5 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1066 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1212 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] | 1359 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] | 1506 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] | 1653 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] | 1800 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | 1946 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | 2093 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | 2240 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] | 2387 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] | 885 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] | 986 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] | 1087 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] | 1188 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] | 1288 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] | 1389 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] | 1490 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] | 1591 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] | 1692 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] | 1793 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] | 656 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] | 672 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] | 688 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] | 704 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] | 720 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] | 736 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] | 752 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] | 768 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] | 784 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] | 800 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | 816 |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | 832 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | 80 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 96 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] | 112 |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 128 |

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|--|-------------|-----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 144 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 224 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 240 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 256 | |
| _CmnVehSpd_Kph_u9p7[0] | 6784 | |
| _CmnVehSpd_Kph_u9p7[1] | 6912 | |
| _CmnVehSpd_Kph_u9p7[2] | 7040 | |
| CmnVehSpd_Kph_u9p7[3] | 7168 | |
| _CmnVehSpd_Kph_u9p7[4] | 7296 | |
| CmnVehSpd_Kph_u9p7[5] | 7424 | |
| _CmnVehSpd_Kph_u9p7[6] | 7552 | |
| _CmnVehSpd_Kph_u9p7[7] | 7680 | |
| _CmnVehSpd_Kph_u9p7[8] | 7808 | |
| | 7936 | |
| _CmnVehSpd_Kph_u9p7[9] | 8064 | |
| _CmnVehSpd_Kph_u9p7[10] | | |
| _CmnVehSpd_Kph_u9p7[11] | 8192 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 20890 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 21299 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 21709 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 22118 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 22528 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 22938 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 23347 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 23757 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 24166 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 24576 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 9120 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 9152 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 9184 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 9216 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 9248 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 9280 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1536 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1544 | |
| DmpDecelGainSlewY_UlspS_u13p3[2] | 1552 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1560 | |
| _DmpDecelGainGlewY_UlspS_u13p3[4] | 1568 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1576 | |
| | 3277 | |
| _DmpFiltKpWlRBlndY_Uls_u2p14[0] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[2] | 494 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1920 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 2080 | |
| FDD_AttenTblY_Uls_u8p8[0] | 65 | |
| FDD_AttenTblY_Uls_u8p8[1] | 68 | |
| FDD_BlendTbIY_Uls_u8p8[0] | 65 | |
| FDD_BlendTblY_Uls_u8p8[1] | 68 | |
| FDD_BlendTblY_Uls_u8p8[2] | 70 | |
| | 73 | |
| FDD_BlendTblY_Uls_u8p8[3] | | |
| FDD_BlendTblY_Uls_u8p8[4] | 75 | |
| FDD_BlendTblY_Uls_u8p8[5] | 78 | |
| FDD_BlendTblY_Uls_u8p8[6] | 80 | |
| _FDD_BlendTbIY_Uls_u8p8[7] | 83 | |
| FDD_BlendTbIY_Uls_u8p8[8] | 86 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 88 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 91 | |
| | | |

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| Name | Input Value | | |
|--|--------------|----------------------|-------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 15 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 17 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 18 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 19 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 20 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 22 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 23 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 24 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 26 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 27 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 28 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 29 | | |
| t RIAstWIRBIndTbIY Uls u2p14[0] | 4915 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 6554 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 8192 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[3] | 9830 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[4] | 11469 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1894 | | |
| t WIRBIndTbIX MtrNm u8p8[1] | 1920 | | |
| t WIRBIndTbIX MtrNm u8p8[2] | 1946 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1971 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[4] | 1997 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -2.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -450.14 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -1.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 30.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 270.06 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 7.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | | | |
| Name | Actual Value | Expected Value | Resul |
| | 1111 | | Resul |
| PreDecelGain_Uls_M_f32 | 127730.086 | 127730.0849 ± 0.0625 | |

| 20 | | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127730.086 | 127730.0849 ± 0.0625 | ✓ |
| Prev1PreAttnComp_MtrNm_M_f32 | 44157.7891 | 44157.78752 ± 0.09 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -224.675308 | -224.6753087 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | -6.5 | -6.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -90.2300034 | -90.23 ± 0.00390625 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | -0.555555522 | -0.555555556 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 3.83605886 | 3.836055556 ± 0.00390625 | ~ |
| tat FraDepDmpnInrtCmp Per1 FraDepDmpnInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| T | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Name | T4 04-9 0.00 /D-94 094 - 4) | ه. |
|--|---|-----------------------------------|
| Preblacidian_Uis_M_132 | Test Step 3.26 (Repeat Count = 1) | · |
| Prest Pert SelDrive Ready S. M. 132 1100 2 | | |
| Prev15cDrVvII Radip S. M. 132 | | |
| Prev2sciDrvel_Ratips_M_R32 3.6 2 Prev1barkny_Hvbbg_M_R32 3.6 2 Prev1barkny_Hvbbg_M_R32 3.8 2 Prev1barkny_Hvbbg_M_R32 3.8 Rie_Inst_Ap_FrqDepDmpnintCmp 19_ Rie_Inst_Ap_FrqDepDmpnintCmp TharvierFillsy_M_R1x_UIs_R32 3.5 Land Nat Ky_UIs_R32 3.5 Land Nat Ky_UIS_R33 3.5 Land Nat Ky_UIS_R34 | | |
| PrevZschOve/L RadpS_M_132 36.2 | | |
| Piert Transing_HwDeg_M_R22 0.8 | Prev2PreAttnComp_MtrNm_M_f32 | |
| Re_Inst_Ap_FrqDepDmpnInfCmp | Prev2SclDrvVel_RadpS_M_f32 | |
| Thankellisty M. str. K. U. 18.72 | PrevTbarAng_HwDeg_M_f32 | 0.8 |
| Toan/velFillsy_M_str.K_Uls_fi2 0.2365 0.23 | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k_CmnSysKinRatio_MirDegpHwDeg_132 | TbarVelFiltSv_M_str.SV_Uls_f32 | -4.5 |
| K, CmnTbarSitff, NmpDeg_f32 3.1 k, DmpDeeclGain FSiew, UlspS_f32 200.02 k, DmpDeeclGain, UlspS_f32 3.9 k, DmpGainOrThresh, KshpS_f32 15.2 k, DmpGainOrThresh, KshpS_f32 40.2 k, InnCmp, Mirroria, AgmSq_f32 0.00036 k, InnCmp, Mirroria, SgmSq_f32 0.89 k, InnCmp, Mirroria, SgmSq_f32 0.89 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[0] 1246 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[1] 1638 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[2] 2030 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[3] 2422 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[6] 3598 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[6] 3598 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[6] 3598 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[7] 3990 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(0)[8] 4382 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(1)[0] 1066 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(1)[1] 1212 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(1)[4] 1653 12-FDD_ADDRollingTbYM_MirrympRadpS_um1p17(1 | TbarVelFiltSv_M_str.K_Uls_f32 | 0.2365 |
| K_DmpDecelGain_Uls_52 3.9 K_DmpDecelGain_Uls_52 3.9 K_DmpGainOrThresh_KphpS_132 15.2 K_DmpGainOrThresh_KphpS_132 40.2 K_IntCmp_Mtrineria_KgmSq_132 0.00036 K_IntCmp_Mtrineria_KgmSq_132 0.89 L_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[0] 1246 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[1] 1638 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[2] 2030 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[3] 2422 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[5] 3206 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[6] 398 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[6] 3990 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[6] 4382 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[6] 4382 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[6] 4382 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(0)[6] 4382 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(1)[6] 1066 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(1)[6] 1066 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(1)[6] 1653 12_FDD_ADDRollingTbYM_MtrimpRadpS_um1p17(1)[6] </td <td>k_CmnSysKinRatio_MtrDegpHwDeg_f32</td> <td>53.12</td> | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 53.12 |
| K_DmpDecelGain_Uls_[32] 3.9 K_DmpGainOrThresh_KphpS_132 15.2 K_DmpGainOrThresh_KphpS_172 40.2 K_Indromp_Mtrhesh_KphpS_172 0.00036 K_Indromp_Mtrhesh_KphpS_172 0.89 K_Indromp_Mtrhesh_KphpS_172 0.89 K_Indromp_Mtrhesh_KphpS_172 0.89 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0](1) 1246 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0](3) 2030 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0](3) 2422 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0)[4) 2814 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0)[7] 3900 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0)[7] 3990 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0)[8] 4382 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(0)[8] 4382 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(1)[8] 1259 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(1)[8] 1359 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(1)[8] 1559 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(1)[8] 1560 L2_FDD_ADDRollingTbYM_MtrhmpRadpS_umfp17(1)[8] 1653 L2_FDD_ADRollingTbYM_MtrhmpRadpS_umfp17(1)[8] | k_CmnTbarStiff_NmpDeg_f32 | 3.1 |
| k_ DmpGainOffTreeh_KphpS_132 15.2 k_ DmpGainOnThreeh_KphpS_132 40.2 k_ InftCmp_Mitrheita_KgmS_132 0.00036 k_ InftCmp_Mitrheita_KgmS_132 0.89 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][0] 1246 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][1] 1638 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][2] 2030 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][3] 2422 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][6] 358 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][6] 3588 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][7] 3990 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][8] 4382 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[0][8] 4382 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][9] 4774 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][9] 1066 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][1] 1212 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][1] 1506 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][1] 1600 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][1] 1600 12_ FDD_ADDRollingTbYM_MitrhmpRadpS_um1p17[1][1] | k_DmpDecelGainFSlew_UlspS_f32 | 200.02 |
| k_ DmpGainOnThreeh_KphpS_I32 40.2 k_ IntCmp_Mtrineria_KgmSq_132 0.00036 k_ IntCmp_Mtrineria_KgmSq_132 0.00036 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[0] 1246 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[1] 1638 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[3] 2422 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[4] 2814 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[6] 3598 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[6] 3598 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[7] 3990 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(0)[9] 4774 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[9] 4774 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[1] 1212 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[1] 122 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[1] 1859 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[1] 1800 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[6] 1800 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[6] 1946 12_FDD_ADDRollingTbVM_MtrNmpRadpS_um1p17(1)[6] 1946 | k_DmpDecelGain_Uls_f32 | 3.9 |
| k_IntCmp_MtrIvetIscaleFactor_UIs_f32 0.00036 k_IntCmp_MtrIvet_ScaleFactor_UIs_f32 0.89 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][0] 1246 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][2] 2030 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][2] 2030 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][6] 3242 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][6] 3206 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][6] 3598 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][7] 3990 12_FDD_ADDRollingTb1/M_MtrIvmpRadps_ump17[0][8] 4382 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[0][8] 4374 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][9] 4774 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][9] 1066 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][1] 112 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][4] 1653 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][4] 1653 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][6] 1946 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][6] 1946 12_FDD_ADDRollingTb1/M_MtrivmpRadps_ump17[1][6] 1946 12_FDD_ADDRollingTb1/M_Mtrivm | k_DmpGainOffThresh_KphpS_f32 | 15.2 |
| k_IntrCmp_MtrVel_ScaleFactor_Uis_f32 0.89 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][0] 1246 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][2] 2030 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][3] 2422 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][6] 296 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][6] 398 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][6] 3998 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][8] 4382 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[0][9] 4774 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][9] 4774 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][1] 1212 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][1] 1259 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][2] 1359 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][3] 1506 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][6] 1946 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][6] 1946 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][8] 2240 12_FDD_ADDRollingTbIYM_MtrNmpRadps_um1p17[1][8] 2387 12_FDD_FreqTbIYM_Hz_u12p4[0][0] 1296 12_FDD_FreqTbIYM_Hz_u12p4[0][1] | k_DmpGainOnThresh_KphpS_f32 | 40.2 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] 1246 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] 1638 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 2030 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2422 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 2814 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3598 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3598 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 3990 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4882 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 4774 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1066 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1212 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1212 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1506 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1666 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1666 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1666 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1666 | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00036 |
| 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[1] 1638 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[2] 2030 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[3] 2422 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[4] 2814 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[5] 3206 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[6] 3598 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[8] 3598 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[9] 4774 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[1] 1212 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[1] 1212 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[4] 1506 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[4] 1653 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[6] 1946 12_FDD_FreqTb\YM_Hz_u12p4(0)[6] 1296 12_FDD_FreqTb\YM_Hz_u12p4(0)[6] 1392 12_FDD_FreqTb\YM_Hz_u12p4(0)[7] 1408 12_FDD_FreqTb\YM_Hz_u12p4(0)[7] 1408 12_FDD_FreqTb\YM_Hz_u12p4(0)[7] 1408 12_FDD_FreqTb\YM_Hz_u12p4(| k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.89 |
| 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(12\) 2814 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(13\) 2814 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 3596 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 3598 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 3598 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 3820 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 3890 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 4882 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(0)\(16\) 4774 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(16\) 1666 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 1666 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 1899 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 1899 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 1890 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 1800 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 1800 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 2993 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 2993 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 2993 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 2993 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 2993 2_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17\(1)\(11\) 2993 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 312 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 312 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 328 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 312 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 312 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 312 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 3160 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(0)\(11\) 3180 2_FDD_FreqTb\YM_Hz_u12p4\(0)\(0)\(| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1246 |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[3] 2422 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[5] 3206 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[5] 3598 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[7] 3990 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[9] 4774 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(0)[9] 4774 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[0] 1066 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[1] 1212 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[2] 1359 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[3] 1506 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[4] 1653 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[6] 1800 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[6] 1946 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[7] 2093 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[7] 2093 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[7] 2387 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[8] 2240 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17(1)[8] 2387 12_FDD_FreqTb\YM_Hz_u12p4[0][0] 1296 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 1312 12_FDD_FreqTb\YM_Hz_u12p4[0][2] 1328 12_FDD_FreqTb\YM_Hz_u12p4[0][3] 1344 12_FDD_FreqTb\YM_Hz_u12p4[0][3] 1344 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 1408 12_F | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 1638 |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][3] 2422 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][5] 2814 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][5] 3206 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][6] 3598 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][7] 3990 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][8] 4382 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][9] 4774 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][0] 1066 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][1] 1212 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][3] 1506 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][3] 1506 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][4] 1653 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 1800 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 1946 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][7] 2093 12_FDD_FactDiryM_MtrNmpRadpS_um1p17[1][8] 2240 12_FDD_FactDFWM_MtrNmpRadpS_um1p17[1][9] 2387 12_FDD_FactDFWM_MtrNmpRadpS_um1p17[1][9] 1326 12_FDD_FactDFWM_Mtr_u12p4[0][1] 1328 12_FDD_FactDFWM_Mtr_u12p4[0][1] | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][2] | 2030 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][4] 2814 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][5] 3206 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][6] 3598 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][7] 3990 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][8] 4382 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][9] 4774 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][9] 4774 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][0] 1066 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][1] 1212 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][2] 1359 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][4] 1653 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][4] 1653 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][6] 1946 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][6] 1946 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][6] 1946 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 2240 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 2240 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 2240 2_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 2387 2_FDD_FreqTblYM_Hz_u12p4[0][0] 1392 2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 2_FDD_FreqTblYM_Hz_u12p4[0][1] 1328 2_FDD_FreqTblYM_Hz_u12p4[0][1] 1328 2_FDD_FreqTblYM_Hz_u12p4[0][5] 1360 2_FDD_FreqTblYM_Hz_u12p4[0][5] 1360 2_FDD_FreqTblYM_Hz_u12p4[0][5] 1360 2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 2_FDD_FreqTblYM_Hz_u12p4[0][5] 1392 2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | 2422 |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[5] 3206 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[6] 3598 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[0] 1066 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[0] 1066 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[1] 1212 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[2] 1359 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[3] 1506 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[4] 1653 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 1946 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 1946 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[7] 2093 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[8] 2240 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[8] 2387 12_FDD_FreqTbIYM_Hz_u12p4[0][0] 1392 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTbIYM_Hz_u12p4[0][2] 1328 12_FDD_FreqTbIYM_Hz_u12p4[0][3] 1344 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 1396 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 1392 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[6] 3598 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[8] 4382 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[9] 4774 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[0] 1066 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[1] 1212 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[2] 1359 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[3] 1506 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[4] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 1800 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 1946 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[7] 2093 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[8] 2240 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[9] 2387 12_FDD_FreqTblYM_Hz_u12p4(0)[0] 1296 12_FDD_FreqTblYM_Hz_u12p4(0)[1] 1312 12_FDD_FreqTblYM_Hz_u12p4(0)[2] 1328 12_FDD_FreqTblYM_Hz_u12p4(0)[3] 1344 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 1360 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 1376 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 1376 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 1392 1 | | |
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| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][8] 4382 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][9] 4774 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][0] 1066 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][1] 1212 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][2] 1359 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][3] 1506 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][4] 1653 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][5] 1800 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 1946 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][7] 2093 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][8] 2240 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][8] 2387 12_FDD_FeqTb\YM_Hz_u12p4[0][0] 1296 12_FDD_FeqTb\YM_Hz_u12p4[0][1] 1312 12_FDD_FeqTb\YM_Hz_u12p4[0][2] 1328 12_FDD_FeqTb\YM_Hz_u12p4[0][3] 1344 12_FDD_FeqTb\YM_Hz_u12p4[0][4] 1360 12_FDD_FeqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FeqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FeqTb\YM_Hz_u12p4[0][6] 1392 12_FDD_FeqTb\YM_Hz_u12p4[0][7] 1408 | | |
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| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1212 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1359 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1506 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1359 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1506 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_HtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 1506 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 12_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 12_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 12_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 12_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 12_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 12_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 12_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 1653 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2387 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 1800 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 1946 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 2093 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 2240 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 2387 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][0] 1296 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] 1312 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][2] 1328 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] 1344 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] 1360 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] 1376 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 1392 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] 1408 | | |
| | | |
| tz_FDD_Freq1blYM_Hz_U1zp4[0][8] 1424 | | |
| | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] 1440 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 96 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 112 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 128 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 144 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 144 |

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|--|-------------|-----------|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 160 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 176 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 192 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 208 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 224 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 240 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 256 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 272 | |
| _CmnVehSpd_Kph_u9p7[0] | 128 | |
| _CmnVehSpd_Kph_u9p7[1] | 256 | |
| _CmnVehSpd_Kph_u9p7[2] | 384 | |
| _CmnVehSpd_Kph_u9p7[3] | 512 | |
| _CmnVehSpd_Kph_u9p7[4] | 640 | |
| CmnVehSpd_Kph_u9p7[5] | 768 | |
| _CmnVehSpd_Kph_u9p7[6] | 896 | |
| _CmnVehSpd_Kph_u9p7[7] | 1024 | |
| _CmnVehSpd_Kph_u9p7[8] | 1152 | |
| _CmnVehSpd_Kph_u9p7[9] | 1280 | |
| CmnVehSpd Kph u9p7[10] | 1408 | |
| CmnVehSpd_Kph_u9p7[11] | 1536 | |
| | | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 24986 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 25395 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 25805 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 26214 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 26624 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 27034 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 27443 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 27853 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 28262 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 28672 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 32320 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 32352 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 32384 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 32416 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 32448 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 32480 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1480 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1488 | |
| DmpDecelGainSlewY UlspS u13p3[2] | 1496 | |
| DmpDecelGainSlewY_UlspS_u13p3[3] | 1504 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1512 | |
| DmpDecelGainSlewY_UlspS_u13p3[5] | 1520 | |
| DmpFiltKpWIRBIndY Uls u2p14[0] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 | |
| | | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1608 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2032 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2455 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2878 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3302 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3725 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 4148 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4572 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[8] | 4995 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[9] | 5419 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 2080 | |
| _FDD_AttenTblX_MtrRadpS_u12p4[1] | 2160 | |
| FDD_AttenTblY_Uls_u8p8[0] | 93 | |
| FDD_AttenTblY_Uls_u8p8[1] | 96 | |
| FDD_BlendTblY_Uls_u8p8[0] | 93 | |
| FDD_BlendTblY_Uls_u8p8[1] | 96 | |
| FDD_BlendTblY_Uls_u8p8[2] | 99 | |
| FDD_BlendTblY_Uls_u8p8[3] | 101 | |
| FDD_BlendTblY_Uls_u8p8[4] | 104 | |
| FDD_BlendTblY_Uls_u8p8[5] | 106 | |
| FDD_BlendTblY_Uls_u8p8[6] | 109 | |
| FDD_BlendTblY_Uls_u8p8[7] | 111 | |
| | 114 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 116 | |
| _FDD_BlendTblY_Uls_u8p8[9] | | |
| _FDD_BlendTblY_Uls_u8p8[10] | 119 | |
| :_FDD_BlendTblY_Uls_u8p8[11] | 122 | |

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| Name | Input Value | | |
|--|--------------|--------------------|-------|
| t InrtCmp ScaleFactorTblY Uls u9p7[0] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 51 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[3] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 77 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[5] | 90 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[6] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 154 | | |
| t InrtCmp ScaleFactorTblY Uls u9p7[11] | 166 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[0] | 31 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 32 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[2] | 33 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[3] | 35 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 36 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[5] | 37 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 38 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 40 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 41 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 42 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 44 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 45 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[0] | 6554 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[1] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[2] | 9830 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[3] | 11469 | | |
| t RIAstWIRBIndTbIY Uls u2p14[4] | 13107 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 794 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 819 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 845 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 870 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 896 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 3.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 550.2 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt FrqDepDmpnInrtCmp Per1 HwTorque HwNm f32.value | 2.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -50 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 280.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 5.2 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistC | | | |
| tgt Rte Inst Ap FrgDepDmpnInrtCmp.FrgDepDmpnInrtCmp Per1 CRFMotorVe | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDm | | | |
| tgt Rte Inst Ap FrgDepDmpnInrtCmp.FrgDepDmpnInrtCmp Per1 FrgDepDmpn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_F | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonA | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpee | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAm | | | |
| Name | Actual Value | Expected Value | Resul |
| PreDecelGain_Uls_M_f32 | 127832.258 | 127832.26 ± 0.0625 | Nesun |
| Prov1ProAttnComp MtrNm M f22 | 2226054.25 | 127632.20 ± 0.0023 | |

| @C | h- 1317 . 14- 14 | | |
|--|--------------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127832.258 | 127832.26 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | -2236951.25 | -2236951.286 ± 9.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | 488.806824 | 488.8068117 ± 0.00390625 | ✓ |
| Prev2PreAttnComp_MtrNm_M_f32 | 7.5 | 7.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -1100.19995 | -1100.2 ± 0.00390625 | ✓ |
| PrevTbarAng_HwDeg_M_f32 | 0.806451619 | 0.806451613 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -2.67284751 | -2.672846774 ± 0.00390625 | ~ |
| tot FrgDepDmpnInrtCmp Per1 FrgDepDmpnInrtCmp MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |

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FrqDepDmpnInrtCmp_Per1

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|------------------------|-------------------------|--------------|
| Actual Function | Count Expected Function | Count Result |

 $Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached$

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| | | M |
|--|-------------|---|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 400 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 416 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 432 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 448 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 464 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 480 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 496 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 512 | |
| _CmnVehSpd_Kph_u9p7[0] | 2560 | |
| _CmnVehSpd_Kph_u9p7[1] | 3840 | |
| CmnVehSpd_Kph_u9p7[2] | 5120 | |
| CmnVehSpd Kph u9p7[3] | 6400 | |
| _CmnVehSpd_Kph_u9p7[4] | 7680 | |
| | 8960 | |
| _CmnVehSpd_Kph_u9p7[5] | | |
| _CmnVehSpd_Kph_u9p7[6] | 10240 | |
| CmnVehSpd_Kph_u9p7[7] | 11520 | |
| _CmnVehSpd_Kph_u9p7[8] | 12800 | |
| CmnVehSpd_Kph_u9p7[9] | 14080 | |
| CmnVehSpd_Kph_u9p7[10] | 15360 | |
| CmnVehSpd_Kph_u9p7[11] | 16640 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 28262 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 28672 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 29082 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 29491 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 29901 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 30310 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 30720 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 31130 | |
| DmpADDCoefX_MtrNm_u4p12[8] | 31539 | |
| DmpADDCoefX_MtrNm_u4p12[9] | 31949 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 30592 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 30624 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 30656 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 30688 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 30720 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 30752 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1208 | |
| | | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1216 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1224 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1232 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1240 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1248 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 1638 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 4915 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 6554 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 8192 | |
| _FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[0] | 1789 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2130 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2471 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[3] | 2811 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3152 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3493 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[6] | 3834 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] FDD ADDStaticTblY MtrNmpRadpS_um1p17[7] | 4175 | |
| | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4515 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4856 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1680 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 2240 | |
| FDD_AttenTblY_Uls_u8p8[0] | 116 | |
| FDD_AttenTblY_Uls_u8p8[1] | 118 | |
| FDD_BlendTbIY_Uls_u8p8[0] | 116 | |
| FDD_BlendTbIY_Uls_u8p8[1] | 118 | |
| FDD_BlendTbIY_Uls_u8p8[2] | 121 | |
| FDD_BlendTblY_Uls_u8p8[3] | 123 | |
| FDD_BlendTblY_Uls_u8p8[4] | 126 | |
| FDD_BlendTblY_Uls_u8p8[5] | 129 | |
| FDD_BlendTblY_Uls_u8p8[6] | 131 | |
| FDD_BlendTblY_Uls_u8p8[7] | 134 | |
| | 136 | |
| FDD_BlendTblY_Uls_u8p8[8] | | |
| _FDD_BlendTblY_Uls_u8p8[9] | 139 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 141 | |
| _FDD_BlendTblY_Uls_u8p8[11] | 144 | |

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| Name | Input Value | | |
|---|--------------|----------------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 192 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 46 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 47 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 49 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 50 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 51 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 52 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 54 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 55 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 56 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 58 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 59 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[11] | 60 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[0] | 8192 | | |
| t_RIAstWIRBIndTbIY_UIs_u2p14[1] | 9830 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[2] | 11469 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[3] | 13107 | | |
| t_RIAstWIRBIndTbIY_Uls_u2p14[4] | 14746 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[0] | 1050 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 1075 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 1101 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 1126 | | |
| t_WIRBIndTblX_MtrNm_u8p8[4] | 1152 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -3.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -550.3 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -2.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | 50 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 290.01 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 1.3 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCm | | Cmd MtrNm f32 | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmp | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 FrqDepDmpnI | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hv | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAc | | _ | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmp | | | |
| Name | Actual Value | Expected Value | Resul |
| PreDecelGain Uls M f32 | 127934.031 | 127934.0349 ± 0.0625 | Ivesui |
| FIEDEGEIGAIII_UI3_IVI_I32 | 121 334.031 | 12/934.U349 ± U.U023 | |

| 2 | | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 127934.031 | 127934.0349 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 415103.719 | 415103.7843 ± 0.9 | ~ |
| Prev1SclDrvVel_RadpS_M_f32 | -164.116653 | -164.1166652 ± 0.00390625 | ~ |
| Prev2PreAttnComp_MtrNm_M_f32 | -7.5 | -7.5 ± 0.00048828125 | ~ |
| Prev2SclDrvVel_RadpS_M_f32 | 250.050003 | 250.05 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | -0.520833313 | -0.520833333 ± 0.00390625 | ~ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.58375692 | 1.583755 ± 0.00390625 | ~ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | ✓ |



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|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Name | T4-04 0.00 (P4-04-1) | |
|---|---|-----------------------------------|
| PrevDeceiGain_UIs_M_132 128036.61 PrevIP-RatInComp_Mnnm_M_132 8.5 PrevZP-RatInComp_Mnnm_M_132 5000.03 PrevZP-RatInComp_Mnnm_M_132 7.7 PrevZP-RatInComp_Mnnm_M_132 38.3 PrevZP-RatInComp_Mnnm_M_132 0.66 Re_Inst_Ap_EnQbepDmpnInrComp tgt_Re_Inst_Ap_EnQbepDmpnInrComp TandvelFillisV_M_str_V_UIs_132 5.5 NomPostGins MitDegpHvDeg_132 46.32 K_CmntParsfill NmpDeg_132 5.2 K_DmpDecelGain FSlew_UIspS_132 100.05 K_DmpDecelGain FSlew_UispS_132 4.8 K_DmpDecelGain FSlew_UispS_132 4.2 K_DmpDecelGain FSlew_UispS_132 4.2 K_DmpDecelGain FSlew_UispS_132 4.2 K_DmpCarlon Tiresh_KphpS_132 4.2 K_DmpCarlon Tiresh_KphpS_132 4.2 K_Infromp_Mnrein KgmSq_162 0.00038 K_Infromp_Mnrein KgmSq_162 0.000000000000000000000000000000 | Test Step 3.28 (Repeat Count = 1) | V |
| Prev1RcdInComp_MRNM M_132 8.5 Prev1ScIDVVel_RadpS_M_132 5000.03 Prev2RcPatRomp_MRNM M_132 7.7 Prev2ScIDVVel_RadpS_M_132 0.66 Prev1ScIDVVel_RadpS_M_132 0.66 Rt_Inst_Ap_FrqDepDmpnInrtCmp Ig_Rie_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFillSV_M_str_SV_UIS_132 4.5 TbarVelFillSV_M_str_SV_UIS_132 0.47866 k_CmnSysKinRatio_MtrDeptHvDeg_132 46.32 k_CmnSysKinRatio_MtrDeptHvDeg_132 48.32 k_DmpDecelGainFSiew_UIspS_132 48.32 k_DmpDecelGainFSiew_UIspS_132 48.32 k_DmpDecelGainFSiew_UIspS_132 48.32 k_DmpDacelGainFSiew_UIspS_132 48.32 k_DmpDacelGainFSiew_UIspS_132 48.32 k_DmpGainOfThresh_KphpS_192 25.3 k_DmpGainOfThresh_KphpS_192 42 k_DmpGainOfThresh_KphpS_193 42 k_InftCmp_MtrVel_ScaleFactor_UIs_132 0.2 L_FDD_ADDRollingTbYM_MtrmpRadpS_um1p17[0][1] 2032 L_FDD_ADDRollingTbYM_MtrmpRadpS_um1p17[0][1] 3725 L_FDD_ADDRollingTbYM_MtrmpRadpS_um1p17[0][1] 4752 L_FDD_ADDRollingTbY | | |
| Prev1ScIDr/Vel RadpS M_132 5000.03 Prev2FexbrithComp_Mirkm M_132 7.7 Prev2FexbrithComp_Mirkm M_132 38.3 Prev1SciDriv/L RadpS M_132 0.66 Rte_Inst_Ap_FrqDepDmpnInrtCmp tgtRe_Inst_Ap_FrqDepDmpnInrtCmp TbarVelFilisV, M_str.K_Uis_132 5.5 K_CmmSystiRisdo, MirtCeptHvDeg_152 46.32 K_CmmSystiRisdo, MirtDeptHvDeg_152 5.2 K_CmmDaristf, NmpDeg_162 5.2 K_DmpDecelGain Filis W_ UispS_162 4.8 K_DmpDecelGain Filis W_ UispS_162 4.8 K_DmpGainOffThreah_KphpS_132 4.2 K_DmpGainOffThreah_KphpS_132 4.2 K_InntCmp_Mirterla_KgmSq_152 0.00038 K_InntCmp_Mirterla_KgmSq_152 0.2 E_FDD_ADDRollingTbYM_MirtmpRadpS_umfp17[0][0] 1608 | | |
| Prev2PerAttnComp_MtrNm_M:32 7.7 Prev2SeChrVvle_Radps_M_/32 38.3 Prev1DsrAng_Hobeg_M_/32 0.66 Rte_Inst_Ap_EnQbepDmpnIntrCmp bg_Re_Inst_Ap_EnQbepDmpnIntrCmp TbarVeFIRISV_M_str K_UIs_/32 -5.5 K_CmnSystKinRatio_MtrDepptrwDeg_/32 48.32 K_CmnSystKinRatio_MtrDepptrwDeg_/32 8.2 K_CmnTbarStiff_NmpDeg_/32 100.05 K_DmpDecelGain_UIs_/32 4.8 K_DmpGainOrThresh_KphpS_/32 25.3 K_DmpGainOrThresh_KphpS_/32 4.2 K_Inntrom_Mtrhenta_Kgmsd_/73 0.00038 K_Inntrom_Mtrhenta_Kgmsd_/73 0.2 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[0] 1608 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[0] 1608 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[1] 2032 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[1] 3032 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[4] 302 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[4] 302 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[6] 4148 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[6] 4148 L_FDD_ADDRollingTbYM_MtrmpRadpS_umtp17(0)[6] </td <td></td> <td></td> | | |
| PrevSciDrivel, RadpS, M. [32] 38.3 PrevTbarAng, HwDeg, M. [32] 0.66 Rie, Inst, Ap, FriqDepDmpintrCmp tgt_Rie_Inst, Ap_FriqDepDmpintrCmp TbarVelFillSV, M. str. SV, Uls. [32] 5.5 k, CmnSysiKnatio, MtrDegpHwDeg, [32] 48.32 k, CmnTbarSiff, NmpDeg, [32] 5.2 k, DmpDecalGain, Siew, Ulsp. g. [32] 100.05 k, DmpDecalGain, Siew, Ulsp. g. [32] 4.8 k, DmpDecalGain, Uls. [32] 4.8 k, DmpDecalGain, Uls. [32] 4.2 k, DmpGainOffThresh, Kapba, [32] 4.2 k, Intromp, Mtrinetia, KgmSa, [32] 0.00038 k, Intromp, Mtrinetia, KgmSa, [32] 0.00038 k, Intromp, Mtrinetia, KgmSa, [32] 0.2 k, Intromp, Mtrinetia, KgmSa, [33] 0.3 k, Intromp, MtrinepRadps, umf177[0][0] 2032 2, FDD, ADDRolling TbYM, MtrimpRadps, umf197[0][3] 3878 2, FDD, ADDRolling TbYM, MtrimpRadps, umf197[0][4] <td>_ : = =</td> <td></td> | _ : = = | |
| PrevTbarAng_HvDeg_M_f32 0.66 Rle_Inst_Ap_FrqDepDmpnInrCmp tgt_Rle_Inst_Ap_FrqDepDmpnInrtCmp DarVeFilisV_M_str.K_Uls_f32 -5.5 TbarVeFilisV_M_str.K_Uls_f32 0.47856 k_CmnSysKinRatio_MtrDepthvDeg_f32 46.32 k_CmnStraftif_NmpDeg_f32 5.2 k_DmpDecelGainFSlew_UlspS_f32 100.05 k_DmpDecelGainFSlew_UlspS_f32 4.8 k_DmpGainOrtTreen_KphpS_f32 4.2 k_DmpGainOrtTreen_KphpS_f32 4.2 k_IntCmp_Mtrlertia_KgmSq_f32 0.00038 k_IntCmp_Mtrlertia_KgmSq_f32 0.2 k_Int | Prev2PreAttnComp_MtrNm_M_f32 | |
| Rte_Inst_Ap_FrqDepDmpnIntCmp | Prev2ScIDrvVel_RadpS_M_f32 | -38.3 |
| TaarVelFillSV_M_str K_Uls_132 | PrevTbarAng_HwDeg_M_f32 | 0.66 |
| ToarVelFiltSv_M_str.K_Uls_132 | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k_CmnSysKinRatio_MtrDegpHvDeg_f32 46.32 k_CmnDarSitf_NmpDeg_f32 5.2 k_DmpDecelGain_Slew_UispS_f32 100.05 k_DmpDecelGain_Uis_f32 4.8 k_DmpGainOffThresh_KphpS_f32 4.2 k_DmpGainOffThresh_KphpS_f32 4.2 k_IntCmp_MtrInertia_KgmSq_f32 0.00038 k_IntCmp_MtrVel_ScaleFactor_Uis_f32 0.2 L_PDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][0] 1608 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][1] 2032 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][3] 2878 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][4] 3302 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 4148 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 4148 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 495 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 495 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 418 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 495 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][6] 495 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][6] 296 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][6] 296 | TbarVelFiltSv_M_str.SV_Uls_f32 | -5.5 |
| k_CmnTbarStiff_NmpDeg_f32 5.2 k_DmpDeeclGainFSlew_UlspS_f32 100.05 k_DmpDeeclGain_Uls_f32 4.8 k_DmpGainOrThresh_KphpS_f32 25.3 k_DmpGainOrThresh_KphpS_f32 4.2 k_InrtCmp_Mtrlnerlia_KgmSq_f32 0.00038 k_InrtCmp_Mtrlvel_ScaleFactor_Uls_f32 0.2 k_InrtCmp_Mtrlvel_ScaleS_ump17f0 i0] 1608 t_InrtCmp_Mtrlvel_ScaleS_ump17f0 i1] 2032 t_InrtCmp_Mtrlvel_ScaleS_ump17f0 i1] 2455 t_InrtCmp_Mtrlvel_ScaleS_ump17f0 i1] 3002 t_InrtCmp_Mtrlvel_ScaleS_ump17f0 i1] 4572 t_InrtCmp_Mtrlvel_ScaleS_ump17f0 i1] 4572 t_InrtCmp_Mtrlvel_ScaleS_ump17f1 i1] 4572 | TbarVelFiltSv_M_str.K_Uls_f32 | 0.47856 |
| k_DmpDecelGainFSlew_UlspS_f32 4.8 k_DmpDecelGain_Uls_f32 4.8 k_DmpGainOrThresh_KphpS_f32 25.3 k_DmpGainOrThresh_KphpS_f32 4.2 k_IntrCmp_MtrInertia_KgmSq_f32 0.00038 k_IntrCmp_MtrVel_ScaleFactor_Uls_f32 0.2 2_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][0] 1608 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][1] 2032 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][2] 2455 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][3] 2878 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][4] 3302 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][5] 3725 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][6] 4148 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][7] 4572 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][8] 4995 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[0][9] 5419 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][9] 1427 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][1] 1665 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][1] 2340 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17[1][4] 2340 12_FDD_ADDRollingTbYM_MtrNmpRa | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 46.32 |
| k_DmpDecelGain_Uls_f32 4.8 k_DmpGainOffThresh_KphpS_f32 25.3 k_DmpGainOnThresh_KphpS_f32 4.2 k_IntCmp_MtrInetia_KgmSq_f32 0.00038 k_IntCmp_MtrIvel_ScaleFactor_Uls_f32 0.2 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[0] 1608 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[1] 2032 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[2] 2455 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[3] 2878 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[4] 3302 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 4148 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 4148 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 495 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 495 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 495 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 4127 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 1427 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 1884 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 2340 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 2340 | k_CmnTbarStiff_NmpDeg_f32 | 5.2 |
| k_DmpGainOfThrest_KphpS_f32 25.3 k_DmpGainOnThrest_KphpS_f32 4.2 k_IntCmp_MtrInertia_KgmSq_f32 0.00038 k_IntCmp_MtrVel_ScaleFactor_Uls_f32 0.2 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[0] 1608 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[1] 2032 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[3] 2455 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[4] 3302 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 3725 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[6] 4148 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[7] 4572 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[8] 4995 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0)[8] 4995 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[1] 1427 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[1] 1427 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[1] 1884 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[1] 2340 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 2340 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 296 t2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1)[6] 296 < | k_DmpDecelGainFSlew_UlspS_f32 | 100.05 |
| k_DmpGainOnThresh_KphpS_i32 4.2 k_IntCmp_Mtrinertia_KgmSq_f32 0.00038 k_IntCmp_MtrVel_ScaleFactor_Uls_f32 0.2 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[0] 1608 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[1] 2032 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[9] 5419 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 12_FDD_ADDRollingTblYM_Mt | k_DmpDecelGain_Uls_f32 | 4.8 |
| k_InrtCmp_MtrInertia_KgmSq_f32 0.00038 k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 0.2 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][0] 1608 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][1] 2032 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][2] 2455 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][1] 1665 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTb\YM_MtrNmpRadpS_ | k_DmpGainOffThresh_KphpS_f32 | 25.3 |
| k_nntCmp_MtrVel_ScaleFactor_Uls_f32 0.2 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] 1608 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] 2032 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 2456 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2878 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3726 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 <tr< td=""><td>k_DmpGainOnThresh_KphpS_f32</td><td>4.2</td></tr<> | k_DmpGainOnThresh_KphpS_f32 | 4.2 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[0] 1608 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[1] 2032 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[9] 4495 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[9] 5419 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[0] 1427 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[1] 1655 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[2] 1884 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[3] 2112 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[4] 2340 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 2796 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 2796 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 2796 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 2796 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 252 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 252 </td <td>k_InrtCmp_MtrInertia_KgmSq_f32</td> <td>0.00038</td> | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00038 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] 2032 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 2455 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2878 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3480 | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.2 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 2455 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2878 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 1608 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2878 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3480 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 2032 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2878 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3480 | | 2455 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | 2878 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3480 | | 3302 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] | | 3725 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] | | 4148 |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][8] | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][9] 5419 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][0] 1427 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 1655 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 1884 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2112 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 2340 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 2568 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 2796 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 3024 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 3252 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 3480 | | |
| | | |
| 12_FDD_FTeq10ftM_F12_u12p4[0][0] 176 | | |
| | | |
| | | |
| | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][3] 224 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][4] 240 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][5] 256 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][6] 272 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] 288 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 304 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] 320 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] 336 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 352 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][1] 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][2] 688 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 704 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 704 |

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| FrqDepDmpnInrtCmp_ | Per1 |
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| Name | Input Value | |
|--|-------------|--|
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 720 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 736 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 752 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 768 | |
| 2 FDD FreqTblYM Hz u12p4[1][8] | 784 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 800 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 816 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 832 | |
| _CmnVehSpd_Kph_u9p7[0] | 12800 | |
| _CmnVehSpd_Kph_u9p7[1] | 12928 | |
| CmnVehSpd Kph u9p7[2] | 13056 | |
| CmnVehSpd Kph u9p7[3] | 13184 | |
| _CmnVehSpd_Kph_u9p7[4] | 13312 | |
| | 13440 | |
| _CmnVehSpd_Kph_u9p7[5] | | |
| _CmnVehSpd_Kph_u9p7[6] | 13568 | |
| CmnVehSpd_Kph_u9p7[7] | 13696 | |
| _CmnVehSpd_Kph_u9p7[8] | 13824 | |
| CmnVehSpd_Kph_u9p7[9] | 13952 | |
| CmnVehSpd_Kph_u9p7[10] | 14080 | |
| CmnVehSpd_Kph_u9p7[11] | 14208 | |
| DmpADDCoefX_MtrNm_u4p12[0] | 4506 | |
| _DmpADDCoefX_MtrNm_u4p12[1] | 4915 | |
| _DmpADDCoefX_MtrNm_u4p12[2] | 5325 | |
| _DmpADDCoefX_MtrNm_u4p12[3] | 5734 | |
| _DmpADDCoefX_MtrNm_u4p12[4] | 6144 | |
| _DmpADDCoefX_MtrNm_u4p12[5] | 6554 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 6963 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 7373 | |
| DmpADDCoefX_MtrNm_u4p12[8] | 7782 | |
| DmpADDCoefX_MtrNm_u4p12[9] | 8192 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 3872 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 3904 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 3936 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 3968 | |
| | 4000 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4032 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1480 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1488 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1496 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1504 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1512 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1520 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 3277 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 4915 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[2] | 6554 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[3] | 8192 | |
| DmpFiltKpWIRBIndY_Uls_u2p14[4] | 9830 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1608 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2032 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2455 | |
| FDD ADDStaticTblY MtrNmpRadpS um1p17[3] | 2878 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3302 | |
| | 3725 | |
| FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[5] FDD_ADDStaticTbIY_MtrNmpRadpS_um1p17[6] | 4148 | |
| | | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 4572 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4995 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 5419 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1648 | |
| FDD_AttenTblX_MtrRadpS_u12p4[1] | 2320 | |
| FDD_AttenTblY_Uls_u8p8[0] | 144 | |
| FDD_AttenTblY_Uls_u8p8[1] | 146 | |
| FDD_BlendTbIY_Uls_u8p8[0] | 144 | |
| FDD_BlendTblY_Uls_u8p8[1] | 146 | |
| FDD_BlendTblY_Uls_u8p8[2] | 149 | |
| FDD_BlendTblY_Uls_u8p8[3] | 152 | |
| FDD_BlendTblY_Uls_u8p8[4] | 154 | |
| FDD_BlendTblY_Uls_u8p8[5] | 157 | |
| | 159 | |
| FDD_BlendTblY_Uls_u8p8[6] | | |
| FDD_BlendTblY_Uls_u8p8[7] | 162 | |
| FDD_BlendTblY_Uls_u8p8[8] | 164 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 167 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 169 | |
| _FDD_BlendTblY_Uls_u8p8[11] | 172 | |

2014-09-19, 16:35:39+0530



| Name | Input Value | | |
|---|--|----------------------|------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 179 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 192 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 205 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 218 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 230 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 243 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 256 | | |
| | 269 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 282 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 294 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 307 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 320 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 61 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 63 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 65 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 67 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 68 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 69 | | |
| :_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 70 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 72 | | |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 73 | | |
| InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 74 | | |
| InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 76 | | |
| RIAstWIRBIndTbiY Uls u2p14[0] | 6554 | | |
| RIAstWIRBIndTbIY UIs u2p14[1] | 8192 | | |
| _RIAstWIRBIndTbIY_UIs_u2p14[2] | 9830 | | |
| :_RIAstWIRBIndTbIY_UIs_u2p14[3] | 11469 | | |
| RIAstWIRBIndTbIY_UIs_u2p14[4] | 13107 | | |
| : WIRBIndTbIX MtrNm u8p8[0] | 1306 | | |
| _WIRBIndTbIX_MtrNm_u8p8[1] | 1331 | | |
| :_WIRBIndTbIX_MtrNm_u8p8[2] | 1357 | | |
| : WIRBIndTbIX MtrNm u8p8[3] | 1382 | | |
| WIRBINDT DIX_MUNIT_uopo[3] WIRBINDT DIX_MUNIT_uopo[3] | 1408 | | |
| gt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 4.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 650.01 | | |
| gt_FrqDepDmpnInrtCmp_Fer1_EckFwlotolver_wittkaup3_132.value gt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 0 | | |
| gt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrtComSvcbit_Cmt_igc.value gt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 3.5 | | |
| | 0 | | |
| gt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | | | |
| gt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 305.05 | | |
| gt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 2.3 | | |
| | tCmc tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32 | | |
| | Vel 1 tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32 | | |
| | mpSi_tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_H | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAc | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmp | | | |
| Name | Actual Value | Expected Value | Resu |
| ProDocalCoin IIIa M f22 | 120026 406 | 120026 4000 ± 0 0625 | |

| | 02 1 1 1 1 1 2 | | |
|--|----------------|--------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 128036.406 | 128036.4099 ± 0.0625 | ~ |
| Prev1PreAttnComp_MtrNm_M_f32 | 34435492 | 34435493.31 ± 99.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | 130.127335 | 130.127343 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | 8.5 | 8.5 ± 0.00048828125 | • |
| Prev2SclDrvVel_RadpS_M_f32 | 5000.02979 | 5000.03 ± 0.00390625 | ~ |
| PrevTbarAng_HwDeg_M_f32 | 0.673076928 | 0.673076923 ± 0.00390625 | • |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 0.261120796 | 0.261126154 ± 0.00390625 | ~ |
| tot FroDenDmnnInrtCmn Per1 FroDenDmnnInrtCmn MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | V |



| Τ | | | | V |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Probacker Prob | T4 04 0 00 (D4 04 4) | |
|--|---|-----------------------------------|
| PinDecidian U.S. M. 52 | Test Step 3.29 (Repeat Count = 1) | · |
| Piew Pre/stanComp, Michin, M., 192 8.5 | | |
| Prev15ch2MCD Mith M 32 | PreDecelGain_Uls_M_f32 | 128138.585 |
| PiewZPeAMComp MRNm M, M, M, S2 | Prev1PreAttnComp_MtrNm_M_f32 | |
| PrevStarAng_HnOge_M_132 | Prev1SclDrvVel_RadpS_M_f32 | -26.3 |
| Piev1barkog_McSeq_McSeq_ Inst_Ap_FrqDepDmpnIntCmp | Prev2PreAttnComp_MtrNm_M_f32 | -6.6 |
| Rel_Inst_Ap_FrqDepDmpnInrtCmp | Prev2ScIDrvVel_RadpS_M_f32 | 175.2 |
| Than/selfistory_M. str. K. U.Is. 132 K. CmmSyskinfata_Introoppin/Deg/32 K. CmmCastistir_MmpDeg/122 K. DmpCealodimrStew_Uispo_132 K. DmpCealodimrStew_Uispo_132 K. DmpCealodimrStew_Uispo_132 K. DmpCealodimrStew_Uispo_132 K. DmpCealodimrStew_Uispo_132 K. DmpCealodimrStew_Uispo_132 K. Intromp_Mirloretia_KgmSq_132 K. Intromp_Mirloretia_Kg | PrevTbarAng_HwDeg_M_f32 | -0.51 |
| Thardrefflist M str K Uis 32 | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k, Cmn7bas0iff NmpDeg_152 6.8 k, Cmm7bas0iff NmpDeg_152 6.8 k, DmpDecelGainr/Siew, Ulsp5_152 200.02 k, DmpDecelGain, Uls_162 5.9 k, DmpDeainr/Simesh, KphpS_132 30.2 k, DmpCainr/Simesh, KphpS_132 8.3 k, IntrCmp, Mirrhertak, KgmSq_128 0.000309 L, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 1789 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 2130 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 3152 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 3152 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 3493 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 3834 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 4176 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 4856 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp170[0] 4866 L2, FDD, ADDRolling TbMM, MirrhyRadgSg_umfp171[1] | TbarVelFiltSv_M_str.SV_Uls_f32 | 6.1 |
| K. CmmDacelGainFSiew_Uipsg_132 6.8 k. CmmDacelGainFSiew_Uipsg_132 200.02 k. DmpDecelGainFSiew_Uipsg_132 5.9 k. DmpCannOfThresh_KphpS_122 30.2 k. DmpCannOfThresh_KphpS_122 8.3 k. IntrCmp_Mitrinesh_KgmSq_152 0.00039 k. IntrCmp_Mitrinesh_KgmSq_152 0.00039 k. IntrCmp_Mitrinesh_KgmSq_152 0.1 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][0] 1789 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][2] 2471 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][3] 2811 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][4] 3152 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][6] 3493 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][6] 3834 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][7] 4175 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[0][8] 4515 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[1][8] 4515 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[1][8] 456 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[1][8] 3725 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[1][8] 3725 12-FDQ_ADDRallingTbYM_MtrXmpRadps_um1p17[1][8] | TbarVelFiltSv_M_str.K_Uls_f32 | 0.58963 |
| Non-pipecelCain-Usin Size | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 28.12 |
| K_DmpDecelGain_Uls_G32 5.9 K_DmpGainOfTrresh_KphpS_f32 30.2 K_DmpGainOfTrresh_KphpS_f32 8.3 K_InfCmp_Mtrheetla_KgmSq_f32 0.00039 K_InfCmp_Mtrheetla_KgmSq_f32 0.00039 L_InfCmp_Mtrheetla_KgmSq_f32 0.1 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[0] 1789 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[1] 2130 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[3] 2811 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[4] 3152 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[6] 3834 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[6] 3834 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[8] 4115 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[8] 4515 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(0)[8] 4516 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(1)[8] 4566 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(1)[8] 456 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(1)[8] 2455 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(1)[8] 3932 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1p17(1)[8] 3926 12_FDD_ADDRollingTbYM_MtrhmpRadpS_um1 | k_CmnTbarStiff_NmpDeg_f32 | 6.8 |
| K_DmpGainOffTreeh_KphpS_132 83 K_DmpGainOffTreeh_KphpS_132 83 K_InfrCmp_Mitrelat_KgmSq_132 0.00039 K_InfrCmp_Mitrelat_KgmSq_132 0.1 V_EPD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(0) 1789 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(1) 2130 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(3) 2811 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(4) 3152 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(5) 3493 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(7) 4176 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(7) 4176 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(7) 4176 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(0)(7) 4176 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(1)(7) 4856 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(1)(7) 4866 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(1)(7) 2032 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(1)(7) 2455 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(1)(8) 3302 12_FDD_ADDRollingTbYM_MitrenpRadpS_um1p17(1)(8) 3302 12_FDD_ADRollingTbYM_MitrenpRadpS_um1p17(1)(8) 4148 <t< td=""><td>k_DmpDecelGainFSlew_UlspS_f32</td><td>200.02</td></t<> | k_DmpDecelGainFSlew_UlspS_f32 | 200.02 |
| k_ DmpGainOnThresh_KphpS_132 8.3 k_ InntCmp_Mitrhentia_KgmSq_132 0.00039 k_ InntCmp_Mitrhentia_KgmSq_132 0.1 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][0] 1789 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][1] 2130 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][2] 2471 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][3] 2811 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][5] 3493 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][6] 3834 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][6] 3834 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][8] 4515 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[0][9] 4856 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][9] 4856 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 302 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 302 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 4572 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] 4572 12_FDD_ADDRollingTbYM_MitrMmpRadpS_um1p17[1][1] | k_DmpDecelGain_Uls_f32 | 5.9 |
| k_IntCmp_MtrVel_ScaleFactor_Uls_132 0.00039 k_IntCmp_MtrVel_ScaleFactor_Uls_132 0.1 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[1] 2130 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[2] 2471 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[3] 2811 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[4] 3152 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[6] 3834 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[6] 3834 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[7] 4175 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[8] 4515 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[8] 4516 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(0)[9] 4856 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[9] 4856 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[1] 2032 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[1] 2032 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[1] 3725 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[6] 4148 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[6] 4148 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[6] 4148 12_FDD_ADDRoilingTbVM_MtrVmpRadpS_um1p17(1)[6] </td <td>k_DmpGainOffThresh_KphpS_f32</td> <td>30.2</td> | k_DmpGainOffThresh_KphpS_f32 | 30.2 |
| k_IntCmp_MtrVel_ScaleFactor_Uls_132 0.1 12_FDD_ADDRollingTbt/M_MtrImpRadps_umfp17(0)[0] 1789 12_FDD_ADDRollingTbt/MM_MtrImpRadps_umfp17(0)[2] 2471 12_FDD_ADDRollingTbt/MM_MtrImpRadps_umfp17(0)[3] 2811 12_FDD_ADDRollingTbt/MM_impRadps_umfp17(0)[4] 3152 12_FDD_ADDRollingTbt/MM_impRadps_umfp17(0)[6] 3493 12_FDD_ADDRollingTbt/MM_impRadps_umfp17(0)[6] 3834 12_FDD_ADDRollingTbt/MM_impRadps_umfp17(0)[6] 3834 12_FDD_ADDRollingTbt/MM_impRadps_umfp17(0)[6] 4175 12_FDD_ADDRollingTbt/MM_impRadps_umfp17(0)[6] 4515 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(0)[6] 456 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 456 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 202 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 2455 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 3726 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 3726 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 4572 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 4572 12_FDD_ADDRollingTbt/MM_immpRadps_umfp17(1)[6] 4572 12_FDD_FreqTbt/MM_iz_ut2p4(0)[1] | k_DmpGainOnThresh_KphpS_f32 | 8.3 |
| 1.2 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 10 2130 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 11 2130 2471 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 2471 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 2471 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 2471 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3152 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3493 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3475 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3476 22 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3476 3493 32 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3476 3493 32 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3476 3493 32 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(0) 21 3456 32 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3232 32 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3232 32 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3202 320 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3202 320 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3202 320 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3202 320 FDD_ADDRollingTbIYM_MirhmpRadpS_umtp17(1) 21 3202 3200 FDD_FDD_FDD_FDD_FDD_FDD_FDD_FDD_FDD_FDD | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.00039 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][1] 2130 2471 22FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][2] 2471 22FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][3] 28B1 22FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][4] 3152 22FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][5] 3493 22FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][6] 3884 22_FDD_ADDRollingTblYM_MtrNmpRadps_umtp17[0][7] 4175 | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.1 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] 2130 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 2120 2471 | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][0] | 1789 |
| 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][2] 2471 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][3] 2811 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][4] 3152 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3493 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3493 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][6] 3493 2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][7] 4175 | | 2130 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[3] 2811 2810 | | |
| 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17[0][4] 3152 3493 | | |
| 12_FDD_ADDROllingTb\YM_Mtr\mpRadpS_um1p17(0)[5] 3493 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[7] 4175 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[8] 4515 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(0)[9] 4856 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[0] 1608 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[1] 2032 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[1] 2032 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[3] 2878 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[4] 3302 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[5] 3725 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[6] 4148 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[6] 4448 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[6] 495 12_FDD_ADDRollingTb\YM_Mtr\mpRadpS_um1p17(1)[6] 496 12_FDD_FreqTb\YM_Hz_u12p4(0)[0] 496 12_FDD_FreqTb\YM_Hz_u12p4(0)[0] 528 12_FDD_FreqTb\YM_Hz_u12p4(0)[3] 544 12_FDD_FreqTb\YM_Hz_u12p4(0)[3] 544 12_FDD_FreqTb\YM_Hz_u12p4(0)[6] 560 12_FDD_FreqTb\YM_Hz_u12p4(0)[6] 562 12_FDD_ | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][6] 3834 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][7] 4175 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][8] 4515 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[0][9] 4856 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][0] 1608 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][1] 2032 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][2] 2455 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][6] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_umtp17[1][8] 4996 12_FDD_FreqTblYM_Hz_u12p4[0][0] 496 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][6] 590 12_FDD_FreqTblYM_Hz_u12p4[0][6] 604 | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[8] 4175 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[8] 4515 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(0)[9] 4856 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[0] 1608 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[1] 2032 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[2] 2455 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[4] 3302 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 3725 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 4148 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 4572 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[8] 4995 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 5419 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 552 12_FDD_FreqTbIYM_Hz_u12p4(0)[2] 528 12_FDD_FreqTbIYM_Hz_u12p4(0)[3] 544 12_FDD_FreqTbIYM_Hz_u12p4(0)[3] 560 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 592 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 692 12_FDD_FreqTbIYM_Hz_u12p4(0)[7] 604 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 624 12_FDD_FreqTbIYM_Hz_u12p4 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0) 8 | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_FeqTbIVM_Hz_u12p4[0][0] 496 12_FDD_FeqTbIVM_Hz_u12p4[0][1] 512 12_FDD_FeqTbIVM_Hz_u12p4[0][2] 528 12_FDD_FeqTbIVM_Hz_u12p4[0][2] 528 12_FDD_FeqTbIVM_Hz_u12p4[0][3] 544 12_FDD_FeqTbIVM_Hz_u12p4[0][6] 560 12_FDD_FeqTbIVM_Hz_u12p4[0][6] 592 12_FDD_FeqTbIVM_Hz_u12p4[0][7] 608 12_FDD_FeqTbIVM_Hz_u12p4[0][7] 608 12_FDD_FeqTbIVM_Hz_u12p4[0][8] 624 12_FDD_FeqTbIVM_Hz_u12p4[0][9] 604 | | |
| 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][0] 1608 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][1] 2032 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][2] 2455 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1][9] 496 12_FDD_FreqTbiYM_Hz_u12p4[0][0] 496 12_FDD_FreqTbiYM_Hz_u12p4[0][1] 512 12_FDD_FreqTbiYM_Hz_u12p4[0][2] 528 12_FDD_FreqTbiYM_Hz_u12p4[0][3] 544 12_FDD_FreqTbiYM_Hz_u12p4[0][4] 560 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 576 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 592 12_FDD_FreqTbiYM_Hz_u12p4[0][6] 604 12_FDD_FreqTbiYM_Hz_u12p4[0][8] 624 12_FDD_FreqTbiYM_Hz_u12p4[0][8] 624 12_FDD_FreqTbiYM_Hz_u12p4[0][8] 624 12_FDD_FreqTbiYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][1] 2032 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][2] 2455 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 496 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 512 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 528 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 544 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 560 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 576 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 592 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 592 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 2 2455 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 3 2878 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 4 3302 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 5 3725 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 6 4148 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 7 4572 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 8 4995 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17[1] 8 4996 12_FDD_FreqTbiYM_Hz_u12p4[0] 0 496 12_FDD_FreqTbiYM_Hz_u12p4[0] 1 512 12_FDD_FreqTbiYM_Hz_u12p4[0] 2 528 12_FDD_FreqTbiYM_Hz_u12p4[0] 3 544 12_FDD_FreqTbiYM_Hz_u12p4[0] 4 560 12_FDD_FreqTbiYM_Hz_u12p4[0] 5 576 12_FDD_FreqTbiYM_Hz_u12p4[0] 6 592 12_FDD_FreqTbiYM_Hz_u12p4[0] 6 592 12_FDD_FreqTbiYM_Hz_u12p4[0] 8 624 12_FDD_FreqTbiYM_Hz_u12p4[0] 8 640 12_FDD_FreqTbiYM_H | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][3] 2878 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_FreqTblYM_Hz_u12p4[0][0] 496 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][6] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3302 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_FreqTblYM_Hz_u12p4[0][0] 5419 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][6] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3725 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_FreqTblYM_Hz_u12p4[0][0] 496 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 4148 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 496 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 512 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 528 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 544 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 560 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 576 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 592 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 608 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4572 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_FreqTblYM_Hz_u12p4[0][0] 496 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4995 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 496 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 512 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 528 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 544 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 560 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 576 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 592 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 608 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 5419 12_FDD_FreqTblYM_Hz_u12p4[0][0] 496 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0] 496 t2_FDD_FreqTbIYM_Hz_u12p4[0][1] 512 t2_FDD_FreqTbIYM_Hz_u12p4[0][2] 528 t2_FDD_FreqTbIYM_Hz_u12p4[0][3] 544 t2_FDD_FreqTbIYM_Hz_u12p4[0][4] 560 t2_FDD_FreqTbIYM_Hz_u12p4[0][5] 576 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 592 t2_FDD_FreqTbIYM_Hz_u12p4[0][7] 608 t2_FDD_FreqTbIYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTbIYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][1] 512 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] 528 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] 544 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 560 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 576 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][6] 592 12_FDD_FreqTblYM_Hz_u12p4[0][7] 608 12_FDD_FreqTblYM_Hz_u12p4[0][8] 624 12_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][7] 608 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 624 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][9] 640 | | |
| | | |
| 40 EDD FTh//// 1140-4[0][40] | | |
| 12_FDD_FreqTbIYM_Hz_u12p4[0][10] 656 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 672 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] | | |
| 12_FDD_FreqTblYM_Hz_u12p4[1][1] 1312 | | |
| 12_FDD_FreqTbIYM_Hz_u12p4[1][2] 1328 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[1][3] 1344 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1344 |

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|---|-------------|
| Name | Input Value |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1360 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] | 1376 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1392 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1408 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1424 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1440 |
| 12_FDD_FreqTblYM_Hz_u12p4[1][10] | 1456 |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1472 |
| t_CmnVehSpd_Kph_u9p7[0] | 15488 |
| t_CmnVehSpd_Kph_u9p7[1] | 15616 |
| t_CmnVehSpd_Kph_u9p7[2] | 15744 |
| t_CmnVehSpd_Kph_u9p7[3] | 15872 |
| t_CmnVehSpd_Kph_u9p7[4] | 16000 |
| | 16128 |
| t_CmnVehSpd_Kph_u9p7[5] | |
| t_CmnVehSpd_Kph_u9p7[6] | 16256 |
| _CmnVehSpd_Kph_u9p7[7] | 16384 |
| CmnVehSpd_Kph_u9p7[8] | 16512 |
| CmnVehSpd_Kph_u9p7[9] | 16640 |
| CmnVehSpd_Kph_u9p7[10] | 16768 |
| CmnVehSpd_Kph_u9p7[11] | 16896 |
| :_DmpADDCoefX_MtrNm_u4p12[0] | 8602 |
| _DmpADDCoefX_MtrNm_u4p12[1] | 9011 |
| t_DmpADDCoefX_MtrNm_u4p12[2] | 9421 |
| _DmpADDCoefX_MtrNm_u4p12[3] | 9830 |
| _DmpADDCoefX_MtrNm_u4p12[4] | 10240 |
| t_DmpADDCoefX_MtrNm_u4p12[5] | 10650 |
| t_DmpADDCoefX_MtrNm_u4p12[6] | 11059 |
| _DmpADDCoefX_MtrNm_u4p12[7] | 11469 |
| | 11878 |
| DmpADDCoefX_MtrNm_u4p12[9] | 12288 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 4192 |
| mpDecelGainSlewX_MtrRadpS_u11p5[1] | 4224 |
| :_DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 4256 |
| :_DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 4288 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 4320 |
| t_DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 4352 |
| t_DmpDecelGainSlewY_UlspS_u13p3[0] | 2408 |
| t_DmpDecelGainSlewY_UlspS_u13p3[1] | 2416 |
| t DmpDecelGainSlewY UlspS u13p3[2] | 2424 |
| _ , _ , _ , | |
| t_DmpDecelGainSlewY_UlspS_u13p3[3] | 2432 |
| t_DmpDecelGainSlewY_UlspS_u13p3[4] | 2440 |
| t_DmpDecelGainSlewY_UlspS_u13p3[5] | 2448 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[0] | 4915 |
| t_DmpFiltKpWIRBIndY_Uls_u2p14[1] | 6554 |
| :_DmpFiltKpWIRBIndY_Uls_u2p14[2] | 8192 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 9830 |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 11469 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 1789 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 2130 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 2471 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 2811 |
| t_FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 3152 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 3493 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 3834 |
| | 4175 |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 4515 |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 4856 |
| _FDD_AttenTblX_MtrRadpS_u12p4[0] | 1616 |
| _FDD_AtterFbiX_MtrRadpS_u12p4[0] | 2400 |
| _FDD_AttenTblY_Uls_u8p8[0] | 172 |
| | 172 |
| _FDD_AttenTblY_Uls_u8p8[1] | |
| _FDD_BlendTblY_Uls_u8p8[0] | 172 |
| _FDD_BlendTblY_Uls_u8p8[1] | 174 |
| _FDD_BlendTblY_Uls_u8p8[2] | 176 |
| _FDD_BlendTblY_Uls_u8p8[3] | 178 |
| _FDD_BlendTblY_Uls_u8p8[4] | 180 |
| _FDD_BlendTblY_Uls_u8p8[5] | 183 |
| _FDD_BlendTblY_Uls_u8p8[6] | 185 |
| _FDD_BlendTblY_Uls_u8p8[7] | 187 |
| _FDD_BlendTblY_Uls_u8p8[8] | 189 |
| _FDD_BlendTblY_Uls_u8p8[9] | 191 |
| | |
| t_FDD_BlendTblY_Uls_u8p8[10] | 193 |

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| Name | Input Value | | |
|--|--------------|----------------|--------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 13 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 26 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 51 | 51 | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 154 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 77 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 78 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 79 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 81 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 82 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 83 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 84 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[7] | 86 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[8] | 87 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[9] | 88 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[10] | 90 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 91 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 1638 | | |
| t RIAstWIRBIndTbIY Uls u2p14[1] | 3277 | | |
| t RIAstWIRBIndTbIY Uls u2p14[2] | 4915 | | |
| t RIAstWIRBIndTbIY Uls u2p14[3] | 6554 | | |
| t_RIAstWIRBIndTblY_Uls_u2p14[4] | 8192 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 282 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 307 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 333 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[3] | 358 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 384 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | -4.4 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | -650.08 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc.value | 1 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | -3.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleLonAccel_KphpS_f32.value | -10.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 315.04 | | |
| tgt FrqDepDmpnInrtCmp Per1 WIRCmdAmpBind MtrNm f32.value | 4.3 | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 BaseAssistCmo | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 CRFMotorVel | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FreqDepDmpS | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_FrqDepDmpnIn | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | _ | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Per1 VehicleSpeed | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpB | | - · - | |
| | | | Bacil |
| Name | Actual value | Expected Value | Result |

| a | | | |
|--|--------------|---------------------------|----------|
| Name | Actual Value | Expected Value | Result |
| PreDecelGain_Uls_M_f32 | 128138.188 | 128138.185 ± 0.0625 | ✓ |
| Prev1PreAttnComp_MtrNm_M_f32 | -420468.938 | -420469.0063 ± 0.9 | ✓ |
| Prev1SclDrvVel_RadpS_M_f32 | -64.6186523 | -64.61864443 ± 0.00390625 | • |
| Prev2PreAttnComp_MtrNm_M_f32 | -8.5 | -8.5 ± 0.00048828125 | ✓ |
| Prev2SclDrvVel_RadpS_M_f32 | -26.2999992 | -26.3 ± 0.00390625 | • |
| PrevTbarAng_HwDeg_M_f32 | -0.514705896 | -0.514705882 ± 0.00390625 | ✓ |
| TbarVelFiltSv_M_str.SV_Uls_f32 | 1.11588478 | 1.115892294 ± 0.00390625 | ✓ |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | 0 | 0 + 0 00048828125 | ✓ |



| Τ | | | | V |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ |
| DecelGain | 1 | DecelGain | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ |

| Proceedings 1.5 May 1.5 | T+ 04 0.00 (D+ 0+ | |
|--|---|-----------------------------------|
| Prebreichedings Minny Minny Minny | Test Step 3.30 (Repeat Count = 1) | ▼ |
| PiewTerMicRoving, Minhmu, M. E. 12 PrevTerSchröver, Basis S. M. 182 PrevTerSchröver, Basis S. M. 182 PrevTerSchröver, Bishing S. M. 183 PrevTerSchröver, Bis | | |
| Prev156-Dr.Vve Facips M, 152 18.2 | | |
| PROVERED AND CONTROL MATERNAL MATERIAN CONTROL MATERIAN C | | |
| Prev28D/Vel. Ratips JL 52 Prev18Da/Aga Probe JL 52 Prev18Da/Aga Prev | | |
| PievTbarAng_I-Mode, M_182 20 20 3.5 1. | Prev2PreAttnComp_MtrNm_M_f32 | |
| Ric mst A Ap FrqDepDmpninriCrop | Prev2SclDrvVel_RadpS_M_f32 | -120.8 |
| TravVerFilsty_M_str_X_Uis_52 | PrevTbarAng_HwDeg_M_f32 | 20 |
| Transferitive, M. str. K. Uis. 122 | Rte_Inst_Ap_FrqDepDmpnInrtCmp | tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp |
| k, Comsylvarioratio, Michaegh (22) 0.5 k, Complocedisanin Siew, Ulsey (122) 0.5 k, Dmplocedisanin Siew, Ulsey (122) 5.8 k, Dmplocedisanin Siew, Ulsey (122) 5.8 k, Dmplocedisanin Siew, Ulsey (122) 35.3 k, Dmplocedin Siew, Ulsey (122) 35.3 k, Intricing, Mirreani, Kepriss, 132 12.5 k, Intricing, Mirreani, Kepriss, 132 0.0004 k, Intricing, Mirreani, Kepriss, 132 0.4 k, Intricing, Mirreani, Mirreani, Kepriss, 132 0.4 k, Intricing, Mirreani, Mirreani, Kepriss, 132 0.4 k, Epol. ADRotelling Third, Mirreani, Kepriss, 132 0.4 k, Epol. ADRotelling Third, Mirreani, Kepriss, 132 0.6 k, Epol. ADRotelling Third, Mirreani, Kepriss, 133 0.6 k, Epol. ADRotelling Third, Mirreani, Kepriss, 134 0.7 k, Epol. ADRotelling Third, Mirreani, Kepriss, 134 0.7 k, Epol. ADRotelling Third, Mirreani, Kepriss, 134 0.7 k, Epol. ADRotelling Third, Mirreani, Kepri | TbarVelFiltSv_M_str.SV_Uls_f32 | -3.5 |
| k_ Comtractifit_MingDeg_128 k_ DimpGeetGainFisiew_UlspS_f32 k_ DimpGeetGainFisiew_UlspS_f32 k_ DimpGeetGainFisiew_UlspS_f32 k_ DimpGeetGainFisiew_UlspS_f32 k_ DimpGeetGainFisiew_UlspS_f32 k_ DimpGeetGainFisiew_UlspS_f32 l_ DimpGeetGainFisiew_UlspS_f32 k_ Indrom_Mirrices_KphpS_f32 k_ Indrom_Mirrices_KphpS_f32 k_ Indrom_Mirrices_KphpS_f32 k_ Indrom_Mirrices_KphpS_f32 l_ DimpGeetGainFisiew_UlspS_f32 l_ DimpGeetGai | TbarVelFiltSv_M_str.K_Uls_f32 | 0.63214 |
| K DmpDecelGain FSiew_UispS_B2 30.03 K DmpDearolThresh_KybpS_D2 5.8 K DmpGainOfThresh_KybpS_D2 35.3 K DmpCainOfThresh_KybpS_D2 12.5 K Indrom_Mitrelia_KymSq_D2 0.0004 K Indrom_Mitrelia_KymSq_D2 0.4 K Indrom_Mitrelia_KymSq_D2 0.4 L FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 161 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 328 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 494 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 827 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 94 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 1160 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 1160 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 1160 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 1893 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp170[0] 1869 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp171[0] 1789 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp171[1] 2130 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp171[1] 2130 2 FDD_ADDRollingTbWM_Mitrelia_Rds_umlp171[1] 3162 | k_CmnSysKinRatio_MtrDegpHwDeg_f32 | 85.13 |
| k_DmpGainOffTrresh_KphpS_132 35.3 k_DmpGainOffTrresh_KphpS_132 12.5 k_Indrom_Mitriesh_KphpS_132 12.5 k_Indrom_Mitriesh_KphpS_132 0.0004 k_Indrom_Mitriesh_KphpS_132 0.4 k_Indrom_Mitriesh_KphpS_132 0.4 k_Indrom_Mitriesh_KphpS_132 0.4 k_Indrom_Mitriesh_KphpS_132 0.4 k_Indrom_Mitriesh_KphpS_246 0.4 k_Indrom_Mitriesh_KphpSa_246 0.4 k_Indrom_Mitriesh_KphpSa_246 0.4 k_Indrom_Mitriesh_KphpSa_246 0.4 k_Indrom_Mitriesh_KphpSa_246 0.4 k_Indrom_Mitriesh_KphpSa_2466 0.4 | k_CmnTbarStiff_NmpDeg_f32 | 0.5 |
| N. DmpGainOrIThresh_Kephs_162 35.3 N. DmpGainOrIThresh_Kephs_172 12.5 N. Infromp_Mirvels_CaleFactor_Uls_152 0.0004 N. Infromp_Mirvels_CaleFactor_Uls_152 0.4 N. E. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[0] 161 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[1] 328 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[2] 494 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 661 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 827 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 194 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 1160 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 1160 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 1493 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(0)[8] 1493 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(1)[8] 1789 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(1)[8] 1789 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(1)[8] 3152 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(1)[8] 3152 12. FDD_ADDRailingToVM_MirkInmpRadps_um1p17(1)[8] 3493 12. FDD_ADDRailingToVM_MirkInmpRadps_u | k_DmpDecelGainFSlew_UlspS_f32 | 300.03 |
| Depth Company Compan | k_DmpDecelGain_Uls_f32 | 5.8 |
| NinCmp_Mtvlnetia_KgmSq_f32 | k_DmpGainOffThresh_KphpS_f32 | 35.3 |
| k_IntCmp_MtVel_ScaleFactor_Uls_132 | k_DmpGainOnThresh_KphpS_f32 | 12.5 |
| P. FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(0) 0 161 | k_InrtCmp_MtrInertia_KgmSq_f32 | 0.0004 |
| 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 328 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[2] 494 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[3] 661 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[4] 827 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[6] 994 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[6] 1160 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[7] 1226 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[7] 1226 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[8] 1493 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[9] 1659 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[9] 1789 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 2130 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 2471 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 2471 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 3493 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 3493 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 3493 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 3493 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[6] 3834 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[6] 3834 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[6] 3834 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 4775 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 4856 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 4856 2_FDD_ADDRollingTbYM_MrmpRadps_um1p17()[1] 4856 2_FDD_FreqTbYM_Hz_u1z4p4()[0] 886 2_FDD_FreqTbYM_Hz_u1z4p4()[0] 886 2_FDD_FreqTbYM_Hz_u1z4p4()[0] 886 2_FDD_FreqTbYM_Hz_u1z4p4()[0] 886 2_FDD_FreqTbYM_Hz_u1z4p4()[0] 992 2_FDD_FreqTbYM_Hz_u1z4p4()[1] 928 2_FDD_FreqTbYM_Hz_u1z4p4()[1] 928 2_FDD_FreqTbYM_Hz_u1z4p4()[1] 926 2_FDD_FreqTbYM_Hz_u | k_InrtCmp_MtrVel_ScaleFactor_Uls_f32 | 0.4 |
| I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[2] 494 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[3] 661 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[6] 827 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[6] 994 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[6] 1160 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[8] 1493 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(0)[9] 1659 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[0] 1789 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[1] 2130 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[1] 2130 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[2] 2471 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[3] 2811 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[4] 3152 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 3493 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 3493 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 3493 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 3493 I2_FDD_ADDRollingTbYM_MrhmpRadpS_um1p17(1)[6] 3493 I2_FDD_FreqTbYM_MrhmpRadpS_um1p17(1)[6] 380 I2_FDD_FreqTbYM_Hz_u1z4p4(0)[6] 886 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][0] | 161 |
| 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][3] 661 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][4] 827 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][6] 994 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][6] 1160 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][7] 1326 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][9] 1659 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[0][9] 1659 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][0] 1789 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][1] 2130 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][2] 2471 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][3] 2811 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][4] 3152 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3493 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3493 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3493 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3493 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3493 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3495 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3495 2_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17[1][6] 3496 2_FDD_FreqTbIYM_Hz_u12p4[0][0] 366 2_FDD_FreqTbIYM_Hz_u12p4[0][0] 366 2_FDD_FreqTbIYM_Hz_u12p4[0][0] 366 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 382 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 380 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 380 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 380 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 380 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 390 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 391 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 392 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 392 2_FDD_FreqTbIYM_Hz_u12p4[0][1] 396 2_FDD_FreqTbIYM_Hz_u12p4[0 | t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[0][1] | 328 |
| 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][3] 661 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][4] 827 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][5] 994 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][6] 1160 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][7] 1326 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][8] 1493 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][9] 1559 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[0][9] 1559 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][0] 1789 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][1] 2130 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][2] 2471 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][3] 2811 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][4] 3152 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][6] 3493 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][6] 3493 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][6] 3493 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][6] 3834 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][6] 3854 12_FDD_ADDRollingTbYM_MtrNmpRadpS_umtp17[1][6] 3856 12_FDD_FreqTbYM_Hz_u12p4[0][0] 386 12_FDD_FreqTbYM_Hz_u12p4[0][0] 386 12_FDD_FreqTbYM_Hz_u12p4[0][0] 386 12_FDD_FreqTbYM_Hz_u12p4[0][1] 382 12_FDD_FreqTbYM_Hz_u12p4[0][1] 382 12_FDD_FreqTbYM_Hz_u12p4[0][1] 384 12_FDD_FreqTbYM_Hz_u12p4[0][1] 386 12 | t2 FDD ADDRollingTblYM MtrNmpRadpS um1p17[0][2] | 494 |
| 12_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0) 4 827 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0) 5 994 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0) 5 1160 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0) 7 1326 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0) 8 1493 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(0) 9 1659 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 0 1789 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 10 1789 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 11 2130 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 2 2471 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 3 2811 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 4 3152 2_FDD_ADDRollingTbiYM_MtrNmpRadpS_um1p17(1) 6 3834 2_FDD_FreqTbiYM_Hz_u12p4(0) 6 380 2_FDD_FreqTbiYM_Hz_u12p4(0) 6 391 2_FDD_FreqTbiYM_Hz_u12p4(0) 6 392 2_FDD_FreqTbiYM_Hz_u12p4(0) 6 392 2_FDD_FreqTbiYM_Hz_u12p4(0) 6 394 2_FDD_FreqTbiYM_Hz_u12p4(0) 6 396 2_FDD_FreqTbiYM_Hz_u12p4(0) | | 661 |
| 12_FDD_ADRollingTbiYM_MtrNmpRadps_umtp17(0) 5 1160 | | 827 |
| 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17[0][6] 1160 12_FDD_ADDRollingTblYM_MrNmpRadpS_um1p17[0][7] 1326 | | 994 |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[8] 1326 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(0)[8] 1493 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[9] 1659 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[0] 1789 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[1] 2130 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[2] 2471 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[4] 3152 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 383 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 3834 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 3834 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 4856 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17(1)[6] 4856 12_FDD_FDADROllingTblYM_MtrNmpRadpS_um1p17(1)[6] 816 12_FDD_FreqTblYM_Hz_u12p4(0)[0] 816 12_FDD_FreqTblYM_Hz_u12p4(0)[1] 832 12_FDD_FreqTblYM_Hz_u12p4(0)[3] 864 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 896 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 912 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 912 12_FDD_FreqTblYM_Hz_u12p4(0)[6] 94 12_FDD_FreqTblYM_Hz_u12p4(0)[10] 960 12_FDD_Fre | | 1160 |
| 1493 | | |
| 12_FDD_ADROllingTbIYM_MtrNmpRadpS_umtp17(1)[9] 1659 12_FDD_ADROllingTbIYM_MtrNmpRadpS_umtp17(1)[1] 2130 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[2] 2471 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[3] 2811 12_FDD_ADROllingTbIYM_MtrNmpRadpS_umtp17(1)[3] 2811 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[5] 3493 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[6] 3834 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[7] 4175 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[8] 4515 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_umtp17(1)[9] 4856 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 832 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 832 12_FDD_FreqTbIYM_Hz_u12p4(0)[2] 848 12_FDD_FreqTbIYM_Hz_u12p4(0)[3] 864 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 886 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 912 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 928 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 944 12_FDD_FreqTbIYM_Hz_u12p4(0)[6] 960 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 992 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 992 12_FDD_FreqTbIYM_Hz_u12p4(0)[1] 992 <td></td> <td></td> | | |
| 1789 1 | | |
| 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][1] 2130 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][2] 2471 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][3] 2811 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][4] 3152 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][5] 3493 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] 4175 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][7] 4175 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 832 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 832 12_FDD_FreqTbIYM_Hz_u12p4[0][3] 864 12_FDD_FreqTbIYM_Hz_u12p4[0][4] 880 12_FDD_FreqTbIYM_Hz_u12p4[0][5] 996 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 912 12_FDD_FreqTbIYM_Hz_u12p4[0][8] 944 12_FDD_FreqTbIYM_Hz_u12p4[0][8] 944 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 996 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 996 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 996 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 992 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 992 12_FDD_FreqTbIYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17(1)[2] 2471 12_FDD_ADDRollingTb/YM_MtrNmpRadpS_um1p17(1)[3] 2811 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1)[4] 3152 12_FDD_ADDRollingTbYM_MtrNmpRadpS_um1p17(1)[5] 3493 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[6] 3834 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[7] 4175 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[8] 4516 12_FDD_ADDRollingTbIYM_MtrNmpRadpS_um1p17(1)[9] 4856 12_FDD_FreqTbIYM_Hz_u12p4[0][0] 816 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 832 12_FDD_FreqTbIYM_Hz_u12p4[0][2] 848 12_FDD_FreqTbIYM_Hz_u12p4[0][3] 864 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 896 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 912 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 912 12_FDD_FreqTbIYM_Hz_u12p4[0][6] 92 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 960 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 992 12_FDD_FreqTbIYM_Hz_u12p4[0][1] 992 12_FDD_FreqTbIYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][3] 2811 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][4] 3152 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][5] 3493 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][6] 3834 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][7] 4175 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][8] 4515 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_ADDRollingTb\YM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_FreqTb\YM_Hz_u12p4[0][0] 816 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 832 12_FDD_FreqTb\YM_Hz_u12p4[0][3] 864 12_FDD_FreqTb\YM_Hz_u12p4[0][3] 864 12_FDD_FreqTb\YM_Hz_u12p4[0][4] 880 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 912 12_FDD_FreqTb\YM_Hz_u12p4[0][6] 912 12_FDD_FreqTb\YM_Hz_u12p4[0][7] 928 12_FDD_FreqTb\YM_Hz_u12p4[0][9] 960 12_FDD_FreqTb\YM_Hz_u12p4[0][9] 960 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 976 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 992 12_FDD_FreqTb\YM_Hz_u12p4[0][1] 992 12_FDD_FreqTb\YM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][4] 3152 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3493 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3834 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4175 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4515 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_FreqTblYM_Hz_u12p4[0][0] 816 12_FDD_FreqTblYM_Hz_u12p4[0][1] 832 12_FDD_FreqTblYM_Hz_u12p4[0][2] 848 12_FDD_FreqTblYM_Hz_u12p4[0][3] 864 12_FDD_FreqTblYM_Hz_u12p4[0][4] 880 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][0] 976 12_FDD_FreqTblYM_Hz_u12p4[0][1] 992 12_FDD_FreqTblYM_Hz_u12p4[0][1] 992 12_FDD_FreqTblYM_Hz_u12p4[0][1] 992 12_FDD_FreqTblYM_Hz_u12p4[0][1] 1136 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][5] 3493 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3834 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4175 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4515 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_FreqTblYM_Hz_u12p4[0][0] 816 12_FDD_FreqTblYM_Hz_u12p4[0][1] 832 12_FDD_FreqTblYM_Hz_u12p4[0][2] 848 12_FDD_FreqTblYM_Hz_u12p4[0][3] 864 12_FDD_FreqTblYM_Hz_u12p4[0][6] 896 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][8] 960 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][6] 3834 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4175 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4515 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 816 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 832 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 848 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 864 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 880 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 896 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 912 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 912 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 944 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 944 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 960 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 976 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][7] 4175 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4515 t2_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4856 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 816 t2_FDD_FreqTblYM_Hz_u12p4[0][1] 832 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 848 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 864 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 896 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 912 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 928 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 944 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 960 t2_FDD_FreqTblYM_Hz_u12p4[0][0] 976 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][8] 4515 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_FreqTblYM_Hz_u12p4[0][0] 816 12_FDD_FreqTblYM_Hz_u12p4[0][1] 832 12_FDD_FreqTblYM_Hz_u12p4[0][2] 848 12_FDD_FreqTblYM_Hz_u12p4[0][3] 864 12_FDD_FreqTblYM_Hz_u12p4[0][4] 880 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_ADDRollingTblYM_MtrNmpRadpS_um1p17[1][9] 4856 12_FDD_FreqTblYM_Hz_u12p4[0][0] 816 12_FDD_FreqTblYM_Hz_u12p4[0][1] 832 12_FDD_FreqTblYM_Hz_u12p4[0][2] 848 12_FDD_FreqTblYM_Hz_u12p4[0][3] 864 12_FDD_FreqTblYM_Hz_u12p4[0][4] 880 12_FDD_FreqTblYM_Hz_u12p4[0][5] 896 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_FreqTbIYM_Hz_u12p4[0][0] 816 t2_FDD_FreqTbIYM_Hz_u12p4[0][1] 832 t2_FDD_FreqTbIYM_Hz_u12p4[0][2] 848 t2_FDD_FreqTbIYM_Hz_u12p4[0][3] 864 t2_FDD_FreqTbIYM_Hz_u12p4[0][4] 880 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 896 t2_FDD_FreqTbIYM_Hz_u12p4[0][6] 912 t2_FDD_FreqTbIYM_Hz_u12p4[0][7] 928 t2_FDD_FreqTbIYM_Hz_u12p4[0][8] 944 t2_FDD_FreqTbIYM_Hz_u12p4[0][9] 960 t2_FDD_FreqTbIYM_Hz_u12p4[0][10] 976 t2_FDD_FreqTbIYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTbIYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][1] 832 t2_FDD_FreqTblYM_Hz_u12p4[0][2] 848 t2_FDD_FreqTblYM_Hz_u12p4[0][3] 864 t2_FDD_FreqTblYM_Hz_u12p4[0][4] 880 t2_FDD_FreqTblYM_Hz_u12p4[0][5] 896 t2_FDD_FreqTblYM_Hz_u12p4[0][6] 912 t2_FDD_FreqTblYM_Hz_u12p4[0][7] 928 t2_FDD_FreqTblYM_Hz_u12p4[0][8] 944 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 960 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 976 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][2] 848 12_FDD_FreqTblYM_Hz_u12p4[0][3] 864 12_FDD_FreqTblYM_Hz_u12p4[0][4] 880 12_FDD_FreqTblYM_Hz_u12p4[0][5] 896 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][3] 864 12_FDD_FreqTblYM_Hz_u12p4[0][4] 880 12_FDD_FreqTblYM_Hz_u12p4[0][5] 896 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][4] 880 12_FDD_FreqTblYM_Hz_u12p4[0][5] 896 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][5] 896 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][6] 912 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][7] 928 12_FDD_FreqTblYM_Hz_u12p4[0][8] 944 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][8] 944 t2_FDD_FreqTblYM_Hz_u12p4[0][9] 960 t2_FDD_FreqTblYM_Hz_u12p4[0][10] 976 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| 12_FDD_FreqTblYM_Hz_u12p4[0][9] 960 12_FDD_FreqTblYM_Hz_u12p4[0][10] 976 12_FDD_FreqTblYM_Hz_u12p4[0][11] 992 12_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][10] 976 t2_FDD_FreqTblYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[0][11] 992 t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][0] 1136 | | |
| / / / _ / _ / _ / _ / _ | | |
| t2 FDD FregTblYM Hz u12p4[1][1] 1152 | | |
| | t2_FDD_FreqTblYM_Hz_u12p4[1][1] | 1152 |
| | t2_FDD_FreqTblYM_Hz_u12p4[1][2] | |
| t2_FDD_FreqTblYM_Hz_u12p4[1][3] 1184 | t2_FDD_FreqTblYM_Hz_u12p4[1][3] | 1184 |

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| Nama | Input Value | |
|--|--------------|--|
| Name | Input Value | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][4] | 1200 1216 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][5] 2_FDD_FreqTblYM_Hz_u12p4[1][6] | 1232 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][7] | 1248 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][8] | 1264 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][9] | 1280 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][10] | 1296 | |
| 2_FDD_FreqTblYM_Hz_u12p4[1][11] | 1312 | |
| _CmnVehSpd_Kph_u9p7[0] | 10368 | |
| _CmnVehSpd_Kph_u9p7[1] | 10496 | |
| CmnVehSpd Kph u9p7[2] | 10624 | |
| CmnVehSpd Kph u9p7[3] | 10752 | |
| CmnVehSpd_Kph_u9p7[4] | 10880 | |
| CmnVehSpd_Kph_u9p7[5] | 11008 | |
| CmnVehSpd_Kph_u9p7[6] | 11136 | |
| CmnVehSpd_Kph_u9p7[7] | 11264 | |
| _CmnVehSpd_Kph_u9p7[8] | 11392 | |
| CmnVehSpd_Kph_u9p7[9] | 11520 | |
| _CmnVehSpd_Kph_u9p7[10] | 11648 | |
| CmnVehSpd_Kph_u9p7[11] | 11776 | |
| _DmpADDCoefX_MtrNm_u4p12[0] | 12698 | |
| DmpADDCoefX_MtrNm_u4p12[1] | 13107 | |
| DmpADDCoefX_MtrNm_u4p12[2] | 13517 | |
| DmpADDCoefX_MtrNm_u4p12[3] | 13926 | |
| DmpADDCoefX_MtrNm_u4p12[4] | 14336 | |
| DmpADDCoefX_MtrNm_u4p12[5] | 14746 | |
| _DmpADDCoefX_MtrNm_u4p12[6] | 15155 | |
| _DmpADDCoefX_MtrNm_u4p12[7] | 15565 | |
| _DmpADDCoefX_MtrNm_u4p12[8] | 15974 | |
| _DmpADDCoefX_MtrNm_u4p12[9] | 16384 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[0] | 5792 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[1] | 5824 | |
| DmpDecelGainSlewX_MtrRadpS_u11p5[2] | 5856 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[3] | 5888 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[4] | 5920 | |
| _DmpDecelGainSlewX_MtrRadpS_u11p5[5] | 5952 | |
| _DmpDecelGainSlewY_UlspS_u13p3[0] | 1208 | |
| _DmpDecelGainSlewY_UlspS_u13p3[1] | 1216 | |
| _DmpDecelGainSlewY_UlspS_u13p3[2] | 1224 | |
| _DmpDecelGainSlewY_UlspS_u13p3[3] | 1232 | |
| _DmpDecelGainSlewY_UlspS_u13p3[4] | 1240 | |
| _DmpDecelGainSlewY_UlspS_u13p3[5] | 1248 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[0] | 6554 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[1] | 8192 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[2] | 9830 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[3] | 11469 | |
| _DmpFiltKpWIRBIndY_Uls_u2p14[4] | 13107 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[0] | 161 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[1] | 328 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[2] | 494 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[3] | 661 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[4] | 827 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[5] | 994 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[6] | 1160 | |
| FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[7] | 1326 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[8] | 1493 | |
| _FDD_ADDStaticTblY_MtrNmpRadpS_um1p17[9] | 1659 | |
| FDD_AttenTblX_MtrRadpS_u12p4[0] | 1648 | |
| FDD_AttenTbIX_MtrRadpS_u12p4[1] | 2480 | |
| FDD_AttenTblY_Uls_u8p8[0] | 218 | |
| _FDD_AttenTblY_Uls_u8p8[1] | 220 | |
| FDD_BlendTbIY_Uls_u8p8[0] | 218 | |
| _FDD_BlendTbIY_Uls_u8p8[1] | 220 | |
| FDD_BlendTbIY_Uls_u8p8[2] | 223 | |
| _FDD_BlendTblY_Uls_u8p8[3] | 225 | |
| _FDD_BlendTblY_Uls_u8p8[4] | 227 | |
| _FDD_BlendTbIY_Uls_u8p8[5] | 230 | |
| _FDD_BlendTbIY_Uls_u8p8[6] | 232 | |
| _FDD_BlendTblY_Uls_u8p8[7] | 234 | |
| _FDD_BlendTblY_Uls_u8p8[8] | 237 | |
| _FDD_BlendTblY_Uls_u8p8[9] | 239 | |
| _FDD_BlendTblY_Uls_u8p8[10] | 241 | |
| _FDD_BlendTblY_Uls_u8p8[11] | 243 | |

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| Name | Input Value | | |
|--|---|----------------|-------|
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[0] | 38 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[1] | 51 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[2] | 64 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[3] | 77 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[4] | 90 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[5] | 102 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[6] | 115 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[7] | 128 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[8] | 141 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[9] | 154 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[10] | 166 | | |
| t_InrtCmp_ScaleFactorTblY_Uls_u9p7[11] | 179 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[0] | 92 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[1] | 93 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[2] | 95 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[3] | 96 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[4] | 97 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[5] | 99 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[6] | 100 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[7] | 101 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[8] | 102 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[9] | 104 | | |
| t_InrtCmp_TBarVel_ScaleFactorTblY_Uls_u9p7[10] | 105 | | |
| t InrtCmp TBarVel ScaleFactorTblY Uls u9p7[11] | 106 | | |
| t RIAstWIRBIndTblY Uls u2p14[0] | 3277 | | |
| t RIAstWIRBIndTblY Uls u2p14[1] | 4915 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[2] | 6554 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[3] | 8192 | | |
| t_RIAstWIRBIndTblY_UIs_u2p14[4] | 9830 | | |
| t WIRBIndTbIX MtrNm u8p8[0] | 538 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[1] | 563 | | |
| t_WIRBIndTbIX_MtrNm_u8p8[2] | 589 | | |
| t WIRBIndTbIX MtrNm u8p8[3] | 614 | | |
| t WIRBIndTbIX MtrNm u8p8[4] | 640 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_BaseAssistCmd_MtrNm_f32.value | 5.5 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_CRFMotorVel_MtrRadpS_f32.value | 110.05 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_Igc.value | 0 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_HwTorque_HwNm_f32.value | 10 | | |
| tgt FrqDepDmpnInrtCmp Per1 VehicleLonAccel KphpS f32.value | 10.03 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_VehicleSpeed_Kph_f32.value | 325.02 | | |
| tgt_FrqDepDmpnInrtCmp_Per1_WIRCmdAmpBInd_MtrNm_f32.value | 5.3 | | |
| tgt_rrqDepDriprinintCirip_Ferr_wikCiridArripBilid_witrini_132.value tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_BaseAssistCm | | | |
| tgt Rte Inst Ap FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp Peri BaseAssistCm | | | |
| | pSr tgt_FrqDepDmpnInrtCmp_Per1_FreqDepDmpSrlComSvcDft_Cnt_lgc | | |
| | | | |
| | onin tgt_FrqDepDmpninrtCmp_Per1_FrqDepDmpninrtCmp_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_HwTorque_Hw | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleLonAcc | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_VehicleSpeed_ | | | |
| tgt_Rte_Inst_Ap_FrqDepDmpnInrtCmp.FrqDepDmpnInrtCmp_Per1_WIRCmdAmpE | | | |
| Name | Actual Value | Expected Value | Resul |
| ProDocal Coin I II M f22 | | | |

| 2 | | 1.2 | | | |
|--|--------------|-------------------------------|----------|--|--|
| Name | Actual Value | Expected Value | Result | | |
| PreDecelGain_Uls_M_f32 | 128239.961 | 128239.9599 ± 0.0625 | ~ | | |
| Prev1PreAttnComp_MtrNm_M_f32 | 224855.719 | 224855.71732493 ± 0.9 | ✓ | | |
| Prev1SclDrvVel_RadpS_M_f32 | 42.4358139 | 42.4358127289631 ± 0.00390625 | ✓ | | |
| Prev2PreAttnComp_MtrNm_M_f32 | 1.29999995 | 1.3 ± 0.00048828125 | ✓ | | |
| Prev2SclDrvVel_RadpS_M_f32 | 18.2000008 | 18.2 ± 0.00390625 | ✓ | | |
| PrevTbarAng_HwDeg_M_f32 | 20 | 20 ± 0.00390625 | ✓ | | |
| TbarVelFiltSv_M_str.SV_Uls_f32 | -1.28751016 | -1.28751 ± 0.00390625 | ✓ | | |
| tot FraDenDmonInrtCmn Per1 FraDenDmonInrtCmn MtrNm f32 value | 8 80000019 | 8 8 + 0 00048828125 | ~ | | |

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| T ✓ | | | | | | | |
|---|-------|---|-------|--------|--|--|--|
| Actual Function | Count | Expected Function | Count | Result | | | |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP0_CheckpointReached | 1 | ~ | | | |
| ADDCoefCalc | 1 | ADDCoefCalc | 1 | ~ | | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 5 | IntplVarXY_u16_u16Xu16Y_Cnt | 5 | ~ | | | |
| DecelGain | 1 | DecelGain | 1 | ~ | | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | | |
| DriverVelCalc | 1 | DriverVelCalc | 1 | • | | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | • | | | |
| FilterCoefCalc | 1 | FilterCoefCalc | 1 | • | | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 4 | IntplVarXY_u16_u16Xu16Y_Cnt | 4 | • | | | |
| GenFddlcCmd | 1 | GenFddlcCmd | 1 | ~ | | | |
| IntplVarXY_u16_u16Xu16Y_Cnt | 1 | IntplVarXY_u16_u16Xu16Y_Cnt | 1 | ~ | | | |
| Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | Rte_Call_FrqDepDmpnInrtCmp_Per1_CP1_CheckpointReached | 1 | ~ | | | |