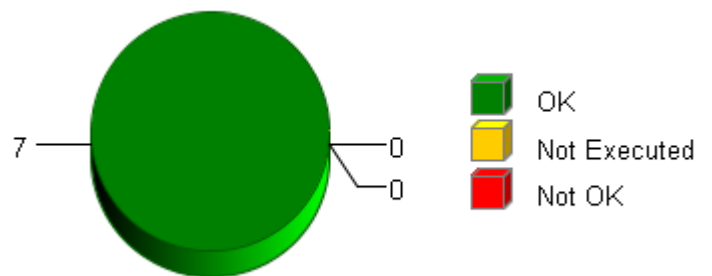


## Summary

**Total Test Objects:** 7  
**Successful:** 7  
**Failed:** 0  
**Not Executed:** 0  
**Date:** 2016-06-03  
**Time:** 15:23:37+0530

## Overall Test Object Results (including Coverage)



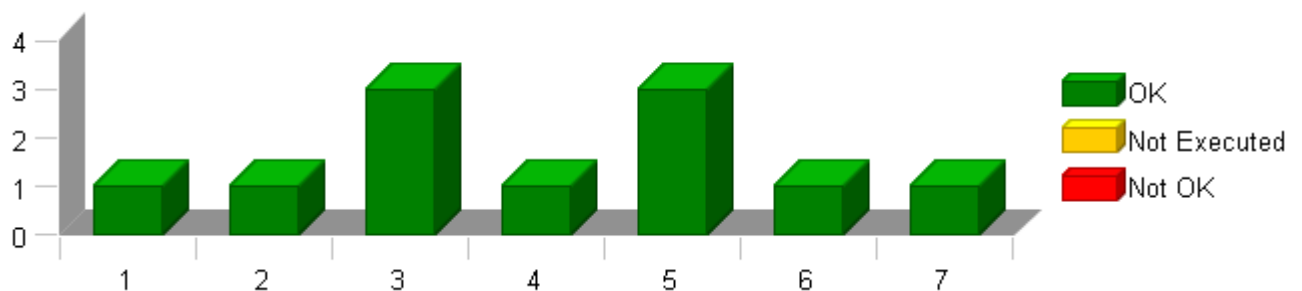
## Selected Project Items

Test Object "CBD\_UnitTest/CDDInterface/CDDInterface\_Init1"  
Test Object "CBD\_UnitTest/CDDInterface/CDDInterface\_Init2"  
Test Object "CBD\_UnitTest/CDDInterface/CDDInterface\_Per1"  
Test Object "CBD\_UnitTest/CDDInterface/CDDInterface\_Per2"  
Test Object "CBD\_UnitTest/CDDInterface/CDDInterface\_Per4"  
Test Object "CBD\_UnitTest/CDDInterface/CDDInterface\_Per5"  
Test Object "CBD\_UnitTest/CDDInterface/CDDPorts\_ApplyMtrElecMechPol"

## Used Test Environments

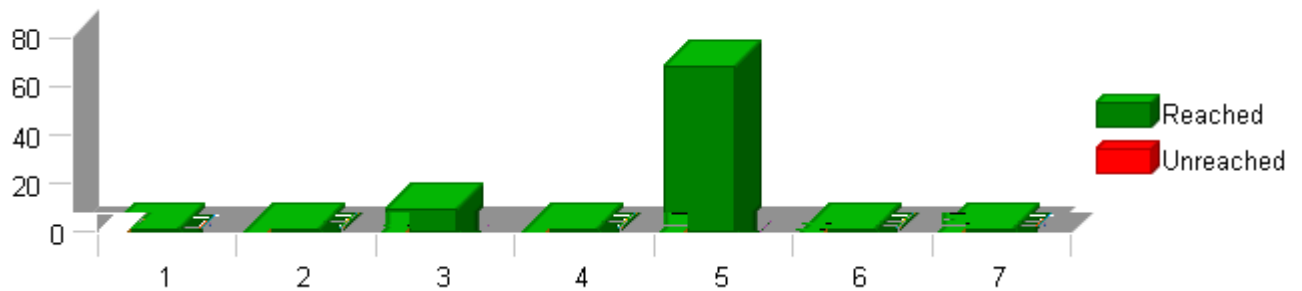
TI TMS 570 PLS UDE (Default)

## Test Case Results for Each Test Object (without Coverage)



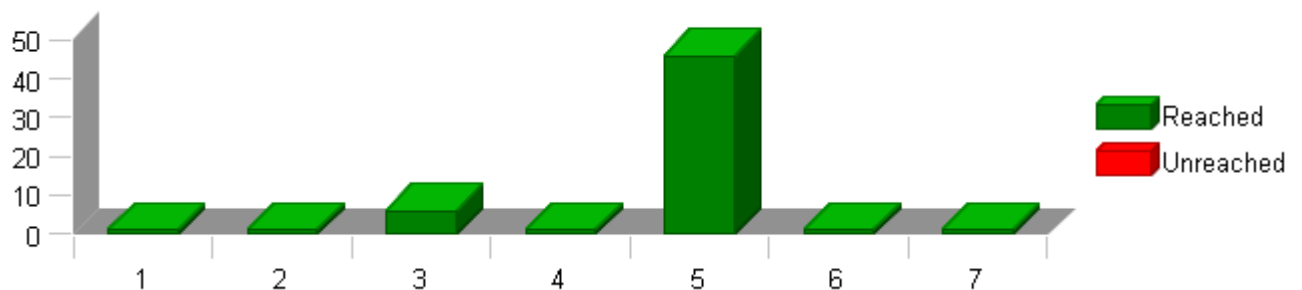
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

### Statement (C0) Coverage: Total Statements for Each Test Object



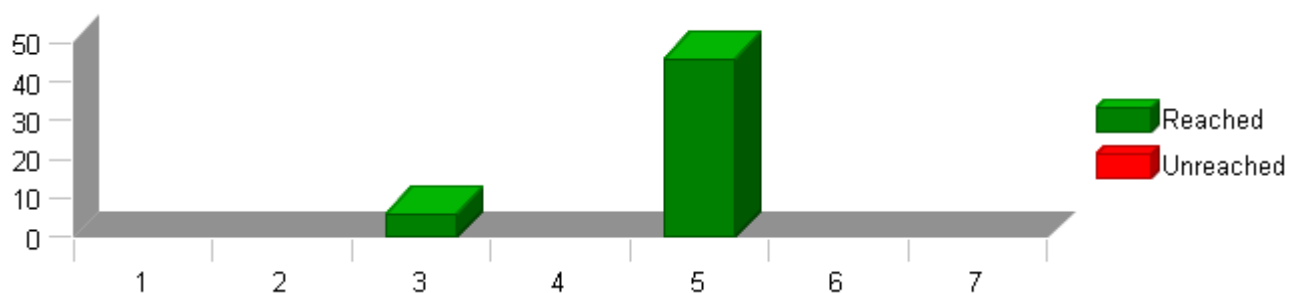
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

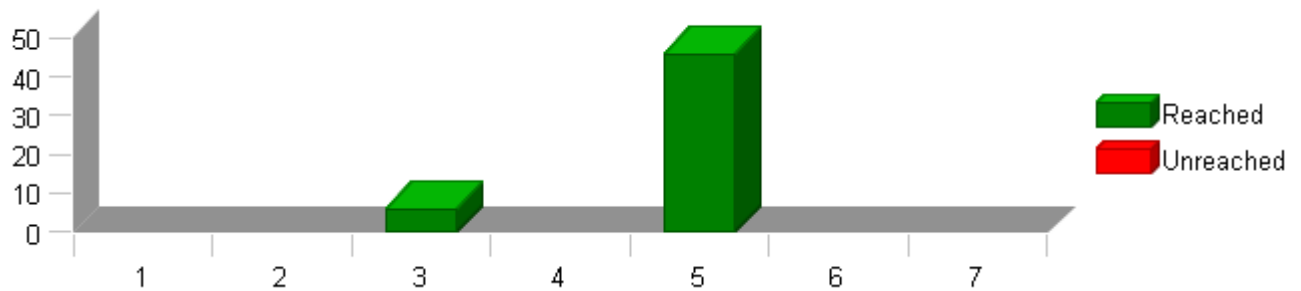
### Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

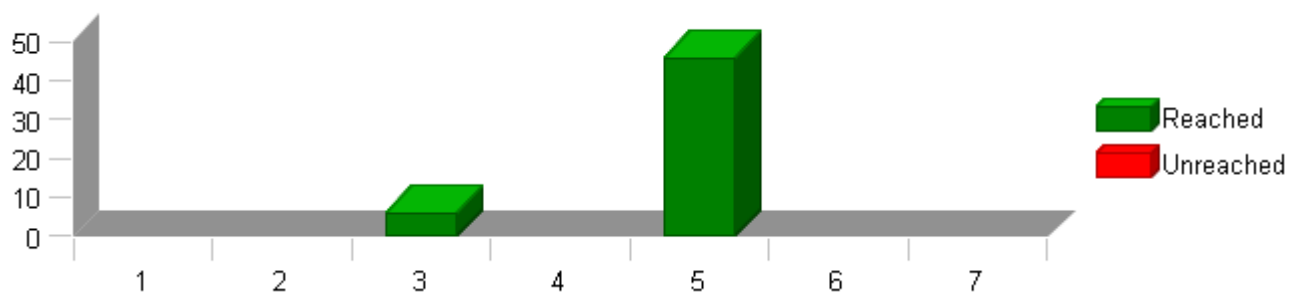
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	CDDInterface_FIASA	100 %	100 %	100 %	100 %	100 %	11 of 11 passed	✓
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	11 of 11 passed	✓
	CDDInterface	100 %	100 %	100 %	100 %	100 %	11 of 11 passed	✓
1	<a href="#">CDDInterface_Init1</a>	100 %	100 %	-	-	-	1 of 1 passed	✓
2	<a href="#">CDDInterface_Init2</a>	100 %	100 %	-	-	-	1 of 1 passed	✓
3	<a href="#">CDDInterface_Per1</a>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
4	<a href="#">CDDInterface_Per2</a>	100 %	100 %	-	-	-	1 of 1 passed	✓
5	<a href="#">CDDInterface_Per4</a>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
6	<a href="#">CDDInterface_Per5</a>	100 %	100 %	-	-	-	1 of 1 passed	✓
7	<a href="#">CDDPorts_ApplyMtrElecMechPol</a>	100 %	100 %	-	-	-	1 of 1 passed	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530

CDDInterface\_Init1



Project	CDDInterface_FIASA
Module	CDDInterface
Test Object	CDDInterface_Init1

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Information *****  Name of Tester: Priyanka Shitole Code File(s) Under Test: Sa_CDDInterface.c Code File(s) Version: 4 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1714 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: Test Date: 6/3/2016 Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested." Note2: "CBD_Sandbox_dbg.map" map file is embedded for reference." *****

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530

CDDInterface\_Init1



Workspace File

D:\Synergy\_Work\_Area\CDDInterface\_FIASA\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530

CDDInterface\_Init1



## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)  
CPU Cycles:

TS1.1 576.00 Cycles  
TS1.2 558.00 Cycles  
TS1.3 558.00 Cycles  
TS1.4 558.00 Cycles  
TS1.5 558.00 Cycles  
TS1.6 558.00 Cycles  
TS1.7 558.00 Cycles  
TS1.8 558.00 Cycles  
TS1.9 558.00 Cycles  
TS1.10 558.00 Cycles  
TS1.11 558.00 Cycles  
TS1.12 558.00 Cycles  
TS1.13 558.00 Cycles  
TS1.14 558.00 Cycles  
TS1.15 558.00 Cycles  
TS1.16 558.00 Cycles  
TS1.17 558.00 Cycles  
TS1.18 558.00 Cycles  
TS1.19 558.00 Cycles  
TS1.20 558.00 Cycles  
TS1.21 558.00 Cycles

**Description** Vector Description:

TS1.1All min  
TS1.2All max  
TS1.3MtrElecMechPolarity\_Cnt\_s08==>Min  
TS1.4MtrElecMechPolarity\_Cnt\_s08==>Max  
TS1.5MtrPosPolarity\_Cnt\_s08==>Min  
TS1.6MtrPosPolarity\_Cnt\_s08==>Max  
TS1.7Nvm\_IgnCntr\_Cnt\_u16==>Min  
TS1.8Nvm\_IgnCntr\_Cnt\_u16==>Max  
TS1.9Nvm\_IgnCntr\_Cnt\_u16==>Pos  
TS1.10NxrMEC\_Uls\_G\_enum==>ProductionMode  
TS1.11NxrMEC\_Uls\_G\_enum==>ManufacturingMode  
TS1.12NxrMEC\_Uls\_G\_enum==>EngineeringMode  
TS1.13HET\_P1ACC\_1.memory.data\_word==>Min  
TS1.14HET\_P1ACC\_1.memory.data\_word==>Max  
TS1.15HET\_P1ACC\_1.memory.data\_word==>Pos  
TS1.16HET\_P2ACC\_1.memory.data\_word==>Min  
TS1.17HET\_P2ACC\_1.memory.data\_word==>Max  
TS1.18HET\_P2ACC\_1.memory.data\_word==>Pos  
TS1.19HET\_P3ACC\_1.memory.data\_word==>Min  
TS1.20HET\_P3ACC\_1.memory.data\_word==>Max  
TS1.21HET\_P3ACC\_1.memory.data\_word==>Pos

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	0		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	0		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	0		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	0	0	✓
PXAccumPrev_Cnt_M_u32[1]	0	0	✓
PXAccumPrev_Cnt_M_u32[2]	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	0	0	✓

T	Actual Function	Count	Expected Function	Count	Result
	CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
	CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

## Test Step 1.2 (Repeat Count = 1) ✓

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	65535		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4294967295		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	536870911	536870911	✓
PXAccumPrev_Cnt_M_u32[1]	536870911	536870911	✓
PXAccumPrev_Cnt_M_u32[2]	536870911	536870911	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	65535	65535	✓

## T ✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 1.3 (Repeat Count = 1) ✓

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	15968		
NxtrMEC_Uls_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	4023279872		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3318966016		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4084006144		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	502909984	502909984	✓
PXAccumPrev_Cnt_M_u32[1]	414870752	414870752	✓
PXAccumPrev_Cnt_M_u32[2]	510500768	510500768	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	15968	15968	✓

## T ✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 1.4 (Repeat Count = 1) ✓

Name	Input Value
Nvm_IgnCntr_Cnt_u16	31442
NxtrMEC_Uls_G_enum	0
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2734137344
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1686146560



# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

Name	Input Value		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3851144192		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

Name	Actual Value	Expected Value	Result
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	7860960	7860960	✓
PXAccumPrev_Cnt_M_u32[1]	67876256	67876256	✓
PXAccumPrev_Cnt_M_u32[2]	290536032	290536032	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	46683	46683	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.7 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	0		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1693420288		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1943306496		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3786180352		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	211677536	211677536	✓
PXAccumPrev_Cnt_M_u32[1]	242913312	242913312	✓
PXAccumPrev_Cnt_M_u32[2]	473272544	473272544	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.8 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	65535		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2495357440		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1466253312		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	1567960576		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✔
CDD_Vecu_Volt_G_f32[0]	5	5	✔
CDD_Vecu_Volt_G_f32[1]	5	5	✔
PXAccumPrev_Cnt_M_u32[0]	311919680	311919680	✔
PXAccumPrev_Cnt_M_u32[1]	183281664	183281664	✔
PXAccumPrev_Cnt_M_u32[2]	195995072	195995072	✔
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✔
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✔
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	65535	65535	✔

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

Test Step 1.9 (Repeat Count = 1)				
Name	Input Value			
Nvm_IgnCnt_Cnt_u16	60140			
NxtrMEC_Uls_G_enum	0			
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1			
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1			
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2557631744			
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3798926080			
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	1098332416			
Name	Actual Value	Expected Value	Result	
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓	
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓	
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓	
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓	
CDD_Vecu_Volt_G_f32[0]	5	5	✓	
CDD_Vecu_Volt_G_f32[1]	5	5	✓	
PXAccumPrev_Cnt_M_u32[0]	319703968	319703968	✓	
PXAccumPrev_Cnt_M_u32[1]	474865760	474865760	✓	
PXAccumPrev_Cnt_M_u32[2]	137291552	137291552	✓	
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓	
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✓	
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	60140	60140	✓	

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

Test Step 1.10 (Repeat Count = 1)				
Name	Input Value			
Nvm_IgnCnt_Cnt_u16	13077			
NxtrMEC_Uls_G_enum	0			
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1			
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1			
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1140567040			
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2417867264			
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4284291072			
Name	Actual Value	Expected Value	Result	
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓	
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓	
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓	
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓	
CDD_Vecu_Volt_G_f32[0]	5	5	✓	
CDD_Vecu_Volt_G_f32[1]	5	5	✓	
PXAccumPrev_Cnt_M_u32[0]	142570880	142570880	✓	
PXAccumPrev_Cnt_M_u32[1]	302233408	302233408	✓	
PXAccumPrev_Cnt_M_u32[2]	535536384	535536384	✓	
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓	
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✓	
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	13077	13077	✓	

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

## Test Step 1.11 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	24670		
NxtrMEC_Uls_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElec	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	3445341952		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3400373504		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	937917184		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	430667744	430667744	✓
PXAccumPrev_Cnt_M_u32[1]	425046688	425046688	✓
PXAccumPrev_Cnt_M_u32[2]	117239648	117239648	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	24670	24670	✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.12 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	58191		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2101839360		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3223979008		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	664031744		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	262729920	262729920	✓
PXAccumPrev_Cnt_M_u32[1]	402997376	402997376	✓
PXAccumPrev_Cnt_M_u32[2]	83003968	83003968	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	58191	58191	✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.13 (Repeat Count = 1)

Name	Input Value
Nvm_IgnCntr_Cnt_u16	27158
NxtrMEC_Uls_G_enum	1
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	0
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2687547136

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

Name	Input Value		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3165244672		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	0	0	✓
PXAccumPrev_Cnt_M_u32[1]	335943392	335943392	✓
PXAccumPrev_Cnt_M_u32[2]	395655584	395655584	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	27158	27158	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 1.14 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	5870		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2930286080		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	1091918848		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	536870911	536870911	✓
PXAccumPrev_Cnt_M_u32[1]	366285760	366285760	✓
PXAccumPrev_Cnt_M_u32[2]	136489856	136489856	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	5870	5870	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 1.15 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	64739		
NxtrMEC_Uls_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecI	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2167769344		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2812078336		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2563682048		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

Name	Actual Value	Expected Value	Result
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	270971168	270971168	✓
PXAccumPrev_Cnt_M_u32[1]	351509792	351509792	✓
PXAccumPrev_Cnt_M_u32[2]	320460256	320460256	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	64739	64739	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.16 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	30094		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1554131456		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	0		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3534665216		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	194266432	194266432	✓
PXAccumPrev_Cnt_M_u32[1]	0	0	✓
PXAccumPrev_Cnt_M_u32[2]	441833152	441833152	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	30094	30094	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.17 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	48106		
NxtrMEC_Uls_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1121993984		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2448072960		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	140249248	140249248	✓
PXAccumPrev_Cnt_M_u32[1]	536870911	536870911	✓
PXAccumPrev_Cnt_M_u32[2]	306009120	306009120	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	48106	48106	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.18 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	18372		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	3193041920		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3714197504		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2107826176		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	399130240	399130240	✓
PXAccumPrev_Cnt_M_u32[1]	464274688	464274688	✓
PXAccumPrev_Cnt_M_u32[2]	263478272	263478272	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	18372	18372	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 1.19 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	14193		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1903748864		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2778103040		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✔
CDD_Vecu_Volt_G_f32[0]	5	5	✔
CDD_Vecu_Volt_G_f32[1]	5	5	✔
PXAccumPrev_Cnt_M_u32[0]	237968608	237968608	✔
PXAccumPrev_Cnt_M_u32[1]	347262880	347262880	✔
PXAccumPrev_Cnt_M_u32[2]	0	0	✔
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✔
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✔
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt C	14193	14193	✔

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:11:30+0530



CDDInterface\_Init1

## Test Step 1.20 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	40629		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElec	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	3616542208		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1427065856		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4294967295		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	452067776	452067776	✓
PXAccumPrev_Cnt_M_u32[1]	178383232	178383232	✓
PXAccumPrev_Cnt_M_u32[2]	536870911	536870911	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	40629	40629	✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 1.21 (Repeat Count = 1)

Name	Input Value		
Nvm_IgnCntr_Cnt_u16	62983		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecI	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1453758720		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	4037074688		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3323480832		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓
PXAccumPrev_Cnt_M_u32[0]	181719840	181719840	✓
PXAccumPrev_Cnt_M_u32[1]	504634336	504634336	✓
PXAccumPrev_Cnt_M_u32[2]	415435104	415435104	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	✓
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	62983	62983	✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓



# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530

CDDInterface\_Per4



Project	CDDInterface_FIASA
Module	CDDInterface
Test Object	CDDInterface_Per4

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

## Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Information *****  Name of Tester: Priyanka Shitole Code File(s) Under Test: Sa_CDDInterface.c Code File(s) Version: 4 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1714 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: Test Date: 6/3/2016 Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2: "CBD_Sandbox_dbg.map" map file is embedded for reference." *****

## Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530

CDDInterface\_Per4



Attributes	
Name	Value
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530

CDDInterface\_Per4



## Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:  TS1.1 1048.00 Cycles TS1.2 391.00 Cycles
Description	Vector Description:  TS1.1"Longest Execution Path: (MtrElecMechPolarity_Cnt_T_s08 != PrevMtrElecMechPolarity_Cnt_M_s08)=>True (D_DEFEATENGON_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>False (D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>False (D_DEFEATKININTDIAG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATKININTDIAG_CNT_B32))=>False (D_DEFEATTHERMALDC_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTHERMALDC_CNT_B32))=>False (D_DEFEATEOTIMPACT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False (D_DEFEATEOTTHERMAL_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTTHERMAL_CNT_B32))=>False (D_DEFEATMTRVELNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRVELNRMLRN_CNT_B32))=>False (D_DEFEATMTRPARMNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRPARMNRMLRN_CNT_B32))=>False (D_DEFEATTQRMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTQRMP_CNT_B32))=>False (D_DEFEATMODIDX_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMODIDX_CNT_B32))=>True (D_DEFEATPULLCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPULLCOMP_CNT_B32))=>True (D_DEFEATWHLIMBREJ_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATWHLIMBREJ_CNT_B32))=>False (D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>False (D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False (D_DEFEATHYSTCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTCOMP_CNT_B32))=>True (D_DEFEATHYSTADD_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>True (D_DEFEATRETURN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRETURN_CNT_B32))=>False (D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>True (D_DEFEATLIMIT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>True (D_DEFEATFRICLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFRICLRN_CNT_B32))=>True (D_DEFEATRXMSG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRXMSG_CNT_B32))=>False (D_DEFEATMULTIGNMASK_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMULTIGNMASK_CNT_B32))=>True" TS1.2"Shortest Execution Path: (MtrElecMechPolarity_Cnt_T_s08 != PrevMtrElecMechPolarity_Cnt_M_s08)=>False (D_DEFEATENGON_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>False (D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>False (D_DEFEATKININTDIAG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATKININTDIAG_CNT_B32))=>False (D_DEFEATTHERMALDC_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTHERMALDC_CNT_B32))=>False (D_DEFEATEOTIMPACT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False (D_DEFEATEOTTHERMAL_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTTHERMAL_CNT_B32))=>False (D_DEFEATMTRVELNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRVELNRMLRN_CNT_B32))=>False (D_DEFEATMTRPARMNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRPARMNRMLRN_CNT_B32))=>False (D_DEFEATTQRMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTQRMP_CNT_B32))=>False (D_DEFEATMODIDX_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMODIDX_CNT_B32))=>False (D_DEFEATPULLCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPULLCOMP_CNT_B32))=>False (D_DEFEATWHLIMBREJ_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATWHLIMBREJ_CNT_B32))=>False (D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>False (D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False (D_DEFEATHYSTCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTCOMP_CNT_B32))=>False (D_DEFEATHYSTADD_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>False (D_DEFEATRETURN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRETURN_CNT_B32))=>False (D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>False (D_DEFEATLIMIT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>False (D_DEFEATFRICLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFRICLRN_CNT_B32))=>False (D_DEFEATRXMSG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRXMSG_CNT_B32))=>False (D_DEFEATMULTIGNMASK_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMULTIGNMASK_CNT_B32))=>False"

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value
CDD_DftAssTbI_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	-2.98521
CDD_EOLSrlComSvcDft_Cnt_G_b32	1972815360
CDD_EnergyModeState_Cnt_G_u08	1
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1

Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-2.98520994	-2.98521	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1972815360	1972815360	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓	
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓	

## Test Step 1.2 (Repeat Count = 1) ✓

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	0		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-16	-16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	✓	

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

## Test Case 2: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)  
CPU Cycles:

TS2.1 391.00 Cycles  
TS2.2 399.00 Cycles  
TS2.3 975.00 Cycles  
TS2.4 1011.00 Cycles  
TS2.5 515.00 Cycles  
TS2.6 432.00 Cycles  
TS2.7 420.00 Cycles  
TS2.8 441.00 Cycles  
TS2.9 475.00 Cycles  
TS2.10 442.00 Cycles  
TS2.11 376.00 Cycles  
TS2.12 987.00 Cycles  
TS2.13 461.00 Cycles  
TS2.14 865.00 Cycles  
TS2.15 383.00 Cycles  
TS2.16 486.00 Cycles  
TS2.17 934.00 Cycles  
TS2.18 968.00 Cycles  
TS2.19 434.00 Cycles  
TS2.20 910.00 Cycles  
TS2.21 462.00 Cycles

**Description** Vector Description:

TS2.1All min  
TS2.2all max  
TS2.3MtrElecMechPolarity\_Cnt\_s08==>Min  
TS2.4MtrElecMechPolarity\_Cnt\_s08==>Max  
TS2.5PrevMtrElecMechPolarity\_Cnt\_M\_s08==>Min  
TS2.6PrevMtrElecMechPolarity\_Cnt\_M\_s08==>Max  
TS2.7CDD\_DftAsstTbl\_Cnt\_G\_lgc==>Min  
TS2.8CDD\_DftAsstTbl\_Cnt\_G\_lgc==>Max  
TS2.9CDD\_DwnldAsstGain\_Uls\_G\_f32==>Min  
TS2.10CDD\_DwnldAsstGain\_Uls\_G\_f32==>Max  
TS2.11CDD\_DwnldAsstGain\_Uls\_G\_f32==>Pos  
TS2.12CDD\_DwnldAsstGain\_Uls\_G\_f32==>Zero  
TS2.13CDD\_DwnldAsstGain\_Uls\_G\_f32==>Neg  
TS2.14CDD\_EOLSrlComSvcDft\_Cnt\_G\_b32==>Min  
TS2.15CDD\_EOLSrlComSvcDft\_Cnt\_G\_b32==>Max  
TS2.16CDD\_EOLSrlComSvcDft\_Cnt\_G\_b32==>Pos  
TS2.17CDD\_EnergyModeState\_Cnt\_G\_u08==>Min  
TS2.18CDD\_EnergyModeState\_Cnt\_G\_u08==>Max  
TS2.19CDD\_EnergyModeState\_Cnt\_G\_u08==>Pos  
TS2.20MfgDiagInhibit\_Uls\_G\_lgc==>Min  
TS2.21MfgDiagInhibit\_Uls\_G\_lgc==>Max

## Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	0		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-16	-16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrICom	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4294967295		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1		
Name	Actual Value	Expected Value	Result
CDD_ModlDxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	16	16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Limits	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4294967295	4294967295	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-2.98521		
CDD_EOLSrlComSvcDft_Cnt_G_b32	1972815360		
CDD_EnergyModeState_Cnt_G_u08	1		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1		
Name	Actual Value	Expected Value	Result
CDD_ModlDxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓



# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlIr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 2.5 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	0
CDD_DwnldAsstGain_Uls_G_f32	13.6616
CDD_EOLSrlComSvcDft_Cnt_G_b32	1621525760
CDD_EnergyModeState_Cnt_G_u08	3
MfgDiagInhibit_Uls_G_lgc	0
PrevMtrElecMechPolarity_Cnt_M_s08	-1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	13.6616001	13.6616	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCol	1621525760	1621525760	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlIr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.6 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	-0.428422
CDD_EOLSrlComSvcDft_Cnt_G_b32	1308570624
CDD_EnergyModeState_Cnt_G_u08	2
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	✓



# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-0.428422004	-0.428422	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1308570624	1308570624	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlInr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.7 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-8.70177		
CDD_EOLSrlComSvcDft_Cnt_G_b32	2401253376		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	✔
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-8.70176983	-8.70177	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	2401253376	2401253376	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlInr	1	1	✔

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530

CDDInterface\_Per4



T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.8 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-0.315127		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4246608640		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-0.315126985	-0.315127	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Limits	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4246608640	4246608640	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.9 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	-16
CDD_EOLSrlComSvcDft_Cnt_G_b32	3700780288
CDD_EnergyModeState_Cnt_G_u08	1
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-16	-16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	3700780288	3700780288	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.10 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	2547153920		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElk	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	16	16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCx	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	2547153920	2547153920	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Thern	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530

CDDInterface\_Per4



## Test Step 2.11 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	6.5678
CDD_EOLSrlComSvcDft_Cnt_G_b32	1897352960
CDD_EnergyModeState_Cnt_G_u08	2
MfgDiagInhibit_Uls_G_lgc	0
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	6.56780005	6.5678	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1897352960	1897352960	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	1	1	✓

## T

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.12 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	0
CDD_DwnldAsstGain_Uls_G_f32	0
CDD_EOLSrlComSvcDft_Cnt_G_b32	3025458688
CDD_EnergyModeState_Cnt_G_u08	1
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	-1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	3025458688	3025458688	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 2.13 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-5.456		
CDD_EOLSrlComSvcDft_Cnt_G_b32	2910339328		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxsrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs:	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-5.45599985	-5.456	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	2910339328	2910339328	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

## Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	1.98992		
CDD_EOLSrlComSvcDft_Cnt_G_b32	0		
CDD_EnergyModeState_Cnt_G_u08	1		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	1.98992002	1.98992	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlInr	0	0	✓

Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-1.69437		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4294967295		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-1.69437003	-1.69437	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	1	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrCo	4294967295	4294967295	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlIr	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	2.48416		
CDD_EOLSrlComSvcDft_Cnt_G_b32	564369664		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	✔
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	2.48415995	2.48416	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	564369664	564369664	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlIr	1	1	✔

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.17 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	0



# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Input Value
CDD_DwnldAsstGain_Uls_G_f32	0.259054
CDD_EOLSrlComSvcDft_Cnt_G_b32	1937593600
CDD_EnergyModeState_Cnt_G_u08	0
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1

Name	Actual Value	Expected Value	Result
CDD_ModlIdxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	0.259054005	0.259054	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1937593600	1937593600	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrgRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

Test Step 2.18 (Repeat Count = 1)				✓
Name		Input Value		
CDD_DftAsstTbl_Cnt_G_lgc		1		
CDD_DwnldAsstGain_Uls_G_f32		6.94041		
CDD_EOLSrlComSvcDft_Cnt_G_b32		3032138752		
CDD_EnergyModeState_Cnt_G_u08		3		
MfgDiagInhibit_Uls_G_lgc		0		
PrevMtrElecMechPolarity_Cnt_M_s08		-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc		1		
Name		Actual Value	Expected Value	Result
CDD_ModlIdxSrlComSvcDft_Cnt_G_u16		0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08		1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp		1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs		1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc		6.94041014	6.94041	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ		3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr		1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr		1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC		1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi		0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc		0	0	✓



# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	3032138752	3032138752	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

## Test Step 2.19 (Repeat Count = 1)

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-6.93429		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4118705920		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✔
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivn	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-6.93428993	-6.93429	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Limits	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIq	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4118705920	4118705920	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✔
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✔

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 2.20 (Repeat Count = 1)

Name	Input Value
CDD_DftAssTbI_Cnt_G_lgc	0
CDD_DwnldAsstGain_Uls_G_f32	-2.38078
CDD_EOLSrlComSvcDft_Cnt_G_b32	364862976

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Input Value		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-2.38077998	-2.38078	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	364862976	364862976	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓
CDD_ApplyPWMmtrElecMechPol	1	CDD_ApplyPWMmtrElecMechPol	1	✓

Test Step 2.21 (Repeat Count = 1)				✓
Name	Input Value			
CDD_DftAsstTbl_Cnt_G_lgc	0			
CDD_DwnldAsstGain_Uls_G_f32	-14.142			
CDD_EOLSrlComSvcDft_Cnt_G_b32	1029220608			
CDD_EnergyModeState_Cnt_G_u08	0			
MfgDiagInhibit_Uls_G_lgc	1			
PrevMtrElecMechPolarity_Cnt_M_s08	1			
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1			
Name	Actual Value	Expected Value	Result	
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	✓	
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drvin	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-14.1420002	-14.142	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓	

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1029220608	1029220608	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	1	1	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0		✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530

CDDInterface\_Per4



## Test Case 3: Path Test

### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)  
CPU Cycles:

TS3.1 376.00 Cycles  
TS3.2 399.00 Cycles  
TS3.3 958.00 Cycles

### Description

Vector Description:

```
TS3.1"(MtrElecMechPolarity_Cnt_T_s08 != PrevMtrElecMechPolarity_Cnt_M_s08)==>False
(D_DEFEATENGON_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>False
(D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>False
(D_DEFEATKININTDIAG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATKININTDIAG_CNT_B32))=>False
(D_DEFEATTHERMALDC_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTHERMALDC_CNT_B32))=>False
(D_DEFEATEOTIMPACT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False
(D_DEFEATEOTTHERMAL_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTTHERMAL_CNT_B32))=>False
(D_DEFEATMTRVRLNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRVRLNRMLRN_CNT_B32))=>False
(D_DEFEATMTRPARNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRPARNRMLRN_CNT_B32))=>False
(D_DEFEATTRQRM_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTRQRM_CNT_B32))=>False
(D_DEFEATMODIDX_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMODIDX_CNT_B32))=>False
(D_DEFEATPULLCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPULLCOMP_CNT_B32))=>False
(D_DEFEATWHLIMBREJ_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATWHLIMBREJ_CNT_B32))=>False
(D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>False
(D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False
(D_DEFEATHYSTCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTCOMP_CNT_B32))=>False
(D_DEFEATHYSTADD_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>False
(D_DEFEATRETURN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRETURN_CNT_B32))=>False
(D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>False
(D_DEFEATLIMIT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>False
(D_DEFEATFRICLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFRICLRN_CNT_B32))=>False
(D_DEFEATRXMSG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRXMSG_CNT_B32))=>False
(D_DEFEATMULTIGNMASK_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMULTIGNMASK_CNT_B32))=>False
TS3.2"(D_DEFEATENGON_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>True
(D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>True
(D_DEFEATKININTDIAG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATKININTDIAG_CNT_B32))=>True
(D_DEFEATTHERMALDC_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTHERMALDC_CNT_B32))=>True
(D_DEFEATEOTIMPACT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>True
(D_DEFEATEOTTHERMAL_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTTHERMAL_CNT_B32))=>True
(D_DEFEATMTRVRLNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRVRLNRMLRN_CNT_B32))=>True
(D_DEFEATMTRPARNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRPARNRMLRN_CNT_B32))=>True
(D_DEFEATTRQRM_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTRQRM_CNT_B32))=>True
(D_DEFEATMODIDX_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMODIDX_CNT_B32))=>True
(D_DEFEATPULLCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPULLCOMP_CNT_B32))=>True
(D_DEFEATWHLIMBREJ_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATWHLIMBREJ_CNT_B32))=>True
(D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>True
(D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>True
(D_DEFEATHYSTCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTCOMP_CNT_B32))=>True
(D_DEFEATHYSTADD_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>True
(D_DEFEATRETURN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRETURN_CNT_B32))=>True
(D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>True
(D_DEFEATLIMIT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>True
(D_DEFEATFRICLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFRICLRN_CNT_B32))=>True
(D_DEFEATRXMSG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRXMSG_CNT_B32))=>True
(D_DEFEATMULTIGNMASK_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMULTIGNMASK_CNT_B32))=>True
TS3.3(MtrElecMechPolarity_Cnt_T_s08 != PrevMtrElecMechPolarity_Cnt_M_s08)==>True
```

## Test Step 3.1 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbI_Cnt_G_igc	0
CDD_DwnldAsstGain_Uls_G_f32	-16
CDD_EOLSrlComSvcDft_Cnt_G_b32	0
CDD_EnergyModeState_Cnt_G_u08	0
MfgDiagInhibit_Uls_G_igc	0
PrevMtrElecMechPolarity_Cnt_M_s08	-1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrElc	-1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivn	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-16	-16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	0	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlInr	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 3.2 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	16
CDD_EOLSrlComSvcDft_Cnt_G_b32	4294967295
CDD_EnergyModeState_Cnt_G_u08	3
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1

Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	1	1	✓
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivn	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	16	16	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4294967295	4294967295	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlInr	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 3.3 (Repeat Count = 1)

Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	-2.98521
CDD_EOLSrlComSvcDft_Cnt_G_b32	1972815360
CDD_EnergyModeState_Cnt_G_u08	1

# TEST DETAILS REPORT

2016-06-03, 15:21:19+0530



CDDInterface\_Per4

Name	Input Value			
MfgDiagInhibit_Uls_G_lgc	1			
PrevMtrElecMechPolarity_Cnt_M_s08	1			
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1			
Name	Actual Value	Expected Value	Result	
CDD_ModIdxSriComSvcDft_Cnt_G_u16	1	1	✓	
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-2.98520994	-2.98521	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTlr	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modlc	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multlg	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SriCo	1972815360	1972815360	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	✓	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllr	0	0	✓	

T					✓
Actual Function	Count	Expected Function	Count	Result	
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	✓	
CDD_ApplyPWMtrElecMechPol	1	CDD_ApplyPWMtrElecMechPol	1	✓	

# TEST DETAILS REPORT

2016-06-03, 15:22:08+0530

CDDInterface\_Per5



Project	CDDInterface_FIASA
Module	CDDInterface
Test Object	CDDInterface_Per5

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS=-Dconst=-I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Information *****  Name of Tester: Priyanka Shitole Code File(s) Under Test: Sa_CDDInterface.c Code File(s) Version: 4 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1714 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: Test Date: 6/3/2016 Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." *****

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

# TEST DETAILS REPORT

2016-06-03, 15:22:08+0530

CDDInterface\_Per5



Workspace File

D:\Synergy\_Work\_Area\CDDInterface\_FIASA\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP



## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)  
CPU Cycles:

TS1.1 18.00 Cycles  
TS1.2 18.00 Cycles  
TS1.3 18.00 Cycles  
TS1.4 18.00 Cycles  
TS1.5 18.00 Cycles  
TS1.6 18.00 Cycles  
TS1.7 18.00 Cycles  
TS1.8 18.00 Cycles  
TS1.9 18.00 Cycles  
TS1.10 18.00 Cycles

**Description** Vector Description:

TS 1.1All Min  
TS 1.2All Max  
TS 1.3CDD\_AppDataFbkPthAccessBfr\_Cnt\_G\_u16==>Min  
TS 1.4CDD\_AppDataFbkPthAccessBfr\_Cnt\_G\_u16==>Max  
TS 1.5CDD\_PhaseAdvanceFinal\_Cnt\_G\_u16==>Min  
TS 1.6CDD\_PhaseAdvanceFinal\_Cnt\_G\_u16==>Max  
TS 1.7CDD\_PhaseAdvanceFinal\_Cnt\_G\_u16==>Pos  
TS 1.8CDD\_ModIndexFinal\_Uls\_G\_u16p16==>Min  
TS 1.9CDD\_ModIndexFinal\_Uls\_G\_u16p16==>Max  
TS 1.10CDD\_ModIndexFinal\_Uls\_G\_u16p16==>Pos

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModIdxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	0		
CDD_PhaseAdvanceFinal_Cnt_G_u16	0		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	0	0	✓

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModIdxFinal_Uls_G_u16p16[0]	1		
CDD_ModIndexFinal_Uls_G_u16p16	1		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535		
CDD_PhaseAdvanceFinal_Cnt_G_u16	65535		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	1	1	✓
CDD_ModIdxFinal_Uls_G_u16p16[1]	1	1	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535	65535	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535	65535	✓

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModIdxFinal_Uls_G_u16p16[0]	1		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	29088		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535		

# TEST DETAILS REPORT

2016-06-03, 15:22:08+0530

CDDInterface\_Per5



Name	Input Value		
CDD_PhaseAdvanceFinal_Cnt_G_u16	29088		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓
CDD_ModIdxFinal_Uls_G_u16p16[1]	1	1	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	29088	29088	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535	65535	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModIdxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	2988		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887		
CDD_PhaseAdvanceFinal_Cnt_G_u16	17887		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	2988	2988	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887	17887	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModIdxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887		
CDD_PhaseAdvanceFinal_Cnt_G_u16	0		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887	17887	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.6 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModIdxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887		
CDD_PhaseAdvanceFinal_Cnt_G_u16	65535		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535	65535	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887	17887	✓

# TEST DETAILS REPORT

2016-06-03, 15:22:08+0530

CDDInterface\_Per5



T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.7 (Repeat Count = 1)

Name	Input Value			
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1			
CDD_ModIdxFinal_Uls_G_u16p16[0]	0			
CDD_ModIdxFinal_Uls_G_u16p16	0			
CDD_PhaseAdvFinal_Cnt_G_u16[0]	21756			
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148			
CDD_PhaseAdvanceFinal_Cnt_G_u16	61148			
Name	Actual Value	Expected Value	Result	
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓	
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓	
CDD_PhaseAdvFinal_Cnt_G_u16[0]	21756	21756	✓	
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148	61148	✓	

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.8 (Repeat Count = 1)

Name	Input Value			
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0			
CDD_ModIdxFinal_Uls_G_u16p16[0]	0			
CDD_ModIdxFinal_Uls_G_u16p16	0			
CDD_PhaseAdvFinal_Cnt_G_u16[0]	16818			
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148			
CDD_PhaseAdvanceFinal_Cnt_G_u16	16818			
Name	Actual Value	Expected Value	Result	
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✓	
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓	
CDD_PhaseAdvFinal_Cnt_G_u16[0]	16818	16818	✓	
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148	61148	✓	

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.9 (Repeat Count = 1)

Name	Input Value			
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1			
CDD_ModIdxFinal_Uls_G_u16p16[0]	1			
CDD_ModIdxFinal_Uls_G_u16p16	1			
CDD_PhaseAdvFinal_Cnt_G_u16[0]	1307			
CDD_PhaseAdvFinal_Cnt_G_u16[1]	5891			
CDD_PhaseAdvanceFinal_Cnt_G_u16	5891			
Name	Actual Value	Expected Value	Result	
CDD_ModIdxFinal_Uls_G_u16p16[0]	1	1	✓	
CDD_ModIdxFinal_Uls_G_u16p16[1]	1	1	✓	
CDD_PhaseAdvFinal_Cnt_G_u16[0]	1307	1307	✓	
CDD_PhaseAdvFinal_Cnt_G_u16[1]	5891	5891	✓	

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:22:08+0530

CDDInterface\_Per5



## Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModIdxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	23492		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	2028		
CDD_PhaseAdvanceFinal_Cnt_G_u16	2028		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	✔
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✔
CDD_PhaseAdvFinal_Cnt_G_u16[0]	23492	23492	✔
CDD_PhaseAdvFinal_Cnt_G_u16[1]	2028	2028	✔

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:22:43+0530

CDDPorts\_ApplyMtrElecMechPol



Project	CDDInterface_FIASA
Module	CDDInterface
Test Object	CDDPorts_ApplyMtrElecMechPol

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Information *****  Name of Tester: Priyanka Shitole Code File(s) Under Test: Sa_CDDInterface.c Code File(s) Version: 4 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1714 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: Test Date: 6/3/2016 Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." *****

## Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

# TEST DETAILS REPORT

2016-06-03, 15:22:43+0530

CDDPorts\_ApplyMtrElecMechPol



Workspace File

D:\Synergy\_Work\_Area\CDDInterface\_FIASA\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP

## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)  
CPU Cycles:

TS1.1 513.00 Cycles  
TS1.2 478.00 Cycles

**Description** Vector Description:

TS1.1All Min  
TS1.2All Max

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
MtrElecMechPol_Cnt_s8	-1		
Name	Actual Value	Expected Value	Result
CDD_MtrElecPol_Cnt_G_s8	-1	-1	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

## Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
MtrElecMechPol_Cnt_s8	1		
Name	Actual Value	Expected Value	Result
CDD_MtrElecPol_Cnt_G_s8	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

TEST DETAILS REPORT

2016-06-03, 15:18:49+0530

CDDInterface\_Per1



Project	
Module	
Test Object	

Instrumentation: Test Object Only

Statement (C0) Coverage	
Decision Coverage	
Branch (C1) Coverage	
MCC Coverage	
MC/DC Coverage	

Statistics

Total Testcases	
Successful	✓
Failed	
Not Executed	

Module Properties

Project Root Directory	
Configuration File	
Target Environment	
Kind of Test	
Linker Options	
Source File(s)	
File	
Compiler Options	

Comments/Description/Specification	
Name	Text

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0



# TEST DETAILS REPORT

2016-06-03, 15:18:49+0530

CDDInterface\_Per1



Attributes	
Name	Value
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

## CDDInterface\_Per1



### Description



## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



Name	Actual Value	Expected Value	Result
			✓
			✓
			✓
			✓
-	-	-	✓
-	-	-	✓
-	-	-	✓
-	-	-	✓
-	-	-	✓

TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

T					✓
Actual Function	Count	Expected Function	Count	Result	
□ .		□ .		✓	



Test Case 2: Boundary Test



# TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

**Specification** Performance Metrics (With "None" Instrumentation  
and WithPS Environment)  
CPU Cycles:

TS2.1 1091.00 Cycles  
TS2.2 1090.00 Cycles  
TS2.3 1051.00 Cycles  
TS2.4 1016.00 Cycles  
TS2.5 1016.00 Cycles  
TS2.6 1016.00 Cycles  
TS2.7 1048.00 Cycles  
TS2.8 1016.00 Cycles  
TS2.9 1016.00 Cycles  
TS2.10 1048.00 Cycles  
TS2.11 1016.00 Cycles  
TS2.12 1016.00 Cycles  
TS2.13 1016.00 Cycles  
TS2.14 1016.00 Cycles  
TS2.15 1016.00 Cycles  
TS2.16 1016.00 Cycles  
TS2.17 1016.00 Cycles  
TS2.18 1016.00 Cycles  
TS2.19 1016.00 Cycles  
TS2.20 1016.00 Cycles  
TS2.21 1038.00 Cycles  
TS2.22 1016.00 Cycles  
TS2.23 1016.00 Cycles  
TS2.24 1016.00 Cycles  
TS2.25 1016.00 Cycles  
TS2.26 1016.00 Cycles  
TS2.27 1016.00 Cycles  
TS2.28 1016.00 Cycles  
TS2.29 1016.00 Cycles  
TS2.30 1016.00 Cycles  
TS2.31 1016.00 Cycles  
TS2.32 1016.00 Cycles  
TS2.33 1016.00 Cycles  
TS2.34 1038.00 Cycles  
TS2.35 1016.00 Cycles  
TS2.36 1016.00 Cycles  
TS2.37 1016.00 Cycles  
TS2.38 1016.00 Cycles  
TS2.39 1016.00 Cycles  
TS2.40 1016.00 Cycles  
TS2.41 1016.00 Cycles  
TS2.42 1044.00 Cycles  
TS2.43 1016.00 Cycles  
TS2.44 1016.00 Cycles  
TS2.45 1038.00 Cycles  
TS2.46 1016.00 Cycles  
TS2.47 1016.00 Cycles  
TS2.48 1016.00 Cycles  
TS2.49 1016.00 Cycles  
TS2.50 1044.00 Cycles  
TS2.51 1016.00 Cycles  
TS2.52 1016.00 Cycles  
TS2.53 1016.00 Cycles  
TS2.54 1016.00 Cycles  
TS2.55 1016.00 Cycles  
TS2.56 1016.00 Cycles  
TS2.57 1016.00 Cycles  
TS2.58 1016.00 Cycles  
TS2.59 1016.00 Cycles  
TS2.60 1024.00 Cycles  
TS2.61 1045.00 Cycles  
TS2.62 1016.00 Cycles  
TS2.63 1016.00 Cycles  
TS2.64 1016.00 Cycles  
TS2.65 1016.00 Cycles  
TS2.66 1016.00 Cycles  
TS2.67 1016.00 Cycles  
TS2.68 1016.00 Cycles  
TS2.69 1016.00 Cycles  
TS2.70 1016.00 Cycles  
TS2.71 1016.00 Cycles  
TS2.72 1016.00 Cycles  
TS2.73 1043.00 Cycles  
TS2.74 1016.00 Cycles  
TS2.75 1016.00 Cycles  
TS2.76 1016.00 Cycles  
TS2.77 1016.00 Cycles  
TS2.78 1016.00 Cycles  
TS2.79 1016.00 Cycles  
TS2.80 1016.00 Cycles  
TS2.81 1024.00 Cycles  
TS2.82 1016.00 Cycles  
TS2.83 1016.00 Cycles  
TS2.84 1038.00 Cycles  
TS2.85 1016.00 Cycles  
TS2.86 1016.00 Cycles  
TS2.87 1016.00 Cycles  
TS2.88 1016.00 Cycles  
TS2.89 1022.00 Cycles  
TS2.90 1016.00 Cycles  
TS2.91 1016.00 Cycles  
TS2.92 1016.00 Cycles  
TS2.93 1016.00 Cycles  
TS2.94 1024.00 Cycles  
TS2.95 1016.00 Cycles  
TS2.96 1026.00 Cycles  
TS2.97 1016.00 Cycles  
TS2.98 1016.00 Cycles  
TS2.99 1016.00 Cycles  
TS2.100 1016.00 Cycles  
TS2.101 1016.00 Cycles  
TS2.102 1016.00 Cycles  
TS2.103 1016.00 Cycles  
TS2.104 1016.00 Cycles  
TS2.105 1016.00 Cycles  
TS2.106 1016.00 Cycles  
TS2.107 1016.00 Cycles  
TS2.108 1026.00 Cycles  
TS2.109 1016.00 Cycles  
TS2.110 1016.00 Cycles

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# TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

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TS2.111	1016.00	Cycles
TS2.112	1024.00	Cycles
TS2.113	1016.00	Cycles
TS2.114	1016.00	Cycles
TS2.115	1038.00	Cycles
TS2.116	1016.00	Cycles
TS2.117	1016.00	Cycles



*CDDInterface\_Per1*



## CDDInterface\_Per1

[illegible]

*CDDInterface\_Per1*

[illegible][illegible]

## CDDInterface\_Per1



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0

0

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

*CDDInterface\_Per1*



T				
Actual Function	Count	Expected Function	Count	Result

*CDDInterface\_Per1*

[illegible]

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## CDDInterface\_Per1



Timeline of the 2019-2020 season showing the progression of the COVID-19 pandemic. The timeline is marked with dates from March to May. Key events include the first case in March, the start of the first wave in April, and the start of the second wave in May. The timeline is divided into three sections: 'First wave', 'Second wave', and 'Third wave'.



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible]

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value



Name	Input Value
$\square$ $\cdot \square \square$	
$\square$ $\cdot \square \square$	
$\square$ $\cdot \square$	
$\square$ $\cdot \square$	
$\cdot \square$	
$\cdot \square$	

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



$T$				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible][illegible]



## CDDInterface\_Per1



• □ □

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



Test Step 2.16 (Repeat Count = 1)		
Name		Input Value
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	• <input type="checkbox"/>	
	• <input type="checkbox"/>	

## CDDInterface\_Per1





## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



*CDDInterface\_Per1*



T				
Actual Function	Count	Expected Function	Count	Result



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## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



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## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result



## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



The timeline illustrates the progression of COVID-19 in the UK from March to May 2020. It features a horizontal axis with dates from 1 March to 1 May. A red line represents the daily number of new cases, which shows a sharp increase starting in late March, peaking in early April, and then gradually declining. A blue line represents the daily number of deaths, which also shows a sharp increase starting in late March, peaking in early April, and then gradually declining. The timeline is divided into three sections: 'Early phase', 'Peak phase', and 'Late phase'. The 'Early phase' covers the period from 1 March to 1 April, the 'Peak phase' covers the period from 1 April to 1 May, and the 'Late phase' covers the period from 1 May to 1 June. The 'Early phase' is characterized by a low number of cases and deaths, the 'Peak phase' by a high number of cases and deaths, and the 'Late phase' by a declining number of cases and deaths.



## CDDInterface\_Per1

[illegible]

Test Step 2.25 (Repeat Count = 1)		
Name		Input Value
□	• □ □	
□	• □ □	
□	• □	
□	• □	
	• □	
	• □	

## CDDInterface\_Per1





## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1





## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



• □ □

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓





## CDDInterface\_Per1





## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓



## CDDInterface\_Per1

[illegible][illegible]

TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

Name	Input Value
------	-------------



## CDDInterface\_Per1

[illegible]

*CDDInterface\_Per1*



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

## CDDInterface\_Per1

[illegible][illegible]

TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

Name	Input Value
------	-------------

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]



*CDDInterface\_Per1*



Name				Actual Value	Expected Value	Result
						✓
						✓
						✓
-	.	.	.			✓
-	.	.	.	-		✓
.	.	.	.	.		✓
-	.	.	.	-		✓
.	.	.	.	.		✓
-	.	.	.	-		✓
.	.	.	.	.		✓
-	.	.	.	-		✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



\* □ □

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓



## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result





## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



• □ □

## CDDInterface\_Per1

[illegible]

TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

Name					Input Value		
	-	-	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		
	-	-	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		
	-	-	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		
Name					Actual Value	Expected Value	Result
							✓
							✓
							✓
							✓

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible]

Test Step 2.61 (Repeat Count = 1)		
Name		Input Value
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	• <input type="checkbox"/>	
	• <input type="checkbox"/>	



## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result



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## CDDInterface\_Per1

[illegible][illegible]

*CDDInterface\_Per1*



• □ □

## CDDInterface\_Per1

[illegible]



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

$T$				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



The timeline illustrates the progression of COVID-19 cases and deaths in the United States from March 2020 to March 2021. The top section shows the number of cases, which rises sharply in March 2020, peaks in May 2020, and then fluctuates with a significant peak in January 2021. The bottom section shows the number of deaths, which also rises sharply in March 2020, peaks in May 2020, and then fluctuates with a significant peak in January 2021. The timeline is divided into three main periods: March 2020, May 2020, and January 2021.

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible]

Test Step 2.70 (Repeat Count = 1)					
Name	Input Value				
	<input type="text"/>	<input checked="" type="checkbox"/>	<input type="text"/>	<input type="text"/>	
	<input type="text"/>	<input checked="" type="checkbox"/>	<input type="text"/>	<input type="text"/>	
	<input type="text"/>	<input checked="" type="checkbox"/>	<input type="text"/>	<input type="text"/>	
	<input type="text"/>	<input checked="" type="checkbox"/>	<input type="text"/>	<input type="text"/>	
	<input checked="" type="checkbox"/>	<input type="text"/>			
	<input checked="" type="checkbox"/>	<input type="text"/>			

## CDDInterface\_Per1



\_\_\_\_\_

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

## CDDInterface\_Per1

[illegible][illegible]



## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓



## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



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## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



The timeline illustrates the progression of COVID-19 cases and deaths in the United States from March 2020 to March 2021. The x-axis represents time, with major ticks for March 2020, May 2020, July 2020, September 2020, November 2020, January 2021, and March 2021. The y-axis represents the number of cases and deaths, with a scale from 0 to 100,000. The 'Cases' line (blue) shows a sharp increase starting in March 2020, peaking in May 2020 at approximately 100,000 cases, and then declining. A second, smaller wave is visible in late 2020. The 'Deaths' line (red) shows a similar pattern, peaking in May 2020 at approximately 10,000 deaths. The timeline is divided into three sections: 'Early 2020', 'Mid 2020', and 'Late 2020'.



*CDDInterface\_Per1*



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible]

Test Step 2.79 (Repeat Count = 1)		
Name		Input Value
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	• <input type="checkbox"/>	
	• <input type="checkbox"/>	

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result



•

## CDDInterface\_Per1

[illegible][illegible]



## CDDInterface\_Per1



• □ □

## CDDInterface\_Per1

[illegible]

*CDDInterface\_Per1*



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

*CDDInterface\_Per1*

[illegible]

Test Step 2.88 (Repeat Count = 1)		
Name		Input Value
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/> <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	<input type="checkbox"/> • <input type="checkbox"/>	
	• <input type="checkbox"/>	
	• <input type="checkbox"/>	

## CDDInterface\_Per1





## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

## CDDInterface\_Per1



$T$				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result



## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]



## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible]

Test Step 2.97 (Repeat Count = 1)					
Name	Input Value				
	<input type="checkbox"/>	*	<input type="checkbox"/>	<input type="checkbox"/>	
	<input type="checkbox"/>	*	<input type="checkbox"/>	<input type="checkbox"/>	
	<input type="checkbox"/>	*	<input type="checkbox"/>	<input type="checkbox"/>	
	<input type="checkbox"/>	*	<input type="checkbox"/>	<input type="checkbox"/>	
	*	<input type="checkbox"/>			
	*	<input type="checkbox"/>			

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1





## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result



•

## CDDInterface\_Per1

[illegible]

Name	Input Value
*	
*	
*	
*	
* *	
* *	
-	
-	
-	
- -	
- -	
*	
*	

## CDDInterface\_Per1



\* □ □

## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓





## CDDInterface\_Per1



\_\_\_\_\_

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1

[illegible]



## CDDInterface\_Per1

[illegible]

## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]

## CDDInterface\_Per1

[illegible]

T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1



*CDDInterface\_Per1*



T				
Actual Function	Count	Expected Function	Count	Result
				✓

## CDDInterface\_Per1

[illegible]

Test Step 2.115 (Repeat Count = 1)					
Name	Input Value				
	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

[illegible]



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result
				✓

[illegible]

## CDDInterface\_Per1



## CDDInterface\_Per1



T				
Actual Function	Count	Expected Function	Count	Result

Description	Q1	Q2	Q3	Q4
1	1	2	3	4
2	1	2	3	4
3	1	2	3	4
4	1	2	3	4
5	1	2	3	4
6	1	2	3	4
7	1	2	3	4
8	1	2	3	4
9	1	2	3	4
10	1	2	3	4
11	1	2	3	4
12	1	2	3	4
13	1	2	3	4
14	1	2	3	4
15	1	2	3	4
16	1	2	3	4
17	1	2	3	4
18	1	2	3	4
19	1	2	3	4
20	1	2	3	4
21	1	2	3	4
22	1	2	3	4
23	1	2	3	4
24	1	2	3	4
25	1	2	3	4
26	1	2	3	4
27	1	2	3	4
28	1	2	3	4
29	1	2	3	4
30	1	2	3	4
31	1	2	3	4
32	1	2	3	4
33	1	2	3	4
34	1	2	3	4
35	1	2	3	4
36	1	2	3	4
37	1	2	3	4
38	1	2	3	4
39	1	2	3	4
40	1	2	3	4
41	1	2	3	4
42	1	2	3	4
43	1	2	3	4
44	1	2	3	4
45	1	2	3	4
46	1	2	3	4
47	1	2	3	4
48	1	2	3	4
49	1	2	3	4
50	1	2	3	4
51	1	2	3	4
52	1	2	3	4
53	1	2	3	4
54	1	2	3	4
55	1	2	3	4
56	1	2	3	4
57	1	2	3	4
58	1	2	3	4
59	1	2	3	4
60	1	2	3	4
61	1	2	3	4
62	1	2	3	4
63	1	2	3	4
64	1	2	3	4
65	1	2	3	4
66	1	2	3	4
67	1	2	3	4
68	1	2	3	4
69	1	2	3	4
70	1	2	3	4
71	1	2	3	4
72	1	2	3	4
73	1	2	3	4
74	1	2	3	4
75	1	2	3	4
76	1	2	3	4
77	1	2	3	4
78	1	2	3	4
79	1	2	3	4
80	1	2	3	4
81	1	2	3	4
82	1	2	3	4
83	1	2	3	4
84	1	2	3	4
85	1	2	3	4
86	1	2	3	4
87	1	2	3	4
88	1	2	3	4
89	1	2	3	4
90	1	2	3	4
91	1	2	3	4
92	1	2	3	4
93	1	2	3	4
94	1	2	3	4

## CDDInterface\_Per1

[illegible][illegible]

## CDDInterface\_Per1



TEST DETAILS REPORT

2016-06-03, 15:18:49+0530



CDDInterface\_Per1

Name	Actual Value	Expected Value	Result
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓
- . . .	-		✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
□ *		□ *		✓	

# TEST DETAILS REPORT

2016-06-03, 15:12:26+0530

CDDInterface\_Init2



Project	CDDInterface_FIASA
Module	CDDInterface
Test Object	CDDInterface_Init2

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Information *****  Name of Tester: Priyanka Shitole Code File(s) Under Test: Sa_CDDInterface.c Code File(s) Version: 4 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1714 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: Test Date: 6/3/2016 Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." *****

## Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

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# TEST DETAILS REPORT

2016-06-03, 15:12:26+0530

CDDInterface\_Init2

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Workspace File

D:\Synergy\_Work\_Area\CDDInterface\_FIASA\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP

---



## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation  
and WithPS Environment)  
CPU Cycles:

TS1.1 499.00 Cycles

**Description** Vector Description:

TS1.1 Check for Stub count

## Test Step 1.1 (Repeat Count = 1)

T				
Actual Function	Count	Expected Function	Count	Result
EnableMtrCtlInterrupt	1	EnableMtrCtlInterrupt	1	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



Project	CDDInterface_FIASA
Module	CDDInterface
Test Object	CDDInterface_Per2

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS=-Dconst=-I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Information *****  Name of Tester: Priyanka Shitole Code File(s) Under Test: Sa_CDDInterface.c Code File(s) Version: 4 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1714 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: Test Date: 6/3/2016 Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." *****

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



Workspace File

D:\Synergy\_Work\_Area\CDDInterface\_FIASA\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP

## Test Case 1: Boundary Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)  
CPU Cycles:

TS1.1 88.00 Cycles  
TS1.2 88.00 Cycles  
TS1.3 88.00 Cycles  
TS1.4 88.00 Cycles  
TS1.5 88.00 Cycles  
TS1.6 88.00 Cycles  
TS1.7 88.00 Cycles  
TS1.8 88.00 Cycles  
TS1.9 88.00 Cycles  
TS1.10 88.00 Cycles  
TS1.11 88.00 Cycles  
TS1.12 88.00 Cycles  
TS1.13 88.00 Cycles  
TS1.14 88.00 Cycles  
TS1.15 88.00 Cycles  
TS1.16 88.00 Cycles  
TS1.17 88.00 Cycles  
TS1.18 88.00 Cycles  
TS1.19 88.00 Cycles  
TS1.20 88.00 Cycles  
TS1.21 88.00 Cycles  
TS1.22 88.00 Cycles  
TS1.23 88.00 Cycles  
TS1.24 88.00 Cycles  
TS1.25 88.00 Cycles  
TS1.26 88.00 Cycles  
TS1.27 88.00 Cycles  
TS1.28 88.00 Cycles  
TS1.29 88.00 Cycles  
TS1.30 88.00 Cycles  
TS1.31 88.00 Cycles  
TS1.32 88.00 Cycles  
TS1.33 88.00 Cycles  
TS1.34 88.00 Cycles

**Description** Vector Description:

TS1.1All min  
TS1.2All max  
TS1.3CDD\_AppDataFwdPthAccessBfr\_Cnt\_G\_u16==>Min  
TS1.4CDD\_AppDataFwdPthAccessBfr\_Cnt\_G\_u16==>Max  
TS1.5CDD\_AppDataFwdPthAccessBfr\_Cnt\_G\_u16==>Pos  
TS1.6CommOffset\_Cnt\_u16==>Min  
TS1.7CommOffset\_Cnt\_u16==>Max  
TS1.8CommOffset\_Cnt\_u16==>Pos  
TS1.9MRFMtrVel\_MtrRadpS\_f32==>Min  
TS1.10MRFMtrVel\_MtrRadpS\_f32==>Max  
TS1.11MRFMtrVel\_MtrRadpS\_f32==>Pos  
TS1.12MRFMtrVel\_MtrRadpS\_f32==>Zero  
TS1.13MRFMtrVel\_MtrRadpS\_f32==>Neg  
TS1.14MtrCurr1TempOffset\_Volt\_f32==>Min  
TS1.15MtrCurr1TempOffset\_Volt\_f32==>Max  
TS1.16MtrCurr1TempOffset\_Volt\_f32==>Pos  
TS1.17MtrCurr2TempOffset\_Volt\_f32==>Min  
TS1.18MtrCurr2TempOffset\_Volt\_f32==>Max  
TS1.19MtrCurr2TempOffset\_Volt\_f32==>Pos  
TS1.20MtrPosPolarity\_Cnt\_s08==>Min  
TS1.21MtrPosPolarity\_Cnt\_s08==>Max  
TS1.22MtrTrqCmdSign\_Cnt\_s16==>Min  
TS1.23MtrTrqCmdSign\_Cnt\_s16==>Max  
TS1.24MtrTrqCmdSign\_Cnt\_s16==>Pos  
TS1.25MtrTrqCmdSign\_Cnt\_s16==>Zero  
TS1.26MtrTrqCmdSign\_Cnt\_s16==>Neg  
TS1.27Rte\_Mode\_SystemState\_Mode=RTE\_MODE\_StaMd\_Mode\_DISABLE  
TS1.28Rte\_Mode\_SystemState\_Mode=RTE\_MODE\_StaMd\_Mode\_OFF  
TS1.29Rte\_Mode\_SystemState\_Mode=RTE\_MODE\_StaMd\_Mode\_OPERATE  
TS1.30Rte\_Mode\_SystemState\_Mode=RTE\_MODE\_StaMd\_Mode\_WARMINIT  
TS1.31Rte\_Mode\_SystemState\_Mode=RTE\_TRANSITION\_StaMd\_Mode  
TS1.32Vecu\_Volt\_f32==>min  
TS1.33Vecu\_Volt\_f32==>Max  
TS1.34Vecu\_Volt\_f32==>Pos

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1350
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	0
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	0
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	0

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	5
CDD_Vecu_Volt_G_f32[1]	5
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1350
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	5
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1350	-1350	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350	-1350	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0	0	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0	0	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	0	0	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	0	0	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	0	0	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0	✓	

Test Step 1.2 (Repeat Count = 1)				✓
Name		Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		1350		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		1350		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		5		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		5		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		5		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		5		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		7150		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		7150		
CDD_MtrPosPolarity_Cnt_G_s08[0]		1		
CDD_MtrPosPolarity_Cnt_G_s08[1]		1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]		1		
CDD_SysState_Cnt_G_Enum[0]		4		
CDD_SysState_Cnt_G_Enum[1]		4		
CDD_Vecu_Volt_G_f32[0]		31		
CDD_Vecu_Volt_G_f32[1]		31		
Rte_ModeMachine_StaMd_SystemState_Mode		4		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value		1350		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value		31		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm		7150		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur		5		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur		5		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos		1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq		1		
Name		Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		1350	1350	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		1350	1350	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		5	5	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		5	5	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		5	5	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		5	5	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		7150	7150	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Actual Value	Expected Value	Result
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150	7150	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	4	4	✓
CDD_SysState_Cnt_G_Enum[1]	4	4	✓
CDD_Vecu_Volt_G_f32[0]	31	31	✓
CDD_Vecu_Volt_G_f32[1]	31	31	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.3 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1039.02002
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-952.192017
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.55293989
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.71498013
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.55293989
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3054
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2608
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	18
CDD_Vecu_Volt_G_f32[1]	13.3718004
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-952.192017
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	13.3718004
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	2608
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.71498013
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun	0.250820011
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1039.02002	-1039.02002	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-952.192017	-952.192017	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.55293989	3.55293989	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.71498013	4.71498013	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.55293989	3.55293989	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011	0.250820011	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3054	3054	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2608	2608	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0	0	✓
CDD_SysState_Cnt_G_Enum[0]	0	0	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	18	18	✓
CDD_Vecu_Volt_G_f32[1]	13.3718004	13.3718004	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



## Test Step 1.4 (Repeat Count = 1) ✓

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1194.65002
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	461.804993
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.01465011
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.89574003
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.01465011
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.89574003
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2806
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5061
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	7
CDD_Vecu_Volt_G_f32[1]	21
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-131.302002
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	29.9487991
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	4315
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.61689997
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.18684006
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-131.302002	-131.302002	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	461.804993	461.804993	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.61689997	3.61689997	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.89574003	1.89574003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.18684006	4.18684006	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.89574003	1.89574003	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4315	4315	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5061	5061	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	0	0	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	29.9487991	29.9487991	✓
CDD_Vecu_Volt_G_f32[1]	21	21	✓

## T ✓

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.5 (Repeat Count = 1) ✓

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-16.8549995
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1260.43994
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.29583001
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.49289
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.29583001
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.49289
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4387
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1204
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	9
CDD_Vecu_Volt_G_f32[1]	5
Rte_ModeMachine_StaMd_SystemState_Mode	1

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1107.20996		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	28.1583004		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	1169		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.768684983		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.66652989		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1107.20996	1107.20996	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1260.43994	1260.43994	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.768684983	0.768684983	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.49289	1.49289	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.66652989	3.66652989	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.49289	1.49289	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1169	1169	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1204	1204	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	28.1583004	28.1583004	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

Test Step 1.6 (Repeat Count = 1)				✓
Name		Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		-191.468994		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		1029.38		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		3.38572001		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		4.29025984		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		3.38572001		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		4.29025984		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		4379		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		4295		
CDD_MtrPosPolarity_Cnt_G_s08[0]		-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]		1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]		1		
CDD_SysState_Cnt_G_Enum[0]		0		
CDD_SysState_Cnt_G_Enum[1]		1		
CDD_Vecu_Volt_G_f32[0]		22		
CDD_Vecu_Volt_G_f32[1]		19		
Rte_ModeMachine_StaMd_SystemState_Mode		1		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value		299.572998		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value		29.7709007		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm		0		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun		1.86571002		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun		0.250820011		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos		1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq		-1		
Name		Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		299.572998	299.572998	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		1029.38	1029.38	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		1.86571002	1.86571002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		4.29025984	4.29025984	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		0.250820011	0.250820011	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		4.29025984	4.29025984	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		0	0	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		4295	4295	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]		1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]		1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		-1	-1	✓



# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Actual Value	Expected Value	Result
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	29.7709007	29.7709007	✓
CDD_Vecu_Volt_G_f32[1]	19	19	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.7 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-282.661011
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	582.482971
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.67427003
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.54254007
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.67427003
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.31112003
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	145
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	27
CDD_Vecu_Volt_G_f32[1]	21.0223999
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	582.482971
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.0223999
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	7150
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	2.54254007
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.31112003
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-282.661011	-282.661011	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	582.482971	582.482971	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.67427003	1.67427003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.54254007	2.54254007	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.67427003	1.67427003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.31112003	1.31112003	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	145	145	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150	7150	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	27	27	✓
CDD_Vecu_Volt_G_f32[1]	21.0223999	21.0223999	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.8 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-886.080994
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-92.3256989
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.36141992

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.73335004		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.36141992		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.77063		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	626		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5541		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	3		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	29		
CDD_Vecu_Volt_G_f32[1]	23.5622005		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-92.3256989		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	23.5622005		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	5541		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.73335004		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.77063		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-886.080994	-886.080994	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-92.3256989	-92.3256989	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.36141992	2.36141992	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.73335004	1.73335004	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.36141992	2.36141992	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.77063	1.77063	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	626	626	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5541	5541	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	29	29	✓
CDD_Vecu_Volt_G_f32[1]	23.5622005	23.5622005	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0		✓

Test Step 1.9 (Repeat Count = 1)		
Name	Input Value	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-966.247009	
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350	
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.806077003	
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.12197995	
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.806077003	
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.23109996	
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3117	
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7069	
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0	
CDD_SysState_Cnt_G_Enum[0]	2	
CDD_SysState_Cnt_G_Enum[1]	1	
CDD_Vecu_Volt_G_f32[0]	30	
CDD_Vecu_Volt_G_f32[1]	24.0664997	
Rte_ModeMachine_StaMd_SystemState_Mode	2	
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1350	
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	24.0664997	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	7069	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.12197995	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.23109996	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1	

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-966.247009	-966.247009	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350	-1350	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.806077003	0.806077003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.12197995	3.12197995	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.806077003	0.806077003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.23109996	1.23109996	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3117	3117	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7069	7069	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0	0	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	30	30	✓
CDD_Vecu_Volt_G_f32[1]	24.0664997	24.0664997	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0		✓

## Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-827.492004		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1350		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.78215003		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.197907999		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.78215003		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.45770001		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1097		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5731		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	2		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	25		
CDD_Vecu_Volt_G_f32[1]	20.7143993		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1350		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.7143993		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	5731		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.197907999		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	1.45770001		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-827.492004	-827.492004	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1350	1350	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.78215003	1.78215003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.197907999	0.197907999	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.78215003	1.78215003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.45770001	1.45770001	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1097	1097	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5731	5731	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	25	25	✓
CDD_Vecu_Volt_G_f32[1]	20.7143993	20.7143993	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.11 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-656.984985
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	39.8400002
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.87867999
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.3110199
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.87867999
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.3110199
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3524
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2114
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	1
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	10
CDD_Vecu_Volt_G_f32[1]	10
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	454.746002
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	26.1818008
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	1694
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.8696599
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.57879996
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	454.746002	454.746002	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	39.8400002	39.8400002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.8696599	4.8696599	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.3110199	4.3110199	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.57879996	1.57879996	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.3110199	4.3110199	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1694	1694	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2114	2114	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	0	0	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	26.1818008	26.1818008	✓
CDD_Vecu_Volt_G_f32[1]	10	10	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.12 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1313.16003
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	0
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.91206002
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.28078985
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.91206002
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.93779993
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1204
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3541
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530









CDDInterface\_Per2

Name	Input Value
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	1
CDD_SysState_Cnt_G_Enum[1]	3
CDD_Vecu_Volt_G_f32[0]	23
CDD_Vecu_Volt_G_f32[1]	26.8638992
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	0
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	26.8638992
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	3541
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.28078985
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.93779993
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1313.16003	-1313.16003	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	0	0	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.91206002	2.91206002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.28078985	4.28078985	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.91206002	2.91206002	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.93779993	2.93779993	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1204	1204	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3541	3541	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	23	23	✓
CDD_Vecu_Volt_G_f32[1]	26.8638992	26.8638992	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

Test Step 1.13 (Repeat Count = 1)				
Name		Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		-140.389999		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		914.89801		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		2.64402008		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		2.40644002		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		2.64402008		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		2.40644002		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		3511		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		6009		
CDD_MtrPosPolarity_Cnt_G_s08[0]		1		
CDD_MtrPosPolarity_Cnt_G_s08[1]		-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]		-1		
CDD_SysState_Cnt_G_Enum[0]		3		
CDD_SysState_Cnt_G_Enum[1]		3		
CDD_Vecu_Volt_G_f32[0]		7		
CDD_Vecu_Volt_G_f32[1]		15		
Rte_ModeMachine_StaMd_SystemState_Mode		4		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value		-1107.76001		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value		25.8773003		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm		3311		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur		4.39323997		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur		0.250820011		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos		1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq		-1		
Name		Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		0	0	
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		-1107.76001	-1107.76001	
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		914.89801	914.89801	
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		4.39323997	4.39323997	
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		2.40644002	2.40644002	

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Actual Value	Expected Value	Result
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.40644002	2.40644002	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3311	3311	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6009	6009	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	4	4	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	25.8773003	25.8773003	✓
CDD_Vecu_Volt_G_f32[1]	15	15	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.14 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-780.861023
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1022.60999
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.25398004
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.99187994
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.25398004
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.99187994
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1434
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1797
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	9
CDD_Vecu_Volt_G_f32[1]	17
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1127.05005
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	30.7206993
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	132
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.92617989
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1127.05005	-1127.05005	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1022.60999	1022.60999	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.99187994	2.99187994	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.92617989	3.92617989	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.99187994	2.99187994	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	132	132	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1797	1797	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	30.7206993	30.7206993	✓
CDD_Vecu_Volt_G_f32[1]	17	17	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

## Test Step 1.15 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1326.69995
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	695.132996
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.86127996
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.91262996
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.86127996
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.91262996
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1538
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4179
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	1
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	24
CDD_Vecu_Volt_G_f32[1]	19
Rte_ModeMachine_StaMd_SystemState_Mode	4
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1207.82996
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	22.3521996
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	6530
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun	5
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun	0.250820011
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1207.82996	1207.82996	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	695.132996	695.132996	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	5	5	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.91262996	1.91262996	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.91262996	1.91262996	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6530	6530	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4179	4179	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✔
CDD_SysState_Cnt_G_Enum[0]	4	4	✔
CDD_SysState_Cnt_G_Enum[1]	1	1	✔
CDD_Vecu_Volt_G_f32[0]	22.3521996	22.3521996	✔
CDD_Vecu_Volt_G_f32[1]	19	19	✔

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.16 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-450.973999
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	956.833008
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.32728004
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.990696013
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.32728004
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.990696013
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2758
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6362
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	3
CDD_Vecu_Volt_G_f32[0]	9
CDD_Vecu_Volt_G_f32[1]	10
Rte_ModeMachine_StaMd_SystemState_Mode	3



# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-458.299011
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.5351009
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	5251
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.5545001
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.828881979
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-458.299011	-458.299011	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	956.833008	956.833008	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.5545001	3.5545001	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.990696013	0.990696013	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.828881979	0.828881979	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.990696013	0.990696013	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5251	5251	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6362	6362	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	20.5351009	20.5351009	✓
CDD_Vecu_Volt_G_f32[1]	10	10	✓

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

Test Step 1.17 (Repeat Count = 1)	
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1204.5
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	428.871002
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.53718996
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.34743023
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.53718996
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34743023
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4613
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2400
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	3
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	29
CDD_Vecu_Volt_G_f32[1]	30
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-840.299988
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	24.0664997
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	1375
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.36310005
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-840.299988	-840.299988	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	428.871002	428.871002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.36310005	4.36310005	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.34743023	4.34743023	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34743023	4.34743023	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1375	1375	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2400	2400	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓



# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Actual Value	Expected Value	Result
[1]	-1	-1	✓
	0	0	✓
	0	0	✓
CDD_Vecu_Volt_G_32[0]	24.0664997	24.0664997	✓
CDD_Vecu_Volt_G_32[1]	30	30	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.56127977		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.62017012		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.56127977		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1702		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1231		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	1		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	10		
CDD_Vecu_Volt_G_f32[1]	24		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1243.14001		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	29.9566002		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	1885		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.441742003		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.5453999		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1243.14001	1243.14001	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	863.776978	863.776978	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.441742003	0.441742003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.56127977	4.56127977	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.5453999	3.5453999	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.56127977	4.56127977	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1885	1885	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1231	1231	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	29.9566002	29.9566002	✓
CDD_Vecu_Volt_G_f32[1]	24	24	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0		✓

Test Step 1.20 (Repeat Count = 1)		
Name	Input Value	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-664.866028	
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	525.135986	
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.699694991	
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.03612006	
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.699694991	
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.03612006	
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4184	
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6922	
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	
CDD_SysState_Cnt_G_Enum[0]	3	
CDD_SysState_Cnt_G_Enum[1]	0	
CDD_Vecu_Volt_G_f32[0]	12	
CDD_Vecu_Volt_G_f32[1]	12	
Rte_ModeMachine_StaMd_SystemState_Mode	1	
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-658.497986	
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	12.4942999	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	2694	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.81072998	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.250820011	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1	

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-658.497986	-658.497986	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	525.135986	525.135986	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.81072998	4.81072998	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.03612006	1.03612006	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.03612006	1.03612006	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2694	2694	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6922	6922	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	12.4942999	12.4942999	✓
CDD_Vecu_Volt_G_f32[1]	12	12	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.21 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-29.5091		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	718.439026		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.96669006		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.76337004		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.96669006		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.657562017		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4418		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3107		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	3		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	25		
CDD_Vecu_Volt_G_f32[1]	10.6225004		
Rte_ModeMachine_StaMd_SystemState_Mode	1		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	718.439026		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	10.6225004		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	3107		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.76337004		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.657562017		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-29.5091	-29.5091	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	718.439026	718.439026	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.96669006	2.96669006	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.76337004	4.76337004	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.96669006	2.96669006	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.657562017	0.657562017	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4418	4418	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3107	3107	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	25	25	✓
CDD_Vecu_Volt_G_f32[1]	10.6225004	10.6225004	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.22 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-384.337006
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1203.10999
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60235
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.37077999
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.60235
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.49700999
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6038
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2623
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	3
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	31
CDD_Vecu_Volt_G_f32[1]	20.5625
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1203.10999
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.5625
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	2623
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.37077999
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	2.49700999
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-384.337006	-384.337006	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1203.10999	1203.10999	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60235	1.60235	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.37077999	3.37077999	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.60235	1.60235	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.49700999	2.49700999	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6038	6038	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2623	2623	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	31	31	✓
CDD_Vecu_Volt_G_f32[1]	20.5625	20.5625	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.23 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-750.877991
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-5.8327899
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.11789989
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.10511005
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.11789989
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.10511005
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	672
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3353
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530









CDDInterface\_Per2

Name	Input Value
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	15
CDD_Vecu_Volt_G_f32[1]	18
Rte_ModeMachine_StaMd_SystemState_Mode	1
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	653.482971
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	28.7024002
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	693
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.441742003
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	1.41585004
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	653.482971	653.482971	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-5.8327899	-5.8327899	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.441742003	0.441742003	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.10511005	1.10511005	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.41585004	1.41585004	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.10511005	1.10511005	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	693	693	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3353	3353	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✔
CDD_SysState_Cnt_G_Enum[0]	1	1	✔
CDD_SysState_Cnt_G_Enum[1]	2	2	✔
CDD_Vecu_Volt_G_f32[0]	28.7024002	28.7024002	✔
CDD_Vecu_Volt_G_f32[1]	18	18	✔

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

Test Step 1.24 (Repeat Count = 1)				
Name		Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		-506.942993		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		489.006012		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		4.23248005		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		4.34561014		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		4.23248005		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		4.34561014		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		4365		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		1567		
CDD_MtrPosPolarity_Cnt_G_s08[0]		-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]		1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]		1		
CDD_SysState_Cnt_G_Enum[0]		1		
CDD_SysState_Cnt_G_Enum[1]		1		
CDD_Vecu_Volt_G_f32[0]		18		
CDD_Vecu_Volt_G_f32[1]		8		
Rte_ModeMachine_StaMd_SystemState_Mode		3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value		972.143982		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value		21.5450993		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm		418		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun		1.16745996		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun		2.43678999		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos		-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq		1		
Name		Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		0	0	
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		972.143982	972.143982	
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		489.006012	489.006012	
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		1.16745996	1.16745996	
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		4.34561014	4.34561014	

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Actual Value	Expected Value	Result
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.43678999	2.43678999	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34561014	4.34561014	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	418	418	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1567	1567	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	1	1	✓
CDD_Vecu_Volt_G_f32[0]	21.5450993	21.5450993	✓
CDD_Vecu_Volt_G_f32[1]	8	8	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0		✓

## Test Step 1.25 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-591.851013
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	80.7081985
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.479669005
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.14370012
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.479669005
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.14370012
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4264
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4415
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	7
CDD_Vecu_Volt_G_f32[1]	21
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	461.765991
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	9.79251957
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	6482
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.299508989
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.16656017
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	461.765991	461.765991	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	80.7081985	80.7081985	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.299508989	0.299508989	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.14370012	4.14370012	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.16656017	4.16656017	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.14370012	4.14370012	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6482	6482	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4415	4415	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	9.79251957	9.79251957	✓
CDD_Vecu_Volt_G_f32[1]	21	21	✓

T					✓
Actual Function	Count	Expected Function	Count	Result	
*none*	0	*** No Call Expected ***	0		✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



## Test Step 1.26 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-844.638
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1228.16003
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.17770004
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.78542995
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.17770004
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.78542995
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5754
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	482
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	9
CDD_Vecu_Volt_G_f32[1]	5
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	487.270996
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	8.03582001
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	4301
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	1.60074997
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.2244401
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	487.270996	487.270996	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1228.16003	1228.16003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60074997	1.60074997	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.78542995	4.78542995	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.2244401	2.2244401	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.78542995	4.78542995	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4301	4301	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	482	482	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	8.03582001	8.03582001	✓
CDD_Vecu_Volt_G_f32[1]	5	5	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.27 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1107.12
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	32.0822983
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.96684003
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.441742003
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.96684003
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2176
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3483
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	22
CDD_Vecu_Volt_G_f32[1]	21.4122009
Rte_ModeMachine_StaMd_SystemState_Mode	0

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	32.0822983		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.4122009		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	3483		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun	0.441742003		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun	0.250820011		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1107.12	-1107.12	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	32.0822983	32.0822983	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.96684003	1.96684003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.441742003	0.441742003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.96684003	1.96684003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011	0.250820011	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2176	2176	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3483	3483	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0	0	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	22	22	✓
CDD_Vecu_Volt_G_f32[1]	21.4122009	21.4122009	✓

Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

Test Step 1.28 (Repeat Count = 1)				✔
Name		Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		-361.294006		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		415.226013		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		0.86658603		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		4.96747017		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		0.86658603		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		4.96747017		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		5848		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		6399		
CDD_MtrPosPolarity_Cnt_G_s08[0]		-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]		-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]		-1		
CDD_SysState_Cnt_G_Enum[0]		1		
CDD_SysState_Cnt_G_Enum[1]		0		
CDD_Vecu_Volt_G_f32[0]		27		
CDD_Vecu_Volt_G_f32[1]		29		
Rte_ModeMachine_StaMd_SystemState_Mode		1		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value		1014.35999		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value		22.7768002		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm		6119		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun		4.86899996		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCun		0.279628009		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos		1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq		1		
Name		Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16		0	0	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[0]		1014.35999	1014.35999	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[1]		415.226013	415.226013	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[0]		4.86899996	4.86899996	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[1]		4.96747017	4.96747017	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[0]		0.279628009	0.279628009	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[1]		4.96747017	4.96747017	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]		6119	6119	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]		6399	6399	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]		1	1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]		-1	-1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[0]		1	1	✔



# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Actual Value	Expected Value	Result
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	22.7768002	22.7768002	✓
CDD_Vecu_Volt_G_f32[1]	29	29	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.29 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-805.948975
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	797.966003
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.72437
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.78024298
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.72437
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.78024298
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3670
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6377
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	29
CDD_Vecu_Volt_G_f32[1]	10
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	120.797997
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	18.8687992
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	3154
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.84580994
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	2.03030992
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	120.797997	120.797997	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	797.966003	797.966003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.84580994	4.84580994	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.78024298	0.78024298	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.03030992	2.03030992	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.78024298	0.78024298	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3154	3154	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6377	6377	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	18.8687992	18.8687992	✓
CDD_Vecu_Volt_G_f32[1]	10	10	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.30 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-367.003998
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1333.90002
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.32502007

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.79194021		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.32502007		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.79194021		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6436		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	338		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	0		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	30		
CDD_Vecu_Volt_G_f32[1]	10		
Rte_ModeMachine_StaMd_SystemState_Mode	3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1074.33997		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.7642994		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	6156		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.99714994		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.444205999		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1074.33997	1074.33997	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1333.90002	1333.90002	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.99714994	3.99714994	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.79194021	4.79194021	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.444205999	0.444205999	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.79194021	4.79194021	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6156	6156	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	338	338	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✔
CDD_SysState_Cnt_G_Enum[0]	3	3	✔
CDD_SysState_Cnt_G_Enum[1]	0	0	✔
CDD_Vecu_Volt_G_f32[0]	20.7642994	20.7642994	✔
CDD_Vecu_Volt_G_f32[1]	10	10	✔

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-191.837006	-191.837006	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	750.221985	750.221985	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60108995	1.60108995	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.792972028	0.792972028	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.47305012	3.47305012	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.792972028	0.792972028	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6795	6795	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4301	4301	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	4	4	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	24.0664997	24.0664997	✓
CDD_Vecu_Volt_G_f32[1]	8	8	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.32 (Repeat Count = 1)

Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-17.7012005		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1226.71997		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.48852992		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.57865		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.48852992		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.57865		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5433		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6343		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	2		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	10		
CDD_Vecu_Volt_G_f32[1]	10		
Rte_ModeMachine_StaMd_SystemState_Mode	3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-731.166992		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	5		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	388		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.94904995		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.94375992		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-731.166992	-731.166992	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1226.71997	1226.71997	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.94904995	3.94904995	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.57865	1.57865	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.94375992	2.94375992	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.57865	1.57865	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	388	388	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6343	6343	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	✓
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	10	10	✓

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530

CDDInterface\_Per2



T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.33 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1180.60999
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	803.369019
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.47603989
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.9499402
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.47603989
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.9499402
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3719
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6795
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	3
CDD_Vecu_Volt_G_f32[0]	23
CDD_Vecu_Volt_G_f32[1]	8
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1123.93005
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	31
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	4030
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.441742003
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.250820011
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1123.93005	-1123.93005	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	803.369019	803.369019	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.441742003	0.441742003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.9499402	4.9499402	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.9499402	4.9499402	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4030	4030	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6795	6795	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	31	31	✓
CDD_Vecu_Volt_G_f32[1]	8	8	✓

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓

## Test Step 1.34 (Repeat Count = 1)

Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-692.187988
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	643.562012
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.872577012
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.78780007
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.872577012
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	3.78780007
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5321
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4074
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1

# TEST DETAILS REPORT

2016-06-03, 15:20:08+0530



CDDInterface\_Per2

Name	Input Value
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	3
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	7
CDD_Vecu_Volt_G_f32[1]	15
Rte_ModeMachine_StaMd_SystemState_Mode	4
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-159.466003
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.5
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	5427
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.43364
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.250820011
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-159.466003	-159.466003	✔
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	643.562012	643.562012	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.43364	2.43364	✔
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.78780007	3.78780007	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	✔
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	3.78780007	3.78780007	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5427	5427	✔
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4074	4074	✔
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✔
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✔
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✔
CDD_SysState_Cnt_G_Enum[0]	4	4	✔
CDD_SysState_Cnt_G_Enum[1]	0	0	✔
CDD_Vecu_Volt_G_f32[0]	21.5	21.5	✔
CDD_Vecu_Volt_G_f32[1]	15	15	✔

T				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	✓