Summary

Total Test Objects:	1
Successful:	1
Failed:	0
Not Executed:	0
Date:	2015-03-23
Time:	19:28:50+0530

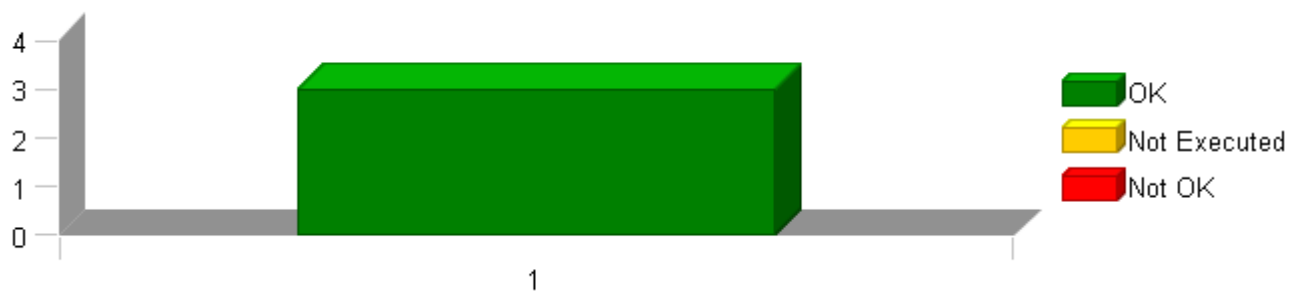## Overall Test Object Results (including Coverage)
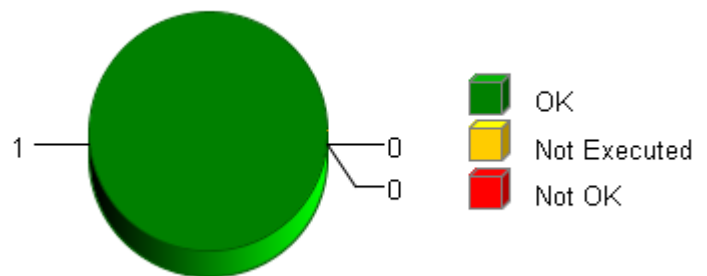
Selected Project Items

Test Object "CBD_UnitTest/ReturnFireWall/ReturnFirewall_Per1"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)

The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

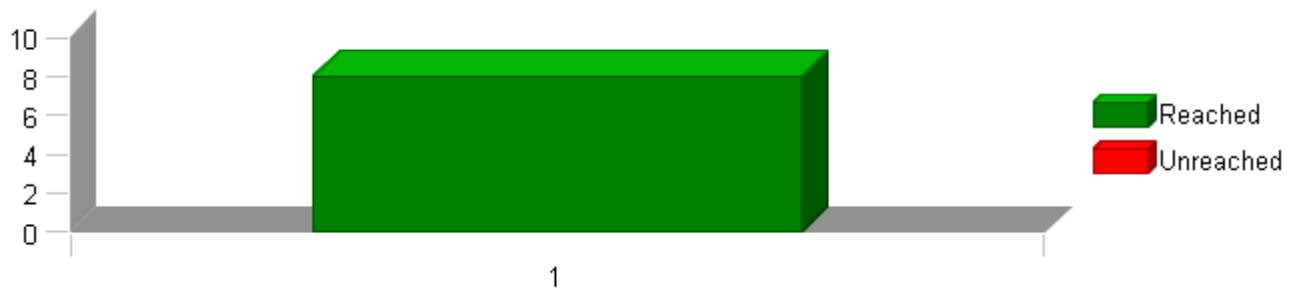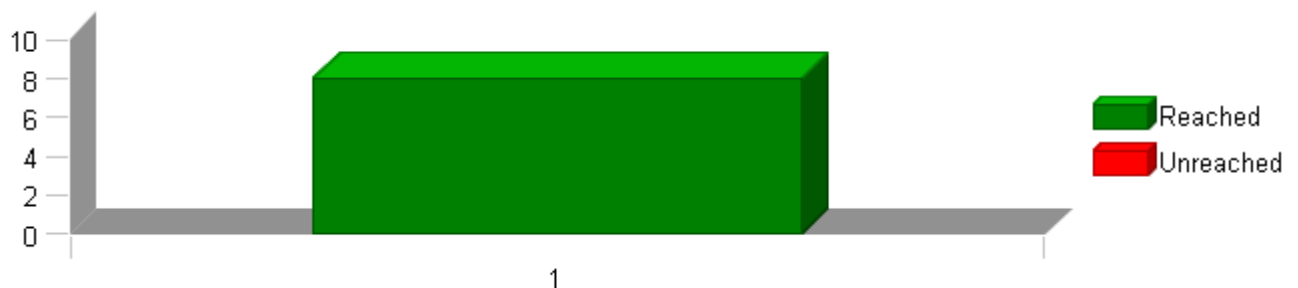## Statement (C0) Coverage: Total Statements for Each Test Object
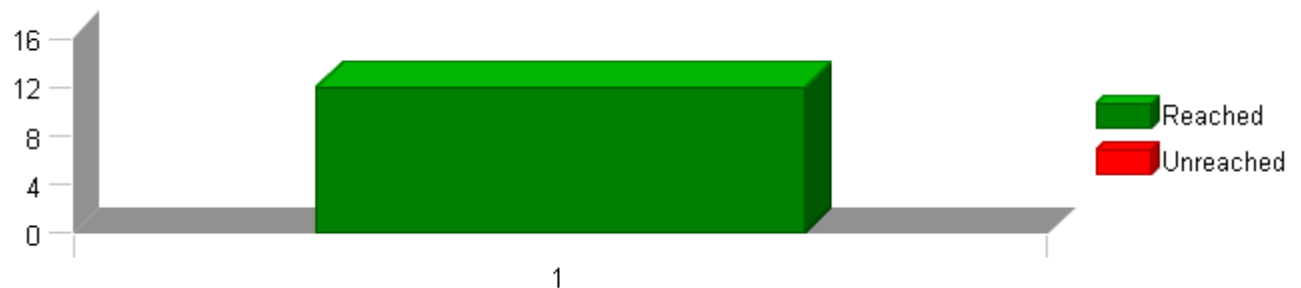
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.
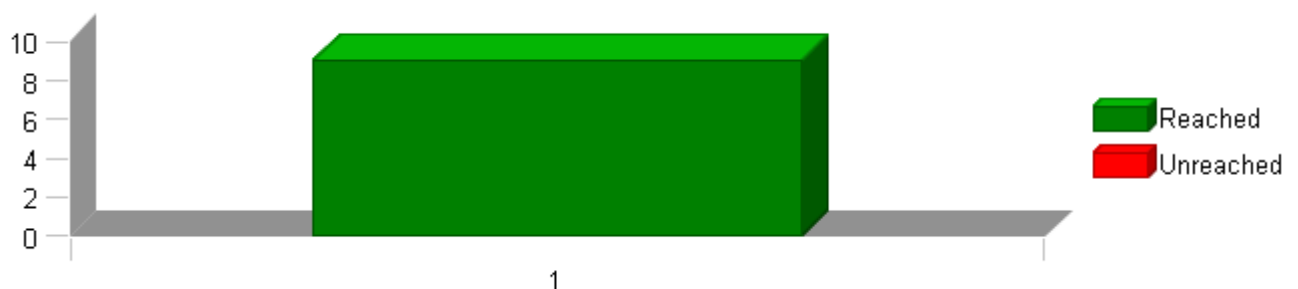
Decision Coverage: Total Decision Outcomes for Each Test Object

The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.
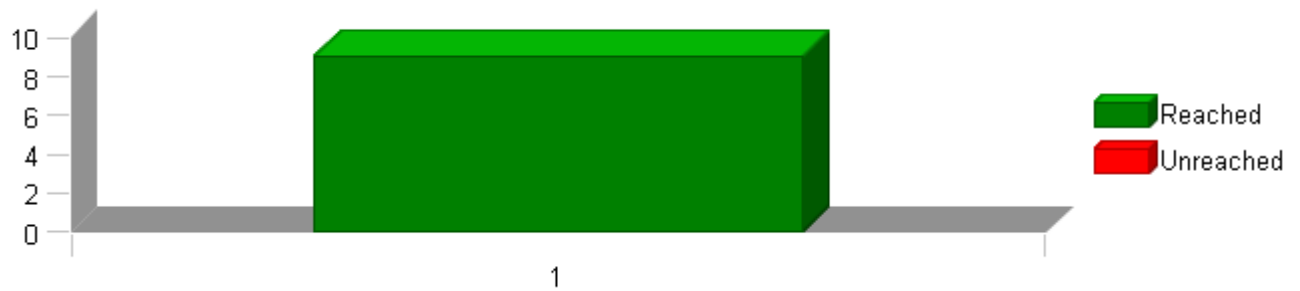
MCC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	Return	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
	ReturnFireWall	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
1	ReturnFirewall_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔

Project	Return
Module	ReturnFireWall
Test Object	ReturnFirewall_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ReturnFirewall_CBD
Configuration File	D:\Synergy_Work_Area\ReturnFirewall_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\ReturnFirewall\src\Ap_ReturnFirewall.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -Dstatic= -I$(PROJECTROOT)\ReturnFirewall\utp\contract -I$(PROJECTROOT)\ReturnFirewall\utp\contract\Ap_ReturnFirewall -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -Dstatic= -I$(PROJECTROOT)\ReturnFirewall\utp\contract -I$(PROJECTROOT)\ReturnFirewall\utp\contract\Ap_ReturnFirewall -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification	
Name	**Text**

Module 'ReturnFireWall'

```
*****************************************************Unit Test Information***********************************************************

Name of Tester: Surbhi Goel
Code File(s) Under Test:Ap_ReturnFirewall.c
Code File(s) Version: 10
Module Design Document:Return_Firewall_MDD.docx
Module Design Document Version: 10
Data Dictionary Version: 9
Unit Test Plan Version: 6
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes): 372
Total RAM Used (Bytes): 9
Total CALS Used (Bytes): 216
Special Test Requirements:
Test Date: 03-23-2015
Comments:
Note1: Inline Function defined in globalmacro.h is not unit tested.


Note2: In ""ReturnFirewall_Per1"" function, ""Defeat_Return_Svc_Cnt_lgc"" always kept FALSE
       to make ""if((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) ""
       condition FALSE except Boundray Test for variables used in Condition and Path coverage.


Note3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.

**********************************************************************************************************************************
```

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 4.0
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\ReturnFirewall_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test ✔

Specification
```
Performance Metrics:
(With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS1.1 794.00 Cycles Shortest Path
TS1.2 1350.00 Cycles Longest Path
```

Description
Vector Description:

TS1.1 Shortest Execution Path: ((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode))=True
TS1.2 Longest Execution Path:((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) = False
LimitedReturn_MtrNm_T_f32 = Limit_m(ReturnCmd_MtrNm_T_f32, LwrBound_MtrNm_T_f32, UprBound_MtrNm_T_f32)=False
(Abs_f32_m(ReturnCmd_MtrNm_T_f32 - LimitedReturn_MtrNm_T_f32)> FLT_EPSILON)= False"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	18022

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280
t_RtrnFWVehSpd_Kph_u9p7[0]	65408
t_RtrnFWVehSpd_Kph_u9p7[1]	65408
t_RtrnFWVehSpd_Kph_u9p7[2]	65408
t_RtrnFWVehSpd_Kph_u9p7[3]	65408
t_RtrnFWVehSpd_Kph_u9p7[4]	65408
t_RtrnFWVehSpd_Kph_u9p7[5]	65408
t_RtrnFWVehSpd_Kph_u9p7[6]	65408
t_RtrnFWVehSpd_Kph_u9p7[7]	65408
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	✔

T				
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Case 2: Boundary Test ✔

Specification	Performance Metrics: (With "None" Instrumentation and "WithPS" Environment) CPU Cycles: TS2.1 1315.00 Cycles TS2.2 1334.00 Cycles TS2.3 1351.00 Cycles TS2.4 1403.00 Cycles TS2.5 1329.00 Cycles TS2.6 1321.00 Cycles TS2.7 1406.00 Cycles TS2.8 1350.00 Cycles TS2.9 1321.00 Cycles TS2.10 1341.00 Cycles TS2.11 1320.00 Cycles TS2.12 1344.00 Cycles TS2.13 1361.00 Cycles TS2.14 1286.00 Cycles TS2.15 1361.00 Cycles TS2.16 1462.00 Cycles TS2.17 1376.00 Cycles TS2.18 1317.00 Cycles TS2.19 1347.00 Cycles TS2.20 1317.00 Cycles TS2.21 1337.00 Cycles TS2.22 1334.00 Cycles TS2.23 1341.00 Cycles TS2.24 1352.00 Cycles TS2.25 1412.00 Cycles TS2.26 1353.00 Cycles TS2.27 1321.00 Cycles TS2.28 1409.00 Cycles TS2.29 1350.00 Cycles TS2.30 1321.00 Cycles TS2.31 1341.00 Cycles TS2.32 1291.00 Cycles TS2.33 794.00 Cycles
Description	Vector Description: TS2.1HandwheelPosition_HwDeg_f32=Min TS2.2HandwheelPosition_HwDeg_f32=Max TS2.3HandwheelPosition_HwDeg_f32=Zero TS2.4HandwheelPosition_HwDeg_f32=Pos TS2.5HandwheelPosition_HwDeg_f32=Neg TS2.6ReturnCmd_MtrNm_f32=Min TS2.7ReturnCmd_MtrNm_f32=Max TS2.8ReturnCmd_MtrNm_f32=Zero TS2.9ReturnCmd_MtrNm_f32=Pos TS2.10ReturnCmd_MtrNm_f32=Neg TS2.11VehicleSpeed_Kph_f32=Min TS2.12VehicleSpeed_Kph_f32=Max TS2.13VehicleSpeed_Kph_f32=Pos TS2.14t_RtrnFWVehSpd_Kph_u9p7[8]=Min TS2.15t_RtrnFWVehSpd_Kph_u9p7[8]=Max TS2.16t_RtrnFWVehSpd_Kph_u9p7[8]=Pos TS2.17t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Min TS2.18t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Max TS2.19t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Mid TS2.20t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Pos TS2.21t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Neg TS2.22t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Min TS2.23t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Max TS2.24t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Mid TS2.25t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Pos TS2.26t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Neg TS2.27Defeat_Return_Svc_Cnt_lgc=Min TS2.28Defeat_Return_Svc_Cnt_lgc=Max TS2.29MEC_Counter_Cnt_enum=Min TS2.30MEC_Counter_Cnt_enum=Max TS2.31MEC_Counter_Cnt_enum=Pos TS2.32All min TS2.33All max

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048

ReturnFirewall_Per1

Name	Input Value
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-1
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.39990234	-4.39990234 ± 0.00048828125	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)	✓
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1760		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	3520		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	25472		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	32640		
t_RtrnFWVehSpd_Kph_u9p7[7]	38272		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.200000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	7.64990234	7.64990234 ± 0.00048828125	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached		1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt		2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached		1	✔

Test Step 2.3 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008

Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	**Actual Value**	**Expected Value**	**Result**
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached		1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt		2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached		1	✔

Test Step 2.4 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	256
t_RtrnFWVehSpd_Kph_u9p7[1]	2816
t_RtrnFWVehSpd_Kph_u9p7[2]	6656
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	28416
t_RtrnFWVehSpd_Kph_u9p7[5]	32256
t_RtrnFWVehSpd_Kph_u9p7[6]	28416
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	400.5
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.699999988
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	45.6500015
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32

Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.44189453	3.44189453 ± 0.00048828125	✓

T					✓
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached		1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt		2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus		1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached		1	✓

Test Step 2.5 (Repeat Count = 1)	✓
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-400.25
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.10000002
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	66.4499969
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.92041016	-4.92041016 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.6 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	50
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	7
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDε	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.54980469	2.54980469 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.7 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800

Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	7808		
t_RtrnFWVehSpd_Kph_u9p7[3]	9728		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	20608		
t_RtrnFWVehSpd_Kph_u9p7[6]	20736		
t_RtrnFWVehSpd_Kph_u9p7[7]	21248		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	**Actual Value**	**Expected Value**	**Result**
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.8 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	10368
t_RtrnFWVehSpd_Kph_u9p7[3]	9984
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	23168
t_RtrnFWVehSpd_Kph_u9p7[6]	24448
t_RtrnFWVehSpd_Kph_u9p7[7]	25344
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	100.800003
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	0
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.2999992
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0	0 ± 0.00048828125	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)		✓
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048

Name	Input Value
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	120.349998
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.5
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12.6999998
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.73730469	2.73730469 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.10 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	145.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-2.25		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	**Actual Value**	**Expected Value**	**Result**
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.52490234	5.52490234 ± 0.00048828125	✔

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600

Name	Input Value
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	169
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.20000005
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	0
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwD€	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-1	-1 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.12 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	14208
t_RtrnFWVehSpd_Kph_u9p7[7]	19840
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	193
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.29999995
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32

Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	1.29999995	1.29999995 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.13 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	505.549988
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.54999995
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	55.5
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.54999995	3.54999995 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	0
t_RtrnFWVehSpd_Kph_u9p7[2]	0
t_RtrnFWVehSpd_Kph_u9p7[3]	0
t_RtrnFWVehSpd_Kph_u9p7[4]	0
t_RtrnFWVehSpd_Kph_u9p7[5]	0
t_RtrnFWVehSpd_Kph_u9p7[6]	0
t_RtrnFWVehSpd_Kph_u9p7[7]	0
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	241.5
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.70000005
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	0
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	1.70000005	1.70000005 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.15 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	65408
t_RtrnFWVehSpd_Kph_u9p7[1]	65408
t_RtrnFWVehSpd_Kph_u9p7[2]	65408
t_RtrnFWVehSpd_Kph_u9p7[3]	65408
t_RtrnFWVehSpd_Kph_u9p7[4]	65408
t_RtrnFWVehSpd_Kph_u9p7[5]	65408
t_RtrnFWVehSpd_Kph_u9p7[6]	65408
t_RtrnFWVehSpd_Kph_u9p7[7]	65408
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	265
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.85000002
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	67
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.16 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	289.450012
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.01999998
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	78.1999969
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.01999998	2.01999998 ± 0.00048828125	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)		✓
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048

Name	Input Value
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-17280
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	313
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.19000006
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	87
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.18 (Repeat Count = 1)	✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	17792		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	30592		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	337		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.3599999		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	**Actual Value**	**Expected Value**	**Result**
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-8.5	-8.5 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.19 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	0
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	0
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008

Name	Input Value
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	361.399994
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.52999997
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54.75
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)	✓
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	8000
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	8000
t_RtrnFWVehSpd_Kph_u9p7[0]	256
t_RtrnFWVehSpd_Kph_u9p7[1]	2816
t_RtrnFWVehSpd_Kph_u9p7[2]	6656
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	28416
t_RtrnFWVehSpd_Kph_u9p7[5]	32000
t_RtrnFWVehSpd_Kph_u9p7[6]	32256
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	385.519989
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.72000003
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.8499985
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32

Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0	0 ± 0.00048828125	✓

T					✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓	
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.21 (Repeat Count = 1)	✓
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-8000
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	409.230011
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.86999989
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12.5
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.86999989	2.86999989 ± 0.00048828125	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔	
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔	

Test Step 2.22 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	19584
t_RtrnFWVehSpd_Kph_u9p7[7]	19840
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	433
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.03999996
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.762695313	-0.762695313 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.23 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	7808
t_RtrnFWVehSpd_Kph_u9p7[3]	9728
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	20608
t_RtrnFWVehSpd_Kph_u9p7[6]	20736
t_RtrnFWVehSpd_Kph_u9p7[7]	21248
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	457
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.21000004
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	23
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.21000004	3.21000004 ± 0.00048828125	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached		1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt		2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached		1	✔

Test Step 2.24 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	0	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	10368
t_RtrnFWVehSpd_Kph_u9p7[3]	9984
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	23168
t_RtrnFWVehSpd_Kph_u9p7[6]	23424
t_RtrnFWVehSpd_Kph_u9p7[7]	24064
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	481.75
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.38000011
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	44.5
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.92919922	3.92919922 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.25 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048

Name	Input Value
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	505.25
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.54999995
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	55.2000008
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.54999995	3.54999995 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.26 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	529.299988		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.72000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	66.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	**Actual Value**	**Expected Value**	**Result**
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.51074219	2.51074219 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.27 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	19584

Name	Input Value
t_RtrnFWVehSpd_Kph_u9p7[7]	19840
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	50
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	7
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.10009766	5.10009766 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.28 (Repeat Count = 1)	✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	7808
t_RtrnFWVehSpd_Kph_u9p7[3]	9728
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	20608
t_RtrnFWVehSpd_Kph_u9p7[6]	20736
t_RtrnFWVehSpd_Kph_u9p7[7]	21248
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32

Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	✓

T				✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.29 (Repeat Count = 1)		✓

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	10368
t_RtrnFWVehSpd_Kph_u9p7[3]	9984
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	23168
t_RtrnFWVehSpd_Kph_u9p7[6]	23424
t_RtrnFWVehSpd_Kph_u9p7[7]	24064
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	100
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	0
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0	0 ± 0.00048828125	✓

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.30 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	120
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.95019531	3.95019531 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.31 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120

Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	145.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-2.25		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.25		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	**Actual Value**	**Expected Value**	**Result**
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.59570313	5.59570313 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.32 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-18022	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-18022

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-18022
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-17280
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-17280
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	0
t_RtrnFWVehSpd_Kph_u9p7[2]	0
t_RtrnFWVehSpd_Kph_u9p7[3]	0
t_RtrnFWVehSpd_Kph_u9p7[4]	0
t_RtrnFWVehSpd_Kph_u9p7[5]	0
t_RtrnFWVehSpd_Kph_u9p7[6]	0
t_RtrnFWVehSpd_Kph_u9p7[7]	0
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	0
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.79980469	8.79980469 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 2.33 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022

Name	Input Value
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280
t_RtrnFWVehSpd_Kph_u9p7[0]	65408
t_RtrnFWVehSpd_Kph_u9p7[1]	65408
t_RtrnFWVehSpd_Kph_u9p7[2]	65408
t_RtrnFWVehSpd_Kph_u9p7[3]	65408
t_RtrnFWVehSpd_Kph_u9p7[4]	65408
t_RtrnFWVehSpd_Kph_u9p7[5]	65408
t_RtrnFWVehSpd_Kph_u9p7[6]	65408
t_RtrnFWVehSpd_Kph_u9p7[7]	65408
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Case 3: Path Test ✔

Specification
```
Performance Metrics:
(With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS3.1 1315.00 Cycles
TS3.2 1334.00 Cycles
TS3.3 1351.00 Cycles
TS3.4 1409.00 Cycles
TS3.5 794.00 Cycles
```

Description
Vector Description:

TS3.1"((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) = False
LimitedReturn_MtrNm_T_f32 = Limit_m(ReturnCmd_MtrNm_T_f32, LwrBound_MtrNm_T_f32, UprBound_MtrNm_T_f32)=True
(Abs_f32_m(ReturnCmd_MtrNm_T_f32 - LimitedReturn_MtrNm_T_f32)> FLT_EPSILON)= True
"

TS3.2"((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) = False
LimitedReturn_MtrNm_T_f32 = Limit_m(ReturnCmd_MtrNm_T_f32, LwrBound_MtrNm_T_f32, UprBound_MtrNm_T_f32)=False
(Abs_f32_m(ReturnCmd_MtrNm_T_f32 - LimitedReturn_MtrNm_T_f32)> FLT_EPSILON)= True"
TS3.3"((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) = False
LimitedReturn_MtrNm_T_f32 = Limit_m(ReturnCmd_MtrNm_T_f32, LwrBound_MtrNm_T_f32, UprBound_MtrNm_T_f32)=False
(Abs_f32_m(ReturnCmd_MtrNm_T_f32 - LimitedReturn_MtrNm_T_f32)> FLT_EPSILON)= False"
TS3.4"((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) = False
LimitedReturn_MtrNm_T_f32 = Limit_m(ReturnCmd_MtrNm_T_f32, LwrBound_MtrNm_T_f32, UprBound_MtrNm_T_f32)=False
(Abs_f32_m(ReturnCmd_MtrNm_T_f32 - LimitedReturn_MtrNm_T_f32)> FLT_EPSILON)= True"
TS3.5((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode))=True

Test Step 3.1 (Repeat Count = 1) ✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
t_RtrnFWVehSpd_Kph_u9p7[5]	19200
t_RtrnFWVehSpd_Kph_u9p7[6]	25600
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-1
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.39990234	-4.39990234 ± 0.00048828125	✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 3.2 (Repeat Count = 1)		✔

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1760
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	3520
t_RtrnFWVehSpd_Kph_u9p7[0]	1152
t_RtrnFWVehSpd_Kph_u9p7[1]	3712
t_RtrnFWVehSpd_Kph_u9p7[2]	7552
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	25472
t_RtrnFWVehSpd_Kph_u9p7[5]	20352
t_RtrnFWVehSpd_Kph_u9p7[6]	32640
t_RtrnFWVehSpd_Kph_u9p7[7]	38272
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.200000003
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	7.64990234	7.64990234 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 3.3 (Repeat Count = 1)		✔
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424

Name	Input Value
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	19328
t_RtrnFWVehSpd_Kph_u9p7[6]	27008
t_RtrnFWVehSpd_Kph_u9p7[7]	32640
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔

Test Step 3.4 (Repeat Count = 1)	✔
Name	**Input Value**
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408

Name	Input Value
t_RtrnFWVehSpd_Kph_u9p7[2]	7808
t_RtrnFWVehSpd_Kph_u9p7[3]	9728
t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[5]	20608
t_RtrnFWVehSpd_Kph_u9p7[6]	20736
t_RtrnFWVehSpd_Kph_u9p7[7]	21248
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✓
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)		✓
Name	**Input Value**	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	18022	

Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280
t_RtrnFWVehSpd_Kph_u9p7[0]	65408
t_RtrnFWVehSpd_Kph_u9p7[1]	65408
t_RtrnFWVehSpd_Kph_u9p7[2]	65408
t_RtrnFWVehSpd_Kph_u9p7[3]	65408
t_RtrnFWVehSpd_Kph_u9p7[4]	65408
t_RtrnFWVehSpd_Kph_u9p7[5]	65408
t_RtrnFWVehSpd_Kph_u9p7[6]	65408
t_RtrnFWVehSpd_Kph_u9p7[7]	65408
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2

Name	Input Value
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✔