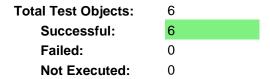


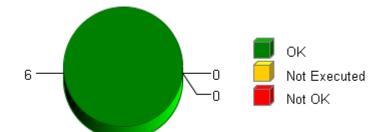
Summary

Overall Test Object Results (including Coverage)



2014-12-26 Date: 16:53:07+0530

Time:



Selected Project Items

Test Object "CBD UnitTest/MtrVel Digi/CalcCoarseVel"

Test Object "CBD UnitTest/MtrVel Digi/MtrVel Init"

Test Object "CBD_UnitTest/MtrVel_Digi/MtrVel_Per1"

Test Object "CBD_UnitTest/MtrVel_Digi/MtrVel_Per2"

Test Object "CBD_UnitTest/MtrVel_Digi/MtrVelBlend"

Test Object "CBD UnitTest/MtrVel Digi/RegressionFit"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

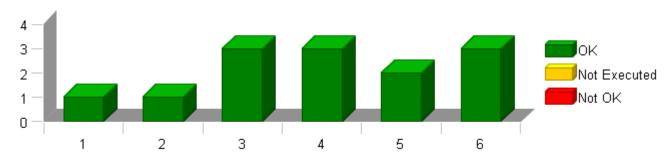
No **Check Interface: Generate Driver:** Yes Yes **Execute Test: Create New Test Run:** No

Test Object Only Instrumentation:

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)

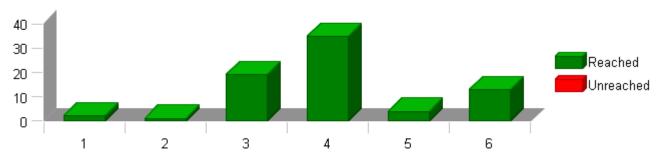


The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results



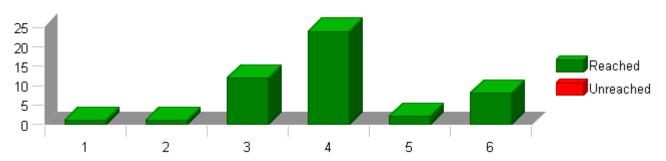
do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



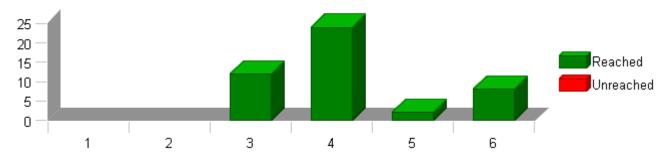
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

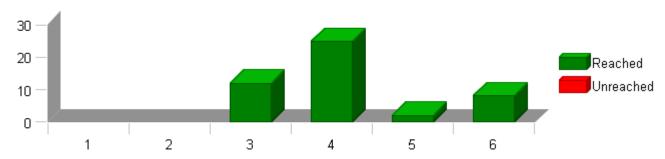


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



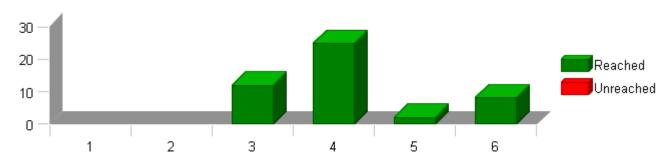
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No. Name C0 C1 DC MC/DC MCC Test Cases Result

MtrVel_Digi 30.15 401.89 cm 1 0 0 1 0 0 Tm8.17sult

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MtrVel_Per2

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -l\$(PROJECTROOT)\NxtrLitb\include -l\$(PROJECTROOT)\StdDef\include -l\$(PROJECTROOT) \MtrVel_Digi\utp\contract -l\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -l\$(PROJECTROOT)\MtrVel_Digi\include -l\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text



Module 'Mtr/vel_Digi'

Name of Tester:Sunita Kore
Code File(s) Under Test:Sa_Mtr/vel.c
Code File(s) Version:5
Module Design Document:Motor/velocity_MDD.doc
Module Design Document Version:0
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total FLASH Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/E2/E0/214
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""Mtr/vel_Per1"*, the values of variable ""MechMtrPos2Timestamp_USec_u32"* and
"Mtr/vel_PrevMtrPosSampleTime2_uS_M_u32"* are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"* in the range of ""1800 - 2200"* as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"*, the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""CalcCoarseVel"*, the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"*, the path 'P5' i.e. "(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE** has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"*) and have been given value as '0' for all array elements.

Note5: ""CBD_Sandbox_dbg.map" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 2900 Cycles TS 1.2 2725 Cycles

Description VECTOR DESCRIPTION:

TS 1.1 "Shortest Execution Path:

 $\label{eq:limit_final} if(MtrVelCorrLimDiff_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32) = False if(DiagFailed_m(MtrVel_DiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE) = False if(HwVelCorrLimDiff_MtrRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32) = True$

I(HwvelCorrLimDiff_MtrRadpS_T_132 > k_HwvelCorrLim_HwRadpS_f32)=True (DiagFailed_m(MtrVel_HwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=FALSE if(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=False if(((TRUE == MtrPosFault1_Cnt_T_lgc)=True)||(TRUE == MtrPosFault2_Cnt_T_lgc))"
TS 1.2 "Longest Execution Path: (MtrVelCorrLimDiff_MtrRadpS_T_132 > k_MtrVelCorrLim_MtrRadpS_f32)=TRUE if(DiagFailed_m(MtrVel_DiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=True if(HwVelCorrLimDiff_MtrRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=False if(DiagFailed_m(MtrVel_DiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=True if(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=True if(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=True if((TRUE == MtrPosFault1_Cnt_T_lgc_)||(TRUE == MtrPosFault2_Cnt_T_lgc_))=>False"

if((TRUE == MtrPosFault1_Cnt_T_lgc) ||(TRUE == MtrPosFault2_Cnt_T_lgc))=>False"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	116		
MtrVel_HwVelDiffAcc_Cnt_M_u16	142		
MtrVel_HwVel_HwRadpS_M_f32	-14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	233		
k_HwVelCorrLim_Cnt_Str.PStep	82		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	-23		
k_MtrVelCorrLim_Cnt_Str.Threshold	124		
k_MtrVelCorrLim_Cnt_Str.PStep	194		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	330		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	110	
MtrVel_HwVelDiffAcc_Cnt_M_u16	128	
MtrVel_HwVel_HwRadpS_M_f32	-32	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	116	
k_HwVelCorrLim_Cnt_Str.PStep	19	
k_HwVelCorrLim_Cnt_Str.NStep	10	
k_HwVelCorrLim_HwRadpS_f32	19	
k_MtrVelCorrLim_Cnt_Str.Threshold	2	
k_MtrVelCorrLim_Cnt_Str.PStep	77	
k_MtrVelCorrLim_Cnt_Str.NStep	1	

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MtrVel_Per2

Name	Input Value		
k_MtrVelCorrLim_MtrRadpS_f32	-1110		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	118	118	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

MtrVel_Per2

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Test Case 2: Boundary Check

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MtrVel_Per2



Specification

Performance Metrics
(With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 2.1 2801 Cycles
TS 2.2 2744 Cycles
TS 2.3 2573 Cycles
TS 2.4 2572 Cycles
TS 2.6 2575 Cycles
TS 2.6 2575 Cycles
TS 2.6 2575 Cycles
TS 2.7 2573 Cycles
TS 2.8 2572 Cycles
TS 2.8 2572 Cycles
TS 2.9 2621 Cycles
TS 2.10 2602 Cycles
TS 2.11 2701 Cycles
TS 2.12 2644 Cycles
TS 2.13 2635 Cycles
TS 2.14 2687 Cycles
TS 2.15 2644 Cycles
TS 2.16 2643 Cycles
TS 2.17 2623 Cycles
TS 2.18 2625 Cycles
TS 2.19 2622 Cycles
TS 2.29 2622 Cycles
TS 2.29 2622 Cycles
TS 2.20 2622 Cycles
TS 2.20 2622 Cycles
TS 2.21 2686 Cycles
TS 2.22 2622 Cycles
TS 2.23 2614 Cycles
TS 2.24 2625 Cycles
TS 2.25 2645 Cycles
TS 2.25 2645 Cycles
TS 2.262 2670 Cycles
TS 2.27 2680 Cycles
TS 2.28 2670 Cycles
TS 2.29 2588 Cycles
TS 2.29 2588 Cycles
TS 2.29 2588 Cycles
TS 2.29 2588 Cycles
TS 2.29 2580 Cycles
TS 2.29 2580 Cycles
TS 2.30 2595 Cycles
TS 2.31 2596 Cycles
TS 2.33 2600 Cycles
TS 2.34 2622 Cycles
TS 2.35 2623 Cycles
TS 2.37 2625 Cycles
TS 2.38 2622 Cycles
TS 2.39 2600 Cycles
TS 2.39 2600 Cycles
TS 2.39 2600 Cycles
TS 2.39 2601 Cycles
TS 2.39 2603 Cycles
TS 2.40 2628 Cycles
TS 2.40 2628 Cycles
TS 2.41 2631 Cycles
TS 2.42 2622 Cycles
TS 2.43 2600 Cycles
TS 2.44 2690 Cycles
TS 2.45 2631 Cycles
TS 2.47 2632 Cycles
TS 2.48 2631 Cycles
TS 2.55 2631 Cycles
TS 2.55 2632 Cycles
TS 2.55 2632 Cycles
TS 2.55 2632 Cycles
TS 2.55 2631 Cycles
TS 2.55 2632 Cycles
TS 2.55 2632 Cycles
TS 2.55 2632 Cycles
TS 2.55 2632 Cycles
TS 2.55 2635 Cycles
TS 2.55 2636 Cycles
TS 2.58 2666 Cycles
TS 2.58 2666 Cycles



Description VECTOR DESCRIPTION:

VECTOR DESCRIPTION.	
TS1.1 all min TS1.2 all max TS1.3 SysCDiagMtrVelMRF MtrRadpS f32=min	
TS1.4 SysCDiagMtrVelMRF_MtrRadpS_f32=max TS1.5 SysCDiagMtrVelMRF_MtrRadpS_f32=mid	
TS1.6 SvsCDiagMtrVeIMRF MtrRadpS f32=pos	
TS1.7 SysCDiagMtrVelMRF_MtrRadpS_f32=neg TS1.8 SysCDiagHwVel HwRadpS f32=min	
TS1.9 SysCDiagHwVel HwRadpS f32=max	
TS1.10 ŚysCDiagHwVel_HwRadpS_f32=mid TS1.11 SysCDiagHwVel_HwRadpS_f32=pos	
TS1.12 SysCDiagHwVel_HwRadpS_f32=neg	
TS1.13 Rte_Call_NxtrDiagMgr_GetNTCFailed[2]=mir TS1.14 Rte_Call_NxtrDiagMgr_GetNTCFailed[2]=mar	n
TS1.15 MtrVel_MtrVelMRF_MtrRadpS_M_f32=min	•^
TS1.16 MtrVel_MtrVelMRF_MtrRadpS_M_f32=max TS1.17 MtrVel_MtrVelMRF_MtrRadpS_M_f32=mid	
TS1.18 MtrVel MtrVelMRF MtrRadpS M f32=pos	
TS1.19 MtrVel_MtrVelMRF_MtrRadpS_M_f32=neg	
TS1.20 k_MtrVelCorrLim_MtrRadpS_f32=min TS1.21 k_MtrVelCorrLim_MtrRadpS_f32=max	
TS1.22 k MtrVelCorrLim MtrRadpS f32=mid	
TS1.23 k_MtrVelCorrLim_MtrRadpS_f32=pos TS1.24 k_MtrVelCorrLim_MtrRadpS_f32=neg	
TS1.25 MtrVel DiffAcc Cnt M u16=min	
TS1.26 MtrVel_DiffAcc_Cnt_M_u16=max TS1.27 MtrVel_DiffAcc_Cnt_M_u16=pos	
TS1.28 k_MtrVelCorrLim_Cnt_Str.Threshold=min	
TS1.29 k_MtrVelCorrLim_Cnt_Str.Threshold=max TS1.30 k MtrVelCorrLim Cnt Str.Threshold=pos	
TS1.31 k MtrVelCorrl im Cnt Str Psten=min	
TS1.32 k_MtrVelCorrLim_Cnt_Str.Pstep=max TS1.33 k_MtrVelCorrLim_Cnt_Str.Pstep=pos	
TS1.34 k_MtrVelCorrLim_Cnt_Str.Nstep=min	
TS1.35 k_MtrVelCorrLim_Cnt_Str.Nstep=max TS1.36 k_MtrVelCorrLim_Cnt_Str.Nstep=pos	
TS1.37 MtrVel HwVel HwRadpS M f32=min	
TS1.38 MtrVel_HwVel_HwRadpS_M_f32=max TS1.39 MtrVel HwVel HwRadpS M f32=mid	
TS1.40 MtrVel_HwVel_HwRadpS_M_f32=pos	
TS1.41 MtrVel_HwVel_HwRadpS_M_f32=neg	
TS1.43 k_HwVelCorrLim_HwRadpS_f32=max	
TS1.44 k_HwVelCorrLim_HwRadpS_f32=mid	
TS1.45 k_HwVelCorrLim_HwRadpS_f32=pos TS1.46 k HwVelCorrLim HwRadpS f32=neg	
TS1.47 MtrVel_HwVelDiffAcc_Cnt_M_u16=min	
TS1.48 MtrVel_HwVelDiffAcc_Cnt_M_u16=max TS1.49 MtrVel_HwVelDiffAcc_Cnt_M_u16=pos	
TS1.50 k_HwVelCorrLim_Cnt_Str.Threshold=min	
TS1.51 k_HwVelCorrLim_Cnt_Str.Threshold=max TS1.52 k_HwVelCorrLim_Cnt_Str.Threshold=pos	
TS1.53 k_HwVelCorrLim_Cnt_Str.Pstep=min	
TS1.54 k_HwVelCorrLim_Cnt_Str.Pstep=max TS1.55 k HwVelCorrLim Cnt Str.Pstep=pos	
TS1.56 k HwVelCorrl im Cnt Str Nsten=min	
TS1.57 k_HwVelCorrLim_Cnt_Str.Nstep=max TS1.58 k_HwVelCorrLim_Cnt_Str.Nstep=pos	
. 11	

Test Step 2.1 (Repeat Count = 1)			y
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	0		
MtrVel_HwVel_HwRadpS_M_f32	-42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	255		
MtrVel_HwVelDiffAcc_Cnt_M_u16	255		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	235	235	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	235	235	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	10		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	-32		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	116		
k_HwVelCorrLim_Cnt_Str.PStep	19		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	19		
k_MtrVelCorrLim_Cnt_Str.Threshold	2		
k_MtrVelCorrLim_Cnt_Str.PStep	77		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	-1110		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_C	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw\	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr\	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel DiffAcc Cnt M u16	2	2	~





Name	Actual Value	Expected Value	Result
MtrVel_HwVelDiffAcc_Cnt_M_u16	15	15	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	12		
MtrVel_HwVelDiffAcc_Cnt_M_u16	38		
MtrVel_HwVel_HwRadpS_M_f32	-30		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-960		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	129		
k_HwVelCorrLim_Cnt_Str.PStep	26		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	21		
k_MtrVelCorrLim_Cnt_Str.Threshold	5		
k_MtrVelCorrLim_Cnt_Str.PStep	90		
k_MtrVelCorrLim_Cnt_Str.NStep	2		
k_MtrVelCorrLim_MtrRadpS_f32	-950		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-39		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	5	5	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	25
MtrVel_HwVelDiffAcc_Cnt_M_u16	51
MtrVel_HwVel_HwRadpS_M_f32	-28
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-920
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	142
k_HwVelCorrLim_Cnt_Str.PStep	33
k_HwVelCorrLim_Cnt_Str.NStep	12
k_HwVelCorrLim_HwRadpS_f32	23.4500008
k_MtrVelCorrLim_Cnt_Str.Threshold	9
k_MtrVelCorrLim_Cnt_Str.PStep	103
k_MtrVelCorrLim_Cnt_Str.NStep	3
k_MtrVelCorrLim_MtrRadpS_f32	-790
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-37.5499992
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc





Name	Input Value			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	9	9	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	39	39	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.6 (Repeat Count = 1)			✓.
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	38		
MtrVel_HwVelDiffAcc_Cnt_M_u16	64		
MtrVel_HwVel_HwRadpS_M_f32	-26		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-330.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	155		
k_HwVelCorrLim_Cnt_Str.PStep	40		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	25		
k_MtrVelCorrLim_Cnt_Str.Threshold	19		
k_MtrVelCorrLim_Cnt_Str.PStep	116		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	-630.340027		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-35		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1000.5		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_	lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelM	IRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	19	19	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	51	51	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•	

Test Step 2.7 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	51	
MtrVel_HwVelDiffAcc_Cnt_M_u16	77	
MtrVel_HwVel_HwRadpS_M_f32	-24.5599995	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-840	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	168	
k_HwVelCorrLim_Cnt_Str.PStep	47	
k_HwVelCorrLim_Cnt_Str.NStep	14	
k_HwVelCorrLim_HwRadpS_f32	27	
k_MtrVelCorrLim_Cnt_Str.Threshold	26	
k_MtrVelCorrLim_Cnt_Str.PStep	129	
k_MtrVelCorrLim_Cnt_Str.NStep	5	

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MtrVel_Per2

Name	Input Value		
k_MtrVelCorrLim_MtrRadpS_f32	-470		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-33		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1000.5		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	26	26	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	63	63	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	64		
MtrVel_HwVelDiffAcc_Cnt_M_u16	90		
MtrVel_HwVel_HwRadpS_M_f32	-22		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-800		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	181		
k_HwVelCorrLim_Cnt_Str.PStep	54		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	29		
k_MtrVelCorrLim_Cnt_Str.Threshold	33		
k_MtrVelCorrLim_Cnt_Str.PStep	142		
k_MtrVelCorrLim_Cnt_Str.NStep	6		
k_MtrVelCorrLim_MtrRadpS_f32	-310		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	33	33	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	75	75	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	-

Test Step 2.9 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	77	
MtrVel_HwVelDiffAcc_Cnt_M_u16	103	
MtrVel_HwVel_HwRadpS_M_f32	-20	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-760	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	194	
k_HwVelCorrLim_Cnt_Str.PStep	61	
k_HwVelCorrLim_Cnt_Str.NStep	16	



MtrVel_Per2

Name	Input Value		
k_HwVelCorrLim_HwRadpS_f32	31		
k_MtrVelCorrLim_Cnt_Str.Threshold	40		
k_MtrVelCorrLim_Cnt_Str.PStep	155		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-150.669998		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-870		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	40	40	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	164	164	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	90		
MtrVel_HwVelDiffAcc_Cnt_M_u16	116		
MtrVel_HwVel_HwRadpS_M_f32	-18.5400009		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-720.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	207		
k_HwVelCorrLim_Cnt_Str.PStep	68		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-25.4500008		
k_MtrVelCorrLim_Cnt_Str.Threshold	47		
k_MtrVelCorrLim_Cnt_Str.PStep	168		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-790		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVeIMRF_M	ftrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	47	47	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	184	184	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.11 (Repeat Count = 1)	√
Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	103
MtrVel_HwVelDiffAcc_Cnt_M_u16	129
MtrVel_HwVel_HwRadpS_M_f32	-16
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-680

tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value

MtrVel_Per2

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Name Input Value Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel $k_HwVelCorrLim_Cnt_Str.Threshold$ 220 k_HwVelCorrLim_Cnt_Str.PStep 75 $k_HwVelCorrLim_Cnt_Str.NStep$ 18 k_HwVelCorrLim_HwRadpS_f32 -24 $k_MtrVelCorrLim_Cnt_Str.Threshold$ 54 k_MtrVelCorrLim_Cnt_Str.PStep 181 $k_MtrVelCorrLim_Cnt_Str.NStep$ 9 k_MtrVelCorrLim_MtrRadpS_f32 170 $tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value$ 20.5 tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value -710.869995 $tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc$ tgt_MtrVel_Per2_HwVelValid_Cnt_lgc $tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32$ tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32 $tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32$ **Expected Value** Result Name **Actual Value** MtrVel_DiffAcc_Cnt_M_u16 94 94 MtrVel_HwVelDiffAcc_Cnt_M_u16 204 204 $Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$ 204 204 Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 1 $Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$ 1

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	116		
MtrVel_HwVelDiffAcc_Cnt_M_u16	142		
MtrVel_HwVel_HwRadpS_M_f32	-14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	233		
k_HwVelCorrLim_Cnt_Str.PStep	82		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	-23		
k_MtrVelCorrLim_Cnt_Str.Threshold	61		
k_MtrVelCorrLim_Cnt_Str.PStep	194		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	330		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20.5		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVeIMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	✓



Test Step 2.13 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	129		
MtrVel_HwVelDiffAcc_Cnt_M_u16	155		
MtrVel_HwVel_HwRadpS_M_f32	-12		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-600		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	246		
k_HwVelCorrLim_Cnt_Str.PStep	89		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	-22		
k_MtrVelCorrLim_Cnt_Str.Threshold	68		
k_MtrVelCorrLim_Cnt_Str.PStep	207		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	490		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	32.8800011		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-550		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	118	118	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	244	244	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	142		
MtrVel_HwVelDiffAcc_Cnt_M_u16	168		
MtrVel_HwVel_HwRadpS_M_f32	-10.3400002		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-560.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	250		
k_HwVelCorrLim_Cnt_Str.PStep	96		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-21.4500008		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	650.669983		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-470.660004		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	130	130	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	250	250	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	✓



Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	155		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	-8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	251		
k_HwVelCorrLim_Cnt_Str.PStep	103		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-20		
k_MtrVelCorrLim_Cnt_Str.Threshold	82		
k_MtrVelCorrLim_Cnt_Str.PStep	233		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	810		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-28		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-390		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mt	rRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	82	82	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	251	251	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	168		
MtrVel_HwVelDiffAcc_Cnt_M_u16	194		
MtrVel_HwVel_HwRadpS_M_f32	-6		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	110		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-19		
k_MtrVelCorrLim_Cnt_Str.Threshold	89		
k_MtrVelCorrLim_Cnt_Str.PStep	246		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	970		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-26		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-310		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	89	89	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_Igc.value	0	0	~



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)			~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	181		
MtrVel_HwVelDiffAcc_Cnt_M_u16	207		
MtrVel_HwVel_HwRadpS_M_f32	-4		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	12		
k_HwVelCorrLim_Cnt_Str.PStep	117		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	-18		
k_MtrVelCorrLim_Cnt_Str.Threshold	96		
k_MtrVelCorrLim_Cnt_Str.PStep	250		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-24		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	96	96	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	12	12	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	194		
MtrVel_HwVelDiffAcc_Cnt_M_u16	220		
MtrVel_HwVel_HwRadpS_M_f32	-2.33999991		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	500.799988		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	25		
k_HwVelCorrLim_Cnt_Str.PStep	124		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-17.6539993		
k_MtrVelCorrLim_Cnt_Str.Threshold	103		
k_MtrVelCorrLim_Cnt_Str.PStep	251		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	20.5599995		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-22.3400002		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-150		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	103	103	•
MtrVel_HwVelDiffAcc_Cnt_M_u16	25	25	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	207		
MtrVel_HwVelDiffAcc_Cnt_M_u16	233		
MtrVel_HwVel_HwRadpS_M_f32	0		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-500.700012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	38		
k_HwVelCorrLim_Cnt_Str.PStep	131		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	-16		
k_MtrVelCorrLim_Cnt_Str.Threshold	110		
k_MtrVelCorrLim_Cnt_Str.PStep	10		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	30		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-70.4000015		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	110	110	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	38	38	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.20 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	220		
MtrVel_HwVelDiffAcc_Cnt_M_u16	246		
MtrVel_HwVel_HwRadpS_M_f32	2		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	51		
k_HwVelCorrLim_Cnt_Str.PStep	138		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	-15		
k_MtrVelCorrLim_Cnt_Str.Threshold	117		
k_MtrVelCorrLim_Cnt_Str.PStep	12		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-18		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	10		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	117	117	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	51	51	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	233		
MtrVel_HwVelDiffAcc_Cnt_M_u16	250		
MtrVel_HwVel_HwRadpS_M_f32	4		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-100.339996		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	64		
k_HwVelCorrLim_Cnt_Str.PStep	145		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	-14		
k_MtrVelCorrLim_Cnt_Str.Threshold	124		
k_MtrVelCorrLim_Cnt_Str.PStep	25		
k_MtrVelCorrLim_Cnt_Str.NStep	19		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-16		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	90		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	214	214	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	64	64	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~



Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	246		
MtrVel_HwVelDiffAcc_Cnt_M_u16	251		
MtrVel_HwVel_HwRadpS_M_f32	6.67000008		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-50		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	16		
k_HwVelCorrLim_Cnt_Str.PStep	152		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-13		
k_MtrVelCorrLim_Cnt_Str.Threshold	131		
k_MtrVelCorrLim_Cnt_Str.PStep	38		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	0		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-14		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	170.759995		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	131	131	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	16	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	250		
MtrVel_HwVelDiffAcc_Cnt_M_u16	169		
MtrVel_HwVel_HwRadpS_M_f32	8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	159		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	-12.4499998		
k_MtrVelCorrLim_Cnt_Str.Threshold	138		
k_MtrVelCorrLim_Cnt_Str.PStep	51		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	250.199997		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	250	250	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	28	28	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓



T .					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓	

Test Step 2.24 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	251		
MtrVel_HwVelDiffAcc_Cnt_M_u16	172		
MtrVel_HwVel_HwRadpS_M_f32	10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	50		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	32		
k_HwVelCorrLim_Cnt_Str.PStep	166		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-11		
k_MtrVelCorrLim_Cnt_Str.Threshold	145		
k_MtrVelCorrLim_Cnt_Str.PStep	64		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	-250.800003		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-10.9499998		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	330		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	145	145	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	32	32	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	175		
MtrVel_HwVel_HwRadpS_M_f32	12.3999996		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	100		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	5		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-10		
k_MtrVelCorrLim_Cnt_Str.Threshold	152		
k_MtrVelCorrLim_Cnt_Str.PStep	16		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	40		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-8		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_C	nt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw\	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr\	/eIMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	16	16	•
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	✓

T .				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	255		
MtrVel_HwVelDiffAcc_Cnt_M_u16	178		
MtrVel_HwVel_HwRadpS_M_f32	14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	150		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	68		
k_HwVelCorrLim_Cnt_Str.PStep	9		
k_HwVelCorrLim_Cnt_Str.NStep	2		
k_HwVelCorrLim_HwRadpS_f32	-9		
k_MtrVelCorrLim_Cnt_Str.Threshold	159		
k_MtrVelCorrLim_Cnt_Str.PStep	28		
k_MtrVelCorrLim_Cnt_Str.NStep	3		
k_MtrVelCorrLim_MtrRadpS_f32	55.6699982		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-6		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	490		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	159	159	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	68	68	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.27 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	120		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	16		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	200.449997		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	86		
k_HwVelCorrLim_Cnt_Str.PStep	13		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	-8		
k_MtrVelCorrLim_Cnt_Str.Threshold	166		
k_MtrVelCorrLim_Cnt_Str.PStep	32		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	68		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-4		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	570.880005		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	152	152	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	86	86	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.28 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	16		
MtrVel_HwVelDiffAcc_Cnt_M_u16	184		
MtrVel_HwVel_HwRadpS_M_f32	18		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	250		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	16		
k_HwVelCorrLim_Cnt_Str.PStep	17		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	-7		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	50		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	79		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-2.3499999		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	650		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	16	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	✓



Test Step 2.29 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	28		
MtrVel_HwVelDiffAcc_Cnt_M_u16	187		
MtrVel_HwVel_HwRadpS_M_f32	20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	300		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	21		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-6.78000021		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	68		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	112		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	730		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	96	96	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	28	28	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.30 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	32		
MtrVel_HwVelDiffAcc_Cnt_M_u16	190		
MtrVel_HwVel_HwRadpS_M_f32	22		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	350.779999		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	32		
k_HwVelCorrLim_Cnt_Str.PStep	25		
k_HwVelCorrLim_Cnt_Str.NStep	6		
k_HwVelCorrLim_HwRadpS_f32	-5		
k_MtrVelCorrLim_Cnt_Str.Threshold	120		
k_MtrVelCorrLim_Cnt_Str.PStep	86		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	145		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	2		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	810		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	118	118	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	32	32	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.31 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	50		
MtrVel_HwVelDiffAcc_Cnt_M_u16	193		
MtrVel_HwVel_HwRadpS_M_f32	24.5599995		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	400		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	29		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	-4		
k_MtrVelCorrLim_Cnt_Str.Threshold	169		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	178		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	4		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	50	50	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Test Step 2.32 (Repeat Count = 1)			~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	68		
MtrVel_HwVelDiffAcc_Cnt_M_u16	196		
MtrVel_HwVel_HwRadpS_M_f32	26		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	450		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	68		
k_HwVelCorrLim_Cnt_Str.PStep	33		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	-3		
k_MtrVelCorrLim_Cnt_Str.Threshold	172		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	211.654999		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	6		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	970		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHv	/Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMt	rVelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	172	172	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	68	68	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
			✓





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt MtrVel Per2 HwVelValid Cnt lgc.value	1	1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	86		
MtrVel_HwVelDiffAcc_Cnt_M_u16	199		
MtrVel_HwVel_HwRadpS_M_f32	28		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	500		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	86		
k_HwVelCorrLim_Cnt_Str.PStep	37		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	-2		
k_MtrVelCorrLim_Cnt_Str.Threshold	175		
k_MtrVelCorrLim_Cnt_Str.PStep	100		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	244		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	8.43999958		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1050.88		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	175	175	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	86	86	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓



Test Step 2.34 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	104		
MtrVel_HwVelDiffAcc_Cnt_M_u16	202		
MtrVel_HwVel_HwRadpS_M_f32	30		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	550		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	104		
k_HwVelCorrLim_Cnt_Str.PStep	41		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-1.34000003		
k_MtrVelCorrLim_Cnt_Str.Threshold	178		
k_MtrVelCorrLim_Cnt_Str.PStep	169		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	277		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1130		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	178	178	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	104	104	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.35 (Repeat Count = 1)			✓	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	122			
MtrVel_HwVelDiffAcc_Cnt_M_u16	205			
MtrVel_HwVel_HwRadpS_M_f32	32			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	600.559998			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	122			
k_HwVelCorrLim_Cnt_Str.PStep	45			
k_HwVelCorrLim_Cnt_Str.NStep	14			
k_HwVelCorrLim_HwRadpS_f32	0			
k_MtrVelCorrLim_Cnt_Str.Threshold	181			
k_MtrVelCorrLim_Cnt_Str.PStep	172			
k_MtrVelCorrLim_Cnt_Str.NStep	20	20		
k_MtrVelCorrLim_MtrRadpS_f32	310.559998			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1210			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_Hw	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF	_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	181	181	✓	
MtrVel_HwVelDiffAcc_Cnt_M_u16	122	122	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓



Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	140		
MtrVel_HwVelDiffAcc_Cnt_M_u16	208		
MtrVel_HwVel_HwRadpS_M_f32 4	34.5600014 58		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	650		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	140		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	1		
k_MtrVelCorrLim_Cnt_Str.Threshold	184		
k_MtrVelCorrLim_Cnt_Str.PStep	175		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	343		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	14		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1290		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	184	184	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	140	140	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•	

Test Step 2.37 (Repeat Count = 1)	✓
Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	158
MtrVel_HwVelDiffAcc_Cnt_M_u16	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	176		
MtrVel_HwVelDiffAcc_Cnt_M_u16	214		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	750		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
k_HwVelCorrLim_Cnt_Str.Threshold	176		
k_HwVelCorrLim_Cnt_Str.PStep	57		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	3		
k_MtrVelCorrLim_Cnt_Str.Threshold	190		
k_MtrVelCorrLim_Cnt_Str.PStep	181		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	409		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	18		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	110		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	190	190	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	176	176	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Test Step 2.39 (Repeat Count = 1)			~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	194		
MtrVel_HwVelDiffAcc_Cnt_M_u16	5		
MtrVel_HwVel_HwRadpS_M_f32	0		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	800.650024		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	194		
k_HwVelCorrLim_Cnt_Str.PStep	61		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	4.55999994		
k_MtrVelCorrLim_Cnt_Str.Threshold	193		
k_MtrVelCorrLim_Cnt_Str.PStep	184		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	442		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20.9799995		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	170		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	193	193	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	66	66	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
			✓

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MtrVel_Per2

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	212		
MtrVel_HwVelDiffAcc_Cnt_M_u16	16		
MtrVel_HwVel_HwRadpS_M_f32	20.1000004		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	850		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	212		
k_HwVelCorrLim_Cnt_Str.PStep	65		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	5		
k_MtrVelCorrLim_Cnt_Str.Threshold	196		
k_MtrVelCorrLim_Cnt_Str.PStep	187		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	475		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	22		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	196	196	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓





Test Step 2.41 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	230		
MtrVel_HwVelDiffAcc_Cnt_M_u16	27		
MtrVel_HwVel_HwRadpS_M_f32	-20.8999996		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	900		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	3		
k_HwVelCorrLim_Cnt_Str.PStep	4		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	6		
k_MtrVelCorrLim_Cnt_Str.Threshold	199		
k_MtrVelCorrLim_Cnt_Str.PStep	190		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	508.649994		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	24		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	290.033997		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	199	199	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	248		
MtrVel_HwVelDiffAcc_Cnt_M_u16	38		
MtrVel_HwVel_HwRadpS_M_f32	-41.2400017		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	950		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	7		
k_HwVelCorrLim_Cnt_Str.PStep	19		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	202		
k_MtrVelCorrLim_Cnt_Str.PStep	193		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	541		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	26		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	202	202	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	7	7	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	-

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	✓



Test Step 2.43 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	5		
MtrVel_HwVelDiffAcc_Cnt_M_u16	49		
MtrVel_HwVel_HwRadpS_M_f32	-39		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1000.45001		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	9		
k_HwVelCorrLim_Cnt_Str.PStep	34		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	205		
k_MtrVelCorrLim_Cnt_Str.PStep	196		
k_MtrVelCorrLim_Cnt_Str.NStep	19		
k_MtrVelCorrLim_MtrRadpS_f32	574		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	28.4500008		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	201	201	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	9	9	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

T+ 04 0 44 (D+ 0+ -4)			
Test Step 2.44 (Repeat Count = 1)			~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	9		
MtrVel_HwVelDiffAcc_Cnt_M_u16	60		
MtrVel_HwVel_HwRadpS_M_f32	-37		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1050		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	21		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	208		
k_MtrVelCorrLim_Cnt_Str.PStep	199		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	607		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	21	21	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)			✓	
Name	Input Value	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	13			
MtrVel_HwVelDiffAcc_Cnt_M_u16	71			
MtrVel_HwVel_HwRadpS_M_f32	-35			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1100			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	33			
k_HwVelCorrLim_Cnt_Str.PStep	64			
k_HwVelCorrLim_Cnt_Str.NStep	3			
k_HwVelCorrLim_HwRadpS_f32	10.6000004			
k_MtrVelCorrLim_Cnt_Str.Threshold	211			
k_MtrVelCorrLim_Cnt_Str.PStep	202			
k_MtrVelCorrLim_Cnt_Str.NStep	0			
k_MtrVelCorrLim_MtrRadpS_f32	640			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	32			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	590.887024			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	13	13	✓	
MtrVel_HwVelDiffAcc_Cnt_M_u16	33	33	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓	

Т					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Input Value				
17				
82				
-33				
1150				
tgt_Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
45 79				
			5	
-10.3000002	-10.3000002			
214				
205				
10	10 767.650024 34 590			
767.650024				
34				
590				
tgt_MtrVel_Per2_HwVelValid_0	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_MtrVel_Per2_SysCDiagHw	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32			
tgt_MtrVel_Per2_SysCDiagMtr	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32			
Actual Value	Expected Value	Result		
7	7	-		
45	45	•		
204	204	•		
	-33 1150 tgt_Rte_Inst_Sa_MtrVel 45 79 5 -10.3000002 214 205 10 767.650024 34 590 tgt_MtrVel_Per2_HwVelValid_0 tgt_MtrVel_Per2_SysCDiagHw tgt_MtrVel_Per2_SysCDiagMtr Actual Value 7 45	-33 1150 tgt_Rte_Inst_Sa_MtrVel 45 79 5 -10.3000002 214 205 10 767.650024 34 590 tgt_MtrVel_Per2_HwVelValid_Cnt_Igc tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32 tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32 Actual Value 7 45 45		

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Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tot MtrVel Per2 HwVelValid Cnt loc value	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.47 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	21		
MtrVel_HwVelDiffAcc_Cnt_M_u16	0		
MtrVel_HwVel_HwRadpS_M_f32	13		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1200.44995		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	57		
k_HwVelCorrLim_Cnt_Str.PStep	94		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	7		
k_MtrVelCorrLim_Cnt_Str.Threshold	217		
k_MtrVelCorrLim_Cnt_Str.PStep	208		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	706		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	36		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	650		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mt	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	20	20	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	57	57	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.48 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	25		
MtrVel_HwVelDiffAcc_Cnt_M_u16	255		
MtrVel_HwVel_HwRadpS_M_f32	12.4499998		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1250		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	69		
k_HwVelCorrLim_Cnt_Str.PStep	109		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	8		
k_MtrVelCorrLim_Cnt_Str.Threshold	220		
k_MtrVelCorrLim_Cnt_Str.PStep	211		
k_MtrVelCorrLim_Cnt_Str.NStep	3		
k_MtrVelCorrLim_MtrRadpS_f32	739		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	38.4399986		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	710		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	22	22	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	69	69	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	29		
MtrVel_HwVelDiffAcc_Cnt_M_u16	120		
MtrVel_HwVel_HwRadpS_M_f32	11		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1300		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	81		
k_HwVelCorrLim_Cnt_Str.PStep	124		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	9.97000027		
k_MtrVelCorrLim_Cnt_Str.Threshold	223		
k_MtrVelCorrLim_Cnt_Str.PStep	214		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	772		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	40		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	770.653992		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	24	24	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	81	81	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~



Test Step 2.50 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	33		
MtrVel_HwVelDiffAcc_Cnt_M_u16	19		
MtrVel_HwVel_HwRadpS_M_f32	10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	25.6700001		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	139		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	10		
k_MtrVelCorrLim_Cnt_Str.Threshold	226		
k_MtrVelCorrLim_Cnt_Str.PStep	217		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	805		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	830		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	26	26	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.51 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	37		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	9		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	75		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	154		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	11		
k_MtrVelCorrLim_Cnt_Str.Threshold	229		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	838.450012		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	28	28	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	179	179	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	✓

Test Step 2.52 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	41		
MtrVel_HwVelDiffAcc_Cnt_M_u16	31		
MtrVel_HwVel_HwRadpS_M_f32	5.55999994		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	125		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	169		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	12		
k_MtrVelCorrLim_Cnt_Str.Threshold	232		
k_MtrVelCorrLim_Cnt_Str.PStep	223		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	871		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_C	:nt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw\	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr\	/eIMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	30	30	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	100	100	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.53 (Repeat Count = 1)			<u> </u>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	45		
MtrVel_HwVelDiffAcc_Cnt_M_u16	37		
MtrVel_HwVel_HwRadpS_M_f32	7		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	175.759995		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	13.75		
k_MtrVelCorrLim_Cnt_Str.Threshold	235		
k_MtrVelCorrLim_Cnt_Str.PStep	226		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	904		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-40.5499992		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1010		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	27	27	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
			✓

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Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tot MtrVel Per2 HwVelValid Cnt lgc.value	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.54 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	49		
MtrVel_HwVelDiffAcc_Cnt_M_u16	43		
MtrVel_HwVel_HwRadpS_M_f32	4		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	225		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	15		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	14		
k_MtrVelCorrLim_Cnt_Str.Threshold	238		
k_MtrVelCorrLim_Cnt_Str.PStep	229		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	937.450012		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	9		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1070		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	29	29	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.55 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	53		
MtrVel_HwVelDiffAcc_Cnt_M_u16	49		
MtrVel_HwVel_HwRadpS_M_f32	3.55999994		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	275		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	50		
k_HwVelCorrLim_Cnt_Str.NStep	12		
k_HwVelCorrLim_HwRadpS_f32	15		
k_MtrVelCorrLim_Cnt_Str.Threshold	241		
k_MtrVelCorrLim_Cnt_Str.PStep	232		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	970		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1130		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	49	49	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	37	37	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	•

T .					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Test Step 2.56 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	57		
MtrVel_HwVelDiffAcc_Cnt_M_u16	55		
MtrVel_HwVel_HwRadpS_M_f32	2		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	325.670013		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	41		
k_HwVelCorrLim_Cnt_Str.PStep	16		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	16.8700008		
k_MtrVelCorrLim_Cnt_Str.Threshold	12		
k_MtrVelCorrLim_Cnt_Str.PStep	235		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	1003		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	11.4499998		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1190.87		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	49	49	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	55	55	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.57 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	61		
MtrVel_HwVelDiffAcc_Cnt_M_u16	61		
MtrVel_HwVel_HwRadpS_M_f32	1		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	375		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	54		
k_HwVelCorrLim_Cnt_Str.PStep	22		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	17		
k_MtrVelCorrLim_Cnt_Str.Threshold	18		
k_MtrVelCorrLim_Cnt_Str.PStep	238		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	1036.56006		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	45	45	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	41	41	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.58 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	65		
MtrVel_HwVelDiffAcc_Cnt_M_u16	67		
MtrVel_HwVel_HwRadpS_M_f32	15.4300003		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	425		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	67		
k_HwVelCorrLim_Cnt_Str.PStep	37		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	18		
k_MtrVelCorrLim_Cnt_Str.Threshold	66		
k_MtrVelCorrLim_Cnt_Str.PStep	241		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	1069		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	13		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1310		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	53	53	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	57	57	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~





T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 3.1 (Repeat Count = 1)			_ ~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	10		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	-32		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	116		
k_HwVelCorrLim_Cnt_Str.PStep	19		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	19		
k_MtrVelCorrLim_Cnt_Str.Threshold	2		
k_MtrVelCorrLim_Cnt_Str.PStep	77		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	-1110		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVe	_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVe	MRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	15	15	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



T						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~		
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓		

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	103		
MtrVel_HwVelDiffAcc_Cnt_M_u16	129		
MtrVel_HwVel_HwRadpS_M_f32	-16		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-680		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	220		
k_HwVelCorrLim_Cnt_Str.PStep	75		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	-24		
k_MtrVelCorrLim_Cnt_Str.Threshold	54		
k_MtrVelCorrLim_Cnt_Str.PStep	181		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	170		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-710		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	94	94	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	77		
MtrVel_HwVelDiffAcc_Cnt_M_u16	103		
MtrVel_HwVel_HwRadpS_M_f32	-20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-760		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
k_HwVelCorrLim_Cnt_Str.Threshold	194		
k_HwVelCorrLim_Cnt_Str.PStep	61		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	31		
k_MtrVelCorrLim_Cnt_Str.Threshold	40		
k_MtrVelCorrLim_Cnt_Str.PStep	155		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-150		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-870		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVe	I_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVe	IMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	40	40	•
MtrVel_HwVelDiffAcc_Cnt_M_u16	164	164	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•
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MtrVel_Per2

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	-

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	90		
MtrVel_HwVelDiffAcc_Cnt_M_u16	116		
MtrVel_HwVel_HwRadpS_M_f32	-18		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-720		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	207		
k_HwVelCorrLim_Cnt_Str.PStep	68		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-25		
k_MtrVelCorrLim_Cnt_Str.Threshold	47		
k_MtrVelCorrLim_Cnt_Str.PStep	168		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-790		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_l	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	47	47	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	184	184	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•	



Test Step 3.5 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	12		
MtrVel_HwVelDiffAcc_Cnt_M_u16	38		
MtrVel_HwVel_HwRadpS_M_f32	-30		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-960		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	129		
k_HwVelCorrLim_Cnt_Str.PStep	26		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	21		
k_MtrVelCorrLim_Cnt_Str.Threshold	5		
k_MtrVelCorrLim_Cnt_Str.PStep	90		
k_MtrVelCorrLim_Cnt_Str.NStep	2		
k_MtrVelCorrLim_MtrRadpS_f32	-950		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-39		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	5	5	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	142		
MtrVel_HwVelDiffAcc_Cnt_M_u16	168		
MtrVel_HwVel_HwRadpS_M_f32	-10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-560		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	250		
k_HwVelCorrLim_Cnt_Str.PStep	96		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-21		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	650		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	130	130	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	250	250	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 3.7 (Repeat Count = 1)			V	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	116	116		
MtrVel_HwVelDiffAcc_Cnt_M_u16	142			
MtrVel_HwVel_HwRadpS_M_f32	-14			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	233			
k_HwVelCorrLim_Cnt_Str.PStep	82			
k_HwVelCorrLim_Cnt_Str.NStep	19			
k_HwVelCorrLim_HwRadpS_f32	-23			
k_MtrVelCorrLim_Cnt_Str.Threshold	61			
k_MtrVelCorrLim_Cnt_Str.PStep	194			
k_MtrVelCorrLim_Cnt_Str.NStep	10			
k_MtrVelCorrLim_MtrRadpS_f32	330			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	106	106	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 3.8 (Repeat Count = 1)			V	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	155	155		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181			
MtrVel_HwVel_HwRadpS_M_f32	-8			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	251			
k_HwVelCorrLim_Cnt_Str.PStep	103			
k_HwVelCorrLim_Cnt_Str.NStep	10			
k_HwVelCorrLim_HwRadpS_f32	-20			
k_MtrVelCorrLim_Cnt_Str.Threshold	82			
k_MtrVelCorrLim_Cnt_Str.PStep	233			
k_MtrVelCorrLim_Cnt_Str.NStep	13			
k_MtrVelCorrLim_MtrRadpS_f32	810			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-28			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-390			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	82	82	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	251	251	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 3.9 (Repeat Count = 1)			✓	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	37			
MtrVel_HwVelDiffAcc_Cnt_M_u16	25			
MtrVel_HwVel_HwRadpS_M_f32	9			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	75			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	255			
k_HwVelCorrLim_Cnt_Str.PStep	154			
k_HwVelCorrLim_Cnt_Str.NStep	20			
k_HwVelCorrLim_HwRadpS_f32	11			
k_MtrVelCorrLim_Cnt_Str.Threshold	229			
k_MtrVelCorrLim_Cnt_Str.PStep	220			
k_MtrVelCorrLim_Cnt_Str.NStep	9			
k_MtrVelCorrLim_MtrRadpS_f32	838			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVeIMRF_MtrF	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	28	28	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	179	179	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~	

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	45			
MtrVel_HwVelDiffAcc_Cnt_M_u16	37			
MtrVel_HwVel_HwRadpS_M_f32	7			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	175			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	10			
k_HwVelCorrLim_Cnt_Str.PStep	0			
k_HwVelCorrLim_Cnt_Str.NStep	8			
k_HwVelCorrLim_HwRadpS_f32	13			
k_MtrVelCorrLim_Cnt_Str.Threshold	235			
k_MtrVelCorrLim_Cnt_Str.PStep	226	226		
k_MtrVelCorrLim_Cnt_Str.NStep	18			
k_MtrVelCorrLim_MtrRadpS_f32	904			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-40			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1010			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_C	nt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwV	el_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrV	elMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	27	27	•	
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•	

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Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.11 (Repeat Count = 1)			
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	41		
MtrVel_HwVelDiffAcc_Cnt_M_u16	31		
MtrVel_HwVel_HwRadpS_M_f32	5		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	125		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	169		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	12		
k_MtrVelCorrLim_Cnt_Str.Threshold	232		
k_MtrVelCorrLim_Cnt_Str.PStep	223		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	871		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	30	30	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	100	100	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓





Test Step 3.12 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	61		
MtrVel_HwVelDiffAcc_Cnt_M_u16	61		
MtrVel_HwVel_HwRadpS_M_f32	1		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	375		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	54		
k_HwVelCorrLim_Cnt_Str.PStep	22		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	17		
k_MtrVelCorrLim_Cnt_Str.Threshold	18		
k_MtrVelCorrLim_Cnt_Str.PStep	238		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	1036		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	45	45	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	41	41	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 3.13 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	175		
MtrVel_HwVel_HwRadpS_M_f32	12.3999996		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	100		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	5		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-10		
k_MtrVelCorrLim_Cnt_Str.Threshold	152		
k_MtrVelCorrLim_Cnt_Str.PStep	16		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	40		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-8		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	16	16	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 3.14 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	9		
MtrVel_HwVelDiffAcc_Cnt_M_u16	60		
MtrVel_HwVel_HwRadpS_M_f32	-37		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1050		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	21		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	208		
k_MtrVelCorrLim_Cnt_Str.PStep	199		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	607		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	21	21	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 3.15 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	212		
MtrVel_HwVelDiffAcc_Cnt_M_u16	16		
MtrVel_HwVel_HwRadpS_M_f32	20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	850		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	212		
k_HwVelCorrLim_Cnt_Str.PStep	65		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	5		
k_MtrVelCorrLim_Cnt_Str.Threshold	196		
k_MtrVelCorrLim_Cnt_Str.PStep	187		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	475		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	22		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	196	196	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_Igc.value	0	0	✓





T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	0		
MtrVel_HwVel_HwRadpS_M_f32	-42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

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MtrVelBlend

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVelBlend

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	2	
Successful	2	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\tinclude
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text





Module 'MtrVel_Digi'

Name of Tester:Sunita Kore
Code File(s) Version:5
Module Design Document.WotorVelocity_MDD.doc
Module Design Document.WotorVelocity_MDD.doc
Module Design Document.WotorVelocity_MDD.doc
Module Design Document Version:3
Unit Test Plan Version:3
Unit Test Plan Version:1
Optimization Level.Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total FALSH Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date: 12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""MtrVel_Per1"*, the values of variable "MechMtrPos2Timestamp_USec_u32"* and
""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"* are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"* as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"*, the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""CalcCoarseVel"*, the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"* has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as "0" for all array elements.

Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: CPU Cycles:

TS 1.1 612 Cycles TS 1.2 663 Cycles

Description VECTOR DESCRIPTION:

TS 1.1 " Shortest Execution Path: (Interpolation function)
TblInput <= TableX(0)=>True"
TS 1.2 "Longest Execution Path: (Interpolation function)
TblInput <= TableX(0)=>False
TblInput>= TableX(Size - 1)"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0		
FinMtrVel_MtrRadpS_T_f32	-654.98		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-654.97998	-654.98	✓

T				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0.95		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	14		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	675.474976	675.475	✓

T				~
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~



Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		· ·
CrsMtrVel_MtrRadpS_T_f32	-1350		
FinMtrVel_MtrRadpS_T_f32	-1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-1350	-1350	✓

Т					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1350		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	1350	1350	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	





Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-36		
FinMtrVel_MtrRadpS_T_f32	-1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-36	-36	~

T					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-58.87		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-58.8699989	-58.87	~

T					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	235		
FinMtrVel_MtrRadpS_T_f32	0		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	235	235	✓

T				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	300.07		
FinMtrVel_MtrRadpS_T_f32	235.5		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	10		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	300.070007	300.07	~

Т					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	





Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-476		
FinMtrVel_MtrRadpS_T_f32	-58.56		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	13		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	14		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-476	-476	~

T				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•

Test Step 2.8 (Repeat Count = 1)		✓	
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-1350		
FinMtrVel_MtrRadpS_T_f32	300		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-1350	-1350	~

Т				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1350		
FinMtrVel_MtrRadpS_T_f32	-476		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	1350	1350	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0		
FinMtrVel_MtrRadpS_T_f32	-654.98		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-654.97998	-654.98	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•





Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1100.19995		
FinMtrVel_MtrRadpS_T_f32	-765		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	1100.19995	1100.19995	✓

Т					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-654.65		
FinMtrVel_MtrRadpS_T_f32	675.87		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	13		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-654.650024	-654.65	✓

Т				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.13 (Repeat Count = 1)			→
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-36		
FinMtrVel_MtrRadpS_T_f32	333		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-36	-36	✓

T				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-58		
FinMtrVel_MtrRadpS_T_f32	987		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-58	-58	~

T .				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

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MtrVelBlend

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	345.96		
FinMtrVel_MtrRadpS_T_f32	-87.43		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	13		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	345.959991	345.96	~

T .				✓
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~



ProjectMtrVel_DigiModuleMtrVel_DigiTest ObjectRegressionFit

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -l\$(PROJECTROOT)\NxtrLib\include -l\$(PROJECTROOT)\StdDef\include -l\$(PROJECTROOT) \MtrVel_Digi\utp\contract -l\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -l\$(PROJECTROOT)\MtrVel_Digi\include -l\$(Compiler Install Path)\tinclude
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MfrVeI _Digi\utp\contract -I\$(PROJECTROOT)\MfrVeI_Digi\utp\contract\Sa_MtrVeI -I\$(PROJECTROOT)\MfrVeI_Digi\utp\contract\Sa_MtrVeI -I\$(PROJECTROOT)\MfrVeI_Digi\utp\contract\Sa_MtrVeI -I\$(PROJECTROOT)\MfrVeI_Digi\utp\contract\Sa_MtrVeI -I\$(PROJECTROOT)\MfrVeI_Digi\utp\contract\Sa_MtrVeI -I\$(PROJECTROOT)\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\

Comments/Description/Spe	ecification
Name	Text



Module 'MtrVel_Digi'

Name of Tester:Sunita Kore Nathe of Tester Journal Note
Code File(s) Under Test:Sa_MtrVel.c
Code File(s) Version:5
Module Design Document:MotorVelocity_MDD.doc

Module Design Document:MotorVelocity_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total RAM Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:

Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"", the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements.

Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

Attuilantaa				
Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl			
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2			
Time Unit	Cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			



Test Case 1

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 1288 Cycles TS 1.2 1365 Cycles

VECTOR DESCRIPTION: Description

TS 1.1 "Shortest Execution Path:

(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = False;

(PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = FALSE

(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = TRUE"

TS 1.2 "Longest Execution Path:

(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = True

(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = TRUE"

Test Step 1.1 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel_OsBufSelect_Cnt_M_u08	1	1	•
RegressionFit()	-833.333313	-833.3333333 ± 0.0625	~

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2
MtrVel3_OsBufPos_Cnt_M_u08[3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3 PosBuffer Rad M f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✓
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
RegressionFit()	-600.838257	-600.8382387 ± 0.0625	✓

Test Case 2

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 2.1 1288 Cycles TS 2.2 1307 Cycles TS 2.3 1365 Cycles TS 2.4 1356 Cycles TS 2.5 1290 Cycles

Description

VECTOR DESCRIPTION:

TS 2.1"(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = TRUE; (PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = FALSE" TS 2.2"(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = FALSE; (PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = TRUE;" TS 2.3(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = TRUE TS 2.4(PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = TRUE TS 2.5(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3 TimeBuffer uS M u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	Actual Value	Expected Value	Result
MtrVel OldPosBuf Cnt M u08	0	0	Kesuit
MtrVel_OsBufSelect_Cnt_M_u08	1	1	
RegressionFit()	-833.333313	-833.3333333 ± 0.0625	
izediessioni iii)	-033.333313	-033.333333 ± 0.0023	

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3		
MtrVel3 PosBuffer Rad M f32[2][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6		
	5		
MtrVel3_PosBuffer_Rad_M_f32[3][7] MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70		
	35		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9285		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel OldPosBuf Cnt M u08	3	3	Result
MtrVel_OsBufSelect_Cnt_M_u08	0	0	_
RegressionFit()	631.181824	631.1818724 ± 0.0625	

Test Step 2.3 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2
MtrVel3_OsBufPos_Cnt_M_u08[3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5



N	In and Value		
Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel OldPosBuf Cnt M u08	2	2	√ V
MtrVel_OsBufSelect_Cnt_M_u08	3	3	-
RegressionFit()	-600.838257	-600.8382387 ± 0.0625	
regression II()	000.000201	000.0002001 ± 0.0020	

Test Step 2.4 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	



Name					
MonVest Probleme Rad M. (2000) 2	lame	Input Value			
Mirviss Probleme Rad M. 2010					
MirVeS Position Red M. 2019 5					
Mirvis Dissibler, Ral, M. 120[10] 5					
MarVeS Dealbuffer Rad, M. 1921 13					
Minvest Postulare Rad M. 2021 19					
Mirves Desider Rad M, 1921/19 Mirves Desider Rad M, 1921/17 4 6 Mirves Desider Rad M, 1921/17 Mirves Desider Rad M, 1921/17 Mirves Desider Rad M, 1921/17 Mirves Desider Rad M, 1921/19 Mirves Desider Rad M, 1921					
Minyas Posture Rad M. 1921 19 3 3 3 3 3 3 3 3 3					
MRYMS_PosBuffer_Rad_M_S2[19] 3 4 6 7 7 7 7 7 7 7 7 7					
Minvest Possibler Rad M. 1921 01 5					
MinyAss Positioner Rad M. 1922 10					
Minvals Position Rad M. 1920 19 1 1 1 1 1 1 1 1 1					
MirVells, PosBuffer, Rad. M. (52/21)3 MirVells, PosBuffer, Rad. M. (52/21)4 MirVells, PosBuffer, Rad. M. (52/21)4 MirVells, PosBuffer, Rad. M. (52/21)5 MirVells, PosBuffer, Rad. M. (52/31)0 MirVells, PosBuffer, Rad. M. (52/31)1 MirVells, PosBuffer, Rad. M. (52/31)1 MirVells, PosBuffer, Rad. M. (52/31)2 MirVells, PosBuffer, Rad. M. (52/31)2 MirVells, PosBuffer, Rad. M. (52/31)2 MirVells, PosBuffer, Rad. M. (52/31)3 MirVells, PosBuffer, Lad. M. (56/01)3 MirVells, TrimeBuffer, Lad. M. (16/60)01 M					
MINVISS_POSBURIER_Rad_M_[52(2)]5 4 MINVISS_POSBURIER_Rad_M_[52(2)]5 4 MINVISS_POSBURIER_Rad_M_[52(2)]5 5 MINVISS_POSBURIER_Rad_M_[52(2)]6 5 MINVISS_POSBURIER_Rad_M_[52(3)]7 6 MINVISS_POSBURIER_Rad_M_[52(3)]7 MINVISS_POSBURIER_Rad_M_[52(3)]7 MINVISS_POSBURIER_Rad_M_[52(3)]7 MINVISS_POSBURIER_Rad_M_[52(3)]8 MINVISS_POSBURIER_Rad_M_[52(3)]8 MINVISS_POSBURIER_Rad_M_[52(3)]8 MINVISS_POSBURIER_Rad_M_[52(3)]8 MINVISS_POSBURIER_Rad_M_[52(3)]9 MINVISS_POSBURIER_RAD_M_[1			
MrtVel3, PosButfer, Rad, M, 152(2) 5 5	/trVel3_PosBuffer_Rad_M_f32[2][3]	2			
MrVet3_PosBuffer_Rad_M_[522][7] 6 6 MrVet3_PosBuffer_Rad_M_[522][7] 6 MrVet3_PosBuffer_Rad_M_[522][7] 6 MrVet3_PosBuffer_Rad_M_[522][7] 7 MrVet3_TimeBuffer_US_M_U15p(0)[7] 7 MrVet3_TimeBuffer_US	/trVel3_PosBuffer_Rad_M_f32[2][4]	3.65			
Mirvels_PosBuffer_Rad_M_S23[2]	/trVel3_PosBuffer_Rad_M_f32[2][5]	4			
Mrtv81_Posbuffer_Rad_M_02(3)[0] 1 2 2 3 3 3 3 3 3 3 3	/trVel3_PosBuffer_Rad_M_f32[2][6]	5			
MtvVel3_PosBuffer_Rad_M_52(3)[1] 2 3 3 3 3 3 3 3 3 3	/trVel3_PosBuffer_Rad_M_f32[2][7]	6			
MirVel3_Posbuffer_Rad_M_[32[3][2] 3 MirVel3_Posbuffer_Rad_M_[32[3][4] 5.5 MirVel3_Posbuffer_Rad_M_[32[3][4] 5.5 MirVel3_Posbuffer_Rad_M_[32[3][6] 6 MirVel3_Posbuffer_Rad_M_[32[3][6] 1 MirVel3_Posbuffer_Rad_M_[32[3][7] 2.4 MirVel3_Posbuffer_Lad_M_[32[3][7] 2.4 MirVel3_Posbuffer_Us_M_U16[0][0] 1700 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1800 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1800 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1800 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1300 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1400 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1400 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1400 MirVel3_TimeBuffer_Us_M_U16[0][0][1] 1800 MirVel3_TimeBuffer_Us_M_U16[0][1][1] 1800 MirVel3_TimeBuffer_Us_M_U16[0][2][1] 1800 MirVel3_TimeBuffer	/trVel3_PosBuffer_Rad_M_f32[3][0]				
MtrVei3_PosBuffer_Rad_M_[32]3[3] 4					
MtrVei3_PosBuffer_Rad_M_[32][s]					
Mtrvel2_PosBuffer_Rad_M_523[15] 6					
MtrVel3_TimeBuffer_US_M_u16p(0)[1] 100 1700					
Mirvel3_PosBuffer_Rad_M_R32[3][7] 2.4					
Mirvel3_TimeBuffer_uS_M_u16p0[0] 0					
Mit/vel3_TimeBuffer_uS_M_u16p0[0][1] 1800 110					
MtrVel3_TimeBuffer_uS_M_u16p0[0][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[0][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[0][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 1600 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 500 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 600 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 600 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800					
MtrVel3_TimeBuffer_uS_M_u16p0[0][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[0][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 500 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 700 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 800 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800					
MtrVel3_TimeBuffer_uS_M_u16p0[0][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[0][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 1600 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 500 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 600 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 700 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 800 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] <					
MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 500 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 600 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 800 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500					
MtrVel3_TimeBuffer_uS_M_u16p0[0][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 1600 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[1][8] 500 MtrVel3_TimeBuffer_uS_M_u16p0[1][8] 700 MtrVel3_TimeBuffer_uS_M_u16p0[1][8] 700 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 <td></td> <td></td> <td></td> <td></td> <td></td>					
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	/trVel3_TimeBuffer_uS_M_u16p0[3][7]				
MtrVel_OsBufSelect_Cnt_M_u08 0	/ltrVel_OldPosBuf_Cnt_M_u08				
	/ltrVel_OsBufSelect_Cnt_M_u08	0			
Name Actual Value Expected Value	lame		•	R	Result
MtrVel_OldPosBuf_Cnt_M_u08 0					~
MtrVel_OsBufSelect_Cnt_M_u08 1					~
RegressionFit() 475.663788 475.6636057 ± 0.0625	RegressionFit()	475.663788	475.6636057 ± 0.0625		

Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0



Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[3]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0	
	0	
MtrVel3_PosBuffer_Rad_M_f32[0][4]		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0	
MtrVel3 PosBuffer Rad M f32[2][2]	0	
	0	
MtrVel3_PosBuffer_Rad_M_f32[2][3]		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	0	
	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	0	
MtrVel3 TimeBuffer uS M u16p0[1][4]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	0	
MtrVel3 TimeBuffer uS M u16p0[1][7]	0	
	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	0	
	0	
Mtr/vel3_TimeBuffer_uS_M_u16p0[3][6]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]		
MtrVel_OldPosBuf_Cnt_M_u08	0	
MtrVel_OsBufSelect_Cnt_M_u08	1	
Name	Actual Value Expected	Value Result
MtrVel_OldPosBuf_Cnt_M_u08	1 1	
MtrVel_OsBufSelect_Cnt_M_u08	2 2	✓
RegressionFit()	0 0 ± 0.0625	✓



Test Case 3

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 3.1 1281 Cycles
TS 3.2 1266 Cycles
TS 3.3 1334 Cycles
TS 3.4 1432 Cycles
TS 3.5 1310 Cycles
TS 3.6 1422 Cycles
TS 3.7 1388 Cycles
TS 3.8 1316 Cycles
TS 3.9 1315 Cycles
TS 3.10 1354 Cycles
TS 3.11 1310 Cycles
TS 3.11 1310 Cycles
TS 3.12 1389 Cycles
TS 3.13 1288 Cycles
TS 3.14 1301 Cycles
TS 3.15 1303 Cycles
TS 3.15 1488 Cycles
TS 3.15 1488 Cycles

Description

TS 3.16 1288 Cycles TS 3.17 1288 Cycles VECTOR DESCRIPTION:

(Cross buffer timestamp = 950)"

TS 3.1 "all min
(Cross buffer timestamp = 250)"
TS 3.2"all max
(Cross buffer timestamp = 4000)"
TS 3.3"MtrVel_OsBufSelect_Cnt_M_u08 = min
(Cross buffer timestamp = 600)"
TS 3.4"MtrVel_OsBufSelect_Cnt_M_u08 = max
(Cross buffer timestamp = 340)"
TS 3.5"MtrVel_OsBufSelect_Cnt_M_u08 = mid
(Cross buffer timestamp = 1000)"
TS 3.6"MtrVel_OsBufSelect_Cnt_M_u08[4] = min
(Cross buffer timestamp = 800)"
TS 3.6"MtrVel3_OsBufPos_Cnt_M_u08[4] = min
(Cross buffer timestamp = 800)"
TS 3.7"MtrVel3_OsBufPos_Cnt_M_u08[4] = mid
(Cross buffer timestamp = 500)"
TS 3.8"MtrVel3_OsBufPos_Cnt_M_u08[4] = mid
(Cross buffer timestamp = 500)"
TS 3.9"MtrVel_OldPosBuf_Cnt_M_u08 = min
(Cross buffer timestamp = 950)"
TS 3.10"MtrVel_OldPosBuf_Cnt_M_u08 = max
(Cross buffer timestamp = 420)"
TS 3.11"MtrVel_OldPosBuf_Cnt_M_u08 = mid
(Cross buffer timestamp = 380)"
TS 3.12"MtrVel3_TimeBuffer_uS_M_u16p0[4][8] = min
(Cross buffer timestamp = 250)"
TS 3.13"MtrVel3_TimeBuffer_uS_M_u16p0[4][8] = max
(Cross buffer timestamp = 4000)"
TS 3.14"MtrVel3_TimeBuffer_uS_M_u16p0[4][8] = mid
(Cross buffer timestamp = 600)"
TS 3.15"MtrVel3_PosBuffer_Rad_M_f32[4][8] = min
(Cross buffer timestamp = 340)"
TS 3.16"MtrVel3_PosBuffer_Rad_M_f32[4][8] = max
(Cross buffer timestamp = 1000)"
TS 3.17"MtrVel3_PosBuffer_Rad_M_f32[4][8] = mid

Test Step 3.1 (Repeat Count = 1)	🗸
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3 PosBuffer Rad M f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450		
MtrVel3 TimeBuffer uS M u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0] MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400		
MtrVel3 TimeBuffer uS M u16p0[2][2]	6300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6000		
	5900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	20000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	19900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500 19400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400		
MttVel3_TimeBuffer_uS_M_u16p0[3][7]	0		
MttVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08		le de la constant	1
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
RegressionFit()	0	0 ± 0.0625	✓

Test Step 3.2 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
MtrVel3_OsBufPos_Cnt_M_u08[1]	7	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433	



Marivas Postularie Rati M. (2017) 0. 28009433	Name	Input Value		
Merival Position Rad M, 1920 12 6.28398433	MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433		
Marves Possbure Rad M, 1920 19	MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433		
Marivas Possibute Red M, 1920 19 0.28308433 0.2	MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433		
Maryola Picaluffer Red M, 1922 19 6.28398433	MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433		
Marvisa Positionic Rad M. 1922 19 0.28308433	MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433		
MeWorls Posible Rad M. 1923 19 0.28998433	MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433		
Mirvids Posibuffer Rad M. 125 11 6 283089433	MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433		
Mirvis2, Posibler, Rad, M. (92(3)) 2 6.28308433	MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
Mirvide] - PosBuffer, Rad. M. [32[31]] 6. 28308433	MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
Mirvels_PosBuffer_Rad_M_[32][3]	MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
Mirvis PosBuffer, Rad, M. [32[3]5 6.28908433	MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MrVv83_ PosBuffer_ Rad_ M_[52[3] 4] 6.28308433 8.2830843 8.28308433 8.2830843	MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MirVel3_PosBuffer_Rad_M_[52[3]6] 6.28308433	MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433		
MirVel3_PoeBuffer_Rad_M_[32]3[7] 6.28308433 6.2830843 6.	MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433		
MirVel3_TimeBuffer_uS_M_u16p0[0] 1200	MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433		
MMVR03_TmeBuffer_uS_M_u16p0[0]10 11875 1	MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MitVel3_TimeBuffer_uS_M_u16p0[0](1) 11875 11750	MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MitVel3_TimeBuffer_uS_M_u16p0[0][2] 11750 11625	MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000		
Mirvel3_TimeBuffer_uS_M_u16p0[0][3] 11625 11500	MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875		
MttVel3_TimeBuffer_uS_M_u16p0[0]4 11500 11375	MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750		
Mittvel3_TimeBuffer_uS_M_u16p00[15] 11250	MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625		
Mitryel3_TimeBuffer_uS_M_u16p0[0][6] 11250	MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500		
Mitryel3_TimeBuffer_uS_M_u16p0[0][7] 11125	MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250		
MttVel3_TimeBuffer_uS_M_u16p0[1][1] 59875 MttVel3_TimeBuffer_uS_M_u16p0[1][2] 59750 MttVel3_TimeBuffer_uS_M_u16p0[1][3] 59625 MttVel3_TimeBuffer_uS_M_u16p0[1][6] 59500 MttVel3_TimeBuffer_uS_M_u16p0[1][6] 59250 MttVel3_TimeBuffer_uS_M_u16p0[1][7] 59126 MttVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 MttVel3_TimeBuffer_uS_M_u16p0[2][1] 2150 MttVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MttVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MttVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MttVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 MttVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MttVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MttVel3_TimeBuffer_uS_M_u16p0[3][7] 2900 MttVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MttVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MttVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MttVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MttVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MttVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MttVel_OldPosBuf_Cnt_M_u08 <td< td=""><td>MtrVel3_TimeBuffer_uS_M_u16p0[0][7]</td><td>11125</td><td></td><td></td></td<>	MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 59750 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 59625 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 59500 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 59375 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 59250 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 59125 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 2150 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OldPosBuf_Cnt_M_u08 3	MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000		
Mitvel_TimeBuffer_uS_M_u16p0[1][3] 59625	MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875		
MtrVels_TimeBuffer_uS_M_u16p0[1][4] 59500 MtrVels_TimeBuffer_uS_M_u16p0[1][5] 59375 MtrVels_TimeBuffer_uS_M_u16p0[1][6] 59250 MtrVels_TimeBuffer_uS_M_u16p0[2][0] 59125 MtrVels_TimeBuffer_uS_M_u16p0[2][0] 2025 MtrVels_TimeBuffer_uS_M_u16p0[2][1] 2150 MtrVels_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVels_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVels_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVels_TimeBuffer_uS_M_u16p0[2][5] 2650 MtrVels_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVels_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVels_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVels_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVels_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVels_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVels_TimeBuffer_uS_M_u16p0[3][6] 6525 MtrVels_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVels_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 Name Actual Value Expected Value Re MtrVel_OsBufSelect_Cnt_M_u08 0 0 MtrVel_OsBu	MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750		
MtrVel3_TimeBuffer_u3_M_u16p0[1][6] 59375 MtrVel3_TimeBuffer_u5_M_u16p0[1][7] 59125 MtrVel3_TimeBuffer_u5_M_u16p0[2][0] 2025 MtrVel3_TimeBuffer_u5_M_u16p0[2][1] 2150 MtrVel3_TimeBuffer_u5_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_u5_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_u5_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_u5_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_u5_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_u5_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_u5_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_u5_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_u5_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_u5_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_u5_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_u5_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_u5_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_OldPosBuf_Cnt_M_u08 3 MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0	MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 59250 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 59125 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 2150 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_OldPosBuf_Cnt_M_u08 3 MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0	MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 59125 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 2150 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6660 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0	MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 2160 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVelOdPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cn_M_u08 3 Name Actual Value Expected Value Re MtrVel_OdPosBuf_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2150 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsbufSelect_Cnt_M_u08 2 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OdBufSelect_Cnt_M_u08 3 3 MtrVel_OdsufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OsbufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 MtrVel_OsbufSelect_Cnt_M_u08 3 3 MtrVel_OsbufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 3 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsbufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Ont_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OlsBufSelect_Cnt_M_u08 3 3 MtrVel_OlsBufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OlsBufSelect_Cnt_M_u08 3 3 MtrVel_OlsBufSelect_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 3 MtrVel_OsBufSelect_Cnt_M_u08 3 3 4 MtrVel_OsBufSelect_Cnt_M_u08 0 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 4 MtrVel_OsBufSelect_Cnt_M_u08 3 3 4 MtrVel_OsBufSelect_Cnt_M_u08 0 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0		6275		
MtrVel3_TimeBuffer_u8_M_u16p0[3][5] 6650 MtrVel3_TimeBuffer_u8_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_u8_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0 0		6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 4 MtrVel_OsBufSelect_Cnt_M_u08 0 0 0	MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0				
MtrVel_OsBufSelect_Cnt_M_u08 3 Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0 0				
Name Actual Value Expected Value Re MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0				
MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel_OsBufSelect_Cnt_M_u08 0 0	MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08 0			the state of the s	Result
				~
RegressionFit() $0 0 \pm 0.0625$				~
	RegressionFit()	0	0 ± 0.0625	

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6



Minvest Passillane Red M. 1901 19	Name	Input Value		
Merival P. Destaller Paul M. (2021)				
Merical Possituring Fast M. (2017)	MtrVel3_PosBuffer_Rad_M_f32[1][2]	5		
Montrell Postularie Paul M. 2011 0 3	MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MonVey Possibutine Faul M. (2021)10	MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
Microla Possibility Rad M. (2021)	MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
Minvella - Pasifilarie - Paul M. (1921)	MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
Mirves Postelute Rad M. (2021)	MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6		
Mirves Desibutine Rad M. 1928/213 2	MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
Mirvis Paskinfer Rad M. (32)[3] 2 3 5 5 5 5 5 5 5 5 5	MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
Minyell Position Faul M, 1920 19	MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
Mirves Posibuline Rad M. 1922 19 5	MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
Mirves Possibler Rad M. 1928 7 6	MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65		
Mirves Posturier Read M. S2 3 7	MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
Mirves Posbuffer Rad M. (32 30 6) 1	MtrVel3_PosBuffer_Rad_M_f32[2][6]			
Mirves Posbuffer Rad M. \$2 \$1 \$ 2	MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
Mirves Posbuffer, Rad M, (32 3 2) 3	MtrVel3_PosBuffer_Rad_M_f32[3][0]			
Mirvela Posibutiner Rad M. (23/23) 4	MtrVel3_PosBuffer_Rad_M_f32[3][1]			
Mirves Posbutfer Rad M 32(3) 5 6				
Mirvis]				
Mirvels PosBulfer Rad M. 52 3 5 24 32 4 32 3 7 24 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 32 4 32 3 7 33 3 7 33				
Mirvels PosBuffer Rad, M. [32]3 7 2.4				
MRV463_TimeBuffer_u.S. M_u16p0[0][0] 1700				
MRV463_TimeBuffer_uS_M_u16p0[0][1] 1800 11000 11000 11000 11000 11000 11000 11000 11000 11000 11000 11000 11000 11000 11000 11000 1100				
Mirvel3_TimeBuffer_u.S.M_u16p0[0]2]				
Mirvel3_TimeBuffer_uS_M_u16p0[0][3] 1200				
MirVel3_TimeBuffer_uS_M_u16p0[0][4] 1300 1400				
MrVel3_TimeBuffer_uS_M_ut6p0[0][5] 1400 1500 MrVel3_TimeBuffer_uS_M_ut6p0[0][7] 1600 160				
MrtVel3_TimeBuffer_uS_M_u16p0[0][6] 1500 MrtVel3_TimeBuffer_uS_M_u16p0[0][7] 1600 MrtVel3_TimeBuffer_uS_M_u16p0[1][0] 1000 MrtVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MrtVel3_TimeBuffer_uS_M_u16p0[1][2] 1200 MrtVel3_TimeBuffer_uS_M_u16p0[1][3] 500 MrtVel3_TimeBuffer_uS_M_u16p0[1][6] 700 MrtVel3_TimeBuffer_uS_M_u16p0[1][7] 700 MrtVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MrtVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MrtVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MrtVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MrtVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MrtVel3_TimeBuffer_uS_M_u16p0[2][6] 500 MrtVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MrtVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MrtVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MrtVel3_TimeBuffer_uS_M_u16p0[3][6] 800 MrtVel3_TimeBuffer_uS_M_u16p0[3][6] 100 MrtVel3_TimeBuffer_uS_M_u16p0[3][6] 100 MrtVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MrtVel3_TimeBuffer_uS_M_u16p0[3][6] 1500				
MtrVel3_TimeBuffer_uS_M_u16p0(1)[0] 1600 MtrVel3_TimeBuffer_uS_M_u16p0(1)[0] 1000 MtrVel3_TimeBuffer_uS_M_u16p0(1)[2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0(1)[2] 1200 MtrVel3_TimeBuffer_uS_M_u16p0(1)[3] 500 MtrVel3_TimeBuffer_uS_M_u16p0(1)[4] 600 MtrVel3_TimeBuffer_uS_M_u16p0(1)[5] 700 MtrVel3_TimeBuffer_uS_M_u16p0(1)[7] 900 MtrVel3_TimeBuffer_uS_M_u16p0(2)[7] 900 MtrVel3_TimeBuffer_uS_M_u16p0(2)[7] 100 MtrVel3_TimeBuffer_uS_M_u16p0(2)[7] 300 MtrVel3_TimeBuffer_uS_M_u16p0(2)[8] 400 MtrVel3_TimeBuffer_uS_M_u16p0(2)[8] 400 MtrVel3_TimeBuffer_uS_M_u16p0(2)[8] 500 MtrVel3_TimeBuffer_uS_M_u16p0(2)[8] 600 MtrVel3_TimeBuffer_uS_M_u16p0(2)[8] 700 MtrVel3_TimeBuffer_uS_M_u16p0(2)[7] 800 MtrVel3_TimeBuffer_uS_M_u16p0(2)[8] 700 MtrVel3_TimeBuffer_uS_M_u16p0(3)[0] 900 MtrVel3_TimeBuffer_uS_M_u16p0(3)[0] 900 MtrVel3_TimeBuffer_uS_M_u16p0(3)[1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0(3)[1] 1000				
MrtVel3_TimeBuffer_uS_M_u16p0f1][0] 1000 MrtVel3_TimeBuffer_uS_M_u16p0f1][1] 1100 MrtVel3_TimeBuffer_uS_M_u16p0f1][3] 500 MrtVel3_TimeBuffer_uS_M_u16p0f1][4] 600 MrtVel3_TimeBuffer_uS_M_u16p0f1][6] 800 MrtVel3_TimeBuffer_uS_M_u16p0f1][6] 800 MrtVel3_TimeBuffer_uS_M_u16p0f1][7] 900 MrtVel3_TimeBuffer_uS_M_u16p0f1][7] 900 MrtVel3_TimeBuffer_uS_M_u16p0f2][1] 200 MrtVel3_TimeBuffer_uS_M_u16p0f2][2] 300 MrtVel3_TimeBuffer_uS_M_u16p0f2][3] 400 MrtVel3_TimeBuffer_uS_M_u16p0f2][4] 500 MrtVel3_TimeBuffer_uS_M_u16p0f2][5] 600 MrtVel3_TimeBuffer_uS_M_u16p0f2][6] 700 MrtVel3_TimeBuffer_uS_M_u16p0f2][7] 800 MrtVel3_TimeBuffer_uS_M_u16p0f2][7] 800 MrtVel3_TimeBuffer_uS_M_u16p0f3][7] 100 MrtVel3_TimeBuffer_uS_M_u16p0f3][7] 100 MrtVel3_TimeBuffer_uS_M_u16p0f3][7] 100 MrtVel3_TimeBuffer_uS_M_u16p0f3][6] 1500 MrtVel3_TimeBuffer_uS_M_u16p0f3][7] 1600 MrtVel3_TimeBuffer_uS_M_u16p0f3][7] 1600				
Mitty-lignary Mitty Mitt				
Mitryel3_TimeBuffer_uS_M_u16p0[1][2] 1200				
Mitryel3_TimeBuffer_uS_M_u16p0[1][3] 500				
MttVel3_TimeBuffer_uS_M_u16p0[1][4] 600 MttVel3_TimeBuffer_uS_M_u16p0[1][5] 700 MttVel3_TimeBuffer_uS_M_u16p0[1][6] 800 MttVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MttVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MttVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MttVel3_TimeBuffer_uS_M_u16p0[2][4] 400 MttVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MttVel3_TimeBuffer_uS_M_u16p0[2][6] 600 MttVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MttVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MttVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MttVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MttVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MttVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MttVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MttVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MttVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MttVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MttVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MttVel_OldPosBuf_Cnt_M_u08 1 MttVel_OldPosBuf_Cnt_M_u08 1				
MttVel3_TimeBuffer_uS_M_u16p0[1][6] 800 MttVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MttVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MttVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MttVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MttVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MttVel3_TimeBuffer_uS_M_u16p0[2][5] 500 MttVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MttVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MttVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MttVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MttVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MttVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MttVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MttVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MttVel3_TimeBuffer_uS_M_u16p0[3][6] 1400 MttVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MttVel_OldPosBuf_Cnt_M_u08 1 MttVel_OsBufSelect_Cnt_M_u08 1 MttVel_OldPosBuf_Cnt_M_u08 0 MttVel_OldPosBuf_Cnt_M_u08 0 MttVel_OldPosBuf_Cnt_M_u08 1 MttVel_OldPosBuf_Cnt_M_u				
MttVel3_TimeBuffer_uS_M_u16p0[1][6] 800 MttVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MttVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MttVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MttVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MttVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MttVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MttVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MttVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MttVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MttVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MttVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MttVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MttVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MttVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MttVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MttVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MttVel_OldPosBuf_Cnt_M_u08 1 MttVel_OldPosBuf_Cnt_M_u08 1 MttVel_OldPosBuf_Cnt_M_u08 0 0 MttVel_OldPosBuf_Cnt_M_u08 1 MttVel_OldPosBuf_Cnt_M_u08 0 0 MttVel_OldPosBuf_Cnt_M_u08 1				
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 900 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel_OldPosBuf_cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 0 0 MtrVel_OsBufSelect_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1 0 0 0 0				
MtrVels_TimeBuffer_us_M_u16p0[2][0] 100 MtrVels_TimeBuffer_us_M_u16p0[2][1] 200 MtrVels_TimeBuffer_us_M_u16p0[2][2] 300 MtrVels_TimeBuffer_us_M_u16p0[2][3] 400 MtrVels_TimeBuffer_us_M_u16p0[2][4] 500 MtrVels_TimeBuffer_us_M_u16p0[2][5] 600 MtrVels_TimeBuffer_us_M_u16p0[2][6] 700 MtrVels_TimeBuffer_us_M_u16p0[2][7] 800 MtrVels_TimeBuffer_us_M_u16p0[3][0] 900 MtrVels_TimeBuffer_us_M_u16p0[3][2] 1100 MtrVels_TimeBuffer_us_M_u16p0[3][2] 1200 MtrVels_TimeBuffer_us_M_u16p0[3][6] 1200 MtrVels_TimeBuffer_us_M_u16p0[3][6] 1500 MtrVels_TimeBuffer_us_M_u16p0[3][6] 1500 MtrVels_TimeBuffer_us_M_u16p0[3][7] 1600 MtrVels_TimeBuffer_us_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1				
MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel0_IdPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 1				
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0				
MtrVel3_TimeBuffer_us_M_u16p0[2][3] 400 MtrVel3_TimeBuffer_us_M_u16p0[2][5] 500 MtrVel3_TimeBuffer_us_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_us_M_u16p0[2][7] 700 MtrVel3_TimeBuffer_us_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_us_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_us_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_us_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_us_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_us_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_us_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_us_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_us_M_u16p0[3][7] 1600 MtrVel3_TimeBuffer_us_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 1				
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 1				
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1 0				
MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 ✓				
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1 ✓				
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 900 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 ✓				
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 1000 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 ✓				
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 1100 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 1200 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 ✓ ✓		1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1 ✓		1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 1300 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 1400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1 ✓		1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 1500 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 1 ✓		1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 1600 MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 1 ✓	MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel_OldPosBuf_Cnt_M_u08 1 MtrVel_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result MtrVel_OldPosBuf_Cnt_M_u08 0 0 ✓ MtrVel_OsBufSelect_Cnt_M_u08 1 1 1 ✓	MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
Mtr/el_OsBufSelect_Cnt_M_u08 0 Name Actual Value Expected Value Result Mtr/el_OsBuf_Cnt_M_u08 0 0 ✓ Mtr/el_OsBufSelect_Cnt_M_u08 1 1 1	MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
Name Actual Value Expected Value Result Mtr/vel_OldPosBuf_Cnt_M_u08 0 0 ✓ Mtr/vel_OsBufSelect_Cnt_M_u08 1 1 1	MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OldPosBuf_Cnt_M_u08 0 0 MtrVel_OsBufSelect_Cnt_M_u08 1 1	MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08 1 1	Name	Actual Value	Expected Value	Result
	MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
	MtrVel_OsBufSelect_Cnt_M_u08	1	1	~
	RegressionFit()	475.663788	475.6636057 ± 0.0625	~

Test Step 3.4 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5.98	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.7		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5.6		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.6		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.0999999		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3		
MtrVei3_PosBuffer_Rad_M_f32[2][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.28000021		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.4		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9285 9625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9660 9695		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9800		
	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9870		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7] MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7] MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08	3	Francisco d Malina	Devel
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7] MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 Name	3 Actual Value	Expected Value	Result
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7] MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 Name MtrVel_OldPosBuf_Cnt_M_u08	2 3 Actual Value 3	3	Result
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7] MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 Name	3 Actual Value	•	Result

Test Step 3.5 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4



Name	Input Value		
MtrVel3_OsBufPos_Cnt_M_u08[1]	5		
MtrVel3_OsBufPos_Cnt_M_u08[2]	6		
MtrVel3_OsBufPos_Cnt_M_u08[3]	7		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2		
	3		
MtrVel3_PosBuffer_Rad_M_f32[0][1]			
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5		
	6		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][1]			
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
	2		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][7]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540		
	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	2	2	
MtrVel_OsBufSelect_Cnt_M_u08	3	3	
RegressionFit()	-657.894653	-657.8947368 ± 0.0625	
. 10g. 00010111 II()	337.007033	001.00T1000 ± 0.0020	



est Step 3.6 (Repeat Count = 1)	Input Value	
	·	
ItrVel3_OsBufPos_Cnt_M_u08[0]	0	
ltrVel3_OsBufPos_Cnt_M_u08[1] ltrVel3 OsBufPos Cnt M u08[2]	0	
	0	
httVel3_OsBufPos_Cnt_M_u08[3]	3.6	
trVel3_PosBuffer_Rad_M_f32[0][0]	0.5	
trVel3_PosBuffer_Rad_M_f32[0][1]	5	
trVel3_PosBuffer_Rad_M_f32[0][2]	6	
ltrVel3_PosBuffer_Rad_M_f32[0][3] ltrVel3 PosBuffer Rad M f32[0][4]	1.6	
	0.1	
ht/vel3_PosBuffer_Rad_M_f32[0][5]	3	
trVel3_PosBuffer_Rad_M_f32[0][6]	0.5	
trVel3_PosBuffer_Rad_M_f32[0][7]	5	
trVel3_PosBuffer_Rad_M_f32[1][0] trVel3_PosBuffer_Rad_M_f32[1][1]	6	
trVel3_PosBuffer_Rad_M_f32[1][2]	0.2	
trVel3_PosBuffer_Rad_M_f32[1][3]	2	
trVel3_PosBuffer_Rad_M_f32[1][4]	3	
trVel3_PosBuffer_Rad_M_f32[1][5]	4.6	
trVel3_PosBuffer_Rad_M_f32[1][6]	5	
trVel3_PosBuffer_Rad_M_f32[1][7]	6	
trVel3_PosBuffer_Rad_M_f32[2][0]	0	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	2	
trVel3_PosBuffer_Rad_M_f32[2][2]	3.7	
trVel3_PosBuffer_Rad_M_f32[2][3]	4	
trVel3_PosBuffer_Rad_M_f32[2][4]	5	
trVel3_PosBuffer_Rad_M_f32[2][5]	0	
trVel3_PosBuffer_Rad_M_f32[2][6]	1	
trVel3_PosBuffer_Rad_M_f32[2][7]	2	
trVel3_PosBuffer_Rad_M_f32[3][0]	3	
trVel3_PosBuffer_Rad_M_f32[3][1]	0	
trVel3_PosBuffer_Rad_M_f32[3][2]	5	
trVel3_PosBuffer_Rad_M_f32[3][3]	6	
trVel3_PosBuffer_Rad_M_f32[3][4]	1	
trVel3_PosBuffer_Rad_M_f32[3][5]	2.7	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	3	
trVel3_PosBuffer_Rad_M_f32[3][7]	0	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	800	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	205	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	290	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	375	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	460	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	545	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	630	
trVel3_TimeBuffer_uS_M_u16p0[0][7]	715	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	1600	
trVel3_TimeBuffer_uS_M_u16p0[1][0] trVel3_TimeBuffer_uS_M_u16p0[1][1]	1005	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	1090	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	1175	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	1260	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	1345	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	1430	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	1515	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	6030	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	6000	
trVel3_TimeBuffer_uS_M_u16p0[2][2]	5970	
trVel3_TimeBuffer_uS_M_u16p0[2][3]	5940	
trVel3_TimeBuffer_u8_M_u16p0[2][4]	5910	
trVel3_TimeBuffer_u8_M_u16p0[2][5]	5880	
trVel3_TimeBuffer_uS_M_u16p0[2][6]	5850	
trVel3_TimeBuffer_uS_M_u16p0[2][7]	5820	
trVel3_TimeBuffer_uS_M_u16p0[3][0]	5790	
trVel3_TimeBuffer_uS_M_u16p0[3][1]	5760	
trVel3_TimeBuffer_uS_M_u16p0[3][2]	5730	
	5700	
trVel3_TimeBuffer_uS_M_u16p0[3][3]	5670	
ltrVel3_TimeBuffer_uS_M_u16p0[3][4]	5640	
htrVel3_TimeBuffer_uS_M_u16p0[3][5]		
htrVel3_TimeBuffer_uS_M_u16p0[3][6]	5610	
ltrVel3_TimeBuffer_uS_M_u16p0[3][7]	5580	



Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✓
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
RegressionFit()	815.12738	815.1272958 ± 0.0625	✓

Test Step 3.7 (Repeat Count = 1)	1 AVI
lame	Input Value
ItrVel3_OsBufPos_Cnt_M_u08[0]	7
trVel3_OsBufPos_Cnt_M_u08[1]	7
trVel3_OsBufPos_Cnt_M_u08[2]	7
ltrVel3_OsBufPos_Cnt_M_u08[3]	7
trVel3_PosBuffer_Rad_M_f32[0][0]	4
trVel3_PosBuffer_Rad_M_f32[0][1]	5
trVel3_PosBuffer_Rad_M_f32[0][2]	6
ltrVel3_PosBuffer_Rad_M_f32[0][3]	1
trVel3_PosBuffer_Rad_M_f32[0][4]	2
trVel3_PosBuffer_Rad_M_f32[0][5]	3
trVel3_PosBuffer_Rad_M_f32[0][6]	4
trVel3_PosBuffer_Rad_M_f32[0][7]	5
trVel3_PosBuffer_Rad_M_f32[1][0]	6
trVel3_PosBuffer_Rad_M_f32[1][1]	1
trVel3_PosBuffer_Rad_M_f32[1][2]	2
trVel3_PosBuffer_Rad_M_f32[1][3]	3
trVel3_PosBuffer_Rad_M_f32[1][4]	4
trVel3_PosBuffer_Rad_M_f32[1][5]	5
trVel3_PosBuffer_Rad_M_f32[1][6]	6
trVel3_PosBuffer_Rad_M_f32[1][7]	1
ltrVel3_PosBuffer_Rad_M_f32[2][0]	2
trVel3_PosBuffer_Rad_M_f32[2][1]	3
ltrVel3_PosBuffer_Rad_M_f32[2][2]	4
ltrVel3_PosBuffer_Rad_M_f32[2][3]	5
ltrVel3_PosBuffer_Rad_M_f32[2][4]	6
trVel3_PosBuffer_Rad_M_f32[2][5]	1
trVel3_PosBuffer_Rad_M_f32[2][6]	2
trVel3_PosBuffer_Rad_M_f32[2][7]	3
ItrVel3_PosBuffer_Rad_M_f32[3][0]	4
ftrVel3_PosBuffer_Rad_M_f32[3][1]	5
ItrVel3_PosBuffer_Rad_M_f32[3][2]	6
htrVel3_PosBuffer_Rad_M_f32[3][3]	1
ItrVel3_PosBuffer_Rad_M_f32[3][4]	2
ItrVel3_PosBuffer_Rad_M_f32[3][5]	3
ltrVel3_PosBuffer_Rad_M_f32[3][6]	4
ltrVel3_PosBuffer_Rad_M_f32[3][7]	5
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
trVel3 TimeBuffer uS M u16p0[0][1]	11875
trVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
trVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
trVel3 TimeBuffer uS M u16p0[0][4]	11500
trVel3_TimeBuffer_uS_M_u16p0[0][5]	11375
trVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
trVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
trVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
trVel3_TimeBuffer_uS_M_u16p0[1][0] trVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
trVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
	59625
trVel3_TimeBuffer_uS_M_u16p0[1][3]	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	59500 50375
trVel3_TimeBuffer_uS_M_u16p0[1][5]	59375
trVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
trVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
trVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
trVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
trVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
trVel3_TimeBuffer_uS_M_u16p0[2][3]	2400
trVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
trVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
ltrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
ltrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
ltrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
ltrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
ItrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
RegressionFit()	517.699097	517.6990817 ± 0.0625	✓

Test Step 3.8 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
VitrVel3 OsBufPos Cnt M u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u00[1] MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
	1	
MtrVel3_OsBufPos_Cnt_M_u08[3]	5	
/trVel3_PosBuffer_Rad_M_f32[0][0]		
MtVel3_PosBuffer_Rad_M_f32[0][1]	6	
Mt/Vel3_PosBuffer_Rad_M_f32[0][2]		
/trVel3_PosBuffer_Rad_M_f32[0][3]	2	
/trVel3_PosBuffer_Rad_M_f32[0][4]	3	
/trVel3_PosBuffer_Rad_M_f32[0][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6	
/trVel3_PosBuffer_Rad_M_f32[1][0]	1	
/trVel3_PosBuffer_Rad_M_f32[1][1]	2	
/trVel3_PosBuffer_Rad_M_f32[1][2]	3	
/trVel3_PosBuffer_Rad_M_f32[1][3]	4	
/ltrVel3_PosBuffer_Rad_M_f32[1][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6	
/trVel3_PosBuffer_Rad_M_f32[1][6]	1	
/trVel3_PosBuffer_Rad_M_f32[1][7]	2	
/trVel3_PosBuffer_Rad_M_f32[2][0]	3	
/trVel3_PosBuffer_Rad_M_f32[2][1]	4	
/trVel3_PosBuffer_Rad_M_f32[2][2]	5	
htrVel3_PosBuffer_Rad_M_f32[2][3]	6	
/trVel3_PosBuffer_Rad_M_f32[2][4]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3	
/trVel3_PosBuffer_Rad_M_f32[2][7]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1	
/trVel3_PosBuffer_Rad_M_f32[3][3]	2	
/trVel3_PosBuffer_Rad_M_f32[3][4]	3	
/trVel3_PosBuffer_Rad_M_f32[3][5]	4	
htrVel3_PosBuffer_Rad_M_f32[3][6]	5	
/trVel3_PosBuffer_Rad_M_f32[3][7]	6	
	6000	
htrVel3_TimeBuffer_uS_M_u16p0[0][0]		
/trVel3_TimeBuffer_uS_M_u16p0[0][1]	6010	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6020	
/trVel3_TimeBuffer_uS_M_u16p0[0][3]	6030	
/trVel3_TimeBuffer_uS_M_u16p0[0][4]	6040	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6050	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6060	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6070	
htrVel3_TimeBuffer_uS_M_u16p0[1][0]	700	
htrVel3_TimeBuffer_uS_M_u16p0[1][1]	770	
htrVel3_TimeBuffer_uS_M_u16p0[1][2]	840	
ltrVel3_TimeBuffer_uS_M_u16p0[1][3]	350	
ltrVel3_TimeBuffer_uS_M_u16p0[1][4]	420	
ltrVel3_TimeBuffer_uS_M_u16p0[1][5]	490	
htrVel3_TimeBuffer_uS_M_u16p0[1][6]	560	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	630	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6080	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6090	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6100	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6110	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6120	



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6130		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6140		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1190		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1260		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	840		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	910		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	980		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1120		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
RegressionFit()	510.956726	510.9567538 ± 0.0625	✓

Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2
MtrVel3_OsBufPos_Cnt_M_u08[3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	9850
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9745
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9760
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9775
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790 9805
MtrVel3_TimeBuffer_uS_M_u16p0[0][5] MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9805
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7] MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	9835
	10
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
RegressionFit()	-673.206726	-673.2066598 ± 0.0625	✓

Test Step 3.10 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5.98	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.7	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5.6	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.6	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.6	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.0999999	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.23000002	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.4	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8245	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8125	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	8005	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	7885	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	7765	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	7645	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	7525	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	7405	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7285	



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7165		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7045		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	6925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	6805		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	6685		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	6565		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6445		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3640		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3760		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3880		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	3160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	3280		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	3400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4020		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	4140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4260		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4380		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3780		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
RegressionFit()	-1009.09265	-1009.09265	~

Test Step 3.11 (Repeat Count = 1) Name	Input Value	
	· ·	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340	
MtrVel3 TimeBuffer uS M u16p0[0][1]	360	
MtrVel3 TimeBuffer uS M u16p0[0][2]	380	



Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
RegressionFit()	-657.894653	-657.8947368 ± 0.0625	✓

Test Step 3.12 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5.9
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.7		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
RegressionFit()	1008.40723	1008.407346 ± 0.0625	~

Test Sten 2 42 (Benest Count = 4)	في المناطقة
Test Step 3.13 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3
MtrVel3_PosBuffer_Rad_M_f32[0][6]	4
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	4125		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	4000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3875		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3750		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7625		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7750		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7375		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	11500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	11625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	11750		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	11875		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	12000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	11375		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✓
MtrVel_OsBufSelect_Cnt_M_u08	0	0	·
RegressionFit()	125.000008	125 ± 0.0625	-

Test Step 3.14 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3
MtrVel3_PosBuffer_Rad_M_f32[0][5]	4
MtrVel3_PosBuffer_Rad_M_f32[0][6]	5
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4
MtrVel3_PosBuffer_Rad_M_f32[1][4]	5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	950		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1100		
MtrVel3 TimeBuffer uS M u16p0[1][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1550		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	4328		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	4278		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	4228		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4178		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4128		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4078		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4028		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3978		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3828		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3778		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3728		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3678		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3628		
MtrVel3 TimeBuffer uS M u16p0[3][7]	3578		
MtrVel OldPosBuf Cnt M u08	0		
MtrVel OsBufSelect Cnt M u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	Result
MtrVel_OsBufSelect_Cnt_M_u08	2	2	
RegressionFit()	-357.669678	-357.6697277 ± 0.0625	
regression ii()	-331.008010	-331.0031211 I U.0023	

Test Step 3.15 (Repeat Count = 1)	🗸
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	3
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	6
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	160		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	170		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	180		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	190		
	200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	210		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]			
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	230		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	240		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	260		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	270		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	280		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	290		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	10000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9300		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
RegressionFit()	0	0 ± 0.0625	

Test Step 3.16 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[0][5] MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433 6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433		
MtrVel3 PosBuffer Rad M f32[1][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][3] MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433 6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][4] MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	999		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	888		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	777		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	555		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	444		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	333		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	222		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	825		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	725		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	525		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	325 225		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	232		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	294		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	356		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	418		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	542		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	604		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1042		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1104		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1166		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	732		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	794		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	856		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	918		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	980		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		1_
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	Y
RegressionFit()	0	0 ± 0.0625	

Test Step 3.17 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2



Name	Input Value		
MtrVel3_OsBufPos_Cnt_M_u08[3]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1 2		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][4] MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	70		
MtrVel3 TimeBuffer uS M u16p0[0][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	120		
MtrVel3 TimeBuffer uS M u16p0[1][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9850		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9745		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9790		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	~
RegressionFit()	263.157898	263.1578947 ± 0.0625	~



CalcCoarseVel

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 Project
 MtrVel_Digi

 Module
 MtrVel_Digi

 Test Object
 CalcCoarseVel

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\tinclude
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Dig\utp\contract -I\$(PROJECTROOT)\MtrVel_Dig\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Dig\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Dig\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Dig\utp\contract\Sa_MtrVel -I\$(Compiler Install Path)\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\\data \text{\text{\$\cup\$}} \tex

Comments/Descript	ion/Specification
Name	Text
Module 'MtrVel_Digi'	Name of Tester:Sunita Kore Code File(s) Under Test:Sa_MtrVel.c Code File(s) Version:5 Module Design Document:MotorVelocity_MDD.doc Module Design Document:MotorVelocity_MDD.doc Module Design Document:MotorVelocity_MDD.doc Module Design Document Version:3 Unit Test Plan Version:3 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):1394 Total FLASH Used (Bytes):42 Total CALS Used (Bytes):54 Special Test Requirements: Test Date:12/E2/6/2014 Comments: "Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested. Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD. Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350. Note4: In function ""CalcCoarseVel"", the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"'' has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements. Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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CalcCoarseVel



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Step 1.1 (Repeat Count = 1)			~
Name	Input Value		
DeltaPos_Rad_T_f32	-6.283185307		
DeltaTime_uS_T_f32	1800		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-3490.65869	-3490.658504 ± 0.0625	~

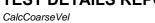
Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	6.283185307		
DeltaTime_uS_T_f32	2200		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	2855.99341	2855.993321 ± 0.0625	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	-6.283185307		
DeltaTime_uS_T_f32	1900		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-3306.9397	-3306.939635 ± 0.0625	~

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	6.283185307		
DeltaTime_uS_T_f32	2100.45		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	2991.35205	2991.351999 ± 0.0625	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	0		
DeltaTime_uS_T_f32	1937		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	0	0 ± 0.0625	~

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Test Step 1.7 (Repeat Count = 1)			~
Name	Input Value		
DeltaPos_Rad_T_f32	-1.6		
DeltaTime_uS_T_f32	2047.56		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-781.417847	-781.4178827 ± 0.0625	•

Test Step 1.8 (Repeat Count = 1)			~
Name	Input Value		
DeltaPos_Rad_T_f32	2.12		
DeltaTime_uS_T_f32	1800		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	1177.77783	1177.777778 ± 0.0625	~

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	4.12		
DeltaTime_uS_T_f32	2200		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	1872.72729	1872.727273 ± 0.0625	•

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	-1.9		
DeltaTime_uS_T_f32	2000.5		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-949.762573	-949.762573 ± 0.0625	~

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MtrVel_Per1

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text



Module 'MtrVel_Digi'

Name of Tester:Sunita Kore
Code File(s) Under Test:Sa_MtrVel.c
Code File(s) Ursion:5
Module Design Document:MotorVelocity_MDD.doc
Module Design Document:MotorVelocity_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level.Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total RAM Used (Bytes):142
Total RAM Used (Bytes):54
Special Test Requirements:
Test Date:12/E2/E0/2014
Comments:
"Note1: Inline functions defined in "'Globalmacro.h" are not unit tested.

Note2: In function "'MtrVel_Per+", the values of variable "'MechMtrPos2Timestamp_USec_u32" and
"'MtrVel_PrevMtrPos2ampleTime2_uS_M_u32" are taken slightly different from the actual range, to get value of "'DeltaTime2_uS_T_f32" in the range of ""1800 - 2200" as per the guidelines mentioned in MDD.

Note3: In function "'CalcCoarseVel", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function "'CalcCoarseVel", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function "RegressionFit", the path 'P5' i.e. "(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable "'MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"') and have been given value as '0' for all array elements.

Note5: "'CBD_Sandbox_dbg.map" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles: TS 1.1 4543 Cycles TS 1.2 4546 Cycles

Description VECTOR DESCRIPTION:

TS 1.1 "Shortest Execution Path:

TS 1.1 "Shortest Execution Path: if (MtrVel_PosBuffer_Rad_T_f32 > D_PI_ULS_F32) = False; (MtrVel_PosBuffer_Rad_T_f32 <= D_PI_ULS_F32) = False (MtrVel_MtrRadpS_T_f32>=D_MTRVELHILMT_MTRRADPS_F32)=False (MtrVel_HwVel_HwRadpS_M_f32>=D_MAXHANDWHEELVEL_HWRADPS_F32)=False" TS 1.2 "Longest Execution Path: if (MtrVel_PosBuffer_Rad_T_f32 > D_PI_ULS_F32) = False; (MtrVel_PosBuffer_Rad_T_f32 <= -D_PI_ULS_F32) = False (MtrVel_MtrRadpS_T_f32>=D_MTRVELHILMT_MTRRADPS_F32)=True (MtrVel_HwVel_HwRadpS_M_f32>=D_MAXHANDWHEELVEL_HWRADPS_F32)=Fa"

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Input Value
0
2
7
4
1.12
1.13
1.14
1.15
1.16
1.17
1.18
1.19
1.2
1.21
1.22
1.23
1.24
1.25
1.26
1.27
1.28
1.29
1.3
1.31
1.32
1.33
1.34
1.35
1.35
1.37
1.37
1.38
1.39
1.4
1.41
1.42
166
65268
65330
65392
65454
65516
42
104
2042
2104
2166
1732
1794
1856
1918
1.0.0
1980

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Will Vel_r el l			(CIE C COIC
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800		
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08			
tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32	tgt MtrVel Per1 HandwheelVel HwR		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	amp USec u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRa		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-4.80000067	-4.8 ± 0.0625	- 100 uit
MtrVel_MtrVelMRF_MtrRadpS_M_f32	40.0000076	40 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	
MtrVel OsBufSelect Cnt M u08	2	2	·
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.00182160223	0 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-4.80000067	-4.8 ± 0.009	_
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-40.0000076	-40 ± 0.0625	✓
-3			

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

40.0000076

-4.80000067

40.0000076

40 ± 0.0625

 -4.8 ± 0.009 40 ± 0.0625

Test Step 1.2 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value





<u>-</u>	
Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1] MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.64 3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480 1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][3] MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1380
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7700
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-227.1569001
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08	0 3
	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076
Mtrvei_PrevMtrPosSampieTime2_uS_ivi_u32 Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
TATO_THOT_OR_THAT VOI	Igi_kie_irisi_5a_mii.vei 0
T Mtr\/elRlendThlY Llls u2n14[0]	
T_MtrVelBlendTblY_Uls_u2p14[0] T_MtrVelBlendTblY_Uls_u2p14[1]	
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
T_MtrVelBlendTblY_Uls_u2p14[1] k_GearRatio_Uls_f32	16384 0.03
T_MtrVelBlendTblY_Uls_u2p14[1]	16384

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MtrVel_Per1

Name	Input Value			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976	897976		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPol	arity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_H	wRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Tim	estamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Re	ev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_Mtr	rRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mt	rRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	40.5	40.5 ± 0.0625	~	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	✓	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.00182160223	0 ± 0.0625	✓	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	✓	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	40.5	40.5 ± 0.009	✓	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✓	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✓	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	40.5	40.5 ± 0.009	✓	
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	~	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

MtrVel_Per1

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Test Case 2: Boundary Test

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MtrVel_Per1



```
Specification
```

```
Performance Metrics
(With "None" Instrumentation and WithPS Environment)
     CPU Cycles:
 CPU Cycles:
TS 2.1 4543 Cycles
TS 2.2 4416 Cycles
TS 2.3 4456 Cycles
TS 2.4 4445 Cycles
TS 2.5 4428 Cycles
TS 2.6 4456 Cycles
TS 2.7 4367 Cycles
TS 2.6 4456 Cycles
TS 2.7 4367 Cycles
TS 2.8 4442 Cycles
TS 2.9 4522 Cycles
TS 2.10 4414 Cycles
TS 2.11 4446 Cycles
TS 2.11 4446 Cycles
TS 2.12 4382 Cycles
TS 2.13 4429 Cycles
TS 2.14 4483 Cycles
TS 2.15 4429 Cycles
TS 2.16 4386 Cycles
TS 2.17 4429 Cycles
TS 2.19 4353 Cycles
TS 2.19 4458 Cycles
TS 2.20 4458 Cycles
TS 2.21 4429 Cycles
TS 2.22 4458 Cycles
TS 2.23 4430 Cycles
TS 2.24 4436 Cycles
TS 2.25 4429 Cycles
TS 2.25 4429 Cycles
TS 2.26 4430 Cycles
TS 2.27 4429 Cycles
TS 2.28 4458 Cycles
TS 2.29 4458 Cycles
TS 2.30 4504 Cycles
TS 2.31 4428 Cycles
TS 2.33 4457 Cycles
TS 2.34 4477 Cycles
TS 2.35 4447 Cycles
TS 2.36 4487 Cycles
TS 2.36 4487 Cycles
TS 2.37 4410 Cycles
TS 2.38 4411 Cycles
TS 2.39 4458 Cycles
TS 2.39 4458 Cycles
 TS 2.38 4418 Cycles
TS 2.39 4458 Cycles
TS 2.40 4430 Cycles
TS 2.41 4429 Cycles
TS 2.42 4381 Cycles
TS 2.43 4430 Cycles
 TS 2.43 4430 Cycles
TS 2.44 4429 Cycles
TS 2.45 4436 Cycles
TS 2.46 4458 Cycles
TS 2.47 4523 Cycles
TS 2.48 4446 Cycles
VECTOR DESCRIPTION:
 TS2.1All Min
```

Description

TS2.2All Max TS2.3AsstAssemblyPolarity_Cnt_s08==>Min TS2.4AsstAssemblyPolarity_Cnt_s08==>Max TS2.5AsstAssemblyPolarity_Cnt_s08==>Pos TS2.6MechMtrPos2Timestamp_USec_u32==>Min TS2.7MechMtrPos2Timestamp_USec_u32==>Max TS2.8MechMtrPos2Timestamp_USec_u32==>Pos TS2.9MechMtrPos2_Rev_u0p16==>Min TS2.10MechMtrPos2_Rev_u0p16==>Max TS2.11MechMtrPos2_Rev_u0p16==>Pos TS2.12MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Min TS2.13MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Max TS2.14MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Pos TS2.15MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Min TS2.16MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Max TS2.17MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Pos TS2.18MtrVel_OsBufSelect_Cnt_M_u08==>Min TS2.19MtrVel_OsBufSelect_Cnt_M_u08==>Max TS2.20MtrVel_OsBufSelect_Cnt_M_u08==>Pos TS2.20Mit/Yel_OSBUISelect_Crit_M_u08(=)=>P0S
TS2.21Mit/Yel3_OSBUIFPOS_Cnt_M_u08[4]==>Min
TS2.22Mit/Yel3_OSBUIFPOS_Cnt_M_u08[4]==>Pos
TS2.23Mit/Yel3_OSBUIFPOS_Cnt_M_u08[4]==>Pos
TS2.24Mit/Yel_OldPosBuf_Cnt_M_u08==>Min TS2.25MtrVel_OldPosBuf_Cnt_M_u08==>Max TS2.26MtrVel_OldPosBuf_Cnt_M_u08==>Pos TS2.27MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Min TS2.28MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Max TS2.29MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Pos TS2.30MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Min TS2.31MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Max TS2.32MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Pos TS2.33t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Min TS2.34L MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Max
TS2.35t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Pos
TS2.36k_GearRatio_Uls_f32==>Min TS2.37k_GearRatio_Uls_f32==>Max TS2.38k_GearRatio_Uls_f32==>Pos TS2.39MtrVel_HwVel_HwRadpS_M_f32==>Min TS2.40MtrVel_HwVel_HwRadpS_M_f32==>Max TS2.41MtrVel_HwVel_HwRadpS_M_f32==>Pos TS2.42MtrVel_HwVel_HwRadpS_M_f32==>Zero TS2.43MtrVel_HwVel_HwRadpS_M_f32==>Neg TS2.44MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Min TS2.45MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Min TS2.45MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Max TS2.46MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Pos TS2.47MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Zero TS2.48MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Neg

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Test Step 2.1 (Repeat Count = 1) Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2] MtrVel3_PosBuffer_Rad_M_f32[0][3]	0 0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0 0
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6] MtrVel3_PosBuffer_Rad_M_f32[1][7]	0
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0 0
MtrVel3_PosBuffer_Rad_M_f32[3][1] MtrVel3_PosBuffer_Rad_M_f32[3][2]	0
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][3] MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	450 460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720
MtrVel3_TimeBuffer_uS_M_u16p0[1][6] MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	730 740
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19900
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19800 19700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500
Will velo_Tillebullet_uo_W_uTopo[5][5]	
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400
	19300
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300





Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1800		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssembly	Polarity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVe	I_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2	Timestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2	_Rev_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF	_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF	_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwh	eelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVe	IMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-0	0 ± 0.0625	*
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✓
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1800	1800	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-0	0 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	-

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433





Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294965495		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt	s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_l		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_t		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa		
Name	Actual Value	Expected Value	Posul
	0	0 ± 0.0625	Resul
MtrVel_HwVel_HwRadpS_M_f32			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	

MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	~





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

T4-04	
Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
	1.42
MtrVel3_PosBuffer_Rad_M_f32[3][7]	166
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800		
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPo	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_h	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Tir	mestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_R	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_M	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_M	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwhee		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelM	RF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-555.555542	-555.555542 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	555.555542	555.555542 ± 0.0625	✓
test Many (al. David Matery) (alMDE ManDadin C 600 vialus	FEE FEEE AD	EEE EEEE 40 . 0.000E	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	-

-555.555542

-555.555542

42

-555.555542 ± 0.0625

-555.555542 ± 0.0625

42 ± 0.009

Test Step 2.4 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value









Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_l	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_N	ftrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-762.023987	-762.0239258 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	294848	294848	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.22
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.26
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.34
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.36
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.38
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.46
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.48
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.54
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.56
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.58
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.64
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.66
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.68
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.74
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.78
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	661		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53 115		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239		
MtrVel_HwVel_HwRadpS_M_f32	-2.767454803		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	676.0312533		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6046		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.14		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7846		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestal	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	Op16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRac	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_F		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	· i	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-968.545654	-968.5455933 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	V
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	V
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7846	7846	V
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-968.545654	-968.545593 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-968.545654	-968.5455933 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	

-968.545654

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value

-968.5455933 ± 0.0625



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	-

Test Step 2.6 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
/trVel3_OsBufPos_Cnt_M_u08[1]	3	
ltrVel3_OsBufPos_Cnt_M_u08[2]	4	
trVel3_OsBufPos_Cnt_M_u08[3]	7	
trVel3_PosBuffer_Rad_M_f32[0][0]	3.2	
trVel3_PosBuffer_Rad_M_f32[0][1]	2.27	
trVel3_PosBuffer_Rad_M_f32[0][2]	2.32	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	2.37	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.42	
trVel3_PosBuffer_Rad_M_f32[0][5]	2.47	
trVel3_PosBuffer_Rad_M_f32[0][6]	2.52	
trVel3_PosBuffer_Rad_M_f32[0][7]	2.57	
trVel3_PosBuffer_Rad_M_f32[1][0]	2.62	
trVel3_PosBuffer_Rad_M_f32[1][1]	2.67	
trVel3_PosBuffer_Rad_M_f32[1][2]	2.72	
trVel3_PosBuffer_Rad_M_f32[1][3]	2.77	
trVel3_PosBuffer_Rad_M_f32[1][4]	2.82	
trVel3_PosBuffer_Rad_M_f32[1][5]	2.87	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	2.92	
trVel3_PosBuffer_Rad_M_f32[1][7]	2.97	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	3.02	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	3.07	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	3.12	
trVel3_PosBuffer_Rad_M_f32[2][3]	3.17	
trVel3_PosBuffer_Rad_M_f32[2][4]	3.22	
trVel3_PosBuffer_Rad_M_f32[2][5]	3.27	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	3.32	
ItrVel3_PosBuffer_Rad_M_f32[2][7]	3.37	
ItrVel3_PosBuffer_Rad_M_f32[3][0]	3.42	
ItrVel3_PosBuffer_Rad_M_f32[3][1]	3.47	
trVel3_PosBuffer_Rad_M_f32[3][2]	3.52	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	3.57	
trVel3_PosBuffer_Rad_M_f32[3][4]	3.62	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	3.67	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	3.72	
trVel3_PosBuffer_Rad_M_f32[3][7]	3.77	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	1926	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	1988	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	1554	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	1616	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	1678	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	1740	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	1802	
trVel3_TimeBuffer_uS_M_u16p0[0][7]	1864	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	2252	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	2314	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	2376	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	2438	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	2004	
trVel3_TimeBuffer_uS_M_u16p0[1][4] trVel3_TimeBuffer_uS_M_u16p0[1][5]	2004	
trVel3_TimeBuller_uS_M_u16p0[1][5] trVel3_TimeBuffer_uS_M_u16p0[1][6]	2128	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	2190	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	927	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	900	
htrVel3_TimeBuffer_uS_M_u16p0[2][2]	894	
htrVel3_TimeBuffer_uS_M_u16p0[2][3]	858	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	801	
/trVel3_TimeBuffer_uS_M_u16p0[2][5]	791	





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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606		
MtrVel_HwVel_HwRadpS_M_f32	-16.68913257		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-412.0075986		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.4		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.17		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	12		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1800		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolar	ity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times	stamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	I_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	967.551514	967.5514526 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	3.14159274	3.141592654 ± 0.0625	

42	42 ± 0.0625	•
967.551514	967.5514526 ± 0.0625	~
1	1	~
2	2	~
3.14159274	3.141592654 ± 0.0625	~
1800	1800	~
42	42 ± 0.009	~
967.551514	967.5514526 ± 0.0625	~
967.551514	967.5514526 ± 0.0625	~
42	42 ± 0.009	~
967.551514	967.5514526 ± 0.0625	~
	967.551514 1 2 3.14159274 1800 42 967.551514 967.551514	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	•
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)		~
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
MtrVel3_OsBufPos_Cnt_M_u08[1]	7	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
	59625
MtrVel3_TimeBuffer_uS_M_u16p0[1][3] MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel OsBufSelect Cnt M u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294965495
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	tgr_rte_iist_sa_wiivei
	16384
T_MtrVelBlendTblY_Uls_u2p14[1]	
k_GearRatio_Uls_f32	1
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCR	F_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMF	RF_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandw	heelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotor\	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✓
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Jama	Innut Value	
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
ItrVel3_OsBufPos_Cnt_M_u08[1]	3	
ItrVel3_OsBufPos_Cnt_M_u08[2]	5	
ItrVel3_OsBufPos_Cnt_M_u08[3]	7	
htrVel3_PosBuffer_Rad_M_f32[0][0]	5.2	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	2.33	
ItrVel3_PosBuffer_Rad_M_f32[0][2]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54	
htrVel3_PosBuffer_Rad_M_f32[0][5]	2.61	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75	
ltrVel3_PosBuffer_Rad_M_f32[1][0]	2.82	
ItrVel3_PosBuffer_Rad_M_f32[1][1]	2.89	
htrVel3_PosBuffer_Rad_M_f32[1][2]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	3.1	
trVel3_PosBuffer_Rad_M_f32[1][5]	3.17	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	3.24	
ItrVel3_PosBuffer_Rad_M_f32[1][7]	3.31	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	3.38	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	3.45	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	3.52	
trVel3_PosBuffer_Rad_M_f32[2][3]	3.59	
ltrVel3_PosBuffer_Rad_M_f32[2][4]	3.66	
trVel3_PosBuffer_Rad_M_f32[2][5]	3.73	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	3.8	
ltrVel3_PosBuffer_Rad_M_f32[2][7]	3.87	
ltrVel3_PosBuffer_Rad_M_f32[3][0]	3.94	
ltrVel3_PosBuffer_Rad_M_f32[3][1]	4.01	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	4.08	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	4.15	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	4.22	
ltrVel3_PosBuffer_Rad_M_f32[3][5]	4.29	
trVel3_PosBuffer_Rad_M_f32[3][6]	4.36	
ltrVel3_PosBuffer_Rad_M_f32[3][7]	4.43	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	675	
ftrVel3_TimeBuffer_uS_M_u16p0[0][1]	635	
ftrVel3_TimeBuffer_uS_M_u16p0[0][2]	600	
ftrVel3_TimeBuffer_uS_M_u16p0[0][3]	576	
/trVel3_TimeBuffer_uS_M_u16p0[0][4]	539	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	500	

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MtrVel_Per1

Mtrvei_Peri			MACILIA
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	465		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7380		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7442		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7504		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7566		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7132		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7194		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7318		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8156		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8218		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8280		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8342		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8404		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8466		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8032		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8094		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1111		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1006		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	967		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	910		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	793		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	739		
MtrVel_HwVel_HwRadpS_M_f32	-21.01813388		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	969.1185812		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3878		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.19		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	15		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	5678		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tqt MtrVel Per1 AsstAssemblyPolarity 0	Cnt s08	
tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	_	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt_MtrVel_Per1_MechMtrPos2Timestam		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelMRF MtrRadpS f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hv	_	
	1		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtr		
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1332.33911	1332.339233 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	2	2	•
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5678	5678	,
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	•
tot MtrVel Per1 SysCHandwheelVel HwRadpS f32 value	42	42 + 0.009	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

1332.33911

42 ± 0.009

1332.339233 ± 0.0625

42

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value



Test Step 2.9 (Repeat Count = 1)	√
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][3] MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4] MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.24 3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6] MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.04 4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7] MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	4.76 6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][7] MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520 1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1380
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1] MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7700 7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6566 6432
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6132 6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-227.1569001
MtrVel_OldPosBuf_Cnt_M_u08	0





Name	Input Value			
MtrVel_OsBufSelect_Cnt_M_u08	3			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6			
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.2			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssembly	Polarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVe	I_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2	Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2	_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF	_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwhe	eelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVel	MRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	29.8089962	29.8089797 ± 0.0625	✓	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	149.044983	149.0448914 ± 0.0625	✓	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✓	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✓	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	•	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	29.8089962	29.8089797 ± 0.009	~	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	•	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	~	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	29.8089962	29.8089797 ± 0.009	✓	
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	•	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	-

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19

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MtrVel_Per1



Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55		
	4.64		
MtrVel3_PosBuffer_Rad_M_f32[3][2]			
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	30300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	30250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	30200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	30150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	30100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	30050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	30020		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	30000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	28876		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	28938		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	29000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	28566		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	28628		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	28690		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	28752		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	28814		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	25400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	25300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	25200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	25160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	25100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	25060		
1 1 1 1			
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	25030		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	25000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	29452		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	29514		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	29576		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	29638		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	29700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	29266		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	29328		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	29390		
MtrVel HwVel HwRadpS M f32	-34.05995619		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	258.9155765		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel OsBufSelect Cnt M u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6478		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.21		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	8378		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt	908	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_		
·			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_l		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_t		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	

8378

42

8378

42 ± 0.009

 $MtrVel_PrevMtrPosSampleTime2_uS_M_u32$





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)	Input Value	
lame	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	6	
MtrVel3_OsBufPos_Cnt_M_u08[2]	5	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.32	
/trVel3_PosBuffer_Rad_M_f32[0][1]	2.42	
/trVel3_PosBuffer_Rad_M_f32[0][2]	2.52	
/trVel3_PosBuffer_Rad_M_f32[0][3]	2.62	
/trVel3_PosBuffer_Rad_M_f32[0][4]	2.72	
/trVel3_PosBuffer_Rad_M_f32[0][5]	2.82	
ftrVel3_PosBuffer_Rad_M_f32[0][6]	2.92	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	3.02	
ftrVel3_PosBuffer_Rad_M_f32[1][0]	3.12	
ftrVel3_PosBuffer_Rad_M_f32[1][1]	3.22	
ltrVel3_PosBuffer_Rad_M_f32[1][2]	3.32	
ltrVel3_PosBuffer_Rad_M_f32[1][3]	3.42	
ftrVel3_PosBuffer_Rad_M_f32[1][4]	3.52	
/trVel3_PosBuffer_Rad_M_f32[1][5]	3.62	
ftrVel3_PosBuffer_Rad_M_f32[1][6]	3.72	
//itrVel3_PosBuffer_Rad_M_f32[1][7]	3.82	
/trVel3_PosBuffer_Rad_M_f32[2][0]	3.92	
/trVel3_PosBuffer_Rad_M_f32[2][1]	4.02	
htrVel3_PosBuffer_Rad_M_f32[2][2]	4.12	
htrVel3_PosBuffer_Rad_M_f32[2][3]	4.22	
/trVel3_PosBuffer_Rad_M_f32[2][4]	4.32	
/trVel3_PosBuffer_Rad_M_f32[2][5]	4.42	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.52	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.62	
	4.72	
htrVel3_PosBuffer_Rad_M_f32[3][0]	4.82	
/trVel3_PosBuffer_Rad_M_f32[3][1]		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.92	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.02	
htrVel3_PosBuffer_Rad_M_f32[3][4]	5.12	
htrVel3_PosBuffer_Rad_M_f32[3][5]	5.22	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	5.32	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.42	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1110	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1172	
/trVel3_TimeBuffer_uS_M_u16p0[0][2]	1234	
ftrVel3_TimeBuffer_uS_M_u16p0[0][3]	800	
ltrVel3_TimeBuffer_uS_M_u16p0[0][4]	862	
ftrVel3_TimeBuffer_uS_M_u16p0[0][5]	924	
/trVel3_TimeBuffer_uS_M_u16p0[0][6]	986	
/ltrVel3_TimeBuffer_uS_M_u16p0[0][7]	1048	
ltrVel3_TimeBuffer_uS_M_u16p0[1][0]	4657	
ltrVel3_TimeBuffer_uS_M_u16p0[1][1]	4609	
htrVel3_TimeBuffer_uS_M_u16p0[1][2]	4576	
ltrVel3_TimeBuffer_uS_M_u16p0[1][3]	4500	
ltrVel3_TimeBuffer_uS_M_u16p0[1][4]	4480	
htrVel3_TimeBuffer_uS_M_u16p0[1][5]	4450	
htrVel3_TimeBuffer_uS_M_u16p0[1][6]	4401	
/trVel3_TimeBuffer_uS_M_u16p0[1][7]	4387	
/trVel3_TimeBuffer_uS_M_u16p0[2][0]	2124	
/trVel3_TimeBuffer_uS_M_u16p0[2][1]	2186	

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Mtrvei_Peri			MACICAU
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2248		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2310		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2372		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2434		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2062		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	7823		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	7800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	7760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	7700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	7650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	7610		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	7570		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	7500		
MtrVel_HwVel_HwRadpS_M_f32	-41.71834391		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-838.1024514		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1653		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.22		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3553		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestar	np_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	wRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	-
MtrVel_MtrVelMRF_MtrRadpS_M_f32	285.048859	285.0487671 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	2	2	-
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	-
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3553	3553	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-285.048859	-285.0487671 ± 0.0625	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Pto Call Mtr\/ol Por1 CP1 ChackpointPoached	1	Pto Call Mtr\/ol Por1 CP1 ChackpointPagehad	1	

285.048859

285.048859

-42

285.0487671 ± 0.0625

285.0487671 ± 0.0625

-42 ± 0.009

Test Step 2.12 (Repeat Count = 1)		√
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.34	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.45	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.56	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.67	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.78	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$

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MtrVel_Per1



Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.89
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.33
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.88
MtrVel3_PosBuffer_Rad_M_f32[1][7] MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.99 4.1
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.43
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.54
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.65
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.98
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.09
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.2
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.31
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.53
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.64
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.75
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	5678
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	5600
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	5570
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5500
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	5340
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	5300
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	13014
MtrVel3_TimeBuffer_uS_M_u16p0[1][1] MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	13076
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	13138 13200
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	12766
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	12828
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	12890
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	12952
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8341
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8300
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8240
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8150
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8100
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8050
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8000
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	14590
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	14652
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	14714
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	14776
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	14838
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	14900
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	14466
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	14528
MtrVel_HwVel_HwRadpS_M_f32	39.33634806
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1200.540927
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2100
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
	0.23
k_GearRatio_Uls_f32 t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0.23
k_GearRatio_Uls_f32	
k_GearRatio_Uls_f32 t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity	/_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	amp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	ı0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_I	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-661.377869	-661.3778076 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4000	4000	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-661.377869	-661.377808 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-661.377869	-661.3778076 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-661.377869	-661.3778076 ± 0.0625	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.13 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.96
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.16
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.4
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.88
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.24
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.36
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.48
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.6
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.84
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.96
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.08
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	2069





Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	2131		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	2193		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1759		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1821		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1883		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1945		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	2007		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3410		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	3370		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	3333		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	3290		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	3207		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	3175		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	3100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2450		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2512		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2574		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2636		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2698		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2760		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2822		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2388		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6890		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6810		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6680		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6610		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6504		
MtrVel_HwVel_HwRadpS_M_f32	20.37118572		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-651.7195906		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5068		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]			
T_MtrVelBlendTblY_Uls_u2p14[1]	0.24		
k_GearRatio_Uls_f32 t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4		
	4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] tgt MtrVel Per1 AsstAssemblyPolarity Cnt s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value			
	6968 58982		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_C	ot s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp		
tgt_Rte_inst_Sa_initivei.mitivei_rein_nandwineervei_nwRadp5_is2 tgt_Rte_inst_Sa_initivei.mitivei_rein_nandwineervei_nwRadp5_is2			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2Timestamp tqt MtrVel Per1 MechMtrPos2 Rev u0p		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MecriMtrPos2_Rev_uup		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrF		
		· i	Daniel
Name Met/Cel Hus/Cel Hus/Dede C. M. 622	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-330.714172	-330.7141418 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	2	2	
MtrVel_OsBufSelect_Cnt_M_u08	3	5 654939437 + 0 0635	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6968	6968	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42 -330 714173	-42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-330.714172 -330.714172	-330.7141418 ± 0.0625 -330.7141418 ± 0.0625	
		NOU / 14 14 10 ± U UD/O	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value			
tgt_MtrVel_rer1_wotorvelmkr_mtrkadpS_i32.value tgt_MtrVel_rer1_SysCHandwheelVel_HwRadpS_i32.value tgt_MtrVel_rer1_SysCMotorVelMRF_MtrRadpS_i32.value	-42 -330.714172	-42 ± 0.009 -330.7141418 ± 0.0625	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
/trVel3_OsBufPos_Cnt_M_u08[2]	3	
ftrVel3_OsBufPos_Cnt_M_u08[3]	4	
ftrVel3_PosBuffer_Rad_M_f32[0][0]	3.3	
ftrVel3_PosBuffer_Rad_M_f32[0][1]	2.51	
ftrVel3_PosBuffer_Rad_M_f32[0][2]	2.64	
ftrVel3_PosBuffer_Rad_M_f32[0][3]	2.77	
ltrVel3_PosBuffer_Rad_M_f32[0][4]	2.9	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	3.03	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	3.16	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	3.29	
ftrVel3_PosBuffer_Rad_M_f32[1][0]	3.42	
ltrVel3_PosBuffer_Rad_M_f32[1][1]	3.55	
ltrVel3_PosBuffer_Rad_M_f32[1][2]	3.68	
ltrVel3_PosBuffer_Rad_M_f32[1][3]	3.81	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	3.94	
ltrVel3_PosBuffer_Rad_M_f32[1][5]	4.07	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	4.2	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	4.33	
htrVel3_PosBuffer_Rad_M_f32[2][0]	4.46	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	4.59	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	4.72	
ltrVel3_PosBuffer_Rad_M_f32[2][3]	4.85	
ltrVel3_PosBuffer_Rad_M_f32[2][4]	4.98	
ltrVel3_PosBuffer_Rad_M_f32[2][5]	5.11	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	5.24	
ftrVel3_PosBuffer_Rad_M_f32[2][7]	5.37	
ftrVel3_PosBuffer_Rad_M_f32[3][0]	5.5	
ftrVel3_PosBuffer_Rad_M_f32[3][1]	5.63	
ftrVel3_PosBuffer_Rad_M_f32[3][2]	5.76	
ItrVel3_PosBuffer_Rad_M_f32[3][3]	5.89	
ItrVel3_PosBuffer_Rad_M_f32[3][4]	6.02	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	3.3	
ItrVel3_PosBuffer_Rad_M_f32[3][6]	3.4	
htrVel3_PosBuffer_Rad_M_f32[3][7]	3.5	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	9770	
htrVel3_TimeBuffer_uS_M_u16p0[0][1]	9832	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	9398	
ltrVel3_TimeBuffer_uS_M_u16p0[0][3]	9460	
ltrVel3_TimeBuffer_uS_M_u16p0[0][4]	9522	
ltrVel3_TimeBuffer_uS_M_u16p0[0][5]	9584	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	9646	
ltrVel3_TimeBuffer_uS_M_u16p0[0][7]	9708	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	20450	
ltrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	20350	
trVel3_TimeBuffer_uS_M_u16p0[1][3]	20306	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	20250	
trVel3_TimeBuffer_uS_M_u16p0[1][5]	20200	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	20156	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	20107	
htrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035	
htrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000	
MtVel3_TimeBuffer_uS_M_u16p0[2][2]	34070	
htrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008	
/trVel3_TimeBuffer_uS_M_u16p0[2][4]	33900	
/trVel3_TimeBuffer_uS_M_u16p0[2][5]	33845	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	8928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	8990		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9052		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9114		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9176		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	8742		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	8804		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	8866		
MtrVel_HwVel_HwRadpS_M_f32	-5.642567992		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1302.259389		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	85754		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.25		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	87654		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_0	cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestam	p_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p	16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	0	0	•
MtrVel_OsBufSelect_Cnt_M_u08	1	1	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87654	87654	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

-42

-1350

-1350

-42

-1350

-42 ± 0.009

-1350 ± 0.0625

-1350 ± 0.0625

-1350 ± 0.0625

-42 ± 0.009

Test Step 2.15 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.39	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.71	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.88	

 $tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value\\ tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value\\$

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value

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MtrVel_Per1



Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.03
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.36
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.52
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.84
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.16
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.48
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.64
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.8
MtrVel3_PosBuffer_Rad_M_f32[2][6] MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.96 6.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.8
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.3
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128 2190
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwVel_HwRadpS_M_f32	30.78114766
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-326.589513
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0
Rte_Inst_Sa_MtrVel T_MtrVelPlandThIV_LIIa_u2p14[0]	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0] T_MtrVelBlendTblY_Uls_u2p14[1]	0
T_MtrVelBlendTblY_Uls_u2p14[1] k_GearRatio_Uls_f32	16384 0.28
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1900
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
.ac	





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_t	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-256.191681	-256.1917114 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1900	1900	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Name	Input Value	
/trVel3 OsBufPos Cnt M u08[0]	7	
/trVel3_OsBufPos_Cnt_M_u08[1]	7	
htrVel3 OsBufPos Cnt M u08[2]	7	
ItrVel3_OsBufPos_Cnt_M_u08[3]	7	
ItrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
ItrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433	
htrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000	
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875	
htrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750	
ltrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625	
htrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500	
ftrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375	

@ Report created by TESSY V3.1.7, report template V2.1

MtrVel Per1

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Input Value MtrVel3 TimeBuffer uS M u16p0[0][6] 11250 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 11125 MtrVel3 TimeBuffer uS M u16p0[1][0] 60000 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 59875 MtrVel3 TimeBuffer uS M u16p0[1][2] 59750 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 59625 MtrVel3 TimeBuffer uS M u16p0[1][4] 59500 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 59375 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 59250 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 59125 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 2150 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 6025 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 6400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 6525 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 6650 MtrVel3 TimeBuffer uS M u16p0[3][6] 6775 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 6900 MtrVel HwVel HwRadpS M f32 42 MtrVel_MtrVelMRF_MtrRadpS_M_f32 1350 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 6.283185307 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 4294965495 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0] 0 T_MtrVelBlendTblY_Uls_u2p14[1] 16384 k GearRatio Uls f32 1 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 8 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 10 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 4294967295 tgt MtrVel Per1 MechMtrPos2Timestamp USec u32.value tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 65535 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 $tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16$ tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 tgt MtrVel Per1 MotorVelMRF MtrRadpS f32 tot Rte Inst Sa MtrVel.MtrVel Per1 MotorVelMRF MtrRadpS f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32 tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32 **Expected Value** Name **Actual Value** Result MtrVel_HwVel_HwRadpS_M_f32 0 0 ± 0.0625 MtrVel MtrVelMRF MtrRadpS M f32 0 0 ± 0.0625 MtrVel_OldPosBuf_Cnt_M_u08 3 3 MtrVel OsBufSelect Cnt M u08 0 0 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 6.28308964 6.283089433 ± 0.0625 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 4294967295 4294967295 $tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value$ 0 0 ± 0.009 tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value 0 0 ± 0.0625 $tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value$ 0 0 ± 0.0625

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

 0 ± 0.009

0 ± 0.0625

0

0

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value





Input Value 4
4
5
6 7
2.48
2.66
2.84
3.02
3.2
3.38
3.55
3.74
3.91
4.1 4.27
4.46
1.5
1.6
1.7
1.8
1.9
2
2.1
2.2
2.3
2.5
2.6
2.7
2.8
2.9
3
3.1
3.2
3.3
3.4 340
360
380
400
420
440
460
480
64495
64520
64545
64570 64595
64620
64445
64470
64850
64875
64900
64925
64950
64975
65000 64825
500
520
540
560
580
600
620
640
18.08023292
654.1182904





Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	8078		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	9978		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_0	Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	o\$_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestam	p_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hv	vRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mt	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-752.657959	-752.6581421 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	9978	9978	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	-

Test Step 2.18 (Repeat Count = 1) Name Name	Input Value
	Input Value
Mary/al2 OaDufDaa Cat M 100001	The state of the s
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.88
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.07
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.26
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.45
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.63
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.83
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4.01
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.3
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.59
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.41
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.52
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.63
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.74
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.85
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.96
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.07
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.18
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4

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MtrVel_Per1



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Name	Input Value			
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.51			
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62			
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.73			
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.84			
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.95			
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.06			
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.17			
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.28			
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.39			
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5			
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700			
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800			
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100			
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200			
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300			
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400			
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500			
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600			
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000			
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100			
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200			
Mtr/Vel3_TimeBuffer_uS_M_u16p0[1][3] Mtr/Vel3_TimeBuffer_uS_M_u46p0[4][4]	500			
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600			
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700			
MtrVel3_TimeBuffer_uS_M_u16p0[1][6] MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900			
, , , , ,	100			
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200			
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300			
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400			
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500			
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600			
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700			
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800			
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900			
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000			
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100			
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200			
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300			
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400			
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500			
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600			
MtrVel_HwVel_HwRadpS_M_f32	-10.02764416			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	570.9573146			
MtrVel_OldPosBuf_Cnt_M_u08	1			
MtrVel_OsBufSelect_Cnt_M_u08	0			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5			
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1467			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.31			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3367			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR			
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	999.058167	999.0581665 ± 0.0625	~	
MtrVel_OldPosBuf_Cnt_M_u08	0	0		
MtrVel_OsBufSelect_Cnt_M_u08	1	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	~	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3367	3367	✓	

42 ± 0.009

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MtrVel_Per1

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)	Innut Value	
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
/trVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
/trVel3_PosBuffer_Rad_M_f32[0][0]	2.52	
ftrVel3_PosBuffer_Rad_M_f32[0][1]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.12	
/trVel3_PosBuffer_Rad_M_f32[0][4]	3.32	
/trVel3_PosBuffer_Rad_M_f32[0][5]	3.52	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	3.71	
/trVel3_PosBuffer_Rad_M_f32[0][7]	3.92	
/trVel3_PosBuffer_Rad_M_f32[1][0]	4.11	
/trVel3_PosBuffer_Rad_M_f32[1][1]	4.32	
/trVel3_PosBuffer_Rad_M_f32[1][2]	4.51	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.72	
/trVel3_PosBuffer_Rad_M_f32[1][4]	1.32	
/trVel3_PosBuffer_Rad_M_f32[1][5]	1.44	
/trVel3_PosBuffer_Rad_M_f32[1][6]	1.56	
/trVel3_PosBuffer_Rad_M_f32[1][7]	1.68	
/trVel3_PosBuffer_Rad_M_f32[2][0]	1.8	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.92	
ftrVel3_PosBuffer_Rad_M_f32[2][2]	2.04	
ItrVel3_PosBuffer_Rad_M_f32[2][3]	2.16	
/trVel3_PosBuffer_Rad_M_f32[2][4]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4	
/trVel3_PosBuffer_Rad_M_f32[2][6]	2.52	
/trVel3_PosBuffer_Rad_M_f32[2][7]	2.64	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.88	
/trVel3_PosBuffer_Rad_M_f32[3][2]	3	
htrVel3_PosBuffer_Rad_M_f32[3][3]	3.12	
/trVel3_PosBuffer_Rad_M_f32[3][4]	3.24	
htrVel3_PosBuffer_Rad_M_f32[3][5]	3.36	
htrVel3_PosBuffer_Rad_M_f32[3][6]	3.48	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.6	
/trVel3_TimeBuffer_uS_M_u16p0[0][0]	70	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431	
htrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396	
ltrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361	
ltrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326	
ltrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291	
ltrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256	
ltrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221	
ltrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186	
ltrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116	
/trVel3_TimeBuffer_uS_M_u16p0[1][7]	65081	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9285		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870		
MtrVel_HwVel_HwRadpS_M_f32	-9.132052481		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-850.7045946		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.7		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1444		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.32		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3444		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	Op16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRac	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	lwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✓
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3444	3444	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✓
Ant May/ol Dord Motor/OlMDE MarDodge (22 volve	1050	1250 - 0.0025	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•

-1350

42

-1350

-1350 ± 0.0625

-1350 ± 0.0625

42 ± 0.009

Test Step 2.20 (Repeat Count = 1)		√
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$

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MtrVel_Per1



Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1] MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480 64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][0] MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	580 600
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel_HwRadpS_M_f32	-21.84720886
MtrVel_mwrel_nwraupS_wi_i32 MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1194.596858
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	442545
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.33
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	444545





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_	_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_U	JSec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRad	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1027.41431	1027.414185 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	444545	444545	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)		~
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	0	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.19	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.2	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.21	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.22	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.23	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.24	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.25	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.26	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.29	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.31	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.34	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.35	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.35	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.37	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.38	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.419	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.43	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.44	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	800	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	205		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	290		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	545		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	630		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	715		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1] MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1005		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1090 1175		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1260		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1345		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1430		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1515		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6030		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5970		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5940		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5910		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5880		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5790		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	5760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	5730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	5700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	5670 5640		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	5610		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5580		
MtrVel_HwVel_HwRadpS_M_f32	-22.67909569		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-685.3485959		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5888		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.34		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7888		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554	nt n00	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_C tgt_MtrVel_Per1_HandwheelVel_HwRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrI		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7888	7888	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	>

-1350

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value

-1350 ± 0.0625



T .				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.22
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.26
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.34
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.36
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.38
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.46
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.48
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.54
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.56
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.58
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.64
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.66
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.68
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.74
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.78
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	700
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	500
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	300
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	5250
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	5270
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	5290
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	5310
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	5330
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	5350
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	5370
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	5390
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	5700
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	5720
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5740
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5760
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5780
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5800
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5820

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5840		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65436		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	65336		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	65236		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	65136		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	65036		
MtrVel_HwVel_HwRadpS_M_f32	-36.06386733		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-814.620298		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	52544		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.35		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	54544		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	idpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRac	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_h	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	/trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-871.69104	-871.69104 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	2	2	•
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54544	54544	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
·			

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

871.69104

-871.69104

-871.69104

42

871.69104 ± 0.0625

-871.69104 ± 0.0625

-871.69104 ± 0.0625

42 ± 0.009

Test Step 2.23 (Repeat Count = 1)		-
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.18	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.21	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.24	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.45	

 $tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value$

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.51
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.63
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.66
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.69 2.72
MtrVel3_PosBuffer_Rad_M_f32[2][2] MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.75
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.81
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.93
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.99
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.05
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.11
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6010
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6020
MtrVel3_TimeBuffer_uS_M_u16p0[0][3] MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6030 6040
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6050
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6060
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6070
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	770
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	840
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	350
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	560
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6090
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6110
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6120 6130
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6140
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1190
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1260
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	840
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	980
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1050
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1120
MtrVel_HwVel_HwRadpS_M_f32	25.16924399
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1286.213058
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	43566
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0] T_MtrVelBlendTblY_Uls_u2p14[1]	0 16384
T_MtrVelBlendTbIY_Uls_u2p14[1] k_GearRatio_Uls_f32	16384 0.36
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	45566
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-20.7045307	-20.70454196 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-57.5125809	-57.5126152 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	45566	45566	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-20.7045307	-20.70454196 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-20.7045307	-20.70454196 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
/trVel3_OsBufPos_Cnt_M_u08[1]	1	
htrVel3_OsBufPos_Cnt_M_u08[2]	2	
ftrVel3_OsBufPos_Cnt_M_u08[3]	3	
htrVel3_PosBuffer_Rad_M_f32[0][0]	2.2	
ftrVel3_PosBuffer_Rad_M_f32[0][1]	2.24	
trVel3_PosBuffer_Rad_M_f32[0][2]	2.28	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	2.32	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.36	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	2.4	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	2.44	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	2.48	
ItrVel3_PosBuffer_Rad_M_f32[1][0]	2.52	
trVel3_PosBuffer_Rad_M_f32[1][1]	2.56	
trVel3_PosBuffer_Rad_M_f32[1][2]	2.6	
trVel3_PosBuffer_Rad_M_f32[1][3]	2.64	
trVel3_PosBuffer_Rad_M_f32[1][4]	2.68	
trVel3_PosBuffer_Rad_M_f32[1][5]	2.72	
trVel3_PosBuffer_Rad_M_f32[1][6]	2.76	
trVel3_PosBuffer_Rad_M_f32[1][7]	2.8	
trVel3_PosBuffer_Rad_M_f32[2][0]	2.84	
trVel3_PosBuffer_Rad_M_f32[2][1]	2.88	
trVel3_PosBuffer_Rad_M_f32[2][2]	2.92	
trVel3_PosBuffer_Rad_M_f32[2][3]	2.96	
trVel3_PosBuffer_Rad_M_f32[2][4]	3	
trVel3_PosBuffer_Rad_M_f32[2][5]	3.04	
trVel3_PosBuffer_Rad_M_f32[2][6]	3.08	
trVel3_PosBuffer_Rad_M_f32[2][7]	3.12	
trVel3_PosBuffer_Rad_M_f32[3][0]	3.16	
trVel3_PosBuffer_Rad_M_f32[3][1]	3.2	
trVel3_PosBuffer_Rad_M_f32[3][2]	3.24	
trVel3_PosBuffer_Rad_M_f32[3][3]	3.28	
trVel3_PosBuffer_Rad_M_f32[3][4]	3.32	
trVel3_PosBuffer_Rad_M_f32[3][5]	3.36	
trVel3_PosBuffer_Rad_M_f32[3][6]	3.4	
trVel3_PosBuffer_Rad_M_f32[3][7]	3.44	
trVel3_Posbuller_Rad_w_i3z[3][7] trVel3_TimeBuffer_uS_M_u16p0[0][0]	9850	
	9745	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	9745	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	9775	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	9775	
htrVel3_TimeBuffer_uS_M_u16p0[0][4] htrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_HwVel_HwRadpS_M_f32	27.92102087		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-870.6542583		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	40358		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVeiBlendTblY_Uis_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.37		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	42358		
tgt MtrVel Per1 MechMtrPos2 Rev u0p16.value	26214		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_C	ont cOR	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt MtrVel Per1 HandwheelVel HwRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp\$		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtrl	i i	ln.
Name	Actual Value	Expected Value	Resu
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	· ·
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1343.38196	-1343.38208 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	2	2	•
MtrVel_OsBufSelect_Cnt_M_u08	3	3	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	42358	42358	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1343.38196	-1343.38208 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1343.38196	-1343.38208 ± 0.0625	
tot MtrVel Per1 SysCHandwheelVel HwRadpS f32 value	-42	-42 + 0 009	

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

-42 ± 0.009 -1343.38208 ± 0.0625

-42

-1343.38196

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value





Test Step 2.25 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47 2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][6] MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.77	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.07	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.37	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.42	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72 3.77	
MtrVel3_PosBuffer_Rad_M_f32[3][7] MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8245	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8125	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	8005	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	7885	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	7765	
MtrVel3 TimeBuffer uS M u16p0[0][5]	7645	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	7525	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	7405	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7285	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7165	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7045	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	6925	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	6805	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	6685	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	6565	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6445	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3640	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3760	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3880	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4000	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	3160	
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	3280	
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	3400	
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3520	
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4020	
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	4140	
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4260	
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4380 4500	
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3660	
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3780	
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3900	
MtrVel_HwVel_HwRadpS_M_f32	-13.75014228	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	421.9869356	
MtrVel_OldPosBuf_Cnt_M_u08	2	





Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	32455		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.38		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	34455		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolari	ty_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwR	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times	tamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_	_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel	_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	320.796387	320.7963257 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	34455	34455	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-320.796387	-320.7963257 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	320.796387	320.7963257 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	320.796387	320.7963257 ± 0.0625	~

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)	<u> </u>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.66
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.78
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.9
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.14
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.26
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.38
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.44
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.5

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.56		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.62		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.68		
MtrVel3 PosBuffer Rad M f32[3][1]	3.74		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.8		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.86		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.92		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.98		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4] MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
	540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640		
MtrVel_HwVel_HwRadpS_M_f32	-6.301655293		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1105.133117		
MtrVel_OldPosBuf_Cnt_M_u08			
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.7		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2343		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.39		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4343		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322	0	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestan	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mt	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1034.97485	1034.974731 ± 0.0625	-
MtrVel_OldPosBuf_Cnt_M_u08	2	2	
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	3 76994967	3 769949534 + 0 0625	→

3.76994967

4343

42

MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32

MtrVel_PrevMtrPosSampleTime2_uS_M_u32

3.769949534 ± 0.0625

4343

42 ± 0.009

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Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1034.97485	1034.97473 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1034.97485	1034.974731 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1034.97485	1034.974731 ± 0.0625	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)	•
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	3
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	6
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38
	3.45
MtrVel3_PosBuffer_Rad_M_f32[2][1]	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8470
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8480
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	8490
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	8500
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	8430
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	8440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	8450
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	8460
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	8710
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	8720
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	8730
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	8740
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	8750
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	8680
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	8690
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	8700
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	10
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	20
IIII VOIO_TIIII BUIITO _ W _ UTOPO[2][1]	20

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Mtrvei_Peri			MACILAG
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-616.0772175		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5788		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7788		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPo	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_h	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Tir	nestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_R	ev_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_M	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_N	ltrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheel	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelM	RF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✓
MtrVel_MtrVelMRF_MtrRadpS_M_f32	399.105316	399.1052551 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✓
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7788	7788	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-399.105316	-399.1052551 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	399.105316	399.1052551 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tot Mtr\/ol Bor1 SygCMotor\/olMBE MtrBodoS (22 yolyo	200 105216	200 1052551 . 0 0625	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

399.105316

399.1052551 ± 0.0625

Test Step 2.28 (Repeat Count = 1)		√
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	





Marrice Productine Pace M. 1920 01 228 229 220		
Mirries Pesture Res J. 1909(1) Annes J. 1909(1) Annes Pesture Res J. 1909(1) Annes J. 1909(1)	Name	Input Value
Mirries Pesture Res J. 1909(1) Annes J. 1909(1) Annes Pesture Res J. 1909(1) Annes J. 1909(1)	MtrVel3 PosBuffer Rad M f32[0][5]	2.68
Marved P. Pestident Res J. 1920 11 232 232 232 232 233 233 233 233 233 234 232 233 234 233 234 233 234 233 234 233 234		2.76
Naves Postular Pack 1921 10 3 3 3 3 3 3 3 3 3		
Mercial Positione Real M, 102(19) 3.00 1.00		
MAYORA Packulure Pack M. 1921 12 13 3.16		
Marvies Productive Facilities F	1	
Mirviss Prostutine Red M. 120[19] 3.28 Mirviss Prostutine Red M. 120[19] 3.48 Mirviss Prostutine Red M. 120[19] 3.49 Mirviss Prostutine Red M. 120[19] 3.77 Mirviss Prostutine Red M. 120[19] 3.88 Mirviss Prostutine Red M. 120[19] 3.88 Mirviss Prostutine Red M. 120[19] 4.98 Mirviss Prostutine Red M. 120[19] 4.98 Mirviss Prostutine Red M. 120[19] 4.12 Mirviss Prostutine Red M. 120[19] 4.12 Mirviss Prostutine Red M. 120[19] 4.12 Mirviss Prostutine Red M. 120[19] 4.28 Mirviss Prostutine Red M. 120[19] 4.29 Mirviss Prostutine Red M. 120[19] 4.29 Mirviss Prostutine Red M. 120[19] 4.29 Mirviss Prostutine Red M. 120[19] 4.70 Mirviss Prostutine Red		
Marwice Pessiture Red M 102[19] Substitute Red M 102[19] Substitute Red M 102[19] Marwice Pessiture Red M 102[19] Substitute Red M 102[19] Substitute Red M 102[19] Marwice Pessiture Red M 102[19] Marwice Pessiture Red M 102[19] Marwice Pessiture Red M 102[19] Substitute Red M 102[19] Marwice Pessiture Red M 102[19] Ale Mar		
Marviol Positurior Red M. 1920 10		
MerVes Deadler Rad M 25(19) MerVes Deadler R	1 11 1	
Merica Deather Rad M. (2001)		
Merives Probabler Red M. 126(12)	1 11 1	
MAYNES, Peachter, Real M. (12020) 38		
Mirviss Probatine Rold M. 1920 1988 1989 19		
Marvia D. Paubler, Rad M. 1920 5		
Martines Possibilitier Rail M. 1920 19	MtrVel3_PosBuffer_Rad_M_f32[2][3]	
MirVAS Paulstine Ral M. 1020 17	MtrVel3_PosBuffer_Rad_M_f32[2][4]	
MinVal Pacibiliter Riad M. 1920 17 4.12	MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MirVAS Pasibuffer Rail M. (202011) 4.28	MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
Mirvis Position Rad M. (2013) 4.28	MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MirvisB Posibler Rad M. 12013 2 4.84	MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MirVals ProBuffer Rad M. (2013)	MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
Minylas Posibufer Rad M. 125 3 Minylas PrimeBufer Las M. Lifepoliti 400 Minylas PrimeBufer Las M. Lifepoliti 375 Minylas PrimeBufer Las M. Lifepolit	MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MrVisB_PosBuffer Rad M_123[9] MrVisB_TimeBuffer u.S. M_urispol[9] MrVisB_TimeBuffer u		4.44
Mr/WS, Poelbufer, Rad. M. 192(3)15 4.6		
MrW48_PosEutifer Rad M_152[9]6 4.88		
MinVels TreeBuffer u.S. M. u1690 00 4125		
Mrtv81. TimeBuffer, u.S. M. u16p0[0]0 4125 Mrtv81. TimeBuffer, u.S. M. u16p0[0]2 3875 Mrtv81. TimeBuffer, u.S. M. u16p0[0]3 3750 Mrtv81. TimeBuffer, u.S. M. u16p0[0]3 3750 Mrtv81. TimeBuffer, u.S. M. u16p0[0]5 3900 Mrtv81. TimeBuffer, u.S. M. u16p0[0]6 3375 Mrtv81. TimeBuffer, u.S. M. u16p0[0]7 3250 Mrtv81. TimeBuffer, u.S. M. u16p0[1]9 3125 Mrtv81. TimeBuffer, u.S. M. u16p0[1]2 2875 Mrtv81. TimeBuffer, u.S. M. u16p0[1]2 2875 Mrtv81. TimeBuffer, u.S. M. u16p0[1]2 2875 Mrtv81. TimeBuffer, u.S. M. u16p0[1]3 2750 Mrtv81. TimeBuffer, u.S. M. u16p0[1]4 2825 Mrtv81. TimeBuffer, u.S. M. u16p0[1]5 2500 Mrtv81. TimeBuffer, u.S. M. u16p0[1]6 2376 Mrtv81. TimeBuffer, u.S. M. u16p0[1]7 7750 Mrtv81. TimeBuffer, u.S. M. u16p0[1]8 7750 Mrtv82. TimeBuffer, u.S. M. u16p0[1]8 7750 Mrtv83. TimeBuffer, u.S. M. u16p0[1]8 7750 Mrtv83. TimeBuffer, u.S. M. u16p0[1]8 7750 Mrtv83. TimeBuffer, u.S. M. u16p0[1]8 775 Mrtv84.		
Mrtv81_TimeBuffer_US_M_U16p0(0) 1 3875		
MrVel3_TimeBuffer_u.S.M_u16p0[0][2] 3875 MrtVel3_TimeBuffer_u.S.M_u16p0[0][3] 3750 MrtVel3_TimeBuffer_u.S.M_u16p0[0][5] 3800 MrtVel3_TimeBuffer_u.S.M_u16p0[0][5] 3800 MrtVel3_TimeBuffer_u.S.M_u16p0[0][7] 3250 MrtVel3_TimeBuffer_u.S.M_u16p0[1][7] 3250 MrtVel3_TimeBuffer_u.S.M_u16p0[1][7] 3000 MrtVel3_TimeBuffer_u.S.M_u16p0[1][8] 2750 MrtVel3_TimeBuffer_u.S.M_u16p0[1][8] 2750 MrtVel3_TimeBuffer_u.S.M_u16p0[1][8] 2750 MrtVel3_TimeBuffer_u.S.M_u16p0[1][8] 2500 MrtVel3_TimeBuffer_u.S.M_u16p0[1][8] 2500 MrtVel3_TimeBuffer_u.S.M_u16p0[1][7] 2500 MrtVel3_TimeBuffer_u.S.M_u16p0[1][7] 2500 MrtVel3_TimeBuffer_u.S.M_u16p0[1][7] 750 MrtVel3_TimeBuffer_u.S.M_u16p0[2][7] 7750 MrtVel3_TimeBuffer_u.S.M_u16p0[2][7] 775 MrtVel3_TimeBuffer_u.S.M_u16p0[2][7] 775 MrtVel3_TimeBuffer_u.S.M_u16p0[2][7] 775 MrtVel3_TimeBuffer_u.S.M_u16p0[2][7] 775 MrtVel3_TimeBuffer_u.S.M_u16p0[3][7] 715 MrtVel3_TimeBuffer_u.S.M_u16p0[3][7		
Mrtvel3_TimeBuffer_uS_M_u16p0[0]3 3750		
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MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 7125 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 7250 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 7550 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 11500 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 11625 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 11750 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 11875 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 12000 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 11125 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 11250 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375 MtrVel_HwR_dpS_M_f32 17.41422999 MtrVel_HwR_dpS_M_f32 -657.4433444 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufselect_Cnt_M_u08 2 MtrVel_DrevMtrPosSampleTime2_uS_M_u32 3.5 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 54544 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0] 0 T_MtrVelBlendTblY_MtrRadpS_u12p4[0] 6 L_MtrVelBlendTblY_MtrRadpS_u12p4[0] 6 L_MtrVelBlendTblY_MtrRadpS_u12p4[1] 6 </td <td>MtrVel3_TimeBuffer_uS_M_u16p0[2][2]</td> <td>7875</td>	MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7875
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 7250 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 7375 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 7500 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 11500 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 11625 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 11750 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 11875 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 12000 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 11125 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375 MtrVel_HwRadpS_M_f32 17.41422999 MtrVel_HwVel_HwRadpS_M_f32 17.41422999 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 3.5 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 54544 Rte_Inst_Sa_MtrVel IgL_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[1] 16384 K_GearRatio_Uls_f32 0.41 LMtrVelBlendTblX_MtrRadpS_u12p4[0] 6 LMtrVelBlendTblX_MtrRadpS_u12p4[1] 6	MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8000
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 7250 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 7375 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 7500 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 11500 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 11625 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 11750 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 11875 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 12000 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 11125 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375 MtrVel_HwRadpS_M_f32 17.41422999 MtrVel_HwVel_HwRadpS_M_f32 17.41422999 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 3.5 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 54544 Rte_Inst_Sa_MtrVel IgL_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[1] 16384 K_GearRatio_Uls_f32 0.41 LMtrVelBlendTblX_MtrRadpS_u12p4[0] 6 LMtrVelBlendTblX_MtrRadpS_u12p4[1] 6	MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7125
MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 7375 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 7500 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 11500 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 11625 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 11750 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 11875 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 12000 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 11125 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 11250 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375 MtrVel_HwVel_HwRadpS_M_f32 17.41422999 MtrVel_HwVel_MrR_MtrRadpS_M_f32 2657.4433444 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsbufSelect_Cnt_M_u08 3 MtrVel_PrevMtrPosSembleTime2_uS_M_u32 3.5 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 34544 Rte_inst_Sa_MtrVel tgt_Rte_inst_Sa_MtrVel T_MtrVelBlendTbiY_UIs_u2p14[0] 0 T_MtrVelBlendTbiY_UIs_u2p14[1] 16384 K_GearRatio_UIs_f32 0.41 t_MtrVelBlendTbiX_MtrRadpS_u12p4[0] 6 t_MtrVelBlendTbiX_MtrRadpS_u12p4[1] 6		
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MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 11250 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375 MtrVel_HwVel_HwRadpS_M_f32 17.41422999 MtrVel_MtrVelMRF_MtrRadpS_M_f32 -657.4433444 MtrVel_OldPosBuf_Cnt_M_u08 2 MtrVel_OsBufSelect_Cnt_M_u08 3 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 3.5 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 54544 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_UIs_u2p14[0] 0 T_MtrVelBlendTblY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.41 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 6 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 6		
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t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 6	k_GearRatio_Uls_f32	0.41
	t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
		6
		1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 56544		56544

MtrVel_Per1





Name	Input Value				
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429	52429			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	USec_u32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16	3			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_	f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	<u>f</u> 32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	adpS_f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	adpS_f32			
Name	Actual Value	Expected Value	Result		
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	763.283752	763.2836914 ± 0.0625	~		
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~		
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✓		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	56544	56544	✓		
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓		
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	✓		
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	-		
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~		
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	~		

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

	🗸
Test Step 2.29 (Repeat Count = 1) Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.64
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1150

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	950		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1800 1450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1550		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	4328		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	4278		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	4228		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4178		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4128		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4078		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4028		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3978		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3828		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3778		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3728		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3678 3628		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3578		
MtrVel_HwVel_HwRadpS_M_f32	-0.304720819		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	732.5839302		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	63534		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.42		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	65534		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982	0-1 -00	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad tgt_MtrVel_Per1_MechMtrPos2Timestar	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	827.414246	827.4141846 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	65534	65534	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tot Mind of Donat Concordate all IMPE Att D. J. C. (CC. J.	1007 44 40 40	1007 44 440 40 0 0000	1 4

827.414246

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value

827.4141846 ± 0.0625



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
/trVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
/trVel3_PosBuffer_Rad_M_f32[0][0]	0
/trVel3_PosBuffer_Rad_M_f32[0][1]	0
/trVel3_PosBuffer_Rad_M_f32[0][2]	0
/trVel3_PosBuffer_Rad_M_f32[0][3]	0
/trVel3_PosBuffer_Rad_M_f32[0][4]	0
ftrVel3_PosBuffer_Rad_M_f32[0][5]	0
/trVel3_PosBuffer_Rad_M_f32[0][6]	0
ltrVel3_PosBuffer_Rad_M_f32[0][7]	0
ftrVel3_PosBuffer_Rad_M_f32[1][0]	0
ftrVel3_PosBuffer_Rad_M_f32[1][1]	0
ltrVel3_PosBuffer_Rad_M_f32[1][2]	0
ltrVel3_PosBuffer_Rad_M_f32[1][3]	0
ftrVel3_PosBuffer_Rad_M_f32[1][4]	0
/trVel3_PosBuffer_Rad_M_f32[1][5]	0
/trVel3_PosBuffer_Rad_M_f32[1][6]	0
/trVel3_PosBuffer_Rad_M_f32[1][7]	0
/trVel3_PosBuffer_Rad_M_f32[2][0]	0
/trVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
ftrVel3_PosBuffer_Rad_M_f32[2][4]	0
ftrVel3_PosBuffer_Rad_M_f32[2][5]	0
/trVel3_PosBuffer_Rad_M_f32[2][6]	0
/trVel3_PosBuffer_Rad_M_f32[2][7]	0
/trVel3_PosBuffer_Rad_M_f32[3][0]	0
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0
/trVel3_PosBuffer_Rad_M_f32[3][2]	0
/trVel3_PosBuffer_Rad_M_f32[3][3]	0
/trVel3_PosBuffer_Rad_M_f32[3][4]	0
/trVel3_PosBuffer_Rad_M_f32[3][5]	0
/trVel3_PosBuffer_Rad_M_f32[3][6]	0
ItrVel3_PosBuffer_Rad_M_f32[3][7]	0
ItrVel3_TimeBuffer_uS_M_u16p0[0][0]	6481
ItrVel3_TimeBuffer_uS_M_u16p0[0][1]	6543
htrVel3_TimeBuffer_uS_M_u16p0[0][2]	6109
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6171
htrVel3_TimeBuffer_uS_M_u16p0[0][4]	6233
htrVel3_TimeBuffer_uS_M_u16p0[0][5]	6295
/trVel3_TimeBuffer_uS_M_u16p0[0][6]	6357
htrVel3_TimeBuffer_uS_M_u16p0[0][7]	6419
/trVel3_TimeBuffer_uS_M_u16p0[1][0]	19800
/trVel3_TimeBuffer_uS_M_u16p0[1][1]	19755
htrVel3_TimeBuffer_uS_M_u16p0[1][2]	19706
htrVel3_TimeBuffer_uS_M_u16p0[1][3]	19676
htrVel3_TimeBuffer_uS_M_u16p0[1][4]	19603
htrVel3_TimeBuffer_uS_M_u16p0[1][5]	19578
ttrVel3_TimeBuffer_uS_M_u16p0[1][6]	19504
htrVel3_TimeBuffer_uS_M_u16p0[1][7]	19460
htrVel3_TimeBuffer_uS_M_u16p0[2][0]	5500
htrVel3_TimeBuller_uS_M_u16p0[2][0] htrVel3_TimeBuffer_uS_M_u16p0[2][1]	5400
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5200 5100
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5050 5000

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	4960		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	7138		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	7200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	7262		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	7324		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	7386		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6952		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	7014		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	7076		
MtrVel_HwVel_HwRadpS_M_f32	28.83693385		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	698.9029244		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	443476		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.43		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	445676		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	adpS_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	868.882874	868.8828125 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	445676	445676	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

-868.882874

868.882874

868.882874

-42

-868.8828125 ± 0.0625

868.8828125 ± 0.0625

868.8828125 ± 0.0625

-42 ± 0.009

Test Step 2.31 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	5	
MtrVel3_OsBufPos_Cnt_M_u08[1]	6	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	

tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value$





_	
Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][6] MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433 6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250 20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][5] MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwVel_HwRadpS_M_f32	-13.1049369
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-298.8190119
MtrVel_OldPosBuf_Cnt_M_u08	3 0
MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1.4 63355
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	tgt_rte_inst_sa_wii vei
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.44
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	65555
•	13107
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF	MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF	F_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwh	neelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVe	elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-28.6764164	-28.6764221 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-65.1736755	-65.1736908 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✓
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	65555	65555	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-28.6764164	-28.6764221 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-28.6764164	-28.6764221 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	✓

T			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

lame	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
/trVel3_OsBufPos_Cnt_M_u08[1]	3	
htrVel3_OsBufPos_Cnt_M_uo8[1]	4	
	7	
htrVel3_OsBufPos_Cnt_M_u08[3]	1.12	
htrVel3_PosBuffer_Rad_M_f32[0][0] htrVel3_PosBuffer_Rad_M_f32[0][1]	1.12	
htrVel3_PosBuffer_Rad_M_f32[0][1]	1.13	
	1.15	
htrVel3_PosBuffer_Rad_M_f32[0][3]	1.16	
htrVel3_PosBuffer_Rad_M_f32[0][4]		
ItrVel3_PosBuffer_Rad_M_f32[0][5]	1.17	
htrVel3_PosBuffer_Rad_M_f32[0][6]	1.18	
htrVel3_PosBuffer_Rad_M_f32[0][7]	1.19	
htrVel3_PosBuffer_Rad_M_f32[1][0]	1.2	
ItrVel3_PosBuffer_Rad_M_f32[1][1]	1.21	
htrVel3_PosBuffer_Rad_M_f32[1][2]	1.22	
ItrVel3_PosBuffer_Rad_M_f32[1][3]	1.23	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	1.24	
ltrVel3_PosBuffer_Rad_M_f32[1][5]	1.25	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	1.26	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	1.27	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	1.28	
trVel3_PosBuffer_Rad_M_f32[2][1]	1.29	
trVel3_PosBuffer_Rad_M_f32[2][2]	1.3	
trVel3_PosBuffer_Rad_M_f32[2][3]	1.31	
trVel3_PosBuffer_Rad_M_f32[2][4]	1.32	
trVel3_PosBuffer_Rad_M_f32[2][5]	1.33	
trVel3_PosBuffer_Rad_M_f32[2][6]	1.34	
trVel3_PosBuffer_Rad_M_f32[2][7]	1.35	
trVel3_PosBuffer_Rad_M_f32[3][0]	1.35	
trVel3_PosBuffer_Rad_M_f32[3][1]	1.37	
trVel3_PosBuffer_Rad_M_f32[3][2]	1.37	
trVel3_PosBuffer_Rad_M_f32[3][3]	1.38	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	1.39	
trVel3_PosBuffer_Rad_M_f32[3][5]	1.4	
trVel3_PosBuffer_Rad_M_f32[3][6]	1.41	
trVel3_PosBuffer_Rad_M_f32[3][7]	1.42	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	9645	
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	9602	
ltrVel3_TimeBuffer_uS_M_u16p0[0][2]	9560	
ltrVel3_TimeBuffer_uS_M_u16p0[0][3]	9501	
ltrVel3_TimeBuffer_uS_M_u16p0[0][4]	9470	
ltrVel3_TimeBuffer_uS_M_u16p0[0][5]	9403	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9366		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9304		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7760		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7822		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7884		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7946		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7512		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7574		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7636		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7698		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6662		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6724		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6786		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6848		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6910		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6476		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6538		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4012		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3946		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3901		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3866		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3752		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3702		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3650		
MtrVel_HwVel_HwRadpS_M_f32	-29.04110444		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-930.6708422		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	86466		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.45		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	88666		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	·	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-325.011383	-325.0114746 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	88666	88666	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	325.011383	325.0114746 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-325.011383	-325.0114746 ± 0.0625	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

-325.011383

42 ± 0.009

-325.0114746 ± 0.0625

42

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value





Test Step 2.33 (Repeat Count = 1)	v
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.21
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][3] MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.39
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.45
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.51
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.57 2.6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.63
MtrVel3_PosBuffer_Rad_M_f32[1][7] MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.66
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.75
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.81
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][1] MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.93 2.96
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.99
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.05
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.11
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	11148
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11210
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	10776
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	10838
MtrVel3_TimeBuffer_uS_M_u16p0[0][4] MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	10900 10962
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11024
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11086
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10100
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	10070
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10010
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	9950
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	9904
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	9860
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	9800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9770 9173
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9173
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9297
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9359
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9421
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8987
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9049
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9111
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6415
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6325
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6225
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6175
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6110 6060
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5960
MtrVel_HwVel_HwRadpS_M_f32	10.2496587
MtrVel_MtrVelMRF_MtrRadpS_M_f32	964.6684363

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MtrVel_Per1

Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.7		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3366		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.49		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	5566		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPola	rity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Time	stamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mtrl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	771.913879	771.9138794 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✓
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5566	5566	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)		V
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.24	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.56	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.64	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.8	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.88	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.04	

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.08		
	3.12		
MtrVel3_PosBuffer_Rad_M_f32[2][7]			
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.16		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.24		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.28		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.32		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.36		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.44		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65352		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65414		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64		
	126		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550		
	660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880		
MtrVel_HwVel_HwRadpS_M_f32	-12.63669056		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	457.1572797		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54343		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	56543		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestal	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad		
	-		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	i i	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1025.73547	-1025.735352 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	5.02656746	5.026567421 + 0.0625	

2 5.02656746

42

56543

MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32

MtrVel_PrevMtrPosSampleTime2_uS_M_u32

5.026567421 ± 0.0625

56543

42 ± 0.009





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1025.73547	1025.735352 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1025.73547	-1025.735352 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1025.73547	-1025.735352 ± 0.0625	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.35 (Repeat Count = 1)	<u> </u>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.77
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.82
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02
	3.02
MtrVel3_PosBuffer_Rad_M_f32[2][1]	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.77
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	661
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277
IIII VOIO_TIIIIODUIIGI_UO_IVI_UTOPO[Z][T]	OVE I

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Mtrvei_Peri			MACICAL
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239		
MtrVel_HwVel_HwRadpS_M_f32	39.80989385		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-93.04319823		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6897678		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.51		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	6899878		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPol	larity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_H	lwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Tim	nestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Re	ev_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_Mt	trRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mt	trRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheel\		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMR		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	297.649353	297.649292 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	3	3	-
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	-
MtrVel PrevMtrPosSampleTime2 uS M u32	6899878	6899878	-
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	-
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	297.649353	297.649292 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	297.649353	297.649292 ± 0.0625	_
tet Mtv/ol Devil CucCillendubeel/ol Hurbodec (22 ualue	40	10 . 0 000	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

42 ± 0.009

297.649292 ± 0.0625

42

297.649353

Test Step 2.36 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	5	
MtrVel3_OsBufPos_Cnt_M_u08[1]	6	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.48	

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.66
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.78
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.9
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.02 3.08
MtrVel3_PosBuffer_Rad_M_f32[1][6]	
MtrVel3_PosBuffer_Rad_M_f32[1][7] MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.14 3.2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.26
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.38
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.44
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.68
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.74
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.8
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.86
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.92
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.98
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
Mtr/cl3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2703
	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2331 2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwRadpS_M_f32	-10.45496923
MtrVel_MtrVelMRF_MtrRadpS_M_f32	179.1975787
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5577
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
=	
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Ci	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hwl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrF	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-0	0 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1323.42847	1323.428223 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	0	0	•
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7777	7777	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1323.42847	-1323.428223 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1323.42847	1323.428223 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1323.42847	1323.428223 ± 0.0625	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.37 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.45	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9645	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9602		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9560		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9403		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9366		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9304		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7760		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7822		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7884		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7946		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7512		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7574		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7636		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7698		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6662		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6724		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6786		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6848		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6910		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6476		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6538		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4012		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3946 3901		
	3866		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3752		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3702		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3650		
MtrVel_HwVel_HwRadpS_M_f32	-16.19755042		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-722.7552819		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2133		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4333		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_st	08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f3	2	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_US	ec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	2	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadp	S_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-42	42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-792.446411 -	792.4464111 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2	2	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	.256617887 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32		1333	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value		42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-792.446411 -	792.4464111 ± 0.0625	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-792.446411 -	792.4464111 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	42 ± 0.009	•



Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✓
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
/trVel3_OsBufPos_Cnt_M_u08[2]	6	
/trVel3_OsBufPos_Cnt_M_u08[3]	1	
/trVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
/trVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
/trVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
/trVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
ftrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	
ftrVel3_PosBuffer_Rad_M_f32[0][5]	2.68	
ftrVel3_PosBuffer_Rad_M_f32[0][6]	2.76	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	2.84	
ftrVel3_PosBuffer_Rad_M_f32[1][0]	2.92	
ftrVel3_PosBuffer_Rad_M_f32[1][1]	3	
ltrVel3_PosBuffer_Rad_M_f32[1][2]	3.08	
ltrVel3_PosBuffer_Rad_M_f32[1][3]	3.16	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	3.24	
ltrVel3_PosBuffer_Rad_M_f32[1][5]	3.32	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	3.4	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	3.48	
ftrVel3_PosBuffer_Rad_M_f32[2][0]	3.56	
ftrVel3_PosBuffer_Rad_M_f32[2][1]	3.64	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	3.72	
ltrVel3_PosBuffer_Rad_M_f32[2][3]	3.8	
trVel3_PosBuffer_Rad_M_f32[2][4]	3.88	
ltrVel3_PosBuffer_Rad_M_f32[2][5]	3.96	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	4.04	
ftrVel3_PosBuffer_Rad_M_f32[2][7]	4.12	
ftrVel3_PosBuffer_Rad_M_f32[3][0]	4.2	
ftrVel3_PosBuffer_Rad_M_f32[3][1]	4.28	
htrVel3_PosBuffer_Rad_M_f32[3][2]	4.36	
htrVel3_PosBuffer_Rad_M_f32[3][3]	4.44	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	4.52	
ltrVel3_PosBuffer_Rad_M_f32[3][5]	4.6	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	4.68	
ltrVel3_PosBuffer_Rad_M_f32[3][7]	4.76	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	6897	
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	6800	
ltrVel3_TimeBuffer_uS_M_u16p0[0][2]	6776	
ltrVel3_TimeBuffer_uS_M_u16p0[0][3]	6700	
ItrVel3_TimeBuffer_uS_M_u16p0[0][4]	6650	
ItrVel3_TimeBuffer_uS_M_u16p0[0][5]	6603	
htrVel3_TimeBuffer_uS_M_u16p0[0][6]	6575	
htrVel3_TimeBuffer_uS_M_u16p0[0][7]	6508	
htrVel3_TimeBuffer_uS_M_u16p0[1][0]	2695	
ItrVel3_TimeBuffer_uS_M_u16p0[1][1]	2757	
htrVel3_TimeBuffer_uS_M_u16p0[1][2]	2819	
trVel3_TimeBuffer_uS_M_u16p0[1][3]	2881	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	2943	
trVel3_TimeBuffer_uS_M_u16p0[1][5]	2509	
ltrVel3_TimeBuffer_uS_M_u16p0[1][6]	2571	
ltrVel3_TimeBuffer_uS_M_u16p0[1][7]	2633	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	3825	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	3887	
ttrVel3_TimeBuffer_uS_M_u16p0[2][2]	3949	
/trVel3_TimeBuffer_uS_M_u16p0[2][3]	4011	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	4073	
MtrVel3_TimeBuffer_u3_M_u16p0[2][5]	4135	
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4197	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3763		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5085		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	5012		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4950		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4908		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4860		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	4805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	4775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	4700		
MtrVel_HwVel_HwRadpS_M_f32	-23.42079562		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1306.267611		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	75688		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.64		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	77888		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_0	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	o_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p	16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-33.4618645	-33.46188599 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-52.2841644	-52.28419876 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	77888	77888	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-33.4618645	-33.46188599 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-52.2841644	-52.28419876 ± 0.0625	✓
A MANUAL BOARD AND AND AND AND AND AND AND AND AND AN	50 0044044	50.00440070 0.0005	

Ţ ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•

-52.2841644

-33.4618645

-52.2841644

-52.28419876 ± 0.0625

-33.46188599 ± 0.009

-52.28419876 ± 0.0625

Test Step 2.39 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$

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MtrVel_Per1



Name Input Value Moreal, Postulare, Fload, M. (1971) 19 3.29 Moreal, Postulare, Fload, M. (1971) 19 3.29 Moreal, Postulare, Fload, M. (1971) 19 3.40 Moreal, Postulare, Fload, M. (1971) 19 3.41 Moreal, Postulare, Fload, M. (1971) 19 3.42 Moreal, Postulare, Fload, M. (1971) 19 3.42 Moreal, Postulare, Fload, M. (1972) 19 4.97 Moreal, Postulare, Fload, M. (1972) 19 4.97 Moreal, Postulare, Fload, M. (1972) 19 4.92 Moreal, Fload, M. (1972) 19 4.92		
Marvior Desidence Fast M. 1970	Name	Input Value
Marvior Desidence Fast M. 1970	MtrVel3 PosBuffer Rad M f32[1][2]	3.2
Mirries Pasiture Fast M 150(1)		
Ministry Destination March 1997 1997 1		
Mirvial Position: Real M. 1907 365 Mirvial Position: Real M. 1907 387 Mirvial Prosition: Real M. 1907 387		
Martines Production Martines Martine		
Merical Postulatine Read M. 1920 10 324 Merical Postulatine Read M. 1920 11 385 Merical Postulatine Read M. 1920 12 401 Merical Postulatine Read M. 1920 13 411 Merical Postulatine Read M. 1920 13 419 Merical Postulatine Read M. 1920 13 419 Merical Postulatine Read M. 1920 13 419 Merical Postulatine Read M. 1920 13 449 Merical Postulatine Read M. 1920 14 429 Merical Postulatine Read M. 1920 15 5 Merical Postulatine Read M. 1920 16 5 Merical Postulatine Read M. 1920 17		
Ministry Seculation Read M. 1920 10 1822 18	MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
Mirviss Footblarte Red M. 1922 19	MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
Mirvisor, Postubrite, Peta D. M. 2023 19 411 Mirvisor, Postubrite, Peta D. M. 2023 19 419 Mirvisor, Postubrite, Peta D. M. 2023 19 428 Mirvisor, Postubrite, Peta D. M. 2023 19 428 Mirvisor, Postubrite, Peta D. M. 2023 19 436 Mirvisor, Postubrite, Peta D. M. 2023 19 436 Mirvisor, Postubrite, Peta D. M. 2023 19 446 Mirvisor, Postubrite, Peta D. M. 2023 19 448 Mirvisor, Postubrite, Peta D. M. 2023 19 449 Mirvisor, Pestatubrite, Peta D. M. 2023 19 459 Mirvisor, Pestatubrite, Peta D. M. 2023 19 450 Mirvisor, Pestatubrite	MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
Marwick Pesiture Rod M 102 9	MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MIYORA Postular Red M. POZI 19	MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MarVesD_Posibility_Rad_M_252(8)		4.1
Marvies Pendamer Rand M. 1920		
Merica Position Rail M. 1907[21] 4-7		
Marwiss, Problemer, Rand, M. 2023 10 446 456 4		
Mirviss_Posbuline_Rad_M.253381 454		
Marvis Position Rou M. 1933 3 473		
Martin Debug Martin M		
MIVVS3_Posibilite_Rad_M.1203[91] MIVVS3_Posibilite_Rad_M.1203[95] MIVVS3_Posibilite_Rad_M.1203[95] MIVVS3_Posibilite_Rad_M.1203[97] MIVVS3_TreeBulfe_U.S.M. u16p0[07] MIVVS3_TreeBulfe_U.S.M. u16p0[17] MIVVS3_TreeBulfe_U.S.M. u16p0[17	MtrVel3_PosBuffer_Rad_M_f32[3][2]	
Mary New Parabuter Rad M. 1203 S 5	MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73
Marvais Problemer Rad M. (303)17 5.99	MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82
Marvais Problemer Rad M. (303)17 5.99	MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91
MARYAS TransBuffer LUS M L16900191 509 166 168 1		
MorNAS TrimeBuffer u.S. M. u1900 019 166		
Mirvis TimeBuffer U.S. M. U1600017 65288	* " *	
MrWeS_TimeBuffer_US_M_utip0[0][2]		
MrtVel3_TrmeBuffer_uS_M_urlep0j0j3		
MrV48_TimeBuffer_US_M_ut6p00[14] 65544		
MinVals TimeBuffer u.S. M. ur6p00[8]		
Mrtvela_TimeBuffer_uis_M_urispoil[0] 42 Mrtvela_TimeBuffer_uis_M_urispoil[1] 2042 Mrtvela_TimeBuffer_uis_M_urispoil[1] 2042 Mrtvela_TimeBuffer_uis_M_urispoil[1] 2104 Mrtvela_TimeBuffer_uis_M_urispoil[1] 2106 Mrtvela_TimeBuffer_uis_M_urispoil[1] 1732 Mrtvela_TimeBuffer_uis_M_urispoil[1] 1794 Mrtvela_TimeBuffer_uis_M_urispoil[1] 1856 Mrtvela_TimeBuffer_uis_M_urispoil[1] 1918 Mrtvela_TimeBuffer_uis_M_urispoil[1] 1980 Mrtvela_TimeBuffer_uis_M_urispoil[1] 20 Mrtvela_TimeBuffer_uis_M_urispoil[2] 300 Mrtvela_TimeBuffer_uis_M_urispoil[2] 40 Mrtvela_TimeBuffer_uis_M_urispoil[2] 500 Mrtvela_TimeBuffer_uis_M_urispoil[2] 60 Mrtvela_TimeBuffer_uis_M_urispoil[2]		
MrtVel3_TimeBuffer_uS_M_u16p0[0] 7		65516
MrWel3_TimeBuffer_uS_M_u16pq1[10] 2942 MitVel3_TimeBuffer_uS_M_u16pq1[11] 2104 MitVel3_TimeBuffer_uS_M_u16pq1[12] 2166 MitVel3_TimeBuffer_uS_M_u16pq1[12] 1794 MitVel3_TimeBuffer_uS_M_u16pq1[12] 1794 MitVel3_TimeBuffer_uS_M_u16pq1[12] 1918 MitVel3_TimeBuffer_uS_M_u16pq1[12] 1918 MitVel3_TimeBuffer_uS_M_u16pq1[12] 190 MitVel3_TimeBuffer_uS_M_u16pq1[12] 100 MitVel3_TimeBuffer_uS_M_u16pq1[12] 200 MitVel3_TimeBuffer_uS_M_u16pq1[12] 300 MitVel3_TimeBuffer_uS_M_u16pq1[12] 400 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 600 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel3_TimeBuffer_uS_M_u16pq1[12] 500 MitVel4_TimeBuffer_uS_M_u16pq1[12] 500	MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42
MrtVel3_TmeBuffer_uS_M_u16p0[1]2 2106 MrtVel3_TmeBuffer_uS_M_u16p0[1]2 2168 MrtVel3_TmeBuffer_uS_M_u16p0[1]2 1792 1793 MrtVel3_TmeBuffer_uS_M_u16p0[1]3 1792 1794 179	MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104
MrtVel3_TmeBuffer_uS_M_u16p0[1]2 1732 1732 1734	MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042
MrtVel3_TmeBuffer_uS_M_u16p0[1]2 1732 1732 1734	MtrVel3 TimeBuffer uS M u16p0[1][1]	2104
MrtVel3_TimeBuffer_uS_M_tri6p0[1]8] 1732 1794	1 1 1 1	
MtrVel3_TimeButfer_uS_M_u16p0[1][4] 1794 1856 1856		
Mitrivel3_TimeBuffer_uS_M_u16p0[1][6] 1856 1918 19	1 1 1 1	
Mirvel3_TimeBuffer_uS_M_u16p0[1]F 1918		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 1980		
Mirvels_TimeBuffer_uS_M_uf6p0[2][0] 100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 400 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 500 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 600 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel4_Hwvel_HwradpS_M_132 42 MtrVel_HwradpS_M_132 42 MtrVel_ObesBuf_Cru_S_M_u16p0[3][7] 800 MtrVel_ObesBuf_Cru_M_u08 0 MtrVel_ObesBuf_Cru_M_u08 1 MtrVel_ObesBuf_Cru_M_u08 1 MtrVel_PrevMtrPosBenpfetime2_uS_M_u23 53233 Rte_Inst_Sa_MtrVel 1 T_MtrVelBlendTbty_UIs_u2p14[0] 0	MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980
MtrVel3_TimeBuffer_u.S. M_u16p0[2][2] 300 MtrVel3_TimeBuffer_u.S. M_u16p0[2][3] 400 MtrVel3_TimeBuffer_u.S. M_u16p0[2][4] 500 MtrVel3_TimeBuffer_u.S. M_u16p0[2][5] 600 MtrVel3_TimeBuffer_u.S. M_u16p0[2][6] 700 MtrVel3_TimeBuffer_u.S. M_u16p0[2][7] 800 MtrVel3_TimeBuffer_u.S. M_u16p0[3][0] 100 MtrVel3_TimeBuffer_u.S. M_u16p0[3][2] 300 MtrVel3_TimeBuffer_u.S. M_u16p0[3][3] 400 MtrVel3_TimeBuffer_u.S. M_u16p0[3][4] 500 MtrVel3_TimeBuffer_u.S. M_u16p0[3][5] 600 MtrVel3_TimeBuffer_u.S. M_u16p0[3][6] 700 MtrVel3_TimeBuffer_u.S. M_u16p0[3][6] 700 MtrVel3_TimeBuffer_u.S. M_u16p0[3][6] 700 MtrVel_HwvRadpSuffer_u.S. M_u16p0[3][6] 700 MtrVel_HwvRadp	MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 600 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 700 MtrVel4_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel_HimVel_HimVel_MradpS_M_132 42 MtrVel_HimVel_HimVel_MradpS_M_132 42 MtrVel_OldPosBuf_Crt_M_u08 0 MtrVel_Dosbuf_Crt_M_u08 1 MtrVel_PrevMtrPosBerchMtrPos2_Rad_M_132 4.3 MtrVel_PrevMtrPosMerchMtrPos2_Rad_M_132 4.3 MtrVel_PrevMtrPosMerchMtrPos2_Rad_M_132 4.3 MtrVel_PrevMtrPosMerchMtrVel 1 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel 4 L_MtrVelBlendTbtY_UI	MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 700 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 100 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 600 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel_MtrVel_MtrS_M_u16p0[3][7] 800 MtrVel_MtrVel_MtrS_M_u16p0[3][7] 800 MtrVel_Diff0_SBuf_Cnt_M_u08 0 MtrVel_Diff0_SBuf_Cnt_M_u08 0 MtrVel_Diff0_SBuf_Cnt_M_u08 1 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 4.3 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rtz_Inst_SB_MtrVel 1 T_MtrVelBlendTbrY_UIs_u2p14[1] 66344 K_GearRatio_UIs_U32 4 L_MtrVelBlendTbrY_MtrRadpS_u12p4[0]	MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 100 MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 200 MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 300 MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 400 MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 500 MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 600 MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel_NtvelMtrPast_McadpS_M_132 -42 MtrVel_NtvelMtrVelMtrS_MrtRadpS_M_132 -42 MtrVel_OldrPosBut_Cnt_M_u08 0 MtrVel_DoltrPosBut_Cnt_M_u08 1 MtrVel_PrevMtrPosBechMtrPos2_Rad_M_132 4.3 MtrVel_PrevMtrPosBachMtrPos2_Rad_M_132 4.3 MtrVel_PrevMtrPosBachMtrPos2_Rad_M_132 4.3 MtrVel_PrevMtrPosBachMtrPos2_Rad_M_132 4.3 MtrVel_DrevMtrPosBachMtrPos2_Rad_M_132 4.3 MtrVel_DrevMtrPosBachMtrPos2_Rad_M_132 4.3 MtrVel_DrevMtrPosBachMtrPos2_Rad_M_132 4.3 <td>1 1 1 1</td> <td>500</td>	1 1 1 1	500
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MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 700 MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 800 MtrVel_HwVel_HwRadpS_M_f32 -42 MtrVel_OldPoSBuf_Cnt_M_u08 -660.4303926 MtrVel_OldPoSBuf_Cnt_M_u08 1 MtrVel_PrevMtrPoSMechMtrPos2_Rad_M_f32 4.3 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_UIs_u2p14[0] 0 T_MtrVelBlendTblY_UIs_u2p14[1] 16384 K_GearRatio_UIs_f32 0.67 L_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 L_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2Rev_u0p16.value 25434 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
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MtrVel_MtrVelMtrS_MtrRadpS_M_f32 -660.4303926 MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 1 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 4.3 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_UIs_u2p14[0] 0 T_MtrVelBlendTblY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.67 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 t_MtrVelBendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
MtrVel_OldPosBuf_Cnt_M_u08 0 MtrVel_OsBufSelect_Cnt_M_u08 1 MtrVel_PrevMtrPosAechMtrPos2_Rad_M_f32 4.3 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_UIs_u2p14[0] 0 T_MtrVelBlendTblY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.67 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 t_MtrVelBendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	·	
MtrVel_OSBufSelect_Cnt_M_u08 1 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 4.3 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_UIs_u2p14[0] 0 T_MtrVelBlendTblY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.67 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 4.3 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTbIY_UIs_u2p14[0] 0 T_MtrVelBlendTbIY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.67 t_MtrVelBlendTbIX_MtrRadpS_u12p4[0] 4 t_MtrVelBendTbIX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32 53233 Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTbIY_UIs_u2p14[0] 0 T_MtrVelBlendTbIY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.67 t_MtrVelBlendTbIX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTbIX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
Rte_Inst_Sa_MtrVel tgt_Rte_Inst_Sa_MtrVel T_MtrVelBlendTbIY_Uls_u2p14[0] 0 T_MtrVelBlendTbIY_Uls_u2p14[1] 16384 k_GearRatio_Uls_f32 0.67 t_MtrVelBlendTbIX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTbIX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
T_MtrVelBlendTbIY_UIs_u2p14[0] 0 T_MtrVelBlendTbIY_UIs_u2p14[1] 16384 k_GearRatio_UIs_f32 0.67 t_MtrVelBlendTbIX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTbIX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	·	
T_MtrVelBlendTblY_Uls_u2p14[1] 16384 k_GearRatio_Uls_f32 0.67 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
k_GearRatio_Uls_f32 0.67 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	T_MtrVelBlendTblY_Uls_u2p14[0]	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 4 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	T_MtrVelBlendTblY_Uls_u2p14[1]	16384
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	k_GearRatio_Uls_f32	0.67
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 4 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 55433 tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 26214 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	,	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tot Rte Inst Sa Mtr/el Mtr/el Per1 MechMtrPos2 Rev u0n16 tot Mtr/el Per1 MechMtrPos2 Pov u0n16		
191_1110_00_1111111 VOLINITU V	tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

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MtrVel_Per1



Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCR	F_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMR	RF_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandw	heelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotor\	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-812.165649	-812.1655273 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✓
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	55433	55433	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	812.165649	812.1655273 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-812.165649	-812.1655273 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	-812.165649	-812.1655273 ± 0.0625	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Name	Innut Value	
	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
htrVel3_PosBuffer_Rad_M_f32[0][0]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.62	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.12	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.22	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.32	
htrVel3_PosBuffer_Rad_M_f32[1][3]	3.42	
ItrVel3_PosBuffer_Rad_M_f32[1][4]	3.52	
ltrVel3_PosBuffer_Rad_M_f32[1][5]	3.62	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.72	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.82	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.92	
ItrVel3_PosBuffer_Rad_M_f32[2][1]	4.02	
htrVel3_PosBuffer_Rad_M_f32[2][2]	4.12	
ltrVel3_PosBuffer_Rad_M_f32[2][3]	4.22	
htrVel3_PosBuffer_Rad_M_f32[2][4]	4.32	
ltrVel3_PosBuffer_Rad_M_f32[2][5]	4.42	
ftrVel3_PosBuffer_Rad_M_f32[2][6]	4.52	
ltrVel3_PosBuffer_Rad_M_f32[2][7]	4.62	
ftrVel3_PosBuffer_Rad_M_f32[3][0]	4.72	
/trVel3_PosBuffer_Rad_M_f32[3][1]	4.82	
/trVel3_PosBuffer_Rad_M_f32[3][2]	4.92	
ftrVel3_PosBuffer_Rad_M_f32[3][3]	5.02	
ftrVel3_PosBuffer_Rad_M_f32[3][4]	5.12	
ftrVel3_PosBuffer_Rad_M_f32[3][5]	5.22	
ftrVel3_PosBuffer_Rad_M_f32[3][6]	5.32	
ltrVel3_PosBuffer_Rad_M_f32[3][7]	5.42	
/trVel3_TimeBuffer_uS_M_u16p0[0][0]	250	
/trVel3_TimeBuffer_uS_M_u16p0[0][1]	65352	
/trVel3_TimeBuffer_uS_M_u16p0[0][2]	65414	
/trVel3_TimeBuffer_uS_M_u16p0[0][3]	65476	
/trVel3_TimeBuffer_uS_M_u16p0[0][4]	2	
/trVel3_TimeBuffer_uS_M_u16p0[0][5]	64	

MtrVel_Per1





Name	Input Value			
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126			
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188			
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626			
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688	1688		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316			
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378			
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440			
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502			
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564			
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110			
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220			
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330			
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440			
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550			
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660			
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770			
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880			
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110			
MtrVel3 TimeBuffer uS M u16p0[3][1]	220			
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330			
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440			
	550			
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	660			
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]				
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770 880			
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]				
MtrVel_HwVel_HwRadpS_M_f32	42			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1222.594758			
MtrVel_OldPosBuf_Cnt_M_u08	0			
MtrVel_OsBufSelect_Cnt_M_u08	1			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6			
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	32378			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.7			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	34578			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt	_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_	<u>f</u> 32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_l	USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f	32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	adpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	dpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~	
MtrVel MtrVelMRF MtrRadpS M f32	246.178558	246.1784821 ± 0.0625	~	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	-	
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	_	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	34578	34578	·	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009		
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	246.178558	246.178482 ± 0.0625	· ·	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	246.178558	246.178482 ± 0.0625		
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~	
tgt_MtrVel_Per1_SysCMatiowneetvel_HwRaupS_i32.value	246.178558	246.1784821 ± 0.0625		
IGC. WILL VOIL 1 OF 1_0730 WICKER VOILVILL INTELL VALUE	270.170000	270.1707021 ± 0.0023		

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓





Test Step 2.41 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.34
MtrVel3_PosBuffer_Rad_M_f32[0][1] MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.56
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.67
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.89
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.33
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][3] MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.55 3.66
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.88
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.99
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.43
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.54
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.65
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][7] MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.87 4.98
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.09
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.2
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.31
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.53
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.64
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.75
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111
MtrVel3_TimeBuffer_uS_M_u16p0[0][1] MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	221 331
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551
MtrVel3 TimeBuffer uS M u16p0[0][5]	661
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901
MtrVel3_TimeBuffer_uS_M_u16p0[1][5] MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	945 990
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	301
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	363 425
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239
MtrVel_HwVel_HwRadpS_M_f32	0
MtrVel_MtrVelMRF_MtrRadpS_M_f32 MtrVel_OldPosBuf_Cnt_M_u08	-1201.403606 2





Name	Input Value	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	3	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5	2.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87234			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.73			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	89434			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPola	rity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hw	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Time	stamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrF	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mtrl	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	el_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF	MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	•	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	577.249817	577.2498169 ± 0.0625	•	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	~	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	89434			
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-577.249817	-577.2498169 ± 0.0625	✓	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	577.249817	577.2498169 ± 0.0625	~	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓	
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	577.249817	577.2498169 ± 0.0625	✓	

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	-

Test Step 2.42 (Repeat Count = 1)	l _j aran kanalan dari kanalan
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.96
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.16
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.4
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.88





Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.12		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.24		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.36		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.48		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.6		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.72		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.84 5.96		
MtrVel3_PosBuffer_Rad_M_f32[3][6] MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.08		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	999		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	888		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	777		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	555		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	444		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	333		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	222		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	825		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	725		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	525		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	325		
Mtr/el3_TimeBuffer_uS_M_u16p0[1][7]	225 666		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	232		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1] MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	294		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	356		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	418		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	542		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	604		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1042		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1104		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1166		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	732		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	794		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	856		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	918		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	980		
MtrVel_HwVel_HwRadpS_M_f32	23.7894063		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	953.297722		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Mtr/el_PrevMtrPosAechMtrPos2_Rad_M_f32	1 2279		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32 Rte_Inst_Sa_MtrVel	2378 tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.76		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	10		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4578		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_	_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_l		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1311.35229	-1311.352173 ± 0.0625	V
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~

Name	Actual value	Expected value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1311.35229	-1311.352173 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4578	4578	✓
tot Mtr\/el Per1 Handwheel\/el HwRadnS f32 value	42	42 + 0.009	_

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MtrVel_Per1

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1311.35229	1311.352173 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1311.35229	-1311.352173 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1311.35229	-1311.352173 ± 0.0625	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1)	•
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	5
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.9
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.16
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.33
	4.46
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.40
MtrVel3_PosBuffer_Rad_M_f32[2][1]	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	843
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	793
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	743
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	693
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	543
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	493
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	443
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	393
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	447
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	407
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	367
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	327
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	287
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	247
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	207
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	167
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2796
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2362
init voic_timebulier_uo_w_u topo[2][1]	2002

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MtrVel_Per1			NAZUICAG
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2424		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2486		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2548		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2610		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2672		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2734		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3672		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3734		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3796		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3362		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3424		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3486		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3548		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3610		
MtrVel_HwVel_HwRadpS_M_f32	-40.36163938		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1131.737475		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	52133		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.79		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	12		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	54333		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPola	arity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hv	wRadpS f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Time	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Re		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt MtrVel Per1 MotorVelCRF Mtr	·	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mt	. =	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelV	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMR		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	9.54013348	9.540119181 ± 0.0625	Rooul
MtrVel_MtrVelMRF_MtrRadpS_M_f32	12.0761175	12.07610035 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	
MtrVel PrevMtrPosSampleTime2 uS M u32	54333	54333	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	9.54013348	9.540119181 ± 0.009	
tgt_MtrVel_Fel1_Handwheelvel_HwRadpS_l32.value	12.0761175	9.540119181 ± 0.009 12.07610035 ± 0.0625	
tgt_iviti vei_rei i_wiotoi veiciti _witttaup3_i3z.value	12.0701173	12.07010035 ± 0.0025	

⊤				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

12.0761175

9.54013348

12.0761175

12.07610035 ± 0.0625

9.540119181 ± 0.009

12.07610035 ± 0.0625

Test Step 2.44 (Repeat Count = 1)		√
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.96	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.24
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.52
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.08
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.22
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.36
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.5
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.78
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.06
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.2
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.34
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.48
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.75
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.8
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.04
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.17
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.7 3.8
MtrVel3_PosBuffer_Rad_M_f32[3][7] MtrVel3_TimeRuffer_us_M_u45p0[0][0]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][0] MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1001 910
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwRadpS_M_f32	29.00406408
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	676689
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.82
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	13
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	14
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	-1 678889





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_C	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p	16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp\$	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	752.194763	752.1947632 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✓
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	678889	678889	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-752.194763	-752.1947632 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	752.194763	752.1947632 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	752.194763	752.1947632 ± 0.0625	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.45 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.87	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.62	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.77	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.92	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.07	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.22	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.37	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.52	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.67	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.82	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.97	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.12	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.27	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.42	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.57	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.7	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.87	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.01	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.16	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.31	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.4	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.9	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	835	

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MtrVel_Per1

Mtrvei_Per1			MACILAB
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	765		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	695		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	660		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	590		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10345		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1] MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10301 10290		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10200		
MtrVel3 TimeBuffer uS M u16p0[1][4]	10175		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	10154		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	10100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	10060		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	712		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	774		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	836		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	898		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	960		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	1022		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	1084		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2922 2984		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2550		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2612		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2674		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2736		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2798		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2860		
MtrVel_HwVel_HwRadpS_M_f32	41.65612358		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2255		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0] T_MtrVelBlendTblY_Uls_u2p14[1]	0 16384		
T_MtrVelBlendTblY_Uls_u2p14[1] k_GearRatio_Uls_f32	0.85		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	15		
t MtrVelBlendTbIX MtrRadpS u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4455		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec	_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_		1_
Name		pected Value	Result
MtrVel_HwVel_HwRadpS_M_f32		± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32		1.2564697 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	3 0		
MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32		13235773 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2.51323361 2.5 4455 445		
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value		± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value		± 0.009 1.25647 ± 0.0625	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value		1.2564697 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value		± 0.009	•
tgt MtrVel Per1 SvsCMotorVelMRF MtrRadpS f32.value		1.2564697 ± 0.0625	

-221.256454

 $tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value$

-221.2564697 ± 0.0625



Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~	
CalcCoarseVel	1	CalcCoarseVel	1	✓	
RegressionFit	1	RegressionFit	1	✓	
MtrVelBlend	1	MtrVelBlend	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	-	

est Step 2.46 (Repeat Count = 1)		
lame	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
ftrVel3_OsBufPos_Cnt_M_u08[1]	4	
ltrVel3_OsBufPos_Cnt_M_u08[2]	6	
trVel3_OsBufPos_Cnt_M_u08[3]	1	
trVel3_PosBuffer_Rad_M_f32[0][0]	2.44	
trVel3_PosBuffer_Rad_M_f32[0][1]	2.6	
trVel3_PosBuffer_Rad_M_f32[0][2]	2.76	
trVel3_PosBuffer_Rad_M_f32[0][3]	2.92	
trVel3_PosBuffer_Rad_M_f32[0][4]	3.08	
trVel3_PosBuffer_Rad_M_f32[0][5]	3.24	
trVel3_PosBuffer_Rad_M_f32[0][6]	3.39	
trVel3_PosBuffer_Rad_M_f32[0][7]	3.56	
trVel3_PosBuffer_Rad_M_f32[1][0]	3.71	
trVel3_PosBuffer_Rad_M_f32[1][1]	3.88	
trVel3_PosBuffer_Rad_M_f32[1][2]	4.03	
trVel3_PosBuffer_Rad_M_f32[1][3]	4.2	
trVel3_PosBuffer_Rad_M_f32[1][4]	4.36	
trVel3_PosBuffer_Rad_M_f32[1][5]	4.52	
trVel3_PosBuffer_Rad_M_f32[1][6]	4.68	
trVel3_PosBuffer_Rad_M_f32[1][7]	4.84	
trVel3_PosBuffer_Rad_M_f32[2][0]	5	
trVel3_PosBuffer_Rad_M_f32[2][1]	5.16	
trVel3_PosBuffer_Rad_M_f32[2][2]	5.32	
trVel3_PosBuffer_Rad_M_f32[2][3]	5.48	
trVel3_PosBuffer_Rad_M_f32[2][4]	5.64	
trVel3_PosBuffer_Rad_M_f32[2][5]	5.8	
trVel3_PosBuffer_Rad_M_f32[2][6]	5.96	
trVel3_PosBuffer_Rad_M_f32[2][7]	6.12	
trVel3_PosBuffer_Rad_M_f32[3][0]	2.4	
trVel3_PosBuffer_Rad_M_f32[3][1]	2.5	
trVel3_PosBuffer_Rad_M_f32[3][2]	2.6	
trVel3_PosBuffer_Rad_M_f32[3][3]	2.7	
trVel3_PosBuffer_Rad_M_f32[3][4]	2.8	
trVel3_PosBuffer_Rad_M_f32[3][5]	4.2	
trVel3_PosBuffer_Rad_M_f32[3][6]	4.3	
trVel3_PosBuffer_Rad_M_f32[3][7]	4.4	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	6142	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	6204	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	6266	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	5832	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	5894	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	5956	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	6018	
trVel3_TimeBuffer_uS_M_u16p0[0][7]	6080	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	1520	
trVel3_TimeBuffer_uS_M_u16p0[1][1]	1500	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	1480	
rVel3_TimeBuffer_uS_M_u16p0[1][3]	1460	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	1440	
rVel3_TimeBuffer_uS_M_u16p0[1][5]	1420	
rVel3_TimeBullet_u3_M_u16p0[1][5]	1400	
rVel3_TimeBuller_u5_M_u16p0[1][6] rVel3_TimeBuffer_uS_M_u16p0[1][7]	1380	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	7777	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	7700	
trVel3_TimeBuffer_uS_M_u16p0[2][2]	7610	
trVel3_TimeBuffer_uS_M_u16p0[2][3]	7590	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500	
ltrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435	

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			1-4-10	,0
Name	Input Value			
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378			
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504			
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566			
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132			
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194			
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256			
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318			
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380			
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442			
MtrVel_HwVel_HwRadpS_M_f32	-40.36163938			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0			
MtrVel_OldPosBuf_Cnt_M_u08	0			
MtrVel_OsBufSelect_Cnt_M_u08	3			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2			
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	443466			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.88			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	445666			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	dpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestar	mp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0)p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	pS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	pS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	lwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	trRadpS_f32		
Name	Actual Value	Expected Value		Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625		~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	804.522583	804.522522 ± 0.0625		~
MtrVel_OldPosBuf_Cnt_M_u08	3	3		~
MtrVel_OsBufSelect_Cnt_M_u08	0	0		~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625		~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	445666	445666		~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009		_
·				

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	✓

804.522583

804.522583

804.522583

42

804.522522 ± 0.0625

804.522522 ± 0.0625 42 ± 0.009

804.522522 ± 0.0625

Test Step 2.47 (Repeat Count = 1)		<u> </u>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.46	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.63	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.8	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.97	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.14	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.65	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.81	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.99	

tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.15
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.33
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.67
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.84 5.01
MtrVel3_PosBuffer_Rad_M_f32[1][7] MtrVel3_PosBuffer_Rad_M_f32[2][0]	5.18
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.35
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.69
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.86
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.03
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.2
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.3
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.4
Mtr/cl3_PosBuffer_Rad_M_f32[3][1]	1.5 1.6
MtrVel3_PosBuffer_Rad_M_f32[3][2] MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.8
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.5
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.7
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	2069
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	2131
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	2193
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1759
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1821
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1883
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1945
MtrVel3_TimeBuffer_uS_M_u16p0[0][7] MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2007 3450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3410
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	3370
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	3333
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	3290
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	3207
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	3175
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	3100
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2450
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2512
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2574 2636
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2698
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2760
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2822
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2388
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6890
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6810
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6680
Mtr/el3_TimeBuffer_uS_M_u16p0[3][5]	6610 6540
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6540 6504
MtrVel_HwRadpS_M_f32	13.81390291
MtrVel_MtrVelMRF_MtrRadpS_M_f32	469.5
MtrVel OldPosBuf Cnt M u08	0
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	754558
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_UIs_f32	0.91
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7 7
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	756758
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRad	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1097.73877	-1097.73877 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	756758	756758	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1097.73877	1097.73877 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1097.73877	-1097.73877 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1097.73877	-1097.73877 ± 0.0625	•

T	T					
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~		
CalcCoarseVel	1	CalcCoarseVel	1	~		
RegressionFit	1	RegressionFit	1	~		
MtrVelBlend	1	MtrVelBlend	1	✓		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~		
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓		

lawa.	Innut Value	
Name	Input Value	
/trVel3_OsBufPos_Cnt_M_u08[0]	5	
ItrVel3_OsBufPos_Cnt_M_u08[1]	6	
ltrVel3_OsBufPos_Cnt_M_u08[2]	7	
ItrVel3_OsBufPos_Cnt_M_u08[3]	4	
ItrVel3_PosBuffer_Rad_M_f32[0][0]	2.48	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	2.66	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	2.84	
ItrVel3_PosBuffer_Rad_M_f32[0][3]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.2	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	3.38	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	3.55	
ItrVel3_PosBuffer_Rad_M_f32[0][7]	3.74	
ItrVel3_PosBuffer_Rad_M_f32[1][0]	3.91	
ItrVel3_PosBuffer_Rad_M_f32[1][1]	4.1	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.27	
ItrVel3_PosBuffer_Rad_M_f32[1][3]	4.46	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	1.5	
trVel3_PosBuffer_Rad_M_f32[1][5]	1.6	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	1.7	
ItrVel3_PosBuffer_Rad_M_f32[1][7]	1.8	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	1.9	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	2	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	2.1	
trVel3_PosBuffer_Rad_M_f32[2][3]	2.2	
ltrVel3_PosBuffer_Rad_M_f32[2][4]	2.3	
trVel3_PosBuffer_Rad_M_f32[2][5]	2.4	
trVel3_PosBuffer_Rad_M_f32[2][6]	2.5	
ltrVel3_PosBuffer_Rad_M_f32[2][7]	2.6	
ltrVel3_PosBuffer_Rad_M_f32[3][0]	2.7	
ltrVel3_PosBuffer_Rad_M_f32[3][1]	2.8	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	2.9	
trVel3_PosBuffer_Rad_M_f32[3][3]	3	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	3.1	
ltrVel3_PosBuffer_Rad_M_f32[3][5]	3.2	
trVel3_PosBuffer_Rad_M_f32[3][6]	3.3	
ltrVel3_PosBuffer_Rad_M_f32[3][7]	3.4	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383	
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445	
ltrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507	
ltrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569	
ftrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631	
htrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	32906		
Mtr/vel3_TimeBuffer_uS_M_u16p0[3][0]	2517		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2579		
MtrVel3_1imeBuffer_uS_M_u16p0[3][2] MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2641 2703		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455		
MtrVel_HwVel_HwRadpS_M_f32	39.10801578		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-421.600006		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	88583		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.94		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	90783		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p10		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	The state of the s	B
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-610.628174	-610.6282349 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	0 1	0	
MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_ProvMtrPosMochMtrPos2_Pad_M_f22	1.2566179	1 1.256617887 ± 0.0625	· ·
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32	90783	90783	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_132.value	-42 -610.628174	-42 ± 0.009 -610.6282349 ± 0.0625	-
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_i32.value	-610.628174	-610.6282349 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_132.value	-610.628174 -42	-610.6282349 ± 0.0625	_
tgt_iviti vet_ret t_oysonatituwiteetvet_nwkaupo_tsz.value	-4L	-42 ± 0.009	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

-610.628174

 $tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value$

-610.6282349 ± 0.0625

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MtrVel_Per1



Test Case 3: Path Test

Specification

Performance Metrics
(With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 3.1 4449 Cycles
TS 3.2 4557 Cycles
TS 3.3 4542 Cycles
TS 3.4 4407 Cycles
TS 3.5 4483 Cycles

VECTOR DEStV T O Description

> MtrVel_P s f er V d T _P _ LS

MtrVel s fP s C t_M MtrVel Ps fer V d

MtrVel P s f er V d

 $\label{eq:matrix} \mbox{MtrVel_P s} \quad \mbox{f er V d T} \qquad \mbox{2} \qquad \mbox{_P _ LS}$ 2 lse

MtrVel_P s f er V d T 2 _P _ LS 2 lse

f er S M 16 MtrVel

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MtrVel_Per1			MACHEAL
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239		
MtrVel_HwVel_HwRadpS_M_f32	-2.767454803		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	676.0312533		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6046		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.14		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7846		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	nt s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt MtrVel Per1 MotorVelCRF MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	="	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwF		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR		
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	Resul
MtrVel_nwvel_nwkaups_w_32 MtrVel_MtrVelMRF_MtrRadpS_M_f32	640.000244	640 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	
MtrVel PrevMtrPosNetchivitPos2_Rad_M_i32 MtrVel PrevMtrPosSampleTime2 uS M u32	7846	7846	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42 640.000244	42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	640.000244	640 ± 0.0625	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

640.000244

640.000244

42

640 ± 0.0625

42 ± 0.009

640 ± 0.0625

Test Step 3.2 (Repeat Count = 1)		<u> </u>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

 $tgt_MtrVel_Per1_SysCH and wheelVel_HwRadpS_f32.value$

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value

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MtrVel3_PosBuffer_Rad_M_[32]0][5] 2. MtrVel3_PosBuffer_Rad_M_[32]0][6] 2. MtrVel3_PosBuffer_Rad_M_[32]0][7] 2. MtrVel3_PosBuffer_Rad_M_[32][1][0] 2. MtrVel3_PosBuffer_Rad_M_[32][1][1] 3. MtrVel3_PosBuffer_Rad_M_[32][1][3] 3. MtrVel3_PosBuffer_Rad_M_[32][1][5] 3. MtrVel3_PosBuffer_Rad_M_[32][1][6] 3. MtrVel3_PosBuffer_Rad_M_[32][1][7] 3. MtrVel3_PosBuffer_Rad_M_[32][1][7] 3. MtrVel3_PosBuffer_Rad_M_[32][2][0] 3. MtrVel3_PosBuffer_Rad_M_[32][2][1] 3. MtrVel3_PosBuffer_Rad_M_[32][2][2] 3. MtrVel3_PosBuffer_Rad_M_[32][2][3] 3. MtrVel3_PosBuffer_Rad_M_[32][2][6] 4. MtrVel3_PosBuffer_Rad_M_[32][2][6] 4. MtrVel3_PosBuffer_Rad_M_[32][3][1] 4. MtrVel3_PosBuffer_Rad_M_[32][3][1] 4. MtrVel3_PosBuffer_Rad_M_[32][3][6] 4. MtrVel3_PosBuffer_Rad_M_[32][3][7] 4. MtrVel3_PosBuffer_Rad_M_[32][3][7] 4. MtrVel3_PosBuffer_Rad_M_[32][3][6] 4. MtrVel3_TimeBuffer_us_M_	.08 .16 .24 .32 .4 .48 .56 .64 .72 .8 .88 .96 .04 .12 .2 .28 .36 .44 .52
MtrVel3_PosBuffer_Rad_M_[32]0][6] 2. MtrVel3_PosBuffer_Rad_M_[32][1][0] 2. MtrVel3_PosBuffer_Rad_M_[32][1][0] 2. MtrVel3_PosBuffer_Rad_M_[32][1][1] 3. MtrVel3_PosBuffer_Rad_M_[32][1][2] 3. MtrVel3_PosBuffer_Rad_M_[32][1][6] 3. MtrVel3_PosBuffer_Rad_M_[32][1][6] 3. MtrVel3_PosBuffer_Rad_M_[32][1][7] 3. MtrVel3_PosBuffer_Rad_M_[32][1][7] 3. MtrVel3_PosBuffer_Rad_M_[32][2][0] 3. MtrVel3_PosBuffer_Rad_M_[32][2][1] 3. MtrVel3_PosBuffer_Rad_M_[32][2][2] 3. MtrVel3_PosBuffer_Rad_M_[32][2][2] 3. MtrVel3_PosBuffer_Rad_M_[32][2][3] 3. MtrVel3_PosBuffer_Rad_M_[32][2][6] 4. MtrVel3_PosBuffer_Rad_M_[32][3][6] 4. MtrVel3_PosBuffer_Rad_M_[32][3][1] 4. MtrVel3_PosBuffer_Rad_M_[32][3][2] 4. MtrVel3_PosBuffer_Rad_M_[32][3][6] 4. MtrVel3_PosBuffer_Rad_M_[32][3][6] 4. MtrVel3_PosBuffer_Rad_M_[32][3][7] 4. MtrVel3_TimeBuffer_uS_M_u16p0[0][1] 6. MtrVel3_TimeBuffer_uS	.76 .84 .92 .08 .08 .16 .24 .32 .4 .48 .56 .64 .72 .8 .88 .96 .04 .12 .2 .28 .36 .44 .552 .6 .68 .76 .142 .204 .266 .832
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MtrVel3_TimeBuffer_uS_M_u16p0[0][1] 66 MtrVel3_TimeBuffer_uS_M_u16p0[0][2] 62 MtrVel3_TimeBuffer_uS_M_u16p0[0][3] 58 MtrVel3_TimeBuffer_uS_M_u16p0[0][4] 58 MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 59 MtrVel3_TimeBuffer_uS_M_u16p0[0][6] 60 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 61 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 77	204 266 832 894
MtrVel3_TimeBuffer_uS_M_u16p0[0][2] 62 MtrVel3_TimeBuffer_uS_M_u16p0[0][3] 58 MtrVel3_TimeBuffer_uS_M_u16p0[0][4] 58 MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 59 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 60 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 61 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 12 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 12 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 77	266 832 894
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MtrVel3_TimeBuffer_uS_M_u16p0[0][4] 58 MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 58 MtrVel3_TimeBuffer_uS_M_u16p0[0][6] 60 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 60 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][6] 77	894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5] 55 MtrVel3_TimeBuffer_uS_M_u16p0[0][6] 66 MtrVel3_TimeBuffer_uS_M_u16p0[0][7] 60 MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 76	
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MtrVel3_TimeBuffer_us_M_u16p0[0][7] 66 MtrVel3_TimeBuffer_us_M_u16p0[1][0] 15 MtrVel3_TimeBuffer_us_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_us_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_us_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_us_M_u16p0[1][4] 14 MtrVel3_TimeBuffer_us_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_us_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_us_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_us_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_us_M_u16p0[2][4] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][5] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][4] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][5] 76	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 12 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	018
MtrVel3_TimeBuffer_uS_M_u16p0[1][1] 15 MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	080
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	520
MtrVel3_TimeBuffer_uS_M_u16p0[1][3] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	480
MtrVel3_TimeBuffer_uS_M_u16p0[1][5] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	460
MtrVel3_TimeBuffer_us_M_u16p0[1][6] 14 MtrVel3_TimeBuffer_us_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_us_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_us_M_u16p0[2][1] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_us_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_us_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_us_M_u16p0[2][5] 74	440
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] 13 MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	420
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][1] 77 MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	380
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] 76 MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 72	777
MtrVel3_TimeBuffer_uS_M_u16p0[2][3] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] 75 MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 74	610
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	590
, , , , ,	500
	435
	400
	504
	504
	566
	132
, , , , ,	194
	256
	318
	380
	442
·	15.40252668
	227.1569001
Mtr/cl_OldPosBuf_Cnt_M_u08 Mtr/cl_OsBufSclopt_Cnt_M_u08	
Mtt/Vel_OsBufSelect_Cnt_M_u08 Mtt/Vel_DrovMtt/DepMachMtt/Dep2_Red_M_f23	
Mtt/Vel_PrevMtrPosAechMtrPos2_Rad_M_f32 6 Mtt/Vel_PrevMtrPosCempleTime3_us_M_u33	
·	96076
	pt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0] 0	
	6384
k_GearRatio_Uls_f32 0.	.L
t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 5	
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 5 tqt MtrVel Per1 AsstAssemblyPolarity Cnt s08.value 1	
0 , , ,	
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestam	np_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	wRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mt	rRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	29.8089962	29.80897903 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	149.044983	149.0448914 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	29.8089962	29.80897903 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	29.8089962	29.80897903 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1] MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2104 2166		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	400 500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800		
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity	Cnt s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt MtrVel Per1 HandwheelVel HwRa		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	· -	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_h	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	ftrRadpS_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-555.555542	-555.555542 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	555.555542	555.555542 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-555.555542	-555.555542 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value tot MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-555.555542	42 ± 0.009 -555,555542 ± 0.0625	
TOT INTERVEL PELL SYSTEMOTOLY BINGE MITRIAGOS 132 VAIDE	L-000.00004Z	L-000.00054Z ± U.U6Z5	

-555.555542

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value

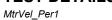
-555.555542 ± 0.0625



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

est Step 3.4 (Repeat Count = 1)		
lame	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
htrVel3_OsBufPos_Cnt_M_u08[1]	2	
ltrVel3_OsBufPos_Cnt_M_u08[2]	4	
ltrVel3_OsBufPos_Cnt_M_u08[3]	7	
ltrVel3_PosBuffer_Rad_M_f32[0][0]	2.14	
trVel3_PosBuffer_Rad_M_f32[0][1]	2.15	
trVel3_PosBuffer_Rad_M_f32[0][2]	2.16	
trVel3_PosBuffer_Rad_M_f32[0][3]	2.17	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.18	
trVel3_PosBuffer_Rad_M_f32[0][5]	2.19	
trVel3_PosBuffer_Rad_M_f32[0][6]	2.2	
trVel3_PosBuffer_Rad_M_f32[0][7]	2.21	
trVel3_PosBuffer_Rad_M_f32[1][0]	2.22	
trVel3_PosBuffer_Rad_M_f32[1][1]	2.23	
trVel3_PosBuffer_Rad_M_f32[1][2]	2.24	
trVel3_PosBuffer_Rad_M_f32[1][3]	2.25	
trVel3_PosBuffer_Rad_M_f32[1][4]	2.26	
trVel3_PosBuffer_Rad_M_f32[1][5]	2.27	
trVel3_PosBuffer_Rad_M_f32[1][6]	2.28	
trVel3_PosBuffer_Rad_M_f32[1][7]	2.29	
trVel3_PosBuffer_Rad_M_f32[2][0]	2.3	
trVel3_PosBuffer_Rad_M_f32[2][1]	2.31	
trVel3_PosBuffer_Rad_M_f32[2][2]	2.32	
trVel3_PosBuffer_Rad_M_f32[2][3]	2.33	
trVel3_PosBuffer_Rad_M_f32[2][4]	2.34	
trVel3_PosBuffer_Rad_M_f32[2][5]	2.35	
trVel3_PosBuffer_Rad_M_f32[2][6]	2.35	
trVel3_PosBuffer_Rad_M_f32[2][7]	2.37	
trVel3_PosBuffer_Rad_M_f32[3][0]	2.38	
trVel3_PosBuffer_Rad_M_f32[3][1]	2.39	
trVel3_PosBuffer_Rad_M_f32[3][2]	2.39	
trVel3_PosBuffer_Rad_M_f32[3][3]	2.4	
trVel3_PosBuffer_Rad_M_f32[3][4]	2.41	
trVel3_PosBuffer_Rad_M_f32[3][5]	2.42	
trVel3_PosBuffer_Rad_M_f32[3][6]	2.43	
trVel3_PosBuffer_Rad_M_f32[3][7]	2.44	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	250	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	65352	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	65414	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	65476	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	2	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	64	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	126	
trVel3_TimeBuffer_uS_M_u16p0[0][7]	188	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	1626	
trVel3_TimeBuffer_uS_M_u16p0[1][1]	1688	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	1750	
trVel3_TimeBuffer_uS_M_u16p0[1][3]	1316	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	1378	
trVel3_TimeBuffer_uS_M_u16p0[1][5]	1440	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	1502	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	1564	
trVel3_TimeBuffer_uS_M_u16p0[1][7] trVel3_TimeBuffer_uS_M_u16p0[2][0]	110	
trVel3_TimeBuller_u3_M_u16p0[2][0] trVel3_TimeBulfer_u8_M_u16p0[2][1]	220	
trVel3_TimeBuffer_u3_M_u16p0[2][1]	330	
trVel3_TimeBuffer_uS_M_u16p0[2][3]	440	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	550	
ttrVel3_TimeBuffer_uS_M_u16p0[2][5] ttrVel3_TimeBuffer_uS_M_u16p0[2][6]	660 770	

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Will V CI_1 CI 1			(
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880		
MtrVel_HwVel_HwRadpS_M_f32	10.93737113		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-573.3094931		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	293048		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.13		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	294848		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hwl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times	stamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	294848	294848	✓
tet Mtr\/ol Dord Handubaal\/al HuDadaC 622 value	42	42 . 0.000	-4

T ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

1350

1350

42

1350

42 ± 0.009

42 ± 0.009

1350 ± 0.0625

1350 ± 0.0625

1350 ± 0.0625

Test Step 3.5 (Repeat Count = 1)		~
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.9	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.16	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55	

tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value

tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value

tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value

tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value

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Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.33
MtrVel3_PosBuffer_Rad_M_f32[2][0] MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.46 4.59
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9770
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9832
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9398
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9460
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9522
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9584
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9646
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9708
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3] MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20306 20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	8928
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	8990
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9052
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9114
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9176
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	8742
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	8804
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	8866
MtrVel_HwVel_HwRadpS_M_f32	-5.642567992
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1302.259389
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosSempleTime2_uS_M_u32	3.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32 Rte_Inct_Sa_MtrVel	85754
Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0]	tgt_Rte_Inst_Sa_MtrVel 0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.25
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	87654
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_rtte_met_ea_wit ver.wit ver_rer_weenwith eaz rimestamp_eeee_dez	13

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MtrVel_Per1

Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCF	RF_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMF	RF_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandv	vheelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotor\	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87654	87654	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓
tgt MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	-1350	-1350 ± 0.0625	✓

T .				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

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MtrVel_Init

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -l\$(PROJECTROOT)\NxtrLitb\include -l\$(PROJECTROOT)\StdDef\include -l\$(PROJECTROOT) \MtrVel_Digi\utp\contract -l\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -l\$(PROJECTROOT)\MtrVel_Digi\include -l\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description	/Specification
Name	Text
Module 'MtrVel_Digi'	Name of Tester:Sunita Kore Code File(s) Under Test:Sa_MtrVel.c Code File(s) Under Test:Sa_MtrVel.c Code File(s) Version:5 Module Design Document:MotorVelocity_MDD.doc Module Design Document Version:6 Data Dictionary Version:3 Unit Test Plan Version:3 Unit Test Plan Version:7 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):1394 Total RAM Used (Bytes):54 Special Test Requirements: Test Date: 12/26/2014 Comments: "Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested. Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD. Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350. Note4: In function ""RegressionFit" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"' has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements. Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

MtrVel_Init

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Attributes		
Name	Value	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



Test Case 1: Boundary Check

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles: TS 1.1 6.00 Cycles

VECTOR DESCRIPTION: Description

TS1.1 Output checked

Test Step 1.1 (Repeat Count = 1)				
Name	Actual Value	Expected Value	Result	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~	