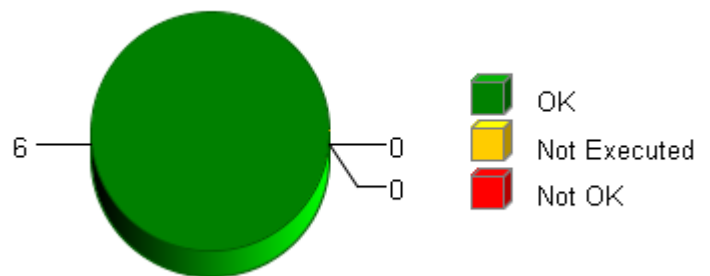## Summary

Total Test Objects: 6
Successful: 6
Failed: 0
Not Executed: 0
Date: 2014-12-26
Time: 16:53:07+0530

Overall Test Object Results (including Coverage)

- OK
- Not Executed
- Not OK

Selected Project Items

Test Object "CBD_UnitTest/MtrVel_Digi/CalcCoarseVel"
Test Object "CBD_UnitTest/MtrVel_Digi/MtrVel_Init"
Test Object "CBD_UnitTest/MtrVel_Digi/MtrVel_Per1"
Test Object "CBD_UnitTest/MtrVel_Digi/MtrVel_Per2"
Test Object "CBD_UnitTest/MtrVel_Digi/MtrVelBlend"
Test Object "CBD_UnitTest/MtrVel_Digi/RegressionFit"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No
Instrumentation: Test Object Only
Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)

- OK
- Not Executed
- Not OK
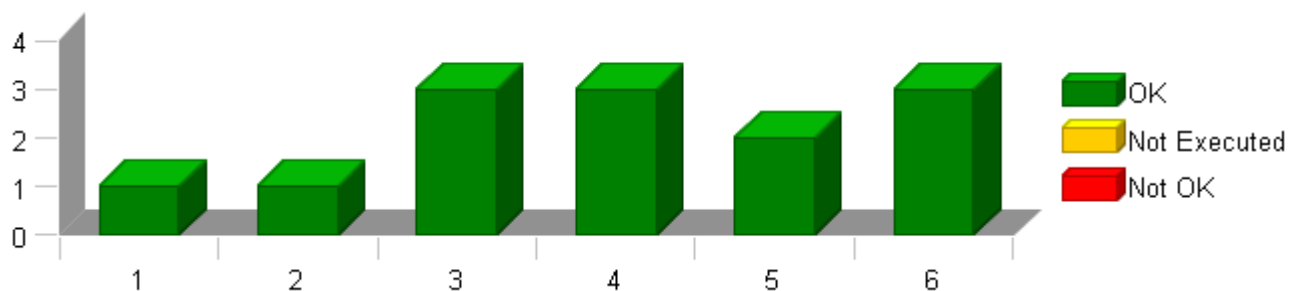
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results

do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).
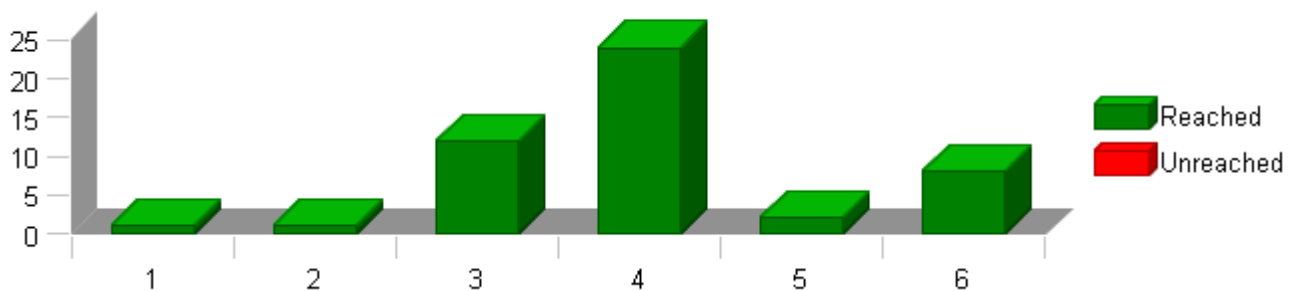
Statement (C0) Coverage: Total Statements for Each Test Object
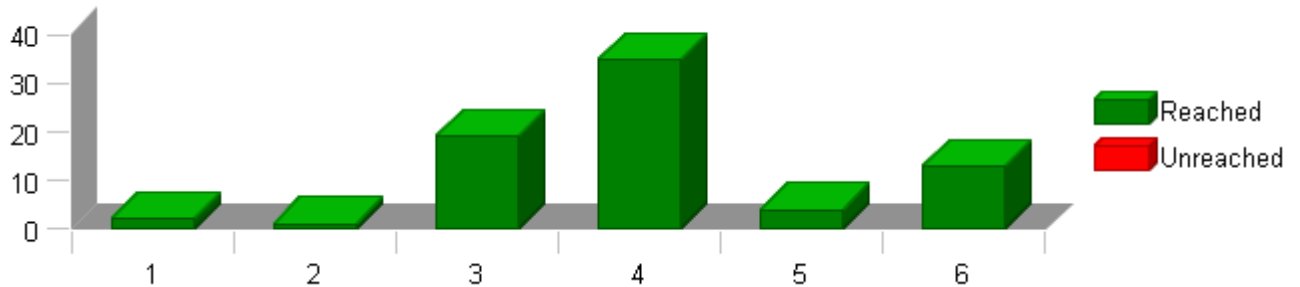
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object
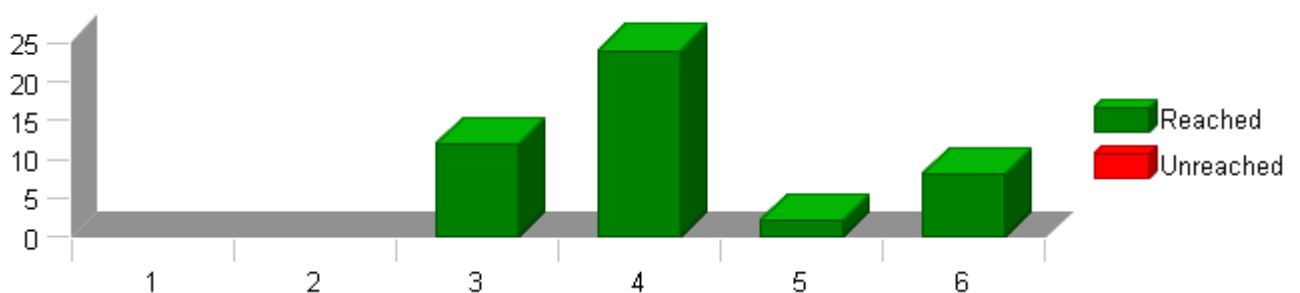
The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

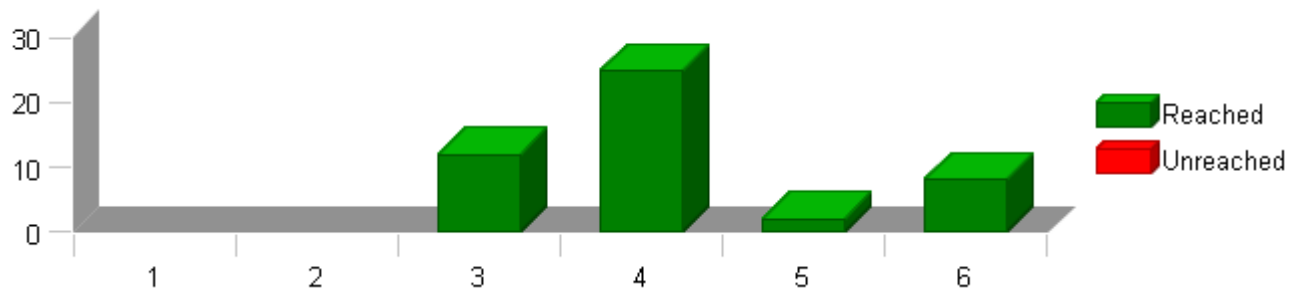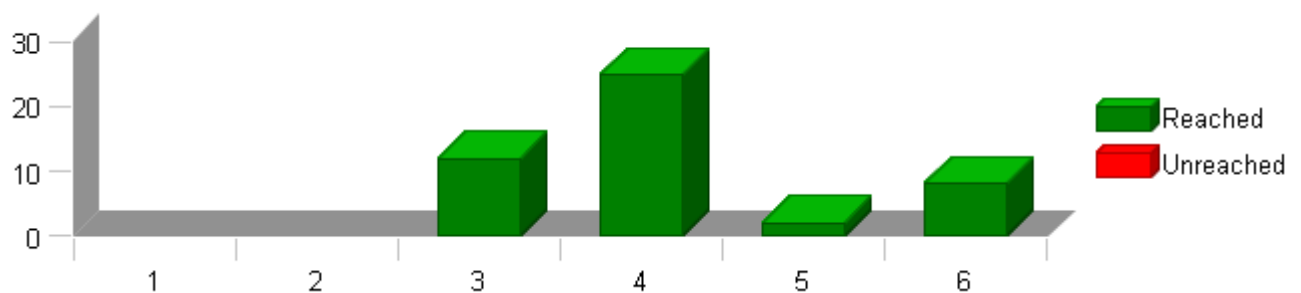## MC/DC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	MtrVel_Digi 30.15 401.89 cm 1 0 0 1 0 0 Tm8.17sult							

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✔
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include

Comments/Description/Specification	
Name	**Text**

Module 'MtrVel_Digi'

```
*****************************************UNIT TEST INFORMATION*****************************************
Name of Tester:Sunita Kore
Code File(s) Under Test:Sa_MtrVel.c
Code File(s) Version:5
Module Design Document:MotorVelocity_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total RAM Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.
```

Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements.

Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

```
*****************************************************************************************************
```

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test ✔

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 2900 Cycles TS 1.2 2725 Cycles

Description	VECTOR DESCRIPTION:
	
	TS 1.1 "Shortest Execution Path:
	if(MtrVelCorrLimDiff_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=False
	if(DiagFailed_m(MtrVel_DiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=False
	if(HwVelCorrLimDiff_MtrRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=True
	(DiagFailed_m(MtrVel_HwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=FALSE
	if(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=False
	if(((TRUE == MtrPosFault1_Cnt_T_lgc)=True) ||(TRUE == MtrPosFault2_Cnt_T_lgc))"
	TS 1.2 "Longest Execution Path:
	(MtrVelCorrLimDiff_MtrRADpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=TRUE
	if(DiagFailed_m(MtrVel_DiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=True
	if(HwVelCorrLimDiff_MtrRADpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=False
	if(DiagFailed_m(MtrVel_HwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=True
	if(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=True
	if((TRUE == MtrPosFault1_Cnt_T_lgc) ||(TRUE == MtrPosFault2_Cnt_T_lgc))=>False"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	116
MtrVel_HwVelDiffAcc_Cnt_M_u16	142
MtrVel_HwVel_HwRadpS_M_f32	-14
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	233
k_HwVelCorrLim_Cnt_Str.PStep	82
k_HwVelCorrLim_Cnt_Str.NStep	19
k_HwVelCorrLim_HwRadpS_f32	-23
k_MtrVelCorrLim_Cnt_Str.Threshold	124
k_MtrVelCorrLim_Cnt_Str.PStep	194
k_MtrVelCorrLim_Cnt_Str.NStep	10
k_MtrVelCorrLim_MtrRadpS_f32	330
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	110
MtrVel_HwVelDiffAcc_Cnt_M_u16	128
MtrVel_HwVel_HwRadpS_M_f32	-32
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	116
k_HwVelCorrLim_Cnt_Str.PStep	19
k_HwVelCorrLim_Cnt_Str.NStep	10
k_HwVelCorrLim_HwRadpS_f32	19
k_MtrVelCorrLim_Cnt_Str.Threshold	2
k_MtrVelCorrLim_Cnt_Str.PStep	77
k_MtrVelCorrLim_Cnt_Str.NStep	1

Name	Input Value
k_MtrVelCorrLim_MtrRadpS_f32	-1110
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	118	118	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

Test Case 2: Boundary Check ✔

Specification

```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 2.1 2801 Cycles
TS 2.2 2744 Cycles
TS 2.3 2573  Cycles
TS 2.4 2572 Cycles
TS 2.5 2573 Cycles
TS 2.6 2575 Cycles
TS 2.7 2573 Cycles
TS 2.8 2572 Cycles
TS 2.9 2621 Cycles
TS 2.10 2602 Cycles
TS 2.11 2701 Cycles
TS 2.12 2644 Cycles
TS 2.13 2635 Cycles
TS 2.14 2687 Cycles
TS 2.15 2644 Cycles
TS 2.16 2643 Cycles
TS 2.17 2623 Cycles
TS 2.18 2625 Cycles
TS 2.19 2623 Cycles
TS 2.20 2622 Cycles
TS 2.21 2686 Cycles
TS 2.22 2622 Cycles
TS 2.23 2614 Cycles
TS 2.24 2625 Cycles
TS 2.25 2645 Cycles
TS 2.26 2657 Cycles
TS 2.27 2639 Cycles
TS 2.28 2657 Cycles
TS 2.29 2598 Cycles
TS 2.30 2595 Cycles
TS 2.31 2596 Cycles
TS 2.32 2660 Cycles
TS 2.33 2623 Cycles
TS 2.34 2622 Cycles
TS 2.35 2623 Cycles
TS 2.36 2622 Cycles
TS 2.37 2625 Cycles
TS 2.38 2622 Cycles
TS 2.39 2603 Cycles
TS 2.40 2628 Cycles
TS 2.41 2623 Cycles
TS 2.42 2622 Cycles
TS 2.43 2600 Cycles
TS 2.44 2690 Cycles
TS 2.45 2632 Cycles
TS 2.46 2631 Cycles
TS 2.47 2632 Cycles
TS 2.48 2631 Cycles
TS 2.49 2634 Cycles
TS 2.50 2631 Cycles
TS 2.51 2736 Cycles
TS 2.52 2631 Cycles
TS 2.53 2632 Cycles
TS 2.54 2667 Cycles
TS 2.55 2652 Cycles
TS 2.56 2613 Cycles
TS 2.57 2616 Cycles
TS 2.58 2668 Cycles
```

```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
```

Description   VECTOR DESCRIPTION:

TS1.1	all min
TS1.2	all max
TS1.3	SysCDiagMtrVelMRF_MtrRadpS_f32=min
TS1.4	SysCDiagMtrVelMRF_MtrRadpS_f32=max
TS1.5	SysCDiagMtrVelMRF_MtrRadpS_f32=mid
TS1.6	SysCDiagMtrVelMRF_MtrRadpS_f32=pos
TS1.7	SysCDiagMtrVelMRF_MtrRadpS_f32=neg
TS1.8	SysCDiagHwVel_HwRadpS_f32=min
TS1.9	SysCDiagHwVel_HwRadpS_f32=max
TS1.10	SysCDiagHwVel_HwRadpS_f32=mid
TS1.11	SysCDiagHwVel_HwRadpS_f32=pos
TS1.12	SysCDiagHwVel_HwRadpS_f32=neg
TS1.13	Rte_Call_NxtrDiagMgr_GetNTCFailed[2]=min
TS1.14	Rte_Call_NxtrDiagMgr_GetNTCFailed[2]=max
TS1.15	MtrVel_MtrVelMRF_MtrRadpS_M_f32=min
TS1.16	MtrVel_MtrVelMRF_MtrRadpS_M_f32=max
TS1.17	MtrVel_MtrVelMRF_MtrRadpS_M_f32=mid
TS1.18	MtrVel_MtrVelMRF_MtrRadpS_M_f32=pos
TS1.19	MtrVel_MtrVelMRF_MtrRadpS_M_f32=neg
TS1.20	k_MtrVelCorrLim_MtrRadpS_f32=min
TS1.21	k_MtrVelCorrLim_MtrRadpS_f32=max
TS1.22	k_MtrVelCorrLim_MtrRadpS_f32=mid
TS1.23	k_MtrVelCorrLim_MtrRadpS_f32=pos
TS1.24	k_MtrVelCorrLim_MtrRadpS_f32=neg
TS1.25	MtrVel_DiffAcc_Cnt_M_u16=min
TS1.26	MtrVel_DiffAcc_Cnt_M_u16=max
TS1.27	MtrVel_DiffAcc_Cnt_M_u16=pos
TS1.28	k_MtrVelCorrLim_Cnt_Str.Threshold=min
TS1.29	k_MtrVelCorrLim_Cnt_Str.Threshold=max
TS1.30	k_MtrVelCorrLim_Cnt_Str.Threshold=pos
TS1.31	k_MtrVelCorrLim_Cnt_Str.Pstep=min
TS1.32	k_MtrVelCorrLim_Cnt_Str.Pstep=max
TS1.33	k_MtrVelCorrLim_Cnt_Str.Pstep=pos
TS1.34	k_MtrVelCorrLim_Cnt_Str.Nstep=min
TS1.35	k_MtrVelCorrLim_Cnt_Str.Nstep=max
TS1.36	k_MtrVelCorrLim_Cnt_Str.Nstep=pos
TS1.37	MtrVel_HwVel_HwRadpS_M_f32=min
TS1.38	MtrVel_HwVel_HwRadpS_M_f32=max
TS1.39	MtrVel_HwVel_HwRadpS_M_f32=mid
TS1.40	MtrVel_HwVel_HwRadpS_M_f32=pos
TS1.41	MtrVel_HwVel_HwRadpS_M_f32=neg
TS1.42	k_HwVelCorrLim_HwRadpS_f32=min
TS1.43	k_HwVelCorrLim_HwRadpS_f32=max
TS1.44	k_HwVelCorrLim_HwRadpS_f32=mid
TS1.45	k_HwVelCorrLim_HwRadpS_f32=pos
TS1.46	k_HwVelCorrLim_HwRadpS_f32=neg
TS1.47	MtrVel_HwVelDiffAcc_Cnt_M_u16=min
TS1.48	MtrVel_HwVelDiffAcc_Cnt_M_u16=max
TS1.49	MtrVel_HwVelDiffAcc_Cnt_M_u16=pos
TS1.50	k_HwVelCorrLim_Cnt_Str.Threshold=min
TS1.51	k_HwVelCorrLim_Cnt_Str.Threshold=max
TS1.52	k_HwVelCorrLim_Cnt_Str.Threshold=pos
TS1.53	k_HwVelCorrLim_Cnt_Str.Pstep=min
TS1.54	k_HwVelCorrLim_Cnt_Str.Pstep=max
TS1.55	k_HwVelCorrLim_Cnt_Str.Pstep=pos
TS1.56	k_HwVelCorrLim_Cnt_Str.Nstep=min
TS1.57	k_HwVelCorrLim_Cnt_Str.Nstep=max
TS1.58	k_HwVelCorrLim_Cnt_Str.Nstep=pos

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	0
MtrVel_HwVelDiffAcc_Cnt_M_u16	0
MtrVel_HwVel_HwRadpS_M_f32	-42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	0
k_HwVelCorrLim_Cnt_Str.PStep	0
k_HwVelCorrLim_Cnt_Str.NStep	0
k_HwVelCorrLim_HwRadpS_f32	-32
k_MtrVelCorrLim_Cnt_Str.Threshold	0
k_MtrVelCorrLim_Cnt_Str.PStep	0
k_MtrVelCorrLim_Cnt_Str.NStep	0
k_MtrVelCorrLim_MtrRadpS_f32	-1118
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.2 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	255
MtrVel_HwVelDiffAcc_Cnt_M_u16	255
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	255
k_HwVelCorrLim_Cnt_Str.PStep	255
k_HwVelCorrLim_Cnt_Str.NStep	20
k_HwVelCorrLim_HwRadpS_f32	32
k_MtrVelCorrLim_Cnt_Str.Threshold	255
k_MtrVelCorrLim_Cnt_Str.PStep	255
k_MtrVelCorrLim_Cnt_Str.NStep	20
k_MtrVelCorrLim_MtrRadpS_f32	1118
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	235	235	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	235	235	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	10
MtrVel_HwVelDiffAcc_Cnt_M_u16	25
MtrVel_HwVel_HwRadpS_M_f32	-32
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	116
k_HwVelCorrLim_Cnt_Str.PStep	19
k_HwVelCorrLim_Cnt_Str.NStep	10
k_HwVelCorrLim_HwRadpS_f32	19
k_MtrVelCorrLim_Cnt_Str.Threshold	2
k_MtrVelCorrLim_Cnt_Str.PStep	77
k_MtrVelCorrLim_Cnt_Str.NStep	1
k_MtrVelCorrLim_MtrRadpS_f32	-1110
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	✔

Name	Actual Value	Expected Value	Result
MtrVel_HwVelDiffAcc_Cnt_M_u16	15	15	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	12
MtrVel_HwVelDiffAcc_Cnt_M_u16	38
MtrVel_HwVel_HwRadpS_M_f32	-30
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-960
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	129
k_HwVelCorrLim_Cnt_Str.PStep	26
k_HwVelCorrLim_Cnt_Str.NStep	11
k_HwVelCorrLim_HwRadpS_f32	21
k_MtrVelCorrLim_Cnt_Str.Threshold	5
k_MtrVelCorrLim_Cnt_Str.PStep	90
k_MtrVelCorrLim_Cnt_Str.NStep	2
k_MtrVelCorrLim_MtrRadpS_f32	-950
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-39
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	5	5	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	25
MtrVel_HwVelDiffAcc_Cnt_M_u16	51
MtrVel_HwVel_HwRadpS_M_f32	-28
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-920
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	142
k_HwVelCorrLim_Cnt_Str.PStep	33
k_HwVelCorrLim_Cnt_Str.NStep	12
k_HwVelCorrLim_HwRadpS_f32	23.4500008
k_MtrVelCorrLim_Cnt_Str.Threshold	9
k_MtrVelCorrLim_Cnt_Str.PStep	103
k_MtrVelCorrLim_Cnt_Str.NStep	3
k_MtrVelCorrLim_MtrRadpS_f32	-790
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-37.5499992
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc

Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	9	9	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	39	39	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.6 (Repeat Count = 1)   ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	38		
MtrVel_HwVelDiffAcc_Cnt_M_u16	64		
MtrVel_HwVel_HwRadpS_M_f32	-26		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-330.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	155		
k_HwVelCorrLim_Cnt_Str.PStep	40		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	25		
k_MtrVelCorrLim_Cnt_Str.Threshold	19		
k_MtrVelCorrLim_Cnt_Str.PStep	116		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	-630.340027		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-35		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1000.5		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	19	19	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	51	51	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.7 (Repeat Count = 1)   ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	51
MtrVel_HwVelDiffAcc_Cnt_M_u16	77
MtrVel_HwVel_HwRadpS_M_f32	-24.5599995
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-840
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	168
k_HwVelCorrLim_Cnt_Str.PStep	47
k_HwVelCorrLim_Cnt_Str.NStep	14
k_HwVelCorrLim_HwRadpS_f32	27
k_MtrVelCorrLim_Cnt_Str.Threshold	26
k_MtrVelCorrLim_Cnt_Str.PStep	129
k_MtrVelCorrLim_Cnt_Str.NStep	5

Name	Input Value		
k_MtrVelCorrLim_MtrRadpS_f32	-470		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-33		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1000.5		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	26	26	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	63	63	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T			✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	64		
MtrVel_HwVelDiffAcc_Cnt_M_u16	90		
MtrVel_HwVel_HwRadpS_M_f32	-22		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-800		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	181		
k_HwVelCorrLim_Cnt_Str.PStep	54		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	29		
k_MtrVelCorrLim_Cnt_Str.Threshold	33		
k_MtrVelCorrLim_Cnt_Str.PStep	142		
k_MtrVelCorrLim_Cnt_Str.NStep	6		
k_MtrVelCorrLim_MtrRadpS_f32	-310		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	33	33	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	75	75	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T			✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.9 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	77
MtrVel_HwVelDiffAcc_Cnt_M_u16	103
MtrVel_HwVel_HwRadpS_M_f32	-20
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-760
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	194
k_HwVelCorrLim_Cnt_Str.PStep	61
k_HwVelCorrLim_Cnt_Str.NStep	16

Name	Input Value		
k_HwVelCorrLim_HwRadpS_f32	31		
k_MtrVelCorrLim_Cnt_Str.Threshold	40		
k_MtrVelCorrLim_Cnt_Str.PStep	155		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-150.669998		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-870		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	40	40	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	164	164	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)   ✓

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	90		
MtrVel_HwVelDiffAcc_Cnt_M_u16	116		
MtrVel_HwVel_HwRadpS_M_f32	-18.5400009		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-720.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	207		
k_HwVelCorrLim_Cnt_Str.PStep	68		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-25.4500008		
k_MtrVelCorrLim_Cnt_Str.Threshold	47		
k_MtrVelCorrLim_Cnt_Str.PStep	168		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-790		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	47	47	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	184	184	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)   ✓

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	103
MtrVel_HwVelDiffAcc_Cnt_M_u16	129
MtrVel_HwVel_HwRadpS_M_f32	-16
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-680

Name	Input Value
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	220
k_HwVelCorrLim_Cnt_Str.PStep	75
k_HwVelCorrLim_Cnt_Str.NStep	18
k_HwVelCorrLim_HwRadpS_f32	-24
k_MtrVelCorrLim_Cnt_Str.Threshold	54
k_MtrVelCorrLim_Cnt_Str.PStep	181
k_MtrVelCorrLim_Cnt_Str.NStep	9
k_MtrVelCorrLim_MtrRadpS_f32	170
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20.5
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-710.869995
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	94	94	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.12 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	116
MtrVel_HwVelDiffAcc_Cnt_M_u16	142
MtrVel_HwVel_HwRadpS_M_f32	-14
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	233
k_HwVelCorrLim_Cnt_Str.PStep	82
k_HwVelCorrLim_Cnt_Str.NStep	19
k_HwVelCorrLim_HwRadpS_f32	-23
k_MtrVelCorrLim_Cnt_Str.Threshold	61
k_MtrVelCorrLim_Cnt_Str.PStep	194
k_MtrVelCorrLim_Cnt_Str.NStep	10
k_MtrVelCorrLim_MtrRadpS_f32	330
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20.5
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.13 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	129
MtrVel_HwVelDiffAcc_Cnt_M_u16	155
MtrVel_HwVel_HwRadpS_M_f32	-12
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-600
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	246
k_HwVelCorrLim_Cnt_Str.PStep	89
k_HwVelCorrLim_Cnt_Str.NStep	20
k_HwVelCorrLim_HwRadpS_f32	-22
k_MtrVelCorrLim_Cnt_Str.Threshold	68
k_MtrVelCorrLim_Cnt_Str.PStep	207
k_MtrVelCorrLim_Cnt_Str.NStep	11
k_MtrVelCorrLim_MtrRadpS_f32	490
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	32.8800011
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-550
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	118	118	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	244	244	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.14 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	142
MtrVel_HwVelDiffAcc_Cnt_M_u16	168
MtrVel_HwVel_HwRadpS_M_f32	-10.3400002
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-560.450012
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	250
k_HwVelCorrLim_Cnt_Str.PStep	96
k_HwVelCorrLim_Cnt_Str.NStep	0
k_HwVelCorrLim_HwRadpS_f32	-21.4500008
k_MtrVelCorrLim_Cnt_Str.Threshold	75
k_MtrVelCorrLim_Cnt_Str.PStep	220
k_MtrVelCorrLim_Cnt_Str.NStep	12
k_MtrVelCorrLim_MtrRadpS_f32	650.669983
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-470.660004
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	130	130	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	250	250	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.15 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	155		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	-8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	251		
k_HwVelCorrLim_Cnt_Str.PStep	103		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-20		
k_MtrVelCorrLim_Cnt_Str.Threshold	82		
k_MtrVelCorrLim_Cnt_Str.PStep	233		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	810		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-28		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-390		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	82	82	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	251	251	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.16 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	168		
MtrVel_HwVelDiffAcc_Cnt_M_u16	194		
MtrVel_HwVel_HwRadpS_M_f32	-6		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	110		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-19		
k_MtrVelCorrLim_Cnt_Str.Threshold	89		
k_MtrVelCorrLim_Cnt_Str.PStep	246		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	970		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-26		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-310		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	89	89	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.17 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	181
MtrVel_HwVelDiffAcc_Cnt_M_u16	207
MtrVel_HwVel_HwRadpS_M_f32	-4
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	12
k_HwVelCorrLim_Cnt_Str.PStep	117
k_HwVelCorrLim_Cnt_Str.NStep	3
k_HwVelCorrLim_HwRadpS_f32	-18
k_MtrVelCorrLim_Cnt_Str.Threshold	96
k_MtrVelCorrLim_Cnt_Str.PStep	250
k_MtrVelCorrLim_Cnt_Str.NStep	15
k_MtrVelCorrLim_MtrRadpS_f32	10
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-24
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-230
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	96	96	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	12	12	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.18 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	194
MtrVel_HwVelDiffAcc_Cnt_M_u16	220
MtrVel_HwVel_HwRadpS_M_f32	-2.33999991
MtrVel_MtrVelMRF_MtrRadpS_M_f32	500.799988
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	25
k_HwVelCorrLim_Cnt_Str.PStep	124
k_HwVelCorrLim_Cnt_Str.NStep	5
k_HwVelCorrLim_HwRadpS_f32	-17.6539993
k_MtrVelCorrLim_Cnt_Str.Threshold	103
k_MtrVelCorrLim_Cnt_Str.PStep	251
k_MtrVelCorrLim_Cnt_Str.NStep	16
k_MtrVelCorrLim_MtrRadpS_f32	20.5599995
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-22.3400002
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-150
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	103	103	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	25	25	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.19 (Repeat Count = 1)   ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	207
MtrVel_HwVelDiffAcc_Cnt_M_u16	233
MtrVel_HwVel_HwRadpS_M_f32	0
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-500.700012
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	38
k_HwVelCorrLim_Cnt_Str.PStep	131
k_HwVelCorrLim_Cnt_Str.NStep	7
k_HwVelCorrLim_HwRadpS_f32	-16
k_MtrVelCorrLim_Cnt_Str.Threshold	110
k_MtrVelCorrLim_Cnt_Str.PStep	10
k_MtrVelCorrLim_Cnt_Str.NStep	17
k_MtrVelCorrLim_MtrRadpS_f32	30
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-70.4000015
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	110	110	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	38	38	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.20 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	220
MtrVel_HwVelDiffAcc_Cnt_M_u16	246
MtrVel_HwVel_HwRadpS_M_f32	2
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-200
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	51
k_HwVelCorrLim_Cnt_Str.PStep	138
k_HwVelCorrLim_Cnt_Str.NStep	9
k_HwVelCorrLim_HwRadpS_f32	-15
k_MtrVelCorrLim_Cnt_Str.Threshold	117
k_MtrVelCorrLim_Cnt_Str.PStep	12
k_MtrVelCorrLim_Cnt_Str.NStep	18
k_MtrVelCorrLim_MtrRadpS_f32	-1118
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-18
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	10
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	117	117	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	51	51	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.21 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	233
MtrVel_HwVelDiffAcc_Cnt_M_u16	250
MtrVel_HwVel_HwRadpS_M_f32	4
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-100.339996
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	64
k_HwVelCorrLim_Cnt_Str.PStep	145
k_HwVelCorrLim_Cnt_Str.NStep	11
k_HwVelCorrLim_HwRadpS_f32	-14
k_MtrVelCorrLim_Cnt_Str.Threshold	124
k_MtrVelCorrLim_Cnt_Str.PStep	25
k_MtrVelCorrLim_Cnt_Str.NStep	19
k_MtrVelCorrLim_MtrRadpS_f32	1118
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-16
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	90
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	214	214	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	64	64	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.22 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	246		
MtrVel_HwVelDiffAcc_Cnt_M_u16	251		
MtrVel_HwVel_HwRadpS_M_f32	6.67000008		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-50		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	16		
k_HwVelCorrLim_Cnt_Str.PStep	152		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-13		
k_MtrVelCorrLim_Cnt_Str.Threshold	131		
k_MtrVelCorrLim_Cnt_Str.PStep	38		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	0		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-14		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	170.759995		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	131	131	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	16	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.23 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	250		
MtrVel_HwVelDiffAcc_Cnt_M_u16	169		
MtrVel_HwVel_HwRadpS_M_f32	8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	159		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	-12.4499998		
k_MtrVelCorrLim_Cnt_Str.Threshold	138		
k_MtrVelCorrLim_Cnt_Str.PStep	51		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	250.199997		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	250	250	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	28	28	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.24 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrVel_DiffAcc_Cnt_M_u16	251		
MtrVel_HwVelDiffAcc_Cnt_M_u16	172		
MtrVel_HwVel_HwRadpS_M_f32	10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	50		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	32		
k_HwVelCorrLim_Cnt_Str.PStep	166		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-11		
k_MtrVelCorrLim_Cnt_Str.Threshold	145		
k_MtrVelCorrLim_Cnt_Str.PStep	64		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	-250.800003		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-10.9499998		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	330		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	145	145	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	32	32	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.25 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	175		
MtrVel_HwVel_HwRadpS_M_f32	12.3999996		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	100		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	5		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-10		
k_MtrVelCorrLim_Cnt_Str.Threshold	152		
k_MtrVelCorrLim_Cnt_Str.PStep	16		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	40		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-8		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	16	16	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.26 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel_DiffAcc_Cnt_M_u16	255	
MtrVel_HwVelDiffAcc_Cnt_M_u16	178	
MtrVel_HwVel_HwRadpS_M_f32	14	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	150	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	68	
k_HwVelCorrLim_Cnt_Str.PStep	9	
k_HwVelCorrLim_Cnt_Str.NStep	2	
k_HwVelCorrLim_HwRadpS_f32	-9	
k_MtrVelCorrLim_Cnt_Str.Threshold	159	
k_MtrVelCorrLim_Cnt_Str.PStep	28	
k_MtrVelCorrLim_Cnt_Str.NStep	3	
k_MtrVelCorrLim_MtrRadpS_f32	55.6699982	
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-6	
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	490	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	159	159	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	68	68	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.27 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	120
MtrVel_HwVelDiffAcc_Cnt_M_u16	181
MtrVel_HwVel_HwRadpS_M_f32	16
MtrVel_MtrVelMRF_MtrRadpS_M_f32	200.449997
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	86
k_HwVelCorrLim_Cnt_Str.PStep	13
k_HwVelCorrLim_Cnt_Str.NStep	3
k_HwVelCorrLim_HwRadpS_f32	-8
k_MtrVelCorrLim_Cnt_Str.Threshold	166
k_MtrVelCorrLim_Cnt_Str.PStep	32
k_MtrVelCorrLim_Cnt_Str.NStep	5
k_MtrVelCorrLim_MtrRadpS_f32	68
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-4
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	570.880005
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	152	152	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	86	86	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.28 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	16
MtrVel_HwVelDiffAcc_Cnt_M_u16	184
MtrVel_HwVel_HwRadpS_M_f32	18
MtrVel_MtrVelMRF_MtrRadpS_M_f32	250
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	16
k_HwVelCorrLim_Cnt_Str.PStep	17
k_HwVelCorrLim_Cnt_Str.NStep	4
k_HwVelCorrLim_HwRadpS_f32	-7
k_MtrVelCorrLim_Cnt_Str.Threshold	0
k_MtrVelCorrLim_Cnt_Str.PStep	50
k_MtrVelCorrLim_Cnt_Str.NStep	7
k_MtrVelCorrLim_MtrRadpS_f32	79
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-2.3499999
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	650
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	16	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.29 (Repeat Count = 1)   ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	28		
MtrVel_HwVelDiffAcc_Cnt_M_u16	187		
MtrVel_HwVel_HwRadpS_M_f32	20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	300		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	21		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-6.78000021		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	68		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	112		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	730		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	96	96	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	28	28	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.30 (Repeat Count = 1)   ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	32		
MtrVel_HwVelDiffAcc_Cnt_M_u16	190		
MtrVel_HwVel_HwRadpS_M_f32	22		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	350.779999		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	32		
k_HwVelCorrLim_Cnt_Str.PStep	25		
k_HwVelCorrLim_Cnt_Str.NStep	6		
k_HwVelCorrLim_HwRadpS_f32	-5		
k_MtrVelCorrLim_Cnt_Str.Threshold	120		
k_MtrVelCorrLim_Cnt_Str.PStep	86		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	145		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	2		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	810		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	118	118	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	32	32	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.31 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	50		
MtrVel_HwVelDiffAcc_Cnt_M_u16	193		
MtrVel_HwVel_HwRadpS_M_f32	24.5599995		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	400		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	29		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	-4		
k_MtrVelCorrLim_Cnt_Str.Threshold	169		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	178		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	4		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	50	50	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.32 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	68		
MtrVel_HwVelDiffAcc_Cnt_M_u16	196		
MtrVel_HwVel_HwRadpS_M_f32	26		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	450		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	68		
k_HwVelCorrLim_Cnt_Str.PStep	33		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	-3		
k_MtrVelCorrLim_Cnt_Str.Threshold	172		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	211.654999		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	6		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	970		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	172	172	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	68	68	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.33 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	86
MtrVel_HwVelDiffAcc_Cnt_M_u16	199
MtrVel_HwVel_HwRadpS_M_f32	28
MtrVel_MtrVelMRF_MtrRadpS_M_f32	500
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	86
k_HwVelCorrLim_Cnt_Str.PStep	37
k_HwVelCorrLim_Cnt_Str.NStep	9
k_HwVelCorrLim_HwRadpS_f32	-2
k_MtrVelCorrLim_Cnt_Str.Threshold	175
k_MtrVelCorrLim_Cnt_Str.PStep	100
k_MtrVelCorrLim_Cnt_Str.NStep	17
k_MtrVelCorrLim_MtrRadpS_f32	244
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	8.43999958
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1050.88
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	175	175	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	86	86	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.34 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	104
MtrVel_HwVelDiffAcc_Cnt_M_u16	202
MtrVel_HwVel_HwRadpS_M_f32	30
MtrVel_MtrVelMRF_MtrRadpS_M_f32	550
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	104
k_HwVelCorrLim_Cnt_Str.PStep	41
k_HwVelCorrLim_Cnt_Str.NStep	13
k_HwVelCorrLim_HwRadpS_f32	-1.34000003
k_MtrVelCorrLim_Cnt_Str.Threshold	178
k_MtrVelCorrLim_Cnt_Str.PStep	169
k_MtrVelCorrLim_Cnt_Str.NStep	0
k_MtrVelCorrLim_MtrRadpS_f32	277
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	10
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1130
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	178	178	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	104	104	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.35 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	122
MtrVel_HwVelDiffAcc_Cnt_M_u16	205
MtrVel_HwVel_HwRadpS_M_f32	32
MtrVel_MtrVelMRF_MtrRadpS_M_f32	600.559998
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	122
k_HwVelCorrLim_Cnt_Str.PStep	45
k_HwVelCorrLim_Cnt_Str.NStep	14
k_HwVelCorrLim_HwRadpS_f32	0
k_MtrVelCorrLim_Cnt_Str.Threshold	181
k_MtrVelCorrLim_Cnt_Str.PStep	172
k_MtrVelCorrLim_Cnt_Str.NStep	20
k_MtrVelCorrLim_MtrRadpS_f32	310.559998
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1210
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	181	181	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	122	122	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.36 (Repeat Count = 1) ✔

Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	140	
MtrVel_HwVelDiffAcc_Cnt_M_u16	208	
MtrVel_HwVel_ HwRadpS_M_f32 4	34.5600014 58	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	650	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	140	
k_HwVelCorrLim_Cnt_Str.PStep	49	
k_HwVelCorrLim_Cnt_Str.NStep	15	
k_HwVelCorrLim_HwRadpS_f32	1	
k_MtrVelCorrLim_Cnt_Str.Threshold	184	
k_MtrVelCorrLim_Cnt_Str.PStep	175	
k_MtrVelCorrLim_Cnt_Str.NStep	10	
k_MtrVelCorrLim_MtrRadpS_f32	343	
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	14	
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1290	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	184	184	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	140	140	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.37 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	158
MtrVel_HwVelDiffAcc_Cnt_M_u16	

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.38 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	176		
MtrVel_HwVelDiffAcc_Cnt_M_u16	214		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	750		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	176		
k_HwVelCorrLim_Cnt_Str.PStep	57		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	3		
k_MtrVelCorrLim_Cnt_Str.Threshold	190		
k_MtrVelCorrLim_Cnt_Str.PStep	181		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	409		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	18		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	110		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	190	190	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	176	176	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.39 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	194		
MtrVel_HwVelDiffAcc_Cnt_M_u16	5		
MtrVel_HwVel_HwRadpS_M_f32	0		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	800.650024		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	194		
k_HwVelCorrLim_Cnt_Str.PStep	61		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	4.55999994		
k_MtrVelCorrLim_Cnt_Str.Threshold	193		
k_MtrVelCorrLim_Cnt_Str.PStep	184		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	442		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20.9799995		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	170		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	193	193	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	66	66	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.40 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel_DiffAcc_Cnt_M_u16	212	
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	
MtrVel_HwVel_HwRadpS_M_f32	20.1000004	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	850	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	212	
k_HwVelCorrLim_Cnt_Str.PStep	65	
k_HwVelCorrLim_Cnt_Str.NStep	19	
k_HwVelCorrLim_HwRadpS_f32	5	
k_MtrVelCorrLim_Cnt_Str.Threshold	196	
k_MtrVelCorrLim_Cnt_Str.PStep	187	
k_MtrVelCorrLim_Cnt_Str.NStep	16	
k_MtrVelCorrLim_MtrRadpS_f32	475	
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	22	
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	230	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	196	196	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.41 (Repeat Count = 1)

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	230
MtrVel_HwVelDiffAcc_Cnt_M_u16	27
MtrVel_HwVel_HwRadpS_M_f32	-20.8999996
MtrVel_MtrVelMRF_MtrRadpS_M_f32	900
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	3
k_HwVelCorrLim_Cnt_Str.PStep	4
k_HwVelCorrLim_Cnt_Str.NStep	20
k_HwVelCorrLim_HwRadpS_f32	6
k_MtrVelCorrLim_Cnt_Str.Threshold	199
k_MtrVelCorrLim_Cnt_Str.PStep	190
k_MtrVelCorrLim_Cnt_Str.NStep	17
k_MtrVelCorrLim_MtrRadpS_f32	508.649994
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	24
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	290.033997
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	199	199	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.42 (Repeat Count = 1)

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	248
MtrVel_HwVelDiffAcc_Cnt_M_u16	38
MtrVel_HwVel_HwRadpS_M_f32	-41.2400017
MtrVel_MtrVelMRF_MtrRadpS_M_f32	950
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	7
k_HwVelCorrLim_Cnt_Str.PStep	19
k_HwVelCorrLim_Cnt_Str.NStep	0
k_HwVelCorrLim_HwRadpS_f32	-32
k_MtrVelCorrLim_Cnt_Str.Threshold	202
k_MtrVelCorrLim_Cnt_Str.PStep	193
k_MtrVelCorrLim_Cnt_Str.NStep	18
k_MtrVelCorrLim_MtrRadpS_f32	541
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	26
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	202	202	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	7	7	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.43 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	5
MtrVel_HwVelDiffAcc_Cnt_M_u16	49
MtrVel_HwVel_HwRadpS_M_f32	-39
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1000.45001
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	9
k_HwVelCorrLim_Cnt_Str.PStep	34
k_HwVelCorrLim_Cnt_Str.NStep	10
k_HwVelCorrLim_HwRadpS_f32	32
k_MtrVelCorrLim_Cnt_Str.Threshold	205
k_MtrVelCorrLim_Cnt_Str.PStep	196
k_MtrVelCorrLim_Cnt_Str.NStep	19
k_MtrVelCorrLim_MtrRadpS_f32	574
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	28.4500008
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	201	201	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	9	9	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.44 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	9
MtrVel_HwVelDiffAcc_Cnt_M_u16	60
MtrVel_HwVel_HwRadpS_M_f32	-37
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1050
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	21
k_HwVelCorrLim_Cnt_Str.PStep	49
k_HwVelCorrLim_Cnt_Str.NStep	1
k_HwVelCorrLim_HwRadpS_f32	0
k_MtrVelCorrLim_Cnt_Str.Threshold	208
k_MtrVelCorrLim_Cnt_Str.PStep	199
k_MtrVelCorrLim_Cnt_Str.NStep	20
k_MtrVelCorrLim_MtrRadpS_f32	607
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	30
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	470
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	21	21	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.45 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	13		
MtrVel_HwVelDiffAcc_Cnt_M_u16	71		
MtrVel_HwVel_HwRadpS_M_f32	-35		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1100		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	33		
k_HwVelCorrLim_Cnt_Str.PStep	64		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	10.6000004		
k_MtrVelCorrLim_Cnt_Str.Threshold	211		
k_MtrVelCorrLim_Cnt_Str.PStep	202		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	640		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	32		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	590.887024		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	13	13	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	33	33	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.46 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	17		
MtrVel_HwVelDiffAcc_Cnt_M_u16	82		
MtrVel_HwVel_HwRadpS_M_f32	-33		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1150		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	45		
k_HwVelCorrLim_Cnt_Str.PStep	79		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-10.3000002		
k_MtrVelCorrLim_Cnt_Str.Threshold	214		
k_MtrVelCorrLim_Cnt_Str.PStep	205		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	767.650024		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	34		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	590		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	7	7	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	45	45	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.47 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	21
MtrVel_HwVelDiffAcc_Cnt_M_u16	0
MtrVel_HwVel_HwRadpS_M_f32	13
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1200.44995
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	57
k_HwVelCorrLim_Cnt_Str.PStep	94
k_HwVelCorrLim_Cnt_Str.NStep	7
k_HwVelCorrLim_HwRadpS_f32	7
k_MtrVelCorrLim_Cnt_Str.Threshold	217
k_MtrVelCorrLim_Cnt_Str.PStep	208
k_MtrVelCorrLim_Cnt_Str.NStep	1
k_MtrVelCorrLim_MtrRadpS_f32	706
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	36
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	650
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	20	20	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	57	57	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.48 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	25
MtrVel_HwVelDiffAcc_Cnt_M_u16	255
MtrVel_HwVel_HwRadpS_M_f32	12.4499998
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1250
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	69
k_HwVelCorrLim_Cnt_Str.PStep	109
k_HwVelCorrLim_Cnt_Str.NStep	9
k_HwVelCorrLim_HwRadpS_f32	8
k_MtrVelCorrLim_Cnt_Str.Threshold	220
k_MtrVelCorrLim_Cnt_Str.PStep	211
k_MtrVelCorrLim_Cnt_Str.NStep	3
k_MtrVelCorrLim_MtrRadpS_f32	739
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	38.4399986
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	710
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	22	22	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	69	69	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.49 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	29
MtrVel_HwVelDiffAcc_Cnt_M_u16	120
MtrVel_HwVel_HwRadpS_M_f32	11
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1300
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	81
k_HwVelCorrLim_Cnt_Str.PStep	124
k_HwVelCorrLim_Cnt_Str.NStep	11
k_HwVelCorrLim_HwRadpS_f32	9.97000027
k_MtrVelCorrLim_Cnt_Str.Threshold	223
k_MtrVelCorrLim_Cnt_Str.PStep	214
k_MtrVelCorrLim_Cnt_Str.NStep	5
k_MtrVelCorrLim_MtrRadpS_f32	772
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	40
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	770.653992
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	24	24	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	81	81	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.50 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	33		
MtrVel_HwVelDiffAcc_Cnt_M_u16	19		
MtrVel_HwVel_HwRadpS_M_f32	10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	25.6700001		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	139		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	10		
k_MtrVelCorrLim_Cnt_Str.Threshold	226		
k_MtrVelCorrLim_Cnt_Str.PStep	217		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	805		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	830		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	26	26	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 2.51 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	37		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	9		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	75		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	154		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	11		
k_MtrVelCorrLim_Cnt_Str.Threshold	229		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	838.450012		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	28	28	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	179	179	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.52 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	41		
MtrVel_HwVelDiffAcc_Cnt_M_u16	31		
MtrVel_HwVel_HwRadpS_M_f32	5.55999994		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	125		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	169		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	12		
k_MtrVelCorrLim_Cnt_Str.Threshold	232		
k_MtrVelCorrLim_Cnt_Str.PStep	223		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	871		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	30	30	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	100	100	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.53 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	45		
MtrVel_HwVelDiffAcc_Cnt_M_u16	37		
MtrVel_HwVel_HwRadpS_M_f32	7		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	175.759995		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	13.75		
k_MtrVelCorrLim_Cnt_Str.Threshold	235		
k_MtrVelCorrLim_Cnt_Str.PStep	226		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	904		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-40.5499992		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1010		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	27	27	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T			✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.54 (Repeat Count = 1)		✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	49
MtrVel_HwVelDiffAcc_Cnt_M_u16	43
MtrVel_HwVel_HwRadpS_M_f32	4
MtrVel_MtrVelMRF_MtrRadpS_M_f32	225
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	15
k_HwVelCorrLim_Cnt_Str.PStep	255
k_HwVelCorrLim_Cnt_Str.NStep	16
k_HwVelCorrLim_HwRadpS_f32	14
k_MtrVelCorrLim_Cnt_Str.Threshold	238
k_MtrVelCorrLim_Cnt_Str.PStep	229
k_MtrVelCorrLim_Cnt_Str.NStep	20
k_MtrVelCorrLim_MtrRadpS_f32	937.450012
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	9
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1070
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	29	29	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T			✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.55 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	53
MtrVel_HwVelDiffAcc_Cnt_M_u16	49
MtrVel_HwVel_HwRadpS_M_f32	3.55999994
MtrVel_MtrVelMRF_MtrRadpS_M_f32	275
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	28
k_HwVelCorrLim_Cnt_Str.PStep	50
k_HwVelCorrLim_Cnt_Str.NStep	12
k_HwVelCorrLim_HwRadpS_f32	15
k_MtrVelCorrLim_Cnt_Str.Threshold	241
k_MtrVelCorrLim_Cnt_Str.PStep	232
k_MtrVelCorrLim_Cnt_Str.NStep	4
k_MtrVelCorrLim_MtrRadpS_f32	970
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	10
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1130
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	49	49	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	37	37	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	Count	Expected Function		Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.56 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	57
MtrVel_HwVelDiffAcc_Cnt_M_u16	55
MtrVel_HwVel_HwRadpS_M_f32	2
MtrVel_MtrVelMRF_MtrRadpS_M_f32	325.670013
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	41
k_HwVelCorrLim_Cnt_Str.PStep	16
k_HwVelCorrLim_Cnt_Str.NStep	0
k_HwVelCorrLim_HwRadpS_f32	16.8700008
k_MtrVelCorrLim_Cnt_Str.Threshold	12
k_MtrVelCorrLim_Cnt_Str.PStep	235
k_MtrVelCorrLim_Cnt_Str.NStep	8
k_MtrVelCorrLim_MtrRadpS_f32	1003
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	11.4499998
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1190.87
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	49	49	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	55	55	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	Count	Expected Function		Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 2.57 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	61
MtrVel_HwVelDiffAcc_Cnt_M_u16	61
MtrVel_HwVel_HwRadpS_M_f32	1
MtrVel_MtrVelMRF_MtrRadpS_M_f32	375
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	54
k_HwVelCorrLim_Cnt_Str.PStep	22
k_HwVelCorrLim_Cnt_Str.NStep	20
k_HwVelCorrLim_HwRadpS_f32	17
k_MtrVelCorrLim_Cnt_Str.Threshold	18
k_MtrVelCorrLim_Cnt_Str.PStep	238
k_MtrVelCorrLim_Cnt_Str.NStep	16
k_MtrVelCorrLim_MtrRadpS_f32	1036.56006
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1250
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	45	45	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	41	41	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 2.58 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	65
MtrVel_HwVelDiffAcc_Cnt_M_u16	67
MtrVel_HwVel_HwRadpS_M_f32	15.4300003
MtrVel_MtrVelMRF_MtrRadpS_M_f32	425
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	67
k_HwVelCorrLim_Cnt_Str.PStep	37
k_HwVelCorrLim_Cnt_Str.NStep	10
k_HwVelCorrLim_HwRadpS_f32	18
k_MtrVelCorrLim_Cnt_Str.Threshold	66
k_MtrVelCorrLim_Cnt_Str.PStep	241
k_MtrVelCorrLim_Cnt_Str.NStep	12
k_MtrVelCorrLim_MtrRadpS_f32	1069
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	13
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1310
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	53	53	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	57	57	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Case 3: Path Check ✔

Specification

```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 3.1 2871 Cycles
TS 3.2 2822 Cycles
TS 3.3 2770 Cycles
TS 3.4 2726 Cycles
TS 3.5 2759 Cycles
TS 3.6 2756 Cycles
TS 3.7 2685 Cycles
TS 3.8 2722 Cycles
TS 3.9 2791 Cycles
TS 3.10 2709 Cycles
TS 3.11 2736 Cycles
TS 3.12 2736 Cycles
TS 3.13 2692 Cycles
TS 3.14 2737 Cycles
TS 3.15 2728 Cycles
TS 3.16 2759 Cycles
```

Description

VECTOR DESCRIPTION:

TS 3.1(MtrVelCorrLimDiff_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=TRUE
TS 3.2(MtrVelCorrLimDiff_MtrRadpS_T_f32 > k_MtrVelCorrLim_MtrRadpS_f32)=FALSE
TS 3.3(DiagFailed_m(MtrVel_DiffAcc_Cnt_M_u16,k_MtrVelCorrLim_Cnt_Str) == TRUE)=TRUE
TS 3.4(HwVelCorrLimDiff_MtrRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=TRUE
TS 3.5(HwVelCorrLimDiff_MtrRadpS_T_f32 > k_HwVelCorrLim_HwRadpS_f32)=FALSE
TS 3.6(DiagFailed_m(MtrVel_HwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=TRUE
TS 3.7(DiagFailed_m(MtrVel_HwVelDiffAcc_Cnt_M_u16,k_HwVelCorrLim_Cnt_Str) == TRUE)=FALSE
TS 3.8(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=TRUE
TS 3.9(NTCCount_Cnt_T_u8 > D_ZERO_CNT_U8)=FALSE
TS 3.10((TRUE == MtrPosFault1_Cnt_T_lgc) ||(TRUE == MtrPosFault2_Cnt_T_lgc))=TRUE
TS 3.11((TRUE == MtrPosFault1_Cnt_T_lgc) ||(TRUE == MtrPosFault2_Cnt_T_lgc))=FALSE
TS 3.12((TRUE == MtrPosFault1_Cnt_T_lgc) =TRUE||(TRUE == MtrPosFault2_Cnt_T_lgc)=FALSE)
TS 3.13MtrVel_DiffAcc_Cnt_M_u16 < (k_MtrVelCorrLim_Cnt_Str.Threshold - k_MtrVelCorrLim_Cnt_Str.Pstep) = True
TS 3.14MtrVel_DiffAcc_Cnt_M_u16 > k_MtrVelCorrLim_Cnt_Str.Nstep = False
TS 3.15MtrVel_HwVelDiffAcc_Cnt_M_u16 > k_HwVelCorrLim_Cnt_Str.Nstep = False
TS 3.16(True = MtrPosFault1_Cnt_T_lgc) = False Or (True = MtrPosFault2_Cnt_T_lgc) = False

Test Step 3.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	10
MtrVel_HwVelDiffAcc_Cnt_M_u16	25
MtrVel_HwVel_HwRadpS_M_f32	-32
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	116
k_HwVelCorrLim_Cnt_Str.PStep	19
k_HwVelCorrLim_Cnt_Str.NStep	10
k_HwVelCorrLim_HwRadpS_f32	19
k_MtrVelCorrLim_Cnt_Str.Threshold	2
k_MtrVelCorrLim_Cnt_Str.PStep	77
k_MtrVelCorrLim_Cnt_Str.NStep	1
k_MtrVelCorrLim_MtrRadpS_f32	-1110
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	15	15	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.2 (Repeat Count = 1) ✔

Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	103	
MtrVel_HwVelDiffAcc_Cnt_M_u16	129	
MtrVel_HwVel_HwRadpS_M_f32	-16	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-680	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	220	
k_HwVelCorrLim_Cnt_Str.PStep	75	
k_HwVelCorrLim_Cnt_Str.NStep	18	
k_HwVelCorrLim_HwRadpS_f32	-24	
k_MtrVelCorrLim_Cnt_Str.Threshold	54	
k_MtrVelCorrLim_Cnt_Str.PStep	181	
k_MtrVelCorrLim_Cnt_Str.NStep	9	
k_MtrVelCorrLim_MtrRadpS_f32	170	
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20	
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-710	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	94	94	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.3 (Repeat Count = 1) ✔

Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	77	
MtrVel_HwVelDiffAcc_Cnt_M_u16	103	
MtrVel_HwVel_HwRadpS_M_f32	-20	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-760	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	194	
k_HwVelCorrLim_Cnt_Str.PStep	61	
k_HwVelCorrLim_Cnt_Str.NStep	16	
k_HwVelCorrLim_HwRadpS_f32	31	
k_MtrVelCorrLim_Cnt_Str.Threshold	40	
k_MtrVelCorrLim_Cnt_Str.PStep	155	
k_MtrVelCorrLim_Cnt_Str.NStep	7	
k_MtrVelCorrLim_MtrRadpS_f32	-150	
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42	
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-870	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	40	40	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	164	164	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 3.4 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	90
MtrVel_HwVelDiffAcc_Cnt_M_u16	116
MtrVel_HwVel_HwRadpS_M_f32	-18
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-720
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	207
k_HwVelCorrLim_Cnt_Str.PStep	68
k_HwVelCorrLim_Cnt_Str.NStep	17
k_HwVelCorrLim_HwRadpS_f32	-25
k_MtrVelCorrLim_Cnt_Str.Threshold	47
k_MtrVelCorrLim_Cnt_Str.PStep	168
k_MtrVelCorrLim_Cnt_Str.NStep	8
k_MtrVelCorrLim_MtrRadpS_f32	10
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-790
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	47	47	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	184	184	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached		1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed		2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus		1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached		1	✔

Test Step 3.5 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	12
MtrVel_HwVelDiffAcc_Cnt_M_u16	38
MtrVel_HwVel_HwRadpS_M_f32	-30
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-960
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	129
k_HwVelCorrLim_Cnt_Str.PStep	26
k_HwVelCorrLim_Cnt_Str.NStep	11
k_HwVelCorrLim_HwRadpS_f32	21
k_MtrVelCorrLim_Cnt_Str.Threshold	5
k_MtrVelCorrLim_Cnt_Str.PStep	90
k_MtrVelCorrLim_Cnt_Str.NStep	2
k_MtrVelCorrLim_MtrRadpS_f32	-950
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-39
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	5	5	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.6 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	142
MtrVel_HwVelDiffAcc_Cnt_M_u16	168
MtrVel_HwVel_HwRadpS_M_f32	-10
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-560
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	250
k_HwVelCorrLim_Cnt_Str.PStep	96
k_HwVelCorrLim_Cnt_Str.NStep	0
k_HwVelCorrLim_HwRadpS_f32	-21
k_MtrVelCorrLim_Cnt_Str.Threshold	75
k_MtrVelCorrLim_Cnt_Str.PStep	220
k_MtrVelCorrLim_Cnt_Str.NStep	12
k_MtrVelCorrLim_MtrRadpS_f32	650
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-470
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	130	130	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	250	250	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.7 (Repeat Count = 1)

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	116		
MtrVel_HwVelDiffAcc_Cnt_M_u16	142		
MtrVel_HwVel_HwRadpS_M_f32	-14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	233		
k_HwVelCorrLim_Cnt_Str.PStep	82		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	-23		
k_MtrVelCorrLim_Cnt_Str.Threshold	61		
k_MtrVelCorrLim_Cnt_Str.PStep	194		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	330		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	106	106	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.8 (Repeat Count = 1)

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	155		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	-8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	251		
k_HwVelCorrLim_Cnt_Str.PStep	103		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-20		
k_MtrVelCorrLim_Cnt_Str.Threshold	82		
k_MtrVelCorrLim_Cnt_Str.PStep	233		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	810		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-28		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-390		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	82	82	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	251	251	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.9 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	37
MtrVel_HwVelDiffAcc_Cnt_M_u16	25
MtrVel_HwVel_HwRadpS_M_f32	9
MtrVel_MtrVelMRF_MtrRadpS_M_f32	75
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	255
k_HwVelCorrLim_Cnt_Str.PStep	154
k_HwVelCorrLim_Cnt_Str.NStep	20
k_HwVelCorrLim_HwRadpS_f32	11
k_MtrVelCorrLim_Cnt_Str.Threshold	229
k_MtrVelCorrLim_Cnt_Str.PStep	220
k_MtrVelCorrLim_Cnt_Str.NStep	9
k_MtrVelCorrLim_MtrRadpS_f32	838
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	28	28	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	179	179	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.10 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	45
MtrVel_HwVelDiffAcc_Cnt_M_u16	37
MtrVel_HwVel_HwRadpS_M_f32	7
MtrVel_MtrVelMRF_MtrRadpS_M_f32	175
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	10
k_HwVelCorrLim_Cnt_Str.PStep	0
k_HwVelCorrLim_Cnt_Str.NStep	8
k_HwVelCorrLim_HwRadpS_f32	13
k_MtrVelCorrLim_Cnt_Str.Threshold	235
k_MtrVelCorrLim_Cnt_Str.PStep	226
k_MtrVelCorrLim_Cnt_Str.NStep	18
k_MtrVelCorrLim_MtrRadpS_f32	904
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-40
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1010
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	27	27	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
			✔

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 3.11 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	41
MtrVel_HwVelDiffAcc_Cnt_M_u16	31
MtrVel_HwVel_HwRadpS_M_f32	5
MtrVel_MtrVelMRF_MtrRadpS_M_f32	125
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	100
k_HwVelCorrLim_Cnt_Str.PStep	169
k_HwVelCorrLim_Cnt_Str.NStep	4
k_HwVelCorrLim_HwRadpS_f32	12
k_MtrVelCorrLim_Cnt_Str.Threshold	232
k_MtrVelCorrLim_Cnt_Str.PStep	223
k_MtrVelCorrLim_Cnt_Str.NStep	11
k_MtrVelCorrLim_MtrRadpS_f32	871
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	950
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	30	30	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	100	100	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔	

Test Step 3.12 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	61
MtrVel_HwVelDiffAcc_Cnt_M_u16	61
MtrVel_HwVel_HwRadpS_M_f32	1
MtrVel_MtrVelMRF_MtrRadpS_M_f32	375
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	54
k_HwVelCorrLim_Cnt_Str.PStep	22
k_HwVelCorrLim_Cnt_Str.NStep	20
k_HwVelCorrLim_HwRadpS_f32	17
k_MtrVelCorrLim_Cnt_Str.Threshold	18
k_MtrVelCorrLim_Cnt_Str.PStep	238
k_MtrVelCorrLim_Cnt_Str.NStep	16
k_MtrVelCorrLim_MtrRadpS_f32	1036
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1250
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	45	45	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	41	41	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.13 (Repeat Count = 1) ✔

Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	0
MtrVel_HwVelDiffAcc_Cnt_M_u16	175
MtrVel_HwVel_HwRadpS_M_f32	12.3999996
MtrVel_MtrVelMRF_MtrRadpS_M_f32	100
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	50
k_HwVelCorrLim_Cnt_Str.PStep	5
k_HwVelCorrLim_Cnt_Str.NStep	1
k_HwVelCorrLim_HwRadpS_f32	-10
k_MtrVelCorrLim_Cnt_Str.Threshold	152
k_MtrVelCorrLim_Cnt_Str.PStep	16
k_MtrVelCorrLim_Cnt_Str.NStep	1
k_MtrVelCorrLim_MtrRadpS_f32	40
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-8
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	16	16	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.14 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	9		
MtrVel_HwVelDiffAcc_Cnt_M_u16	60		
MtrVel_HwVel_HwRadpS_M_f32	-37		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1050		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	21		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	208		
k_MtrVelCorrLim_Cnt_Str.PStep	199		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	607		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	0	0	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	21	21	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.15 (Repeat Count = 1) ✔

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	212		
MtrVel_HwVelDiffAcc_Cnt_M_u16	16		
MtrVel_HwVel_HwRadpS_M_f32	20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	850		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	212		
k_HwVelCorrLim_Cnt_Str.PStep	65		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	5		
k_MtrVelCorrLim_Cnt_Str.Threshold	196		
k_MtrVelCorrLim_Cnt_Str.PStep	187		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	475		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	22		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	196	196	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Test Step 3.16 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	0		
MtrVel_HwVel_HwRadpS_M_f32	-42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_DiffAcc_Cnt_M_u16	0	0	✔
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	✔
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	✔
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	✔

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVelBlend

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	2
Successful	2 ✔
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include

Comments/Description/Specification	
Name	**Text**

Module 'MtrVel_Digi'

```
*****************************************UNIT TEST INFORMATION*****************************************
Name of Tester:Sunita Kore
Code File(s) Under Test:Sa_MtrVel.c
Code File(s) Version:5
Module Design Document:MotorVelocity_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total RAM Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.
```

Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements.

Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

```
*****************************************************************************************************
```

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test ✔

Specification
```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
CPU Cycles:

TS  1.1 612 Cycles
TS  1.2 663 Cycles
```

Description   VECTOR DESCRIPTION:

TS 1.1 " Shortest Execution Path:
(Interpolation function)
 TblInput <= TableX(0)=>True"
TS 1.2 "Longest Execution Path:
(Interpolation function)
 TblInput <= TableX(0)=>False
 TblInput>= TableX(Size - 1)"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0		
FinMtrVel_MtrRadpS_T_f32	-654.98		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-654.97998	-654.98	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0.95		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	14		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	675.474976	675.475	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔

Test Case 2: Boundary Check ✔

Specification
```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS  2.1 663 Cycles
TS  2.2 612 Cycles
TS  2.3 612 Cycles
TS  2.4 625 Cycles
TS  2.5 612 Cycles
TS  2.6 612 Cycles
TS  2.7 612 Cycles
TS  2.8 612 Cycles
TS  2.9 625 Cycles
TS  2.10 599 Cycles
TS  2.11 612 Cycles
TS  2.12 625 Cycles
TS  2.13 612 Cycles
TS  2.14 612 Cycles
TS  2.15 625 Cycles
```

Description
VECTOR DESCRIPTION:
TS 2.1all min
TS 2.2all max
TS 2.3FinMtrVel_MtrRadpS_T_f32 = min
TS 2.4FinMtrVel_MtrRadpS_T_f32 = max
TS 2.5FinMtrVel_MtrRadpS_T_f32 = zero
TS 2.6FinMtrVel_MtrRadpS_T_f32 = pos
TS 2.7FinMtrVel_MtrRadpS_T_f32 = neg
TS 2.8CrsMtrVel_MtrRadpS_T_f32 = min
TS 2.9CrsMtrVel_MtrRadpS_T_f32 = max
TS 2.10CrsMtrVel_MtrRadpS_T_f32 = zero
TS 2.11CrsMtrVel_MtrRadpS_T_f32 = pos
TS 2.12CrsMtrVel_MtrRadpS_T_f32 = neg
TS 2.13t_MtrVelBlendTblX_MtrRadpS_u12p4[2] = min
TS 2.14t_MtrVelBlendTblX_MtrRadpS_u12p4[2] = max
TS 2.15t_MtrVelBlendTblX_MtrRadpS_u12p4[2] = mid

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-1350		
FinMtrVel_MtrRadpS_T_f32	-1350		
T_MtrVelBlendTblY_UIs_u2p14[0]	0		
T_MtrVelBlendTblY_UIs_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-1350	-1350	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.2 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1350		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_UIs_u2p14[0]	0		
T_MtrVelBlendTblY_UIs_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	1350	1350	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-36		
FinMtrVel_MtrRadpS_T_f32	-1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-36	-36	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-58.87		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-58.8699989	-58.87	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	235		
FinMtrVel_MtrRadpS_T_f32	0		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	235	235	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.6 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	300.07		
FinMtrVel_MtrRadpS_T_f32	235.5		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	10		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	300.070007	300.07	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.7 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-476		
FinMtrVel_MtrRadpS_T_f32	-58.56		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	13		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	14		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-476	-476	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-1350		
FinMtrVel_MtrRadpS_T_f32	300		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-1350	-1350	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔

Test Step 2.9 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1350		
FinMtrVel_MtrRadpS_T_f32	-476		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	1350	1350	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔

Test Step 2.10 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0		
FinMtrVel_MtrRadpS_T_f32	-654.98		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-654.97998	-654.98	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔

Test Step 2.11 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1100.19995		
FinMtrVel_MtrRadpS_T_f32	-765		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	1100.19995	1100.19995	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.12 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-654.65		
FinMtrVel_MtrRadpS_T_f32	675.87		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	13		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-654.650024	-654.65	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.13 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-36		
FinMtrVel_MtrRadpS_T_f32	333		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-36	-36	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.14 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-58		
FinMtrVel_MtrRadpS_T_f32	987		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	-58	-58	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

Test Step 2.15 (Repeat Count = 1) ✔

Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	345.96		
FinMtrVel_MtrRadpS_T_f32	-87.43		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	13		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVelBlend()	345.959991	345.96	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔

TEST DETAILS REPORT

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	RegressionFit

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✔
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include

Comments/Description/Specification	
Name	Text

Module 'MtrVel_Digi'

```
*****************************************UNIT TEST INFORMATION*****************************************
Name of Tester:Sunita Kore
Code File(s) Under Test:Sa_MtrVel.c
Code File(s) Version:5
Module Design Document:MotorVelocity_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total RAM Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.
```

Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements.

Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

```
*****************************************************************************************************
```

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1 ✔

Specification
```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 1.1 1288 Cycles
TS 1.2 1365 Cycles
```

Description VECTOR DESCRIPTION:

TS 1.1 "Shortest Execution Path:
(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = False;
(PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = FALSE
(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = TRUE"
TS 1.2 "Longest Execution Path:
(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = True
(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = TRUE"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
RegressionFit()	-833.333313	-833.3333333 ± 0.0625	✔

Test Step 1.2 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	1	
MtrVel3_OsBufPos_Cnt_M_u08[2]	2	
MtrVel3_OsBufPos_Cnt_M_u08[3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60	

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
RegressionFit()	-600.838257	-600.8382387 ± 0.0625	✔

Test Case 2 ✔

Specification
```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 2.1 1288 Cycles
TS 2.2 1307  Cycles
TS 2.3 1365 Cycles
TS 2.4 1356 Cycles
TS 2.5 1290 Cycles
```

Description   VECTOR DESCRIPTION:

TS 2.1"(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = TRUE;
(PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = FALSE"
TS 2.2"(PosDiff_Rad_T_f32 > D_PI_ULS_F32) = FALSE;
(PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = TRUE;"
TS 2.3(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = TRUE
TS 2.4(PosDiff_Rad_T_f32 <= -D_PI_ULS_F32) = TRUE
TS 2.5(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
RegressionFit()	-833.333313	-833.3333333 ± 0.0625	✔

Test Step 2.2 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2	

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9285		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	631.181824	631.1818724 ± 0.0625	✔

Test Step 2.3 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	1	
MtrVel3_OsBufPos_Cnt_M_u08[2]	2	
MtrVel3_OsBufPos_Cnt_M_u08[3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5	

TEST DETAILS REPORT

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
RegressionFit()	-600.838257	-600.8382387 ± 0.0625	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	0

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
RegressionFit()	475.663788	475.6636057 ± 0.0625	✔

Test Step 2.5 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0

TEST DETAILS REPORT

Name	Input Value		
MtrVel3_OsBufPos_Cnt_M_u08[3]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	0		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
RegressionFit()	0	0 ± 0.0625	✔

Test Case 3 ✔

Specification
```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS 3.1 1281 Cycles
TS 3.2 1266 Cycles
TS 3.3 1334 Cycles
TS 3.4 1432 Cycles
TS 3.5 1310 Cycles
TS 3.6 1422 Cycles
TS 3.7 1388 Cycles
TS 3.8 1316 Cycles
TS 3.9 1315 Cycles
TS 3.10 1354 Cycles
TS 3.11 1310 Cycles
TS 3.12 1389 Cycles
TS 3.13 1288 Cycles
TS 3.14 1301 Cycles
TS 3.15 1303 Cycles
TS 3.16 1288 Cycles
TS 3.17 1288 Cycles
```

Description

VECTOR DESCRIPTION:

TS 3.1 "all min
(Cross buffer timestamp = 250)"
TS 3.2"all max
(Cross buffer timestamp = 4000)"
TS 3.3"MtrVel_OsBufSelect_Cnt_M_u08 = min
(Cross buffer timestamp = 600)"
TS 3.4"MtrVel_OsBufSelect_Cnt_M_u08 = max
(Cross buffer timestamp = 340)"
TS 3.5"MtrVel_OsBufSelect_Cnt_M_u08 = mid
(Cross buffer timestamp = 1000)"
TS 3.6"MtrVel3_OsBufPos_Cnt_M_u08[4] = min
(Cross buffer timestamp = 800)"
TS 3.7"MtrVel3_OsBufPos_Cnt_M_u08[4] = max
(Cross buffer timestamp = 450)"
TS 3.8"MtrVel3_OsBufPos_Cnt_M_u08[4] = mid
(Cross buffer timestamp = 500)"
TS 3.9"MtrVel_OldPosBuf_Cnt_M_u08 = min
(Cross buffer timestamp = 950)"
TS 3.10"MtrVel_OldPosBuf_Cnt_M_u08 = max
(Cross buffer timestamp = 420)"
TS 3.11"MtrVel_OldPosBuf_Cnt_M_u08 = mid
(Cross buffer timestamp = 380)"
TS 3.12"MtrVel3_TimeBuffer_uS_M_u16p0[4][8] = min
(Cross buffer timestamp = 250)"
TS 3.13"MtrVel3_TimeBuffer_uS_M_u16p0[4][8] = max
(Cross buffer timestamp = 4000)"
TS 3.14"MtrVel3_TimeBuffer_uS_M_u16p0[4][8] = mid
(Cross buffer timestamp = 600)"
TS 3.15"MtrVel3_PosBuffer_Rad_M_f32[4][8] = min
(Cross buffer timestamp = 340)"
TS 3.16"MtrVel3_PosBuffer_Rad_M_f32[4][8] = max
(Cross buffer timestamp = 1000)"
TS 3.17"MtrVel3_PosBuffer_Rad_M_f32[4][8] = mid
(Cross buffer timestamp = 950)"

Test Step 3.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
RegressionFit()	0	0 ± 0.0625	✔

Test Step 3.2 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
MtrVel3_OsBufPos_Cnt_M_u08[1]	7	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	0	0 ± 0.0625	✔

Test Step 3.3 (Repeat Count = 1)		✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.4		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
RegressionFit()	475.663788	475.6636057 ± 0.0625	✔

Test Step 3.4 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5.98	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.7
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5.6
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.0999999
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.28000021
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9285
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	-1097.93042	-1097.93042	✔

Test Step 3.5 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
RegressionFit()	-657.894653	-657.8947368 ± 0.0625	✔

Test Step 3.6 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	5
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0.5
MtrVel3_PosBuffer_Rad_M_f32[1][0]	5
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	5
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.7
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	205
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	290
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	375
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	545
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	630
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	715
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1005
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1090
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1175
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1260
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1345
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1430
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1515
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6030
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5970
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5940
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5910
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5880
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5850
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5820
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5790
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	5760
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	5730
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	5700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	5670
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	5640
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	5610
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5580
MtrVel_OldPosBuf_Cnt_M_u08	0

TEST DETAILS REPORT

Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	1		

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
RegressionFit()	815.12738	815.1272958 ± 0.0625	✔

Test Step 3.7 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
MtrVel3_OsBufPos_Cnt_M_u08[1]	7	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525	
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650	
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775	
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900	
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025	
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150	
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275	

TEST DETAILS REPORT

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	517.699097	517.6990817 ± 0.0625	✔

Test Step 3.8 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6000	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6010	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6020	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6030	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6040	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6050	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6060	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6070	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	700	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	770	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	840	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	350	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	420	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	490	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	560	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	630	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6080	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6090	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6100	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6110	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6120	

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6130		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6140		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1190		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1260		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	840		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	910		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	980		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1120		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	3		

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	510.956726	510.9567538 ± 0.0625	✔

Test Step 3.9 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	1	
MtrVel3_OsBufPos_Cnt_M_u08[2]	2	
MtrVel3_OsBufPos_Cnt_M_u08[3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.5	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.4	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70	

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
RegressionFit()	-673.206726	-673.2066598 ± 0.0625	✔

Test Step 3.10 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5.98	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.7	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5.6	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.6	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.6	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.0999999	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.23000002	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.4	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8245	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8125	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	8005	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	7885	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	7765	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	7645	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	7525	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	7405	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7285	

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7165		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7045		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	6925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	6805		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	6685		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	6565		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6445		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3640		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3760		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3880		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	3160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	3280		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	3400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4020		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	4140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4260		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4380		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3780		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	-1009.09265	-1009.09265	✔

Test Step 3.11 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
RegressionFit()	-657.894653	-657.8947368 ± 0.0625	✔

Test Step 3.12 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	0	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0.5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.4	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5.9	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.5	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
RegressionFit()	1008.40723	1008.407346 ± 0.0625	✔

Test Step 3.13 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3
MtrVel3_PosBuffer_Rad_M_f32[0][6]	4
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	4125		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	4000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3875		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3750		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7625		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7750		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7375		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	11500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	11625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	11750		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	11875		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	12000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	11375		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	125.000008	125 ± 0.0625	✔

Test Step 3.14 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3	

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	950		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1550		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	4328		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	4278		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	4228		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4178		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4128		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4078		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4028		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3978		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3828		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3778		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3728		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3678		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3628		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3578		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
RegressionFit()	-357.669678	-357.6697277 ± 0.0625	✔

Test Step 3.15 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	5	
MtrVel3_OsBufPos_Cnt_M_u08[3]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0	

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	160		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	170		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	180		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	190		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	210		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	230		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	240		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	260		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	270		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	280		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	290		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	10000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9300		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	0	0 ± 0.0625	✔

Test Step 3.16 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	

TEST DETAILS REPORT

Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	999		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	888		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	777		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	555		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	444		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	333		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	222		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	825		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	725		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	525		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	325		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	225		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	232		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	294		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	356		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	418		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	542		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	604		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1042		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1104		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1166		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	732		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	794		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	856		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	918		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	980		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
RegressionFit()	0	0 ± 0.0625	✔

Test Step 3.17 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	1	
MtrVel3_OsBufPos_Cnt_M_u08[2]	2	

Name	Input Value		
MtrVel3_OsBufPos_Cnt_M_u08[3]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9850		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9745		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9790		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✓
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✓
RegressionFit()	263.157898	263.1578947 ± 0.0625	✓

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	CalcCoarseVel

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrVel_Digi'	**UNIT TEST INFORMATION** Name of Tester:Sunita Kore Code File(s) Under Test:Sa_MtrVel.c Code File(s) Version:5 Module Design Document:MotorVelocity_MDD.doc Module Design Document Version:6 Data Dictionary Version:3 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):1394 Total RAM Used (Bytes):42 Total CALS Used (Bytes):54 Special Test Requirements: Test Date:12/26/2014 Comments: "Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested. Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD. Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350. Note4: In function ""RegressionFit"" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements. Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference." **

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes

Name	Value
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Check　✔

Specification

```
Performance Metrics:
   (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS 1.1 31 Cycles
TS 1.2 31 Cycles
TS 1.3 31 Cycles
TS 1.4 31 Cycles
TS 1.5 31 Cycles
TS 1.6 31 Cycles
TS 1.7 31 Cycles
TS 1.8 31 Cycles
TS 1.9 31 Cycles
TS 1.10 31 Cycles
```

Description　　VECTOR DESCRIPTION:

TS1.1　all min
TS1.2　all max
TS1.3　DeltaPos_Rad_T_f32 = min
TS1.4　DeltaPos_Rad_T_f32 = max
TS1.5　DeltaPos_Rad_T_f32 = zero
TS1.6　DeltaPos_Rad_T_f32 = pos
TS1.7　DeltaPos_Rad_T_f32 = neg
TS1.8　DeltaTime_uS_T_f32 = min
TS1.9　DeltaTime_uS_T_f32 = max
TS1.10　DeltaTime_uS_T_f32 = mid

Test Step 1.1 (Repeat Count = 1)　✔

Name	Input Value		
DeltaPos_Rad_T_f32	-6.283185307		
DeltaTime_uS_T_f32	1800		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	-3490.65869	-3490.658504 ± 0.0625	✔

Test Step 1.2 (Repeat Count = 1)　✔

Name	Input Value		
DeltaPos_Rad_T_f32	6.283185307		
DeltaTime_uS_T_f32	2200		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	2855.99341	2855.993321 ± 0.0625	✔

Test Step 1.3 (Repeat Count = 1)　✔

Name	Input Value		
DeltaPos_Rad_T_f32	-6.283185307		
DeltaTime_uS_T_f32	1900		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	-3306.9397	-3306.939635 ± 0.0625	✔

Test Step 1.4 (Repeat Count = 1)　✔

Name	Input Value		
DeltaPos_Rad_T_f32	6.283185307		
DeltaTime_uS_T_f32	2100.45		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	2991.35205	2991.351999 ± 0.0625	✔

Test Step 1.5 (Repeat Count = 1)　✔

Name	Input Value		
DeltaPos_Rad_T_f32	0		
DeltaTime_uS_T_f32	1937		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	0	0 ± 0.0625	✔

Test Step 1.6 (Repeat Count = 1) ✔

Name	Input Value		
DeltaPos_Rad_T_f32	2.2		
DeltaTime_uS_T_f32	2038		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	1079.48975	1079.489696 ± 0.0625	✔

Test Step 1.7 (Repeat Count = 1) ✔

Name	Input Value		
DeltaPos_Rad_T_f32	-1.6		
DeltaTime_uS_T_f32	2047.56		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	-781.417847	-781.4178827 ± 0.0625	✔

Test Step 1.8 (Repeat Count = 1) ✔

Name	Input Value		
DeltaPos_Rad_T_f32	2.12		
DeltaTime_uS_T_f32	1800		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	1177.77783	1177.777778 ± 0.0625	✔

Test Step 1.9 (Repeat Count = 1) ✔

Name	Input Value		
DeltaPos_Rad_T_f32	4.12		
DeltaTime_uS_T_f32	2200		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	1872.72729	1872.727273 ± 0.0625	✔

Test Step 1.10 (Repeat Count = 1) ✔

Name	Input Value		
DeltaPos_Rad_T_f32	-1.9		
DeltaTime_uS_T_f32	2000.5		
Name	**Actual Value**	**Expected Value**	**Result**
CalcCoarseVel()	-949.762573	-949.762573 ± 0.0625	✔

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✔
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include

Comments/Description/Specification	
Name	**Text**

Module 'MtrVel_Digi'

```
****************************************UNIT TEST INFORMATION****************************************
Name of Tester:Sunita Kore
Code File(s) Under Test:Sa_MtrVel.c
Code File(s) Version:5
Module Design Document:MotorVelocity_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total RAM Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and
""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in
the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not
following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have
been given value as '0' for all array elements.

Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

****************************************************************************************************
```

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test ✔

Specification
```
Performance Metrics
(With "None" Instrumentation and WithPS Environment)

CPU Cycles:
TS 1.1 4543 Cycles
TS 1.2 4546 Cycles
```

Description
VECTOR DESCRIPTION:

TS 1.1 "Shortest Execution Path:
if (MtrVel_PosBuffer_Rad_T_f32 > D_PI_ULS_F32) = False;
(MtrVel_PosBuffer_Rad_T_f32 <= -D_PI_ULS_F32) = False
(MtrVel_MtrRadpS_T_f32>=D_MTRVELHILMT_MTRRADPS_F32)=False
(MtrVel_HwVel_HwRadpS_M_f32>=D_MAXHANDWHEELVEL_HWRADPS_F32)=False"
TS 1.2 "Longest Execution Path:
if (MtrVel_PosBuffer_Rad_T_f32 > D_PI_ULS_F32) = False;
(MtrVel_PosBuffer_Rad_T_f32 <= -D_PI_ULS_F32) = False
(MtrVel_MtrRadpS_T_f32>=D_MTRVELHILMT_MTRRADPS_F32)=True
(MtrVel_HwVel_HwRadpS_M_f32>=D_MAXHANDWHEELVEL_HWRADPS_F32)=Fa"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.12
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-4.80000067	-4.8 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	40.0000076	40 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.00182160223	0 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-4.80000067	-4.8 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-40.0000076	-40 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	40.0000076	40 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-4.80000067	-4.8 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	40.0000076	40 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 1.2 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1380
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7700
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-227.1569001
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.03
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	14
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1

Name	Input Value
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	40.5	40.5 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.00182160223	0 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	40.5	40.5 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	40.5	40.5 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Case 2: Boundary Test ✔

Test Case 2: Boundary Test

Specification

```
Performance Metrics
  (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 2.1 4543 Cycles
TS 2.2 4416 Cycles
TS 2.3 4456 Cycles
TS 2.4 4445 Cycles
TS 2.5 4428 Cycles
TS 2.6 4456 Cycles
TS 2.7 4367 Cycles
TS 2.8 4442 Cycles
TS 2.9 4522 Cycles
TS 2.10 4414 Cycles
TS 2.11 4446 Cycles
TS 2.12 4382 Cycles
TS 2.13 4429 Cycles
TS 2.14 4483 Cycles
TS 2.15 4429 Cycles
TS 2.16 4386 Cycles
TS 2.17 4429 Cycles
TS 2.18 4458 Cycles
TS 2.19 4353 Cycles
TS 2.20 4458 Cycles
TS 2.21 4429 Cycles
TS 2.22 4458 Cycles
TS 2.23 4430 Cycles
TS 2.24 4436 Cycles
TS 2.25 4429 Cycles
TS 2.26 4430 Cycles
TS 2.27 4429 Cycles
TS 2.28 4458 Cycles
TS 2.29 4458 Cycles
TS 2.30 4504 Cycles
TS 2.31 4428 Cycles
TS 2.32 4465 Cycles
TS 2.33 4458 Cycles
TS 2.34 4374 Cycles
TS 2.35 4447 Cycles
TS 2.36 4487 Cycles
TS 2.37 4410 Cycles
TS 2.38 4411 Cycles
TS 2.39 4458 Cycles
TS 2.40 4430 Cycles
TS 2.41 4429 Cycles
TS 2.42 4381 Cycles
TS 2.43 4430 Cycles
TS 2.44 4429 Cycles
TS 2.45 4436 Cycles
TS 2.46 4458 Cycles
TS 2.47 4523 Cycles
TS 2.48 4446 Cycles
```

Description   VECTOR DESCRIPTION:

TS2.1All Min
TS2.2All Max
TS2.3AsstAssemblyPolarity_Cnt_s08==>Min
TS2.4AsstAssemblyPolarity_Cnt_s08==>Max
TS2.5AsstAssemblyPolarity_Cnt_s08==>Pos
TS2.6MechMtrPos2Timestamp_USec_u32==>Min
TS2.7MechMtrPos2Timestamp_USec_u32==>Max
TS2.8MechMtrPos2Timestamp_USec_u32==>Pos
TS2.9MechMtrPos2_Rev_u0p16==>Min
TS2.10MechMtrPos2_Rev_u0p16==>Max
TS2.11MechMtrPos2_Rev_u0p16==>Pos
TS2.12MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Min
TS2.13MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Max
TS2.14MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Pos
TS2.15MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Min
TS2.16MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Max
TS2.17MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Pos
TS2.18MtrVel_OsBufSelect_Cnt_M_u08==>Min
TS2.19MtrVel_OsBufSelect_Cnt_M_u08==>Max
TS2.20MtrVel_OsBufSelect_Cnt_M_u08==>Pos
TS2.21MtrVel3_OsBufPos_Cnt_M_u08[4]==>Min
TS2.22MtrVel3_OsBufPos_Cnt_M_u08[4]==>Max
TS2.23MtrVel3_OsBufPos_Cnt_M_u08[4]==>Pos
TS2.24MtrVel_OldPosBuf_Cnt_M_u08==>Min
TS2.25MtrVel_OldPosBuf_Cnt_M_u08==>Max
TS2.26MtrVel_OldPosBuf_Cnt_M_u08==>Pos
TS2.27MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Min
TS2.28MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Max
TS2.29MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Pos
TS2.30MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Min
TS2.31MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Max
TS2.32MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Pos
TS2.33t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Min
TS2.34t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Max
TS2.35t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Pos
TS2.36k_GearRatio_Uls_f32==>Min
TS2.37k_GearRatio_Uls_f32==>Max
TS2.38k_GearRatio_Uls_f32==>Pos
TS2.39MtrVel_HwVel_HwRadpS_M_f32==>Min
TS2.40MtrVel_HwVel_HwRadpS_M_f32==>Max
TS2.41MtrVel_HwVel_HwRadpS_M_f32==>Pos
TS2.42MtrVel_HwVel_HwRadpS_M_f32==>Zero
TS2.43MtrVel_HwVel_HwRadpS_M_f32==>Neg
TS2.44MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Min
TS2.45MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Max
TS2.46MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Pos
TS2.47MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Zero
TS2.48MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Neg

Test Step 2.1 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300
MtrVel_HwVel_HwRadpS_M_f32	-42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350
MtrVel_OldPosBuf_Cnt_M_u08	0

Name	Input Value
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1800
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-0	0 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1800	1800	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-0	0 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔

T					✔
Actual Function		**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel		1	CalcCoarseVel	1	✔
RegressionFit		1	RegressionFit	1	✔
MtrVelBlend		1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt		1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.2 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294965495
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	1
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✔

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✓
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)		✓
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.12
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-555.555542	-555.555542 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	555.555542	555.555542 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-555.555542	-555.555542 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-555.555542	-555.555542 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.4 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.19
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.21
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.23
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.24
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.25
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.26
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.27
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.31
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.33
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.34
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.38
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.41
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.43
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65352
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65414
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880
MtrVel_HwVel_HwRadpS_M_f32	10.93737113
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-573.3094931
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	293048
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.13
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	294848

Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-762.023987	-762.0239258 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	294848	294848	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.5 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.16	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.18	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.2	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.22	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.24	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.26	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.34	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.38	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.46	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.5	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.56	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.58	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.64	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.74	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.78	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	661
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239
MtrVel_HwVel_HwRadpS_M_f32	-2.767454803
MtrVel_MtrVelMRF_MtrRadpS_M_f32	676.0312533
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6046
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.14
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7846
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-968.545654	-968.5455933 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7846	7846	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-968.545654	-968.545593 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-968.545654	-968.5455933 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-968.545654	-968.5455933 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔	
CalcCoarseVel	1	CalcCoarseVel	1	✔	
RegressionFit	1	RegressionFit	1	✔	
MtrVelBlend	1	MtrVelBlend	1	✔	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔	

Test Step 2.6 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.77
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.82
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.07
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.77
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwVel_HwRadpS_M_f32	-16.68913257
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-412.0075986
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.17
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	12
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	12
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1800
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	967.551514	967.5514526 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1800	1800	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	967.551514	967.5514526 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	967.551514	967.5514526 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	967.551514	967.5514526 ± 0.0625	✔

T					✔
Actual Function	Count	Expected Function		Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.7 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294965495
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	1
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔

T					✔
Actual Function		**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel		1	CalcCoarseVel	1	✔
RegressionFit		1	RegressionFit	1	✔
MtrVelBlend		1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt		1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.8 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	5	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.45	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	675	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	635	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	600	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	576	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	539	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	500	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	465
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7380
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7442
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7504
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7566
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7132
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7194
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7256
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7318
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8156
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8218
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8280
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8342
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8404
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8466
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8032
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8094
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1111
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1006
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	967
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	878
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	820
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	793
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	739
MtrVel_HwVel_HwRadpS_M_f32	-21.01813388
MtrVel_MtrVelMRF_MtrRadpS_M_f32	969.1185812
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3878
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.19
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	15
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	5678
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1332.33911	1332.339233 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5678	5678	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.9 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1380
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7700
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-227.1569001
MtrVel_OldPosBuf_Cnt_M_u08	0

Name	Input Value
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.2
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	29.8089962	29.8089797 ± 0.0625	✓
MtrVel_MtrVelMRF_MtrRadpS_M_f32	149.044983	149.0448914 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✓
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	29.8089962	29.8089797 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	29.8089962	29.8089797 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✓

T			✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✓
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)		✓
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.64
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	30300
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	30250
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	30200
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	30150
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	30100
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	30050
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	30020
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	30000
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	28876
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	28938
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	29000
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	28566
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	28628
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	28690
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	28752
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	28814
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	25400
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	25300
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	25200
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	25160
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	25100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	25060
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	25030
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	25000
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	29452
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	29514
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	29576
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	29638
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	29700
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	29266
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	29328
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	29390
MtrVel_HwVel_HwRadpS_M_f32	-34.05995619
MtrVel_MtrVelMRF_MtrRadpS_M_f32	258.9155765
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6478
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.21
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	8378
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✓
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	8378	8378	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.11 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.62
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.82
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.12
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.52
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.62
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.82
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.02
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.22
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.42
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.72
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.92
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.02
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.12
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.22
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.32
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1110
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1172
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1234
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	862
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	924
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	986
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1048
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	4657
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	4609
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	4576
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	4500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	4480
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	4450
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	4401
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	4387
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2124
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2186

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2248
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2310
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2372
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2434
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2062
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	7823
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	7800
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	7760
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	7700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	7650
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	7570
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	7500
MtrVel_HwVel_HwRadpS_M_f32	-41.71834391
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-838.1024514
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1653
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.22
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3553
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	285.048859	285.0487671 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3553	3553	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-285.048859	-285.0487671 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	285.048859	285.0487671 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	285.048859	285.0487671 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.12 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.34	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.45	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.56	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.67	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.78	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.89
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.33
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.88
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.99
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.43
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.54
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.65
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.98
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.09
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.2
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.31
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.53
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.64
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.75
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	5678
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	5600
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	5570
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5500
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	5340
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	5300
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	13014
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	13076
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	13138
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	13200
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	12766
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	12828
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	12890
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	12952
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8341
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8300
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8240
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8150
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8100
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8050
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8000
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	14590
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	14652
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	14714
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	14776
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	14838
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	14900
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	14466
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	14528
MtrVel_HwVel_HwRadpS_M_f32	39.33634806
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1200.540927
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2100
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.23
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4000

Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-661.377869	-661.3778076 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4000	4000	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-661.377869	-661.377808 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-661.377869	-661.3778076 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-661.377869	-661.3778076 ± 0.0625	✔

T			✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.13 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.96
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.16
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.4
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.88
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.24
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.36
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.48
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.6
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.84
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.96
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.08
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	2069

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	2131
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	2193
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1759
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1821
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1883
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1945
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	2007
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3410
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	3370
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	3333
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	3290
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	3207
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	3175
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	3100
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2450
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2512
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2574
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2636
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2698
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2760
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2822
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2388
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6890
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6810
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6680
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6610
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6540
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6504
MtrVel_HwVel_HwRadpS_M_f32	20.37118572
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-651.7195906
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5068
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.24
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	6968
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-330.714172	-330.7141418 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6968	6968	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-330.714172	-330.7141418 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-330.714172	-330.7141418 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-330.714172	-330.7141418 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.14 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.9	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.16	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.68	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.33	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.46	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.59	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.37	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9770	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9832	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9398	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9460	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9522	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9584	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9646	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9708	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900	
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845	
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	8928
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	8990
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9052
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9114
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9176
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	8742
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	8804
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	8866
MtrVel_HwVel_HwRadpS_M_f32	-5.642567992
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1302.259389
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	85754
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.25
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	87654
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87654	87654	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.15 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.39	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.71	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.88	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.03
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.36
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.52
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.84
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.16
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.48
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.64
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.8
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.96
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.8
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.3
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwVel_HwRadpS_M_f32	30.78114766
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-326.589513
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.28
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1900
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-256.191681	-256.1917114 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1900	1900	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	✔

T					✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.16 (Repeat Count = 1)		✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294965495
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	1
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.17 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.02
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.38
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.55
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.74
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.91
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.1
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.27
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.46
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.7
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.8
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.9
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.8
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.2
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel_HwVel_HwRadpS_M_f32	18.08023292
MtrVel_MtrVelMRF_MtrRadpS_M_f32	654.1182904
MtrVel_OldPosBuf_Cnt_M_u08	2

Name	Input Value
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	8078
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.3
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	9978
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-752.657959	-752.6581421 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	9978	9978	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.18 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.88
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.07
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.26
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.45
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.63
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.83
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4.01
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.3
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.59
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.41
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.52
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.63
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.74
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.85
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.96
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.07
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.18
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.51
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.73
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.84
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.95
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.06
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.17
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.28
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.39
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600
MtrVel_HwVel_HwRadpS_M_f32	-10.02764416
MtrVel_MtrVelMRF_MtrRadpS_M_f32	570.9573146
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1467
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.31
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3367
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	999.058167	999.0581665 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	■
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3367	3367	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.19 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.92
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.12
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.32
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.52
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.71
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4.11
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.32
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.51
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.72
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.44
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.68
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.8
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.92
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.04
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.16
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.28
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.52
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.64
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.76
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.88
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.12
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.36
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.48
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.6
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9285
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870
MtrVel_HwVel_HwRadpS_M_f32	-9.132052481
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-850.7045946
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.7
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1444
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.32
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3444
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3444	3444	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.20 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel_HwVel_HwRadpS_M_f32	-21.84720886
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1194.596858
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	442545
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.33
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	444545

Name	Input Value
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1027.41431	1027.414185 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	444545	444545	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.21 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.19
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.21
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.23
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.24
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.25
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.26
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.27
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.31
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.33
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.34
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.38
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.419
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.43
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	800

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	205
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	290
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	375
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	545
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	630
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	715
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1005
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1090
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1175
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1260
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1345
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1430
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1515
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6030
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5970
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5940
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5910
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5880
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5850
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5820
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5790
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	5760
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	5730
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	5700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	5670
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	5640
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	5610
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5580
MtrVel_HwVel_HwRadpS_M_f32	-22.67909569
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-685.3485959
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5888
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.34
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7888
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7888	7888	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.22 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.22
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.26
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.34
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.36
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.38
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.46
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.48
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.54
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.56
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.58
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.64
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.66
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.68
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.74
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.78
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	700
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	500
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	300
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	5250
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	5270
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	5290
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	5310
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	5330
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	5350
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	5370
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	5390
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	5700
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	5720
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5740
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5760
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5780
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5800
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5820

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5840
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	200
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	0
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65436
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	65336
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	65236
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	65136
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	65036
MtrVel_HwVel_HwRadpS_M_f32	-36.06386733
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-814.620298
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	52544
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.35
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	54544
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-871.69104	-871.69104 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54544	54544	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	871.69104	871.69104 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-871.69104	-871.69104 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-871.69104	-871.69104 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.23 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.18	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.21	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.24	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.45	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.51
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.63
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.66
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.75
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.81
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.93
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.99
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.05
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.11
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6010
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6020
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6030
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6040
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6050
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6060
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6070
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	770
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	840
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	350
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	560
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6090
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6110
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6120
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6130
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6140
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1190
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1260
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	840
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	980
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1050
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1120
MtrVel_HwVel_HwRadpS_M_f32	25.16924399
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1286.213058
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	43566
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.36
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	45566
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-20.7045307	-20.70454196 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-57.5125809	-57.5126152 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	45566	45566	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-20.7045307	-20.70454196 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-20.7045307	-20.70454196 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔	
CalcCoarseVel	1	CalcCoarseVel	1	✔	
RegressionFit	1	RegressionFit	1	✔	
MtrVelBlend	1	MtrVelBlend	1	✔	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔	

Test Step 2.24 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2
MtrVel3_OsBufPos_Cnt_M_u08[3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.52
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.56
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.64
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.68
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.8
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.88
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.96
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.04
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.16
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.24
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.28
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.32
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.36
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160
MtrVel_HwVel_HwRadpS_M_f32	27.92102087
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-870.6542583
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	40358
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.37
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	42358
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1343.38196	-1343.38208 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	42358	42358	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1343.38196	-1343.38208 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1343.38196	-1343.38208 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1343.38196	-1343.38208 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.25 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.77
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.82
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.07
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.77
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8245
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8125
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	8005
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	7885
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	7765
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	7645
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	7525
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	7405
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7285
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7165
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7045
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	6925
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	6805
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	6685
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	6565
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6445
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3640
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3760
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3880
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4000
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	3160
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	3280
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	3400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3520
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4020
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	4140
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4260
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4380
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4500
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3780
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3900
MtrVel_HwVel_HwRadpS_M_f32	-13.75014228
MtrVel_MtrVelMRF_MtrRadpS_M_f32	421.9869356
MtrVel_OldPosBuf_Cnt_M_u08	2

Name	Input Value
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	32455
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.38
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	34455
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	320.796387	320.7963257 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	34455	34455	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-320.796387	-320.7963257 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	320.796387	320.7963257 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	320.796387	320.7963257 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.26 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.78	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.9	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.14	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.26	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.32	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.44	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.5	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.68
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.74
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.8
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.86
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.92
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.98
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel_HwVel_HwRadpS_M_f32	-6.301655293
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1105.133117
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.7
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2343
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.39
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4343
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1034.97485	1034.974731 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4343	4343	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1034.97485	1034.97473 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1034.97485	1034.974731 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1034.97485	1034.974731 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.27 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	5	
MtrVel3_OsBufPos_Cnt_M_u08[3]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.45	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8470	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8480	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	8490	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	8500	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	8430	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	8440	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	8450	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	8460	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	8710	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	8720	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	8730	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	8740	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	8750	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	8680	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	8690	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	8700	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	20	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	30
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	40
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	50
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	60
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	70
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	80
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-616.0772175
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5788
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.4
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7788
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	399.105316	399.1052551 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7788	7788	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-399.105316	-399.1052551 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	399.105316	399.1052551 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	399.105316	399.1052551 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.28 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	4125
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	4000
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3875
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3750
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3625
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3500
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3375
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3250
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3125
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3000
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2875
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2750
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2625
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2500
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2375
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2250
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7625
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7750
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7875
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8000
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7125
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7250
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7375
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	11375
MtrVel_HwVel_HwRadpS_M_f32	17.41422999
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-657.4433444
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54544
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.41
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	56544

Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	763.283752	763.2836914 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	56544	56544	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.29 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.64	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1150	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	850
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	900
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	950
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1050
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1700
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1800
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1450
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1550
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1650
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	4328
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	4278
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	4228
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4178
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4128
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4078
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4028
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3978
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3928
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3878
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3828
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3778
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3728
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3678
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3628
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3578
MtrVel_HwVel_HwRadpS_M_f32	-0.304720819
MtrVel_MtrVelMRF_MtrRadpS_M_f32	732.5839302
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	63534
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.42
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	65534
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	827.414246	827.4141846 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	65534	65534	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.30 (Repeat Count = 1)

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6481
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6543
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6109
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6171
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6233
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6295
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6357
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6419
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	19800
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	19755
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	19706
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	19676
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	19603
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	19578
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	19504
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	19460
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	5500
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	5400
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	5300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	5200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5050
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5000

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	4960
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	7138
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	7200
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	7262
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	7324
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	7386
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6952
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	7014
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	7076
MtrVel_HwVel_HwRadpS_M_f32	28.83693385
MtrVel_MtrVelMRF_MtrRadpS_M_f32	698.9029244
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	443476
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.43
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	445676
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	868.882874	868.8828125 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	445676	445676	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-868.882874	-868.8828125 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	868.882874	868.8828125 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	868.882874	868.8828125 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.31 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	5
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwVel_HwRadpS_M_f32	-13.1049369
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-298.8190119
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	63355
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.44
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	65555
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-28.6764164	-28.6764221 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-65.1736755	-65.1736908 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	65555	65555	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-28.6764164	-28.6764221 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-28.6764164	-28.6764221 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.32 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9645
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9602
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9560
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9501
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9470
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9403

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9366
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9304
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7760
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7822
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7884
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7946
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7512
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7574
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7636
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7698
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6662
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6724
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6786
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6848
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6910
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6476
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6538
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6600
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4012
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3946
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3901
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3866
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3805
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3752
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3702
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3650
MtrVel_HwVel_HwRadpS_M_f32	-29.04110444
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-930.6708422
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	86466
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.45
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	88666
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-325.011383	-325.0114746 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	88666	88666	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	325.011383	325.0114746 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-325.011383	-325.0114746 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-325.011383	-325.0114746 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.33 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.21
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.27
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.39
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.45
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.51
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.63
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.66
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.75
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.81
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.93
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.99
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.05
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.11
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	11148
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11210
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	10776
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	10838
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	10900
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	10962
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11024
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11086
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10100
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	10070
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10010
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	9950
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	9904
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	9860
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	9800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	9770
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9173
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9235
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9297
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9359
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9421
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8987
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9049
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9111
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6415
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6325
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6225
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6175
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6110
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6060
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5960
MtrVel_HwVel_HwRadpS_M_f32	10.2496587
MtrVel_MtrVelMRF_MtrRadpS_M_f32	964.6684363
MtrVel_OldPosBuf_Cnt_M_u08	2

Name	Input Value
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.7
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3366
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.49
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	5566
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✓
MtrVel_MtrVelMRF_MtrRadpS_M_f32	771.913879	771.9138794 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✓
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5566	5566	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	✓

T			✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✓
CalcCoarseVel	1	CalcCoarseVel	1	✓
RegressionFit	1	RegressionFit	1	✓
MtrVelBlend	1	MtrVelBlend	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)	✓
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.52
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.56
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.64
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.68
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.8
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.88
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.96
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.04

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.16
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.24
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.28
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.32
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.36
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65352
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65414
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880
MtrVel_HwVel_HwRadpS_M_f32	-12.63669056
MtrVel_MtrVelMRF_MtrRadpS_M_f32	457.1572797
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54343
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.5
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	56543
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1025.73547	-1025.735352 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	56543	56543	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1025.73547	1025.735352 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1025.73547	-1025.735352 ± 0.0625	✓
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1025.73547	-1025.735352 ± 0.0625	✓

T					✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✓	
CalcCoarseVel	1	CalcCoarseVel	1	✓	
RegressionFit	1	RegressionFit	1	✓	
MtrVelBlend	1	MtrVelBlend	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.35 (Repeat Count = 1)		✓
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.77	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.07	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.37	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.42	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.77	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	661	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239
MtrVel_HwVel_HwRadpS_M_f32	39.80989385
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-93.04319823
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6897678
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.51
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	6899878
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	297.649353	297.649292 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6899878	6899878	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	297.649353	297.649292 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	297.649353	297.649292 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	297.649353	297.649292 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.36 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	5	
MtrVel3_OsBufPos_Cnt_M_u08[1]	6	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.48	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.66
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.78
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.9
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.14
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.26
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.38
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.44
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.68
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.74
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.8
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.86
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.92
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.98
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwVel_HwRadpS_M_f32	-10.45496923
MtrVel_MtrVelMRF_MtrRadpS_M_f32	179.1975787
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5577
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7777

Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_HwVel_HwRadpS_M_f32	-0	0 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1323.42847	1323.428223 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7777	7777	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1323.42847	-1323.428223 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1323.42847	1323.428223 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1323.42847	1323.428223 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.37 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.45
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9645

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9602
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9560
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9501
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9470
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9403
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9366
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9304
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7760
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7822
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7884
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7946
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7512
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7574
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7636
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7698
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6662
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6724
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6786
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6848
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6910
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6476
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6538
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6600
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4012
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3946
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3901
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3866
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3805
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3752
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3702
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3650
MtrVel_HwVel_HwRadpS_M_f32	-16.19755042
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-722.7552819
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2133
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	1
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4333
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-792.446411	-792.4464111 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4333	4333	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-792.446411	-792.4464111 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-792.446411	-792.4464111 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-792.446411	-792.4464111 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.38 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6897
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6776
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6700
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6603
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6575
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6508
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2695
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2757
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2819
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2881
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2943
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2509
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2571
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2633
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3825
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3887
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3949
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4011
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4073
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4135
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4197

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3763
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5085
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	5012
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4950
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4908
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4860
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	4805
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	4775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	4700
MtrVel_HwVel_HwRadpS_M_f32	-23.42079562
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1306.267611
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	75688
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.64
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	77888
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-33.4618645	-33.46188599 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-52.2841644	-52.28419876 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	77888	77888	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-33.4618645	-33.46188599 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-52.2841644	-52.28419876 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-52.2841644	-52.28419876 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-33.4618645	-33.46188599 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-52.2841644	-52.28419876 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.39 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.64
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800
MtrVel_HwVel_HwRadpS_M_f32	-42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-660.4303926
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	53233
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.67
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	55433
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-812.165649	-812.1655273 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	55433	55433	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	812.165649	812.1655273 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-812.165649	-812.1655273 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-812.165649	-812.1655273 ± 0.0625	✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.40 (Repeat Count = 1)		✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.62
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.82
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.12
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.52
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.62
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.82
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.02
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.22
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.42
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.72
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.92
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.02
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.12
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.22
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.32
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65352
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65414
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1222.594758
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	32378
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.7
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	34578
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	246.178558	246.1784821 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	34578	34578	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	246.178558	246.178482 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	246.178558	246.1784821 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	246.178558	246.1784821 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.41 (Repeat Count = 1) ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.34
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.45
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.56
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.67
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.89
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.33
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.88
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.99
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.43
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.54
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.65
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.98
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.09
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.2
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.31
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.53
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.64
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.75
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	661
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239
MtrVel_HwVel_HwRadpS_M_f32	0
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1201.403606
MtrVel_OldPosBuf_Cnt_M_u08	2

Name	Input Value
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87234
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.73
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	89434
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	577.249817	577.2498169 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	89434	89434	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-577.249817	-577.2498169 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	577.249817	577.2498169 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	577.249817	577.2498169 ± 0.0625	✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.42 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.96
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.16
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.4
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.88

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.24
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.36
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.48
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.6
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.84
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.96
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.08
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	999
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	888
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	777
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	666
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	555
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	444
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	333
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	222
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	925
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	825
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	725
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	625
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	525
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	425
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	325
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	225
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	666
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	232
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	294
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	356
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	418
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	480
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	542
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	604
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1042
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1104
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1166
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	732
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	794
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	856
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	918
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	980
MtrVel_HwVel_HwRadpS_M_f32	23.7894063
MtrVel_MtrVelMRF_MtrRadpS_M_f32	953.297722
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2378
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.76
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	10
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4578
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1311.35229	-1311.352173 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4578	4578	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1311.35229	1311.352173 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1311.35229	-1311.352173 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1311.35229	-1311.352173 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.43 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	5
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.9
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.16
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.33
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.59
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	843
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	793
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	743
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	693
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	543
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	493
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	443
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	393
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	447
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	407
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	367
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	327
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	287
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	247
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	207
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	167
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2796
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2362

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2424
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2486
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2548
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2610
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2672
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2734
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3672
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3734
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3796
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3362
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3424
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3486
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3548
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3610
MtrVel_HwVel_HwRadpS_M_f32	-40.36163938
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1131.737475
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	52133
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.79
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	12
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	12
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	54333
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	9.54013348	9.540119181 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	12.0761175	12.07610035 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54333	54333	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	9.54013348	9.540119181 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	12.0761175	12.07610035 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	12.0761175	12.07610035 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	9.54013348	9.540119181 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	12.0761175	12.07610035 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.44 (Repeat Count = 1)   ✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.82
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.96

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.24
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.52
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.08
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.22
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.36
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.5
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.78
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.06
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.2
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.34
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.48
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.75
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.8
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.04
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.17
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.7
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.8
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwVel_HwRadpS_M_f32	29.00406408
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	676689
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.82
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	13
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	14
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	678889

Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	**Actual Value**	**Expected Value**	**Result**
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	752.194763	752.1947632 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	678889	678889	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-752.194763	-752.1947632 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	752.194763	752.1947632 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	752.194763	752.1947632 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.45 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.87	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.62	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.77	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.92	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.07	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.22	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.37	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.52	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.67	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.82	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.97	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.12	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.27	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.42	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.57	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.7	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.87	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.01	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.16	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.31	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.4	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.9	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	835	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	765
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	730
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	695
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	625
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	590
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10345
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	10301
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10290
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	10200
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	10175
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	10154
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	10100
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	10060
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	712
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	774
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	836
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	898
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	960
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	1022
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	1084
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	650
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2922
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2984
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2550
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2612
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2674
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2736
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2798
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2860
MtrVel_HwVel_HwRadpS_M_f32	41.65612358
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2255
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.85
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	15
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4455
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-221.256454	-221.2564697 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4455	4455	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-221.256454	-221.25647 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-221.256454	-221.2564697 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-221.256454	-221.2564697 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.46 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.39	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.71	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.88	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.03	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.36	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.52	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.68	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.84	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.16	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.32	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.48	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.64	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.8	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.96	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.12	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.5	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.8	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.3	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.4	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1380	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7700	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500	
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435	
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400	

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-40.36163938
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	443466
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.88
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	445666
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	804.522583	804.522522 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	445666	445666	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	804.522583	804.522522 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	804.522583	804.522522 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	804.522583	804.522522 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 2.47 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.46	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.63	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.8	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.97	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.14	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.65	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.81	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.99	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.15
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.33
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.67
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.84
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5.01
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5.18
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.35
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.69
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.86
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.03
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.2
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.3
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.6
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.8
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.5
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.7
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	2069
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	2131
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	2193
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1759
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1821
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1883
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1945
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	2007
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3410
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	3370
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	3333
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	3290
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	3207
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	3175
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	3100
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2450
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2512
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2574
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2636
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2698
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2760
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2822
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2388
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6890
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6810
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6680
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6610
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6540
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6504
MtrVel_HwVel_HwRadpS_M_f32	13.81390291
MtrVel_MtrVelMRF_MtrRadpS_M_f32	469.5
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	754558
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.91
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	756758
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1097.73877	-1097.73877 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	2	2	✔
MtrVel_OsBufSelect_Cnt_M_u08	3	3	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	756758	756758	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1097.73877	1097.73877 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1097.73877	-1097.73877 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1097.73877	-1097.73877 ± 0.0625	✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 2.48 (Repeat Count = 1)	✔

Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	5
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.02
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.38
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.55
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.74
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.91
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.1
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.27
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.46
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.7
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.8
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.9
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.8
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.2
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwVel_HwRadpS_M_f32	39.10801578
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-421.600006
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	88583
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.94
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	90783
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-610.628174	-610.6282349 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	90783	90783	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-610.628174	-610.6282349 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-610.628174	-610.6282349 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-610.628174	-610.6282349 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Case 3: Path Test   ✔

Specification   Performance Metrics
(With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 3.1 4449 Cycles
TS 3.2 4557 Cycles
TS 3.3 4542 Cycles
TS 3.4 4407 Cycles
TS 3.5 4483 Cycles

Description   VECTOR DEStV   T   O

MtrVel_P   s   f   er   V   d   T   2   _P _ LS   2   T

MtrVel   s   fP   s   C   t_M
MtrVel   P   s   f   er   V   d   2

MtrVel   P   s   f   er   V   d   2

MtrVel_P   s   f   er   V   d   T   2   _P _ LS   2   lse

MtrVel_P   s   f   er   V   d   T   2   _P _ LS   2   lse

MtrVel   f   er   S   M   16

MtrVel_P   s   f   er   V   d   T   2   _P _ LS   2   lse
MtrVel_P   s   f   er   V   d   T   2   _P _ LS   2   T

T 3.   MtrVel_MtrV   d   S   T   2   0   T
T 3.   MtrVel_MtrV   d   S   T   2   0   T

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239
MtrVel_HwVel_HwRadpS_M_f32	-2.767454803
MtrVel_MtrVelMRF_MtrRadpS_M_f32	676.0312533
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6046
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.14
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7846
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	640.000244	640 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7846	7846	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	640.000244	640 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	640.000244	640 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	640.000244	640 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	✔
CalcCoarseVel	1	CalcCoarseVel		1	✔
RegressionFit	1	RegressionFit		1	✔
MtrVelBlend	1	MtrVelBlend		1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	✔

Test Step 3.2 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1380
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7700
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-227.1569001
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.2
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976

Name	Input Value
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	29.8089962	29.80897903 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	149.044983	149.0448914 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	3	3	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	29.8089962	29.80897903 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	29.8089962	29.80897903 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 3.3 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.12
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-555.555542	-555.555542 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	555.555542	555.555542 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-555.555542	-555.555542 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-555.555542	-555.555542 ± 0.0625	✔

T					✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔	
CalcCoarseVel	1	CalcCoarseVel	1	✔	
RegressionFit	1	RegressionFit	1	✔	
MtrVelBlend	1	MtrVelBlend	1	✔	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔	

Test Step 3.4 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.19
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.21
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.23
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.24
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.25
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.26
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.27
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.31
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.33
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.34
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.38
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.41
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.43
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65352
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65414
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770

Name	Input Value
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880
MtrVel_HwVel_HwRadpS_M_f32	10.93737113
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-573.3094931
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	293048
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.13
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	294848
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	2	2	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	294848	294848	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel	1	CalcCoarseVel	1	✔
RegressionFit	1	RegressionFit	1	✔
MtrVelBlend	1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Test Step 3.5 (Repeat Count = 1)	✔
Name	**Input Value**
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.9
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.16
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55

Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.33
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.59
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9770
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9832
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9398
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9460
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9522
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9584
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9646
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9708
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	8928
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	8990
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9052
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9114
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9176
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	8742
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	8804
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	8866
MtrVel_HwVel_HwRadpS_M_f32	-5.642567992
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1302.259389
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	85754
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.25
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	87654
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16

Name	Input Value
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	✔
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	✔
MtrVel_OldPosBuf_Cnt_M_u08	0	0	✔
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✔
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✔
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87654	87654	✔
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✔
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✔

T					✔
Actual Function		**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_MtrVel_Per1_CP0_CheckpointReached		1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	✔
CalcCoarseVel		1	CalcCoarseVel	1	✔
RegressionFit		1	RegressionFit	1	✔
MtrVelBlend		1	MtrVelBlend	1	✔
IntplVarXY_u16_u16Xu16Y_Cnt		1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✔
Rte_Call_MtrVel_Per1_CP1_CheckpointReached		1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	✔

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include
File	$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrVel_Digi\utp\contract -I$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I$(PROJECTROOT)\MtrVel_Digi\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrVel_Digi'	************************************UNIT TEST INFORMATION************************************ Name of Tester:Sunita Kore Code File(s) Under Test:Sa_MtrVel.c Code File(s) Version:5 Module Design Document:MotorVelocity_MDD.doc Module Design Document Version:6 Data Dictionary Version:3 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):1394 Total RAM Used (Bytes):42 Total CALS Used (Bytes):54 Special Test Requirements: Test Date:12/26/2014 Comments: "Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested. Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD. Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350. Note4: In function ""RegressionFit"" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements. Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference." ***

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes

Name	Value
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Check ✔

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 6.00 Cycles
Description	VECTOR DESCRIPTION: TS1.1 Output checked

Test Step 1.1 (Repeat Count = 1) ✔

Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	✔
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✔

Test Case 1: Boundary Check