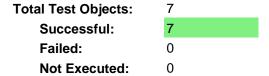
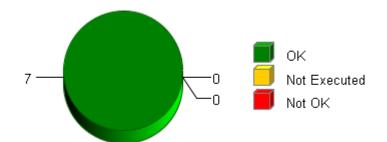


Summary

Overall Test Object Results (including Coverage)



Date: 2016-06-03 **Time:** 15:08:31+0530



Selected Project Items

Test Object "CBD UnitTest/CDDInterface/CDDInterface Init1"

Test Object "CBD UnitTest/CDDInterface/CDDInterface Init2"

Test Object "CBD_UnitTest/CDDInterface/CDDInterface_Per1"

Test Object "CBD UnitTest/CDDInterface/CDDInterface Per2"

Test Object "CBD_UnitTest/CDDInterface/CDDInterface_Per4"

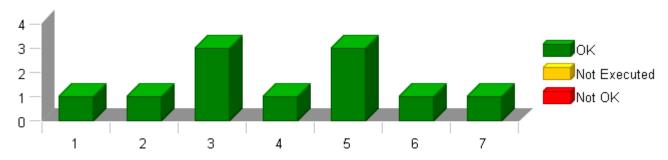
Test Object "CBD UnitTest/CDDInterface/CDDInterface Per5"

Test Object "CBD_UnitTest/CDDInterface/CDDPorts_ApplyMtrElecMechPol"

Used Test Environments

TI TMS 570 PLS UDE (Default)

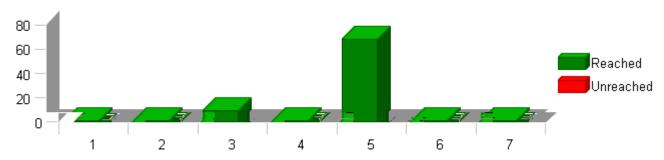
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

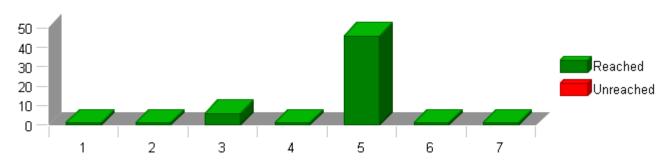


Statement (C0) Coverage: Total Statements for Each Test Object



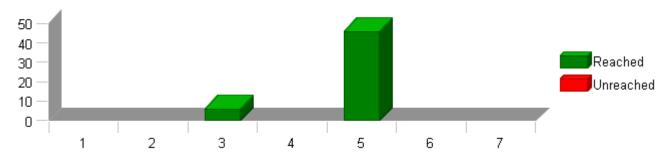
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

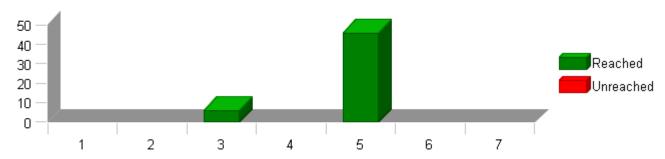


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



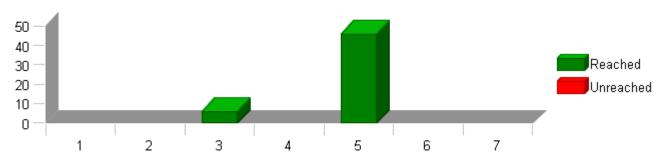
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

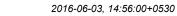
The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	МСС	Test Cases Resul
	CDDInterface_FIASA	100 %	100 %	100 %	100 %	100 %	11 of 11 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	11 of 11 passed
	CDDInterface	100 %	100 %	100 %	100 %	100 %	11 of 11 passed
1	CDDInterface Init1	100 %	100 %	-	-	-	1 of 1 passed
2	CDDInterface Init2	100 %	100 %	-	-	-	1 of 1 passed
3	CDDInterface Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
4	CDDInterface Per2	100 %	100 %	-	-	-	1 of 1 passed
5	CDDInterface Per4	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
6	CDDInterface Per5	100 %	100 %	-	-	-	1 of 1 passed
7	CDDPorts_ApplyMtrElecMechPol	100 %	100 %	-	-	-	1 of 1 passed

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CDDInterface_Init1





Project CDDInterface_FIASA

Module CDDInterface **Test Object** CDDInterface_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT) \StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'CDDInterface'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Sa_CDDInterface.c Code File(s) Version:4 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version:1
	Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):1714 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:6/3/2016 Comments:"Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2:""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

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Workspace File

CDDInterface_Init1

D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP





Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

CPU Cycles:
TS1.1 576.00 Cycles
TS1.2 558.00 Cycles
TS1.3 558.00 Cycles
TS1.3 558.00 Cycles
TS1.4 558.00 Cycles
TS1.5 558.00 Cycles
TS1.5 558.00 Cycles
TS1.6 558.00 Cycles
TS1.7 558.00 Cycles
TS1.8 558.00 Cycles
TS1.10 558.00 Cycles
TS1.11 558.00 Cycles
TS1.11 558.00 Cycles
TS1.12 558.00 Cycles
TS1.13 558.00 Cycles
TS1.14 558.00 Cycles
TS1.15 558.00 Cycles
TS1.16 558.00 Cycles
TS1.17 558.00 Cycles
TS1.17 558.00 Cycles
TS1.18 558.00 Cycles
TS1.19 558.00 Cycles
TS1.19 558.00 Cycles
TS1.20 558.00 Cycles
TS1.21 558.00 Cycles
TS1.21 558.00 Cycles
TS1.22 558.00 Cycles

Description

Vector Description:

TS1.1All min

TS1.1All min
TS1.2All max
TS1.3MtrElecMechPolarity_Cnt_s08==>Min
TS1.4MtrElecMechPolarity_Cnt_s08==>Max
TS1.5MtrPosPolarity_Cnt_s08==>Min
TS1.6MtrPosPolarity_Cnt_s08==>Max
TS1.5MtrPosPolarity_Cnt_s08==>Max
TS1.7Nvm_lgnCntr_Cnt_u16==>Max
TS1.8Nvm_lgnCntr_Cnt_u16==>Max
TS1.9Nvm_lgnCntr_Cnt_u16==>Pos
TS1.10NxtrMEC_Uls_G_enum==>ProductionMode
TS1.11NxtrMEC_Uls_G_enum==>PanufacturingMode
TS1.12NxtrMEC_Uls_G_enum==>EngineeringMode
TS1.13HET_P1ACC_1.memory.data_word==>Max
TS1.15HET_P1ACC_1.memory.data_word==>Min
TS1.15HET_P2ACC_1.memory.data_word==>Min
TS1.17HET_P2ACC_1.memory.data_word==>Min
TS1.19HET_P3ACC_1.memory.data_word==>Max
TS1.19HET_P3ACC_1.memory.data_word==>Pos
TS1.19HET_P3ACC_1.memory.data_word==>Min
TS1.20HET_P3ACC_1.memory.data_word==>Max
TS1.21HET_P3ACC_1.memory.data_word==>Max

Test Step 1.1 (Repeat Count = 1)

rest Stop 1.1 (Repeat South 1)	
Name	Input Value
Nvm_IgnCntr_Cnt_u16	0
NxtrMEC_UIs_G_enum	0
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecN$	-1
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPerson (CDDInterface_Init1.Rte_MtrPosPerson (CD$	-1
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	0
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	0
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	0

e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	0	0	•
PXAccumPrev_Cnt_M_u32[1]	0	0	~
PXAccumPrev_Cnt_M_u32[2]	0	0	•
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	-
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	0	0	~

T						
Actual Function	Count	Expected Function	Count	Result		
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~		
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•		





Test Step 1.2 (Repeat Count = 1)			V
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	65535		
NxtrMEC_UIs_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElect	1		
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPace_Results = 0.0000000000000000000000000000000000$	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4294967295		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	536870911	536870911	~
PXAccumPrev_Cnt_M_u32[1]	536870911	536870911	~
PXAccumPrev_Cnt_M_u32[2]	536870911	536870911	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	65535	65535	~

T .						
Actual Function	Count	Expected Function	Count	Result		
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~		
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓		

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_IgnCntr_Cnt_u16	15968		
NxtrMEC_UIs_G_enum	1		
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrElect	v -1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosF	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	4023279872		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3318966016		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4084006144		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	•
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	502909984	502909984	~
PXAccumPrev_Cnt_M_u32[1]	414870752	414870752	~
PXAccumPrev_Cnt_M_u32[2]	510500768	510500768	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_	15968	15968	~

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

Test Step 1.4 (Repeat Count = 1)	✓
Name	Input Value
Nvm_IgnCntr_Cnt_u16	31442
NxtrMEC_UIs_G_enum	0
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecN	1
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPerson (CDDInterface_CDDInterface_Init1.Rte_MtrPosPerson (CDDInterface_Init1.Rte_MtrPosPerson (CDDINTerface_Init1.Rte_Mtr$	1
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2734137344
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1686146560

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CDDInterface_Init1

Name	Input Value		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3851144192		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	•
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	•
PXAccumPrev_Cnt_M_u32[0]	341767168	341767168	~
PXAccumPrev_Cnt_M_u32[1]	210768320	210768320	~
PXAccumPrev_Cnt_M_u32[2]	481393024	481393024	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	•
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	31442	31442	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	22027		
NxtrMEC_Uls_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPe	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	125461504		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2418913792		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2585546752		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	15682688	15682688	~
PXAccumPrev_Cnt_M_u32[1]	302364224	302364224	~
PXAccumPrev_Cnt_M_u32[2]	323193344	323193344	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte IgnCnt C	22027	22027	~

Τ				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	-

Test Step 1.6 (Repeat Count = 1)			V
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	46683		
NxtrMEC_UIs_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElect	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPe	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	62887680		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	543010048		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2324288256		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~

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Name	Actual Value	Expected Value	Result
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	7860960	7860960	•
PXAccumPrev_Cnt_M_u32[1]	67876256	67876256	~
PXAccumPrev_Cnt_M_u32[2]	290536032	290536032	•
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	•
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte IgnCnt C	46683	46683	~

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	0		
NxtrMEC_UIs_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElect	v -1		
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosFace_Init1.Rte_MtrPosFace_Init1.Rte_Mtr$	h -1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1693420288		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1943306496		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3786180352		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	•
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	211677536	211677536	~
PXAccumPrev_Cnt_M_u32[1]	242913312	242913312	~
PXAccumPrev_Cnt_M_u32[2]	473272544	473272544	✓
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_lgnCnt_	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

Test Step 1.8 (Repeat Count = 1)			a
Name	Input Value		· ·
Nvm IgnCntr Cnt u16	65535		
NxtrMEC UIs G enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecN	1		
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrPosPe	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2495357440		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1466253312		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	1567960576		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	✓
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	•
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	311919680	311919680	~
PXAccumPrev_Cnt_M_u32[1]	183281664	183281664	~
PXAccumPrev_Cnt_M_u32[2]	195995072	195995072	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	65535	65535	~





Т				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_IgnCntr_Cnt_u16	60140		
NxtrMEC_UIs_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrPosPe	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2557631744		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3798926080		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	1098332416		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	319703968	319703968	~
PXAccumPrev_Cnt_M_u32[1]	474865760	474865760	~
PXAccumPrev_Cnt_M_u32[2]	137291552	137291552	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	•
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	60140	60140	~

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	13077		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1140567040		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2417867264		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4284291072		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	•
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	142570880	142570880	•
PXAccumPrev_Cnt_M_u32[1]	302233408	302233408	~
PXAccumPrev_Cnt_M_u32[2]	535536384	535536384	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	~
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte IgnCnt C	13077	13077	~

T						
Actual Function	Count	Expected Function	Count	Result		
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~		
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•		



Test Step 1.11 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	24670		
NxtrMEC_UIs_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPe	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	3445341952		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3400373504		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	937917184		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	430667744	430667744	~
PXAccumPrev_Cnt_M_u32[1]	425046688	425046688	~
PXAccumPrev_Cnt_M_u32[2]	117239648	117239648	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	24670	24670	~

Τ						
Actual Function	Count	Expected Function	Count	Result		
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~		
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓		

Test Step 1.12 (Repeat Count = 1)				
Name	Input Value			
Nvm_IgnCntr_Cnt_u16	58191			
NxtrMEC_Uls_G_enum	2			
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrElecM	-1			
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPace_RB.Rte_Sa_CDDInterface_RB.Rte_RB.R$	1			
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2101839360			
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3223979008			
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	664031744			
Name	Actual Value	Expected Value	Result	
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~	
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~	
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~	
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~	
CDD_Vecu_Volt_G_f32[0]	5	5	~	
CDD_Vecu_Volt_G_f32[1]	5	5	~	
PXAccumPrev_Cnt_M_u32[0]	262729920	262729920	~	
PXAccumPrev_Cnt_M_u32[1]	402997376	402997376	~	
PXAccumPrev_Cnt_M_u32[2]	83003968	83003968	~	
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~	
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~	
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	58191	58191	~	

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.13 (Repeat Count = 1)	✓
Name	Input Value
Nvm_IgnCntr_Cnt_u16	27158
NxtrMEC_UIs_G_enum	1
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecN	-1
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPerson (CDDInterface_CDDInterface_Init1.Rte_MtrPosPerson (CDDInterface_Init1.Rte_MtrPosPerson (CDDINTerface_Init1.Rte_Mtr$	1
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	0
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2687547136

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Name	Input Value		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3165244672		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	0	0	~
PXAccumPrev_Cnt_M_u32[1]	335943392	335943392	~
PXAccumPrev_Cnt_M_u32[2]	395655584	395655584	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	•
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	27158	27158	~

Τ				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.14 (Repeat Count = 1)			~
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	5870		
NxtrMEC_UIs_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPe	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2930286080		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	1091918848		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	536870911	536870911	~
PXAccumPrev_Cnt_M_u32[1]	366285760	366285760	~
PXAccumPrev_Cnt_M_u32[2]	136489856	136489856	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte IgnCnt C	5870	5870	~

T					
Actual Function	Count	Expected Function	Count	Result	
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~	
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	-	

Test Step 1.15 (Repeat Count = 1)			
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	64739		
NxtrMEC_UIs_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPeters	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	2167769344		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2812078336		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2563682048		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~

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Name	Actual Value
CDD_Vecu_Volt_G_f32[1]	5

Name	Actual Value	Expected Value	Result
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	270971168	270971168	•
PXAccumPrev_Cnt_M_u32[1]	351509792	351509792	~
PXAccumPrev_Cnt_M_u32[2]	320460256	320460256	•
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	✓
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte IgnCnt C	64739	64739	~

Τ				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	-

Test Step 1.16 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_IgnCntr_Cnt_u16	30094		
NxtrMEC_UIs_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1554131456		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	0		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3534665216		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	194266432	194266432	~
PXAccumPrev_Cnt_M_u32[1]	0	0	•
PXAccumPrev_Cnt_M_u32[2]	441833152	441833152	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	30094	30094	~

Т				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.17 (Repeat Count = 1)			√
Name	Input Value		
Nvm_IgnCntr_Cnt_u16	48106		
NxtrMEC_UIs_G_enum	1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElecM	-1		
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrPosPe	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1121993984		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	4294967295		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2448072960		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	•
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	140249248	140249248	~
PXAccumPrev_Cnt_M_u32[1]	536870911	536870911	~
PXAccumPrev_Cnt_M_u32[2]	306009120	306009120	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	1	1	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_C	48106	48106	~



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T					
Actual Function	Count	Expected Function	Count	Result	
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~	
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•	

Test Step 1.18 (Repeat Count = 1)			V
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	18372		
NxtrMEC_UIs_G_enum	2		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElect	-1		
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrPosP	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	3193041920		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	3714197504		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	2107826176		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	399130240	399130240	•
PXAccumPrev_Cnt_M_u32[1]	464274688	464274688	~
PXAccumPrev_Cnt_M_u32[2]	263478272	263478272	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	2	2	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	18372	18372	~

T				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

Test Step 1.19 (Repeat Count = 1)			~
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	14193		
NxtrMEC_Uls_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElect	v 1		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosP	1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1903748864		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	2778103040		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	5	5	•
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	237968608	237968608	✓
PXAccumPrev_Cnt_M_u32[1]	347262880	347262880	•
PXAccumPrev_Cnt_M_u32[2]	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_(14193	14193	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•





Test Step 1.20 (Repeat Count = 1)			~
Name	Input Value		
Nvm_lgnCntr_Cnt_u16	40629		
NxtrMEC_UIs_G_enum	0		
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrElect	ν 1		
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosPace_Results = 0.0000000000000000000000000000000000$	1 -1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	3616542208		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	1427065856		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	4294967295		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	452067776	452067776	~
PXAccumPrev_Cnt_M_u32[1]	178383232	178383232	~
PXAccumPrev_Cnt_M_u32[2]	536870911	536870911	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	40629	40629	~

T				V
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.21 (Repeat Count = 1)			✓
Name	Input Value		
Nvm_IgnCntr_Cnt_u16	62983		
NxtrMEC_Uls_G_enum	0		
Rte Task Init 10.Rte RB.Rte Sa CDDInterface CDDInterface Init1.Rte MtrElecM	1		
$Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_MtrPosParticles (CDDInterface_Init1) and (CDDInterfa$	-1		
e_HETPROGRAM1_UN.Program1_ST.P1ACC_1.memory.data_word	1453758720		
e_HETPROGRAM1_UN.Program1_ST.P2ACC_1.memory.data_word	4037074688		
e_HETPROGRAM1_UN.Program1_ST.P3ACC_1.memory.data_word	3323480832		
Name	Actual Value	Expected Value	Result
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0	0	~
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_CDDDataAccessBfr_Cnt_G_u16	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	5	5	~
PXAccumPrev_Cnt_M_u32[0]	181719840	181719840	~
PXAccumPrev_Cnt_M_u32[1]	504634336	504634336	~
PXAccumPrev_Cnt_M_u32[2]	415435104	415435104	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_Init_10.Rte_TB.Rte_I_CDDInterface_MEC_Cnt_enum.value	0	0	~
Rte_Task_Init_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Init1.Rte_IgnCnt_0	62983	62983	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•

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CDDInterface_Per5

Project CDDInterface_FIASA

 Module
 CDDInterface

 Test Object
 CDDInterface_Per5

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT) \StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'CDDInterface'	**************************************
	Name of Tester:Priyanka Shitole
	Code File(s) Under Test:Sa_CDDInterface.c
	Code File(s) Version:4
	Module Design Document:NA
	Module Design Document Version:NA
	Data Dictionary Version:1 Unit Test Plan Version:1
	Ontinization Level:Level 2
	Compiler (CodeGen) Version:TMS470 4.9.5
	Model Type:Excel Macro
	Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32
	Total FLASH Used (Bytes):1714
	Total RAM Used (Bytes):0
	Total CALS Used (Bytes):0
	Special Test Requirements:
	Test Date:6/3/2016
	Comments: "Note1: Inline Function defined in GlobalMacro.h is not unit tested.
	Note2:""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

CDDInterface_Per5

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Workspace File

D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModldxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	0		
CDD_PhaseAdvanceFinal_Cnt_G_u16	0		
Name	Actual Value	Expected Value	Result
CDD_ModldxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModldxFinal_Uls_G_u16p16[1]	0	0	~
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0	0	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModIdxFinal_Uls_G_u16p16[0]	1		
CDD_ModIndexFinal_UIs_G_u16p16	1		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535		
CDD_PhaseAdvanceFinal_Cnt_G_u16	65535		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	1	1	~
CDD_ModldxFinal_Uls_G_u16p16[1]	1	1	•
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535	65535	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535	65535	•

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.3 (Repeat Count = 1)	
Name	Input Value
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0
CDD_ModldxFinal_Uls_G_u16p16[0]	1
CDD_ModIndexFinal_Uls_G_u16p16	0
CDD_PhaseAdvFinal_Cnt_G_u16[0]	29088
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535

CDDInterface_Per5

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Name	Input Value		
CDD_PhaseAdvanceFinal_Cnt_G_u16	29088		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModldxFinal_Uls_G_u16p16[1]	1	1	~
CDD_PhaseAdvFinal_Cnt_G_u16[0]	29088	29088	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	65535	65535	~

Т					
Actual Function	Count	Expected Function	Count	Resul	
none	0	*** No Call Expected ***	0		

Test Step 1.4 (Repeat Count = 1)	Test Step 1.4 (Repeat Count = 1)		
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModldxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	2988		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887		
CDD_PhaseAdvanceFinal_Cnt_G_u16	17887		
Name	Actual Value	Expected Value	Result
CDD_ModldxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModldxFinal_Uls_G_u16p16[1]	0	0	~
CDD_PhaseAdvFinal_Cnt_G_u16[0]	2988	2988	~
CDD PhaseAdvFinal Cnt G u16[1]	17887	17887	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModldxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887		
CDD_PhaseAdvanceFinal_Cnt_G_u16	0		
Name	Actual Value	Expected Value	Result
CDD_ModldxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModldxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	0	0	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887	17887	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModldxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887		
CDD_PhaseAdvanceFinal_Cnt_G_u16	65535		
Name	Actual Value	Expected Value	Result
CDD_ModldxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModldxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	65535	65535	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	17887	17887	~



Τ					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModIdxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	21756		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148		
CDD_PhaseAdvanceFinal_Cnt_G_u16	61148		
Name	Actual Value	Expected Value	Result
CDD_ModldxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	21756	21756	✓
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148	61148	✓

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	0		
CDD_ModldxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	16818		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148		
CDD_PhaseAdvanceFinal_Cnt_G_u16	16818		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	0	0	~
CDD_ModIdxFinal_Uls_G_u16p16[1]	0	0	~
CDD_PhaseAdvFinal_Cnt_G_u16[0]	16818	16818	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	61148	61148	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModIdxFinal_Uls_G_u16p16[0]	1		
CDD_ModIndexFinal_Uls_G_u16p16	1		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	1307		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	5891		
CDD_PhaseAdvanceFinal_Cnt_G_u16	5891		
Name	Actual Value	Expected Value	Result
CDD_ModIdxFinal_Uls_G_u16p16[0]	1	1	~
CDD_ModIdxFinal_Uls_G_u16p16[1]	1	1	~
CDD_PhaseAdvFinal_Cnt_G_u16[0]	1307	1307	~
CDD PhaseAdvFinal Cnt G u16[1]	5891	5891	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

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Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFbkPthAccessBfr_Cnt_G_u16	1		
CDD_ModldxFinal_Uls_G_u16p16[0]	0		
CDD_ModIndexFinal_Uls_G_u16p16	0		
CDD_PhaseAdvFinal_Cnt_G_u16[0]	23492		
CDD_PhaseAdvFinal_Cnt_G_u16[1]	2028		
CDD_PhaseAdvanceFinal_Cnt_G_u16	2028		
Name	Actual Value	Expected Value	Result
CDD_ModldxFinal_Uls_G_u16p16[0]	0	0	✓
CDD_ModldxFinal_Uls_G_u16p16[1]	0	0	✓
CDD_PhaseAdvFinal_Cnt_G_u16[0]	23492	23492	~
CDD_PhaseAdvFinal_Cnt_G_u16[1]	2028	2028	~

T				~
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

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 Project
 CDDInterface_FIASA

 Module
 CDDInterface

 Test Object
 CDDInterface_Per4

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT) \StdDef\tinclude -I\$(Compiler Install Path)\tinclude

Name	Text
Module 'CDDInterface'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Sa_CDDInterface.c Code File(s) Version:4 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version:1 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):1714 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:6/3/2016 Comments:"Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2:""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0

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Attributes	
Name	Value
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS1.1 1048.00 Cycles TS1.2 391.00 Cycles

Description Vector Description:

TS1.1"Longest Execution Path: (MtrEleoMechPolarity_Cnt_T_s08 != PrevMtrElecMechPolarity_Cnt_M_s08)=>True
(D_DEFEATENGON_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>False
(D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>False (D_DEFEATENGON_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>False
(D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>False
(D_DEFEATHININTDIAG_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRONTDIAG_CNT_B32))=>False
(D_DEFEATHERMALDC_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False
(D_DEFEATEOTIMPACT_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False
(D_DEFEATEOTIMPACT_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False
(D_DEFEATEOTTHERMAL_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATEOTTHERMAL_CNT_B32))=>False
(D_DEFEATMTRVELNRMLRN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATMTRVELNRMLRN_CNT_B32))=>False
(D_DEFEATMTRPARMNRMLRN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRRPARMNRMLRN_CNT_B32))=>False
(D_DEFEATTRRRMP_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRRRMP_CNT_B32))=>False
(D_DEFEATMODIDX_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRQRMP_CNT_B32))=>False
(D_DEFEATPULLCOMP_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWILLIMBREJ_CNT_B32))=>False
(D_DEFEATPARKAST_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>False
(D_DEFEATDRIVINGDYN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False
(D_DEFEATHYSTCOMP_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False
(D_DEFEATHYSTADD_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>False
(D_DEFEATRTURN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRURN_CNT_B32))=>False
(D_DEFEATRTURN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRURN_CNT_B32))=>False
(D_DEFEATRECLRN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRONNING_CNT_B32))=>False
(D_DEFEATRECLRN_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRONNING_CNT_B32))=>False
(D_DEFEATRONNING_CNT_B32)== (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRONNING_CNT_B32))=>Frue
(D_DEFEATRONNING_CNT_B32)== (TS1.2"Shortest Execution Path:

(MtrElecMechPolarity_Cnt_T_s08 != PrevMtrElecMechPolarity_Cnt_M_s08)=>False

(D_DEFEATENGON_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATENGON_ENT_B32))=>False

(D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATFREQDEPDMP_CNT_B32))=>False

(D_DEFEATFREQDEPDMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATKININTDIAG_CNT_B32))=>False

(D_DEFEATKININTDIAG_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTHERMALDC_CNT_B32))=>False

(D_DEFEATTHERMALDC_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False

(D_DEFEATEOTIMPACT_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTIMPACT_CNT_B32))=>False

(D_DEFEATMTRVELNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATEOTITHERMAL_CNT_B32))=>False

(D_DEFEATMTRVELNRMLRN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRVELNRMLRN_CNT_B32))=>False

(D_DEFEATTRQRMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMTRPARMNRMLRN_CNT_B32))=>False

(D_DEFEATMODIDX_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTRQRMP_CNT_B32))=>False

(D_DEFEATMODIDX_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTRQRMP_CNT_B32))=>False

(D_DEFEATPULLCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTPULLCOMP_CNT_B32))=>False

(D_DEFEATPHLIMBREJ_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATTPULLGOMP_CNT_B32))=>False

(D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATWHLIMBREJ_CNT_B32))=>False

(D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATWHLIMBREJ_CNT_B32))=>False

(D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False

(D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False TS1.2"Shortest Execution Path: (D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>False (D_DEFEATDRIVINGDYN_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDRIVINGDYN_CNT_B32))=>False (D_DEFEATHYSTCOMP_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTCOMP_CNT_B32))=>False (D_DEFEATHYSTADD_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>False (D_DEFEATRETURN_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRETURN_CNT_B32))=>False (D_DEFEATDAMPING_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>False (D_DEFEATLIMIT_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>False (D_DEFEATRICLRN_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRICLRN_CNT_B32))=>False (D_DEFEATRICLRN_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRICLRN_CNT_B32))=>False (D_DEFEATRIVIITIGNMASK_CNT_B32) == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATRIVIITIGNMASK_CNT_B32) => (CDD_EOLSRlComSvcDft_C (D_DEFEATMULTIGNMASK_CNT_B32 == (CDD_EOLSrlComSvcDft_Cnt_G_b32 & D_DEFEATMULTIGNMASK_CNT_B32))=>False"

Test Step 1.1 (Repeat Count = 1)	✓
Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	-2.98521
CDD_EOLSrlComSvcDft_Cnt_G_b32	1972815360
CDD_EnergyModeState_Cnt_G_u08	1
MfgDiagInhibit_Uls_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1

Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-2.98520994	-2.98521	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HystC	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitSalander (CDDInterface_Per4.Rte_LimitSalander (CDDInterface) (CDDInterface_Per4.Rte_LimitSalander (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInter$	1	1	~

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Test Step 1.2 (Repeat Count = 1)

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCol	1972815360	1972815360	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	~

T			V	
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	0		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte DftAss	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnloberserver and the property of the pro$	-16	-16	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngOInterface_CDDInterface_Per4.Rte_EngOInterf$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HysAc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Multig		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCol		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	

T .				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Case 2: Boundary Test

Specification

```
Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
```

TS2.1 391.00 Cycles
TS2.2 399.00 Cycles
TS2.3 975.00 Cycles
TS2.3 975.00 Cycles
TS2.4 1011.00 Cycles
TS2.5 515.00 Cycles
TS2.6 432.00 Cycles
TS2.7 420.00 Cycles
TS2.8 441.00 Cycles
TS2.10 442.00 Cycles
TS2.10 442.00 Cycles
TS2.11 376.00 Cycles
TS2.11 376.00 Cycles
TS2.12 987.00 Cycles
TS2.13 461.00 Cycles
TS2.14 865.00 Cycles
TS2.15 383.00 Cycles
TS2.15 486.00 Cycles
TS2.17 934.00 Cycles
TS2.18 968.00 Cycles
TS2.19 434.00 Cycles
TS2.19 434.00 Cycles
TS2.19 434.00 Cycles
TS2.19 424.00 Cycles
TS2.19 424.00 Cycles
TS2.19 424.00 Cycles
TS2.20 910.00 Cycles
TS2.21 462.00 Cycles

Description Vector Description:

TS2.1All min
TS2.2all max
TS2.3MtrElecMechPolarity_Cnt_s08==>Min
TS2.4MtrElecMechPolarity_Cnt_s08==>Max
TS2.5PrevMtrElecMechPolarity_Cnt_M_s08==>Min
TS2.6PrevMtrElecMechPolarity_Cnt_M_s08==>Max
TS2.7CDD_DftAsstTbl_Cnt_G_lgc==>Min
TS2.6CDD_DftAsstTbl_Cnt_G_lgc==>Min
TS2.8CDD_DthAsstTbl_Cnt_G_lgc==>Max
TS2.9CDD_DwnldAsstGain_Uls_G_f32==>Min
TS2.10CDD_DwnldAsstGain_Uls_G_f32==>Pos
TS2.10CDD_DwnldAsstGain_Uls_G_f32==>Pos
TS2.12CDD_DwnldAsstGain_Uls_G_f32==>Neg
TS2.13CDD_DwnldAsstGain_Uls_G_f32==>Neg
TS2.14CDD_EOLSrlComSvcDft_Cnt_G_b32==>Min
TS2.15CDD_EOLSrlComSvcDft_Cnt_G_b32==>Max
TS2.17CDD_EOLSrlComSvcDft_Cnt_G_b32==>Pos
TS2.17CDD_EnergyModeState_Cnt_G_u08==>Min
TS2.18CDD_EnergyModeState_Cnt_G_u08==>Max
TS2.19CDD_EnergyModeState_Cnt_G_u08==>Max
TS2.20MfgDiagInhibit_Uls_G_lgc==>Min
TS2.21MfgDiagInhibit_Uls_G_lgc==>Max

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	0
CDD_DwnldAsstGain_Uls_G_f32	-16
CDD_EOLSrlComSvcDft_Cnt_G_b32	0
CDD_EnergyModeState_Cnt_G_u08	0
MfgDiagInhibit_Uls_G_lgc	0
PrevMtrElecMechPolarity_Cnt_M_s08	-1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1

€ -1			
Actual Value	Expected Value	Result	
0	0	~	
-1	-1	~	
0	0	~	
0	0	•	
0	0	~	
-16	-16	•	
0	0	~	
0	0	~	
0	0	~	
0	0	•	
0	0	~	
0	0	~	
0	0	~	
0	0	~	
0	0	~	
0	0	•	
0	0	~	
0	0	•	
0	0	~	
0	0	•	
0	0	~	
0	0	~	
0	0	~	
0	0	~	
0	0	~	
	Actual Value 0 -1 0 0 0 -16 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Actual Value Expected Value 0 0 -1 -1 0 0 0 0 -16 -16 0 0 <t< td=""></t<>	

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.2 (Repeat Count = 1) ✓					
Name	Input Value				
CDD_DftAsstTbl_Cnt_G_lgc	1				
CDD_DwnldAsstGain_Uls_G_f32	16				
CDD_EOLSrlComSvcDft_Cnt_G_b32	4294967295				
CDD_EnergyModeState_Cnt_G_u08	3				
MfgDiagInhibit_Uls_G_lgc	1				
PrevMtrElecMechPolarity_Cnt_M_s08	1				
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1				
Name	Actual Value	Expected Value	Result		
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~		
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~		
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Drivin	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlo	16	16	•		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	1	1	•		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	1	1	•		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	•		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAd	1	1	~		
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HystC	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~		
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte PullCo	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4294967295	4294967295	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	1	1	~		
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRresults (Application of the Computation of the Computat$	1	1	~		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	1	1	~		

Τ				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.3 (Repeat Count = 1)			*
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-2.98521		
CDD_EOLSrlComSvcDft_Cnt_G_b32	1972815360		
CDD_EnergyModeState_Cnt_G_u08	1		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface	e_Per4.Rte_MtrEle -1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	•
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface	e Per4.Rte Damp 1	1	•





Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs:	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-2.98520994	-2.98521	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT$	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EngO	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC$	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc$	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrPa	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1972815360	1972815360	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte TrqRr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_WhlIm	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-6.06063		
CDD_EOLSrlComSvcDft_Cnt_G_b32	494039296		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEl	€ 1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	-
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Driving	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnle	-6.06062984	-6.06063	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTI	r 0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EOTT	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energy	3	3	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngC	0	0	✓
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqDinterface_CDDInterface_Per4.Rte_FreqDinterface_Per4.R$	0	0	✓
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLine (CDDInterface_Per4.Rte_FricLine (CDDInterface)) (CDDInterface_Per4.Rte_FricLine (CDDInterface)) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterface) (CDDInterf$	r 1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysA	α 0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystOrgenset (CDDInterface_Per4.Rte_HystOrgenset (CDDInterface_Per4.Rt$	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInterface_CDDInterface_Per4.Rte_KinInterface_CDDInterface_Per4.Rte_KinInterface_Per4.	t 0	0	•
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitStates and the states are also as a finite of the states and the states are also as a finite of the states are also as$	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MfgD	i 0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modle and the control of the$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe		0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	4 1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Returned and the property of the property $	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	5 1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte SrlCo	494039296	494039296	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therr		0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRi	n 1	1	<u> </u>

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Name	Actual Value	Expected Value	Result
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Whllm	0	0	~

Т				V
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 2.5 (Repeat Count = 1) ✓				
Name	Input Value			
CDD_DftAsstTbl_Cnt_G_lgc	0			
CDD_DwnldAsstGain_Uls_G_f32	13.6616			
CDD_EOLSrlComSvcDft_Cnt_G_b32	1621525760			
CDD_EnergyModeState_Cnt_G_u08	3			
MfgDiagInhibit_Uls_G_lgc	0			
PrevMtrElecMechPolarity_Cnt_M_s08	-1			
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1			
Name	Actual Value	Expected Value	Result	
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	~	
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	0	0	•	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	13.6616001	13.6616	•	
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EOTIr	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	~	
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO$	0	0	•	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~	
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~	
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte LimitS	1	1	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe		0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg		0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~	
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte RxMs	0	0	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1621525760	1621525760	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~	
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$		1	~	
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whilm	0	0	~	

Т				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-0.428422		
CDD_EOLSrlComSvcDft_Cnt_G_b32	1308570624		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	~

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-0.428422004	-0.428422	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte FricLr	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitSalander and the control of the contro$	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc$	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MultIg	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkAnderselbergeren auch and the property of the property$	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1308570624	1308570624	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Thern	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-8.70177		
CDD_EOLSrlComSvcDft_Cnt_G_b32	2401253376		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_UIs_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Damp	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlo	-8.70176983	-8.70177	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLrescord (CDDInterface_Per4) and (CDDInterface_Per4) a$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAt	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystCDInterface_Per4.Rte_Hys$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIcelland $	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrVe	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCol		2401253376	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Whilm	1	1	~





T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-0.315127		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4246608640		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlo	-0.315126985	-0.315127	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIn	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MfgDi	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Returned and the property of the property $	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte SrlCo	4246608640	4246608640	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRresults (Application of the Computation of the Computat$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	3700780288		
CDD_EnergyModeState_Cnt_G_u08	1		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivings and the property of the property $	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlober (CDDInterface_Per4.Rte_Dwnlober) (CDDInterface_Per4.Rte$	-16	-16	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EOTIr	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT_Results = 1000 to 100$	0	0	~
			✓

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC$	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte KinInt	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Returi	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	3700780288	3700780288	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	1	1	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	2547153920		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Dwnlc	16	16	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HystC	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	2547153920	2547153920	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Thern	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	6.5678		
CDD_EOLSrlComSvcDft_Cnt_G_b32	1897352960		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_UIs_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Dwnlo	6.56780005	6.5678	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	2	2	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAd	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HystC	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCol	1897352960	1897352960	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	0		
CDD_EOLSrlComSvcDft_Cnt_G_b32	3025458688		
CDD_EnergyModeState_Cnt_G_u08	1		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damperschafter=0.00000000000000000000000000000000000$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAsserter_Asse$	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Drivin	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnloberserver (CDDInterface_Per4.Rte_Dwnloberserver) (CDDInterface$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTINTERFACE_PER4.Rte_EOTINTERFACE_PER4.Rt$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT_Results = 1000 to 100$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energent (Compared to the Compared to the $	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngOInterface_Per4.Rte_$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLrescord (CDDInterface_Per4.Rte_FricLrescord (CDDInterface_Per4.Rt$	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HysAc	1	1	~

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr 1 Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whilir 1

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrVe	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	3025458688	3025458688	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Whllm	0	0	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	•
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-5.456		
CDD_EOLSrlComSvcDft_Cnt_G_b32	2910339328		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_UIs_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlo	-5.45599985	-5.456	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIn	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLrescord (CDDInterface_Per4) and (CDDInterface_Per4) a$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitStarter = CDDInterface_Per4.Rte_LimitStarter = CD$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Modlo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVerserv$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo$	2910339328	2910339328	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Thern	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$		1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	

T				✓
Actual Function	Count	Expected Function	Coun	Result
none	0	*** No Call Expected ***	0	~



Test Step 2.14 (Repeat Count = 1)			
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	1.98992		
CDD_EOLSrlComSvcDft_Cnt_G_b32	0		
CDD_EnergyModeState_Cnt_G_u08	1		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrE	le 1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dam	p 0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte DftA	s: 0	0	✓

Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	1.98992002	1.98992	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Modic	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCol	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Thern	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whilm	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	•

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	-1.69437		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4294967295		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	-
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlo	-1.69437003	-1.69437	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIn	1	1	-
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EOTT	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energy	3	3	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	1	1	~

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgD	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullC	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4294967295	4294967295	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Whlln	1	1	~

T					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	2.48416		
CDD_EOLSrlComSvcDft_Cnt_G_b32	564369664		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1		
Name	Actual Value	Expected Value	Result
CDD ModldxSrlComSvcDft Cnt G u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Damp	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte DftAss	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	2.48415995	2.48416	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqC	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HysAc	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Multig	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	564369664	564369664	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whilm	1	1	

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 2.17 (Repeat Count = 1)		
Name	Input Value	
CDD_DftAsstTbl_Cnt_G_lgc	0	

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Name	Input Value
CDD_DwnldAsstGain_Uls_G_f32	0.259054
CDD_EOLSrlComSvcDft_Cnt_G_b32	1937593600
CDD_EnergyModeState_Cnt_G_u08	0
MfgDiagInhibit_UIs_G_lgc	1
PrevMtrElecMechPolarity_Cnt_M_s08	1
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1

Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	0.259054005	0.259054	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrVe	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1937593600	1937593600	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr$	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Whilm	0	0	~

Τ				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 2.18 (Repeat Count = 1)		✓	
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	6.94041		
CDD_EOLSrlComSvcDft_Cnt_G_b32	3032138752		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Dt. Teel, 40ms, 40 Dt. DD Dt. Co ODDIstories ODDIstories Dev4 Dt. Description	4	4	

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModIdxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAst	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc$	6.94041014	6.94041	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Energ	3	3	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInterface_Per4.Rte_FredDInter$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC$	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Modic	0	0	~

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	3032138752	3032138752	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	~

T and the second se				
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-6.93429		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4118705920		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrE	le 1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dam	p 1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftA:	s: 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivi	n 1	1	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Dwn	lc -6.93428993	-6.93429	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOT	Ir O	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOT	Т 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Ener	g 2	2	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Eng(0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Freq	□ 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricL	r 1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Hys/	λι 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Hyst	C 1	1	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Kinlr	nt O	0	✓
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Limit_Results = 0.0000000000000000000000000000000000$	S 1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgL	Di 0	0	✓
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Models and the property of the property of$	lc 1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrP	a 0	0	✓
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrV$	e 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multi	g 1	1	✓
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Parketers (CDDInterface_Per4.Rte_Parketers) (CDDInterface_Per4.Rte_Parkete$	A 1	1	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte PullO	Ct 1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Retu	n 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxM	s 0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCertification to the control of the cont$	4118705920	4118705920	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_There	m 0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRestriction (CDDInterface_Per4.Rte_TrqRestriction (CDDInterface_Per4.Rte_Tr$	tr 1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_While	m 0	0	

T T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.20 (Repeat Count = 1)	✓
Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	0
CDD_DwnldAsstGain_Uls_G_f32	-2.38078
CDD_EOLSrlComSvcDft_Cnt_G_b32	364862976

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi 0

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic 1 Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa 0

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe 0

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig 0

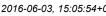
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte ParkA 1 Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCt 1 Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return 1 Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs 0

Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Therm 0

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr 0

Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCoi 364862976

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CDDInterface_Per4		1	tazorrat
Name	Input Value		
CDD_EnergyModeState_Cnt_G_u08	2		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_M	AtrEle 1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_D	Damp 1	1	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte D	OftAs: 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_D	Orivin 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_D	OwnIc -2.38077998	-2.38078	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_E	OTIr 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_E	EOTT 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_E	Energ 2	2	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_E	EngO 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_F	FreqC 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_F	ricLr 1	1	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte H	łysAc 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_H	HystC 1	1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_K	CinInt 0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_L	imitS 1	1	✓

0

0

0

0

0

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0

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Rte_lask_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.R	te_Whiin	1 1		~
T				V
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓

rest Step 2.21 (Repeat Count - 1)			
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-14.142		
CDD_EOLSrlComSvcDft_Cnt_G_b32	1029220608		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Damp	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc$	-14.1420002	-14.142	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO$	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAr	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIc$	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte MtrVe	0	0	✓

Test Step 2.21 (Repeat Count = 1)

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1029220608	1029220608	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Whllm	1	1	✓

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS3.1 376.00 Cycles TS3.2 399.00 Cycles TS3.3 958.00 Cycles

Description Vector Description:

Vector Description:

TS3.1"(MtrElecMechPolarity_Cnt_T_s08 |= PrevMtrElecMechPolarity_Cnt_M_s08)=>False
(D_DEFEATENGON_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATENGON_CNT_B32))=>False
(D_DEFEATENEGODEPDMP_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATENGODEPDMP_CNT_B32))=>False
(D_DEFEATKININTDIAG_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRININTDIAG_CNT_B32))=>False
(D_DEFEATRININTDIAG_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATRININTDIAG_CNT_B32))=>False
(D_DEFEATTHERMAL_DC_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTHERMAL_CNT_B32))=>False
(D_DEFEATEOTIMPACT_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTHERMAL_CNT_B32))=>False
(D_DEFEATTORTHERMAL_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTHERMAL_CNT_B32))=>False
(D_DEFEATTHORTMENEN_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTHERMAL_CNT_B32))=>False
(D_DEFEATTMORTMENEN_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRORMP_CNT_B32))=>False
(D_DEFEATTMORTMENEN_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRORMP_CNT_B32))=>False
(D_DEFEATTRORMP_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRORMP_CNT_B32))=>False
(D_DEFEATTWILLCOMP_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRORMP_CNT_B32))=>False
(D_DEFEATTWILLCOMP_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWILLCOMP_CNT_B32))=>False
(D_DEFEATTWILLMAREJ_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWILLMARES_CNT_B32))=>False
(D_DEFEATTHERMALGNT_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWILLMARES_CNT_B32))=>False
(D_DEFEATTHINTIAGNYN_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWILLMARES_CNT_B32))=>False
(D_DEFEATTHINTIAGNYN_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWILLMARES_CNT_B32))=>False
(D_DEFEATHYSTADD_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTWINTIAGNYN_CNT_B32))=>False
(D_DEFEATTHYSTADD_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRORNS_CNT_B32))=>False
(D_DEFEATTRORNS_CNT_B32 == (CDD_EOLSriComSvcDft_Cnt_G_b32 & D_DEFEATTRORNS_CNT_B32))=>False
(D (D_DEFEATMTRVELNRMLRN_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATMTRVELNRMLRN_CNT_B32))=>True (D_DEFEATMTRPARMNRMLRN_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATMTRPARMNRMLRN_CNT_B32))=>True (D_DEFEATMTRQRMP_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATTRQRMP_CNT_B32))=>True (D_DEFEATMOIDX_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATMOIDX_CNT_B32))=>True (D_DEFEATMOIDX_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATMOIDX_CNT_B32))=>True (D_DEFEATPULLCOMP_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATPULLCOMP_CNT_B32))=>True (D_DEFEATWHLIMBREJ_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATPULLCOMP_CNT_B32))=>True (D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>True (D_DEFEATPARKAST_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATPARKAST_CNT_B32))=>True (D_DEFEATPARTYSTCOMP_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATPARTYSTCOMP_CNT_B32))=>True (D_DEFEATHYSTCOMP_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATHYSTCOMP_CNT_B32))=>True (D_DEFEATRUN_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>True (D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATHYSTADD_CNT_B32))=>True (D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>True (D_DEFEATDAMPING_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATDAMPING_CNT_B32))=>True (D_DEFEATLIMIT_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>True (D_DEFEATRICLRN_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATLIMIT_CNT_B32))=>True (D_DEFEATRICLRN_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATRICLRN_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32 == (CDD_EOLSriComSvcDf_Cnt_G_b32 & D_DEFEATRICLRN_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_CNT_B32))=>True (D_DEFEATMULTIGNMASK_

CDD_DftAsstTbl_Cnt_G_lgc	0		
CDD_DwnldAsstGain_Uls_G_f32	-16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	0		
CDD_EnergyModeState_Cnt_G_u08	0		
MfgDiagInhibit_Uls_G_lgc	0		
PrevMtrElecMechPolarity_Cnt_M_s08	-1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	0	0	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte DftAss	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlober (CDDInterface_Per4.Rte_Dwnlober) (CDDInterface_Per4.Rte$	-16	-16	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr$	0	0	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HysAc	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystCDInterface_Per4.Rte_Hys$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt$	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitSupplies and the property of the prop$	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	0	0	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ModIcation (Compared to the Compared to th$	0	0	~

Input Value

Test Step 3.1 (Repeat Count = 1)

Name

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Name	Actual Value	Expected Value	Result
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Multig	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	✓
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	0	0	✓
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
CDD_DftAsstTbl_Cnt_G_lgc	1		
CDD_DwnldAsstGain_Uls_G_f32	16		
CDD_EOLSrlComSvcDft_Cnt_G_b32	4294967295		
CDD_EnergyModeState_Cnt_G_u08	3		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Drivin	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlo	16	16	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte EOTIr	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	3	3	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAd	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HystC	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte LimitS	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_PullCo	1	1	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Return	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	4294967295	4294967295	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	1	1	~
$Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRresults (Application of the Computation of the Computat$	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whilm	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
CDD_DftAsstTbl_Cnt_G_lgc	1
CDD_DwnldAsstGain_Uls_G_f32	-2.98521
CDD_EOLSrlComSvcDft_Cnt_G_b32	1972815360
CDD_EnergyModeState_Cnt_G_u08	1

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Name	Input Value		
MfgDiagInhibit_Uls_G_lgc	1		
PrevMtrElecMechPolarity_Cnt_M_s08	1		
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrEle	-1		
Name	Actual Value	Expected Value	Result
CDD_ModldxSrlComSvcDft_Cnt_G_u16	1	1	~
PrevMtrElecMechPolarity_Cnt_M_s08	-1	-1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Damp	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_DftAss	1	1	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte Drivin	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Dwnlc	-2.98520994	-2.98521	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTIr	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EOTT	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Energ	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_EngO	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FreqD	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_FricLr	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_HysAc	1	1	•
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte HystC	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_KinInt	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_LimitS	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MfgDi	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Modic	1	1	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrPa	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MtrVe	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_MultIg	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_ParkA	0	0	~
Rte Task 10ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per4.Rte PullCo	1	1	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Return	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_RxMs	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_SrlCo	1972815360	1972815360	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Therm	0	0	~
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_TrqRr	0	0	•
Rte_Task_10ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per4.Rte_Whllm	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDDPorts_ApplyMtrElecMechPol	1	CDDPorts_ApplyMtrElecMechPol	1	~
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	✓



Project CDDInterface_FIASA

Module CDDInterface

Test Object CDDPorts_ApplyMtrElecMechPol

Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT) \StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Name Module 'CDDInterface'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Sa_CDDInterface.c Code File(s) Version:4 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version:1 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):1714 Total RAM Used (Bytes):0 Special Test Requirements: Test Date:6/3/2016
	Comments:"Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2:""CBD Sandbox dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg



Workspace File

D:\Synergy Work Area\CDDInterface FIASA\UnitTestEny\config\UDE TMS570 DEBUG.WSI



Test Case 1: Boundary Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS1.1 513.00 Cycles TS1.2 478.00 Cycles

Description Vector Description:

TS1.1All Min TS1.2All Max

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
MtrElecMechPol_Cnt_s8	-1		
Name	Actual Value	Expected Value	Result
CDD_MtrElecPol_Cnt_G_s8	-1	-1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrElecMechPol_Cnt_s8	1		
Name	Actual Value	Expected Value	Result
CDD_MtrElecPol_Cnt_G_s8	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
CDD_ApplyPWMMtrElecMechPol	1	CDD_ApplyPWMMtrElecMechPol	1	~

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Project CDDInterface_FIASA

Module CDDInterface

Test Object CDDInterface_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT) \StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'CDDInterface'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Sa_CDDInterface.c Code File(s) Version:4 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version:1
	Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):1714 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:6/3/2016 Comments:"Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2:""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

CDDInterface_Per2

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Workspace File

D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

```
Specification
```

```
Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS1.1 88.00 Cycles
TS1.2 88.00 Cycles
TS1.3 88.00 Cycles
TS1.4 88.00 Cycles
TS1.4 88.00 Cycles
TS1.5 88.00 Cycles
TS1.6 88.00 Cycles
TS1.7 88.00 Cycles
TS1.7 88.00 Cycles
TS1.9 88.00 Cycles
TS1.18 88.00 Cycles
TS1.11 88.00 Cycles
TS1.12 88.00 Cycles
TS1.12 88.00 Cycles
TS1.13 88.00 Cycles
TS1.14 88.00 Cycles
TS1.15 88.00 Cycles
TS1.16 88.00 Cycles
TS1.17 88.00 Cycles
TS1.18 88.00 Cycles
TS1.19 88.00 Cycles
TS1.19 88.00 Cycles
TS1.21 88.00 Cycles
TS1.22 88.00 Cycles
TS1.23 88.00 Cycles
TS1.24 88.00 Cycles
TS1.25 88.00 Cycles
TS1.26 88.00 Cycles
TS1.27 88.00 Cycles
TS1.28 88.00 Cycles
TS1.29 88.00 Cycles
TS1.30 88.00 Cycles
TS1.31 88.00 Cycles
TS1.33 88.00 Cycles
TS1.34 88.00 Cycles
TS1.33 88.00 Cycles
TS1.34 88.00 Cycles
TS1.34 88.00 Cycles
```

Description

Vector Description:

```
TS1.1All min
TS1.2All max
```

TS1.3CDD_AppDataFwdPthAccessBfr_Cnt_G_u16==>Min TS1.4CDD_AppDataFwdPthAccessBfr_Cnt_G_u16==>Max TS1.5CDD_AppDataFwdPthAccessBfr_Cnt_G_u16==>Pos

TS1.6CDD_AppDataFw0FthAccessbff_Cnt TS1.6CommOffset_Cnt_u16==>Min TS1.7CommOffset_Cnt_u16==>Max TS1.8CommOffset_Cnt_u16==>Pos TS1.9MRFMtrVel_MtrRadpS_f32==>Min TS1.10MRFMtrVel_MtrRadpS_f32==>Max TS1.11MRFMtrVel_MtrRadpS_f32==>Pos

TS1.12MRFMtrVel_MtrRadpS_f32==>Zero

TS1.13MRFMtrVel_MtrRadpS_f32==>Neg TS1.14MtrCurr1TempOffset_Volt_f32==>Min TS1.15MtrCurr1TempOffset_Volt_f32==>Max

TS1.16MtrCurr1TempOffset_Volt_f32==>Pos TS1.17MtrCurr2TempOffset_Volt_f32==>Min TS1.18MtrCurr2TempOffset_Volt_f32==>Max TS1.19MtrCurr2TempOffset_Volt_f32==>Pos

TS1.20MtrPosPolarity_Cnt_s08==>Min TS1.21MtrPosPolarity_Cnt_s08==>Max

TS1.21MtrPosPolarity_Cnt_s08==>Max
TS1.22MtrTrqCmdSign_Cnt_s16==>Min
TS1.23MtrTrqCmdSign_Cnt_s16==>Min
TS1.23MtrTrqCmdSign_Cnt_s16==>Pos
TS1.24MtrTrqCmdSign_Cnt_s16==>Zero
TS1.25MtrTrqCmdSign_Cnt_s16==>Neg
TS1.27Rte_Mode_SystemState_Mode=RTE_MODE_StaMd_Mode_DISABLE
TS1.28Rte_Mode_SystemState_Mode=RTE_MODE_StaMd_Mode_OFF
TS1.29Rte_Mode_SystemState_Mode=RTE_MODE_StaMd_Mode_OPERATE
TS1.30Rte_Mode_SystemState_Mode=RTE_MODE_StaMd_Mode_WARMINIT
TS1.31Rte_Mode_SystemState_Mode=RTE_MODE_StaMd_Mode_WARMINIT

TS1.31Rte_Mode_SystemState_Mode=RTE_TRANSITION_StaMd_Mode

TS1.32Vecu_Volt_f32==>min TS1.33Vecu_Volt_f32==>Max TS1.34Vecu_Volt_f32==>Pos

Test Step 1.1 (Repeat Count = 1)		
Name	Input Value	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1350	
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350	
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0	
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0	
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0	
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0	
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	0	
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	0	
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	
CDD SysState Cnt G Enum[0]	0	

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Name	Input Value
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	5
CDD_Vecu_Volt_G_f32[1]	5
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1350
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	5
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte Commo	0
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurrent and the control of the control of$	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos$	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1350	-1350	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350	-1350	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0	0	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0	0	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	0	0	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	0	0	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	0	0	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	•
CDD_Vecu_Volt_G_f32[0]	5	5	✓
CDD_Vecu_Volt_G_f32[1]	5	5	~

T					V
Actual Function	Co	ount	Expected Function	Count	Result
none	0		*** No Call Expected ***	0	~

Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1350		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1350		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	5		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	5		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	5		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	5		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	7150		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	4		
CDD_SysState_Cnt_G_Enum[1]	4		
CDD_Vecu_Volt_G_f32[0]	31		
CDD_Vecu_Volt_G_f32[1]	31		
Rte_ModeMachine_StaMd_SystemState_Mode	4		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1350		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	31		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	7150		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCu	n 5		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrCu	n 5		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPo	s 1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrd	q(1		
Name	Actual Value	Expected Value	Result
CDD AppDataFwdPthAccessBfr Cnt G u16	0	0	

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1350	1350	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1350	1350	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	5	5	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	5	5	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	5	5	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	5	5	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	7150	7150	✓

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Name	Actual Value	Expected Value	Result
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150	7150	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	•
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	•
CDD_SysState_Cnt_G_Enum[0]	4	4	•
CDD_SysState_Cnt_G_Enum[1]	4	4	•
CDD_Vecu_Volt_G_f32[0]	31	31	•
CDD_Vecu_Volt_G_f32[1]	31	31	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1039.02002		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-952.192017		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.55293989		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.71498013		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.55293989		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3054		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2608		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0		
CDD_SysState_Cnt_G_Enum[0]	0		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	18		
CDD_Vecu_Volt_G_f32[1]	13.3718004		
Rte_ModeMachine_StaMd_SystemState_Mode	0		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-952.192017		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	13.3718004		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	2608		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.71498013		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.250820011		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1039.02002	-1039.02002	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-952.192017	-952.192017	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.55293989	3.55293989	•
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.71498013	4.71498013	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.55293989	3.55293989	•
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011	0.250820011	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3054	3054	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2608	2608	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0	0	~
CDD_SysState_Cnt_G_Enum[0]	0	0	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	18	18	~
	12 2710004		

Τ				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

13.3718004

13.3718004

CDD_Vecu_Volt_G_f32[1]





Test Step 1.4 (Repeat Count = 1)			~
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1194.65002		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	461.804993		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.01465011		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.89574003		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.01465011		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.89574003		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2806		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5061		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	0		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	7		
CDD_Vecu_Volt_G_f32[1]	21		
Rte_ModeMachine_StaMd_SystemState_Mode	0		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-131.302002		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	29.9487991		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commit	4315		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.61689997		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.18684006		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	1		
Name	Actual Value	Expected Value	Result

Title_Task_zins_To.title_tast.title_oa_obblittenace_obblittenace_i ciz.title_iviti Tiqt	'		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-131.302002	-131.302002	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	461.804993	461.804993	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.61689997	3.61689997	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.89574003	1.89574003	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.18684006	4.18684006	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.89574003	1.89574003	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4315	4315	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5061	5061	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	0	0	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	29.9487991	29.9487991	~
CDD_Vecu_Volt_G_f32[1]	21	21	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.5 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-16.8549995
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1260.43994
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.29583001
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.49289
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.29583001
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.49289
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4387
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1204
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	9
CDD_Vecu_Volt_G_f32[1]	5
Rte_ModeMachine_StaMd_SystemState_Mode	1

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Name	Input Value		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1107.20996		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	28.1583004		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	1169		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.768684983		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.66652989		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrTrq	-1		
Name	Actual Value	Expected Value	Result
CDD AppDetaEvidDthAccessDfr Cpt C u16	0	0	-

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1107.20996	1107.20996	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1260.43994	1260.43994	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.768684983	0.768684983	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.49289	1.49289	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.66652989	3.66652989	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.49289	1.49289	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1169	1169	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1204	1204	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	✓
CDD_Vecu_Volt_G_f32[0]	28.1583004	28.1583004	~
CDD_Vecu_Volt_G_f32[1]	5	5	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.6 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-191.468994
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1029.38
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.38572001
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.29025984
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.38572001
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.29025984
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4379
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4295
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	22
CDD_Vecu_Volt_G_f32[1]	19
Rte_ModeMachine_StaMd_SystemState_Mode	1
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	299.572998
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	29.7709007
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.86571002
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.250820011
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	299.572998	299.572998	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1029.38	1029.38	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.86571002	1.86571002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.29025984	4.29025984	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.29025984	4.29025984	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	0	0	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4295	4295	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓

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Name	Actual Value	Expected Value	Result
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	✓
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	29.7709007	29.7709007	✓
CDD_Vecu_Volt_G_f32[1]	19	19	✓

Т				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.7 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-282.661011
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	582.482971
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.67427003
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.54254007
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.67427003
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.31112003
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	145
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	27
CDD_Vecu_Volt_G_f32[1]	21.0223999
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	582.482971
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.0223999
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	7150
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	2.54254007
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCuri	1.31112003
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1

RIE_Task_zms_10.Rie_RB.Rie_Sa_CDDinienace_CDDinienace_Perz.Rie_	iviti i iqt - i		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-282.661011	-282.661011	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	582.482971	582.482971	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.67427003	1.67427003	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.54254007	2.54254007	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.67427003	1.67427003	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.31112003	1.31112003	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	145	145	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7150	7150	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	27	27	✓
CDD_Vecu_Volt_G_f32[1]	21.0223999	21.0223999	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.8 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-886.080994
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-92.3256989
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.36141992

CDDInterface_Per2

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Input Value CDD_MtrCurr1TempOffset_Volt_G_f32[1] 1.73335004 CDD_MtrCurr2TempOffset_Volt_G_f32[0] 2.36141992 CDD_MtrCurr2TempOffset_Volt_G_f32[1] 1.77063 CDD_MtrCurrOffComOffset_Cnt_G_u16[0] 626 CDD_MtrCurrOffComOffset_Cnt_G_u16[1] 5541 CDD_MtrPosPolarity_Cnt_G_s08[0] -1 CDD_MtrPosPolarity_Cnt_G_s08[1] -1 CDD_MtrTrqCmdSign_Cnt_G_s16[0] -1 CDD_MtrTrqCmdSign_Cnt_G_s16[1] -1 3 CDD_SysState_Cnt_G_Enum[0] CDD_SysState_Cnt_G_Enum[1] 1 CDD_Vecu_Volt_G_f32[0] 29 CDD_Vecu_Volt_G_f32[1] 23.5622005 Rte_ModeMachine_StaMd_SystemState_Mode 2 Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value -92.3256989 Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value 23.5622005 Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commt 5541 Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCuri 1.73335004 Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurl 1.77063 Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos -1

Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_	_MtrTrq(-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-886.080994	-886.080994	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-92.3256989	-92.3256989	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.36141992	2.36141992	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.73335004	1.73335004	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.36141992	2.36141992	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.77063	1.77063	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	626	626	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5541	5541	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	~
CDD_Vecu_Volt_G_f32[0]	29	29	~
CDD_Vecu_Volt_G_f32[1]	23.5622005	23.5622005	✓

T				~
Actual Function	Count	Expected Function	Count	t Result
none	0	*** No Call Expected ***	0	-

Test Step 1.9 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-966.247009
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.806077003
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.12197995
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.806077003
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.23109996
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3117
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7069
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	30
CDD_Vecu_Volt_G_f32[1]	24.0664997
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1350
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	24.0664997
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commi	7069
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrCur	3.12197995
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	1.23109996
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
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Name	Input Value		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrqt	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-966.247009	-966.247009	•
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-1350	-1350	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.806077003	0.806077003	•
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.12197995	3.12197995	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.806077003	0.806077003	•
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.23109996	1.23109996	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3117	3117	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	7069	7069	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	•
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0	0	•
CDD_SysState_Cnt_G_Enum[0]	2	2	•
CDD_SysState_Cnt_G_Enum[1]	2	2	•
CDD_Vecu_Volt_G_f32[0]	30	30	•
CDD_Vecu_Volt_G_f32[1]	24.0664997	24.0664997	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.10 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-827.492004
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1350
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.78215003
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.197907999
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.78215003
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.45770001
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1097
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5731
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	25
CDD_Vecu_Volt_G_f32[1]	20.7143993
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1350
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.7143993
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	5731
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.197907999
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	1.45770001
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	1

Rte_Task_zilis_10.Rte_Rb.Rte_Sa_CDDIIItellace_CDDIIItellace_Felz.Rte_Mti Tiqt	·		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-827.492004	-827.492004	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1350	1350	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.78215003	1.78215003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.197907999	0.197907999	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.78215003	1.78215003	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.45770001	1.45770001	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1097	1097	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	5731	5731	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	2	2	~
CDD_SysState_Cnt_G_Enum[1]	2	2	~
CDD_Vecu_Volt_G_f32[0]	25	25	~
CDD_Vecu_Volt_G_f32[1]	20.7143993	20.7143993	~



T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.11 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-656.984985		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	39.8400002		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.87867999		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.3110199		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.87867999		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.3110199		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3524		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2114		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	1		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	10		
CDD_Vecu_Volt_G_f32[1]	10		
Rte_ModeMachine_StaMd_SystemState_Mode	0		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	454.746002		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	26.1818008		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	1694		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.8696599		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrCurr	1.57879996		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPoserserver and the property of the prop$	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1		
Name	Actual Value	Expected Value	Result

Rte_Task_zms_10.Rte_Rb.Rte_Sa_CDDinterface_CDDinterface_Perz.Rte_Mtr Trqt	-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	454.746002	454.746002	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	39.8400002	39.8400002	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.8696599	4.8696599	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.3110199	4.3110199	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.57879996	1.57879996	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.3110199	4.3110199	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1694	1694	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2114	2114	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	0	0	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	26.1818008	26.1818008	~
CDD_Vecu_Volt_G_f32[1]	10	10	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.12 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1313.16003
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	0
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.91206002
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.28078985
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.91206002
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.93779993
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1204
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3541
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1

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CDDInterface_Per2

Name	Input Value
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	1
CDD_SysState_Cnt_G_Enum[1]	3
CDD_Vecu_Volt_G_f32[0]	23
CDD_Vecu_Volt_G_f32[1]	26.8638992
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	0
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	26.8638992
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm0	3541
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.28078985
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	2.93779993
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1313.16003	-1313.16003	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	0	0	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.91206002	2.91206002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.28078985	4.28078985	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.91206002	2.91206002	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.93779993	2.93779993	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1204	1204	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3541	3541	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	1	1	✓
CDD_SysState_Cnt_G_Enum[1]	3	3	✓
CDD_Vecu_Volt_G_f32[0]	23	23	✓
CDD_Vecu_Volt_G_f32[1]	26.8638992	26.8638992	✓

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.13 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-140.389999		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	914.89801		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.64402008		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.40644002		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.64402008		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.40644002		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3511		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6009		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	3		
CDD_SysState_Cnt_G_Enum[1]	3		
CDD_Vecu_Volt_G_f32[0]	7		
CDD_Vecu_Volt_G_f32[1]	15		
Rte_ModeMachine_StaMd_SystemState_Mode	4		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1107.76001		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	25.8773003		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	3311		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCui	4.39323997		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCui	0.250820011		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	(-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1107.76001	-1107.76001	→

914.89801

4.39323997

2.40644002

914.89801

4.39323997

2.40644002

CDD_MRFMtrVel_MtrRadpS_G_f32[1]

CDD_MtrCurr1TempOffset_Volt_G_f32[0]

CDD_MtrCurr1TempOffset_Volt_G_f32[1]

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Name	Actual Value	Expected Value	Result
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.40644002	2.40644002	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3311	3311	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6009	6009	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	4	4	~
CDD_SysState_Cnt_G_Enum[1]	3	3	~
CDD_Vecu_Volt_G_f32[0]	25.8773003	25.8773003	~
CDD_Vecu_Volt_G_f32[1]	15	15	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.14 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-780.861023		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1022.60999		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.25398004		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.99187994		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.25398004		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.99187994		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1434		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1797		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	2		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	9		
CDD_Vecu_Volt_G_f32[1]	17		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1127.05005		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	30.7206993		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte Comme	132		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurrent Comparison of the Compar$	0		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.92617989		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrqt	-1		
Name	Actual Value	Expected Value	Result
	٥	0	

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1127.05005	-1127.05005	•
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1022.60999	1022.60999	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0	0	•
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	2.99187994	2.99187994	•
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.92617989	3.92617989	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.99187994	2.99187994	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	132	132	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1797	1797	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	•
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	2	2	~
CDD_SysState_Cnt_G_Enum[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	30.7206993	30.7206993	~
CDD_Vecu_Volt_G_f32[1]	17	17	~

Τ				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Test Step 1.15 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1326.69995		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	695.132996		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.86127996		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.91262996		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.86127996		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.91262996		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1538		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4179		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	1		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	24		
CDD_Vecu_Volt_G_f32[1]	19		
Rte_ModeMachine_StaMd_SystemState_Mode	4		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1207.82996		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	22.3521996		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	6530		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	5		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.250820011		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	1		
Name	Actual Value	Expected Value	Result

Nie_Task_2iiis_10.Nie_Nb.Nie_Sa_CDDiinienace_CDDiinienace_Fei2.Nie_wiii 11q	1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1207.82996	1207.82996	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	695.132996	695.132996	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	5	5	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.91262996	1.91262996	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.91262996	1.91262996	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6530	6530	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4179	4179	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	4	4	~
CDD_SysState_Cnt_G_Enum[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	22.3521996	22.3521996	~
CDD_Vecu_Volt_G_f32[1]	19	19	~

T				✓
Actual Function	Count	Expected Function	Coun	t Result
none	0	*** No Call Expected ***	0	~

Test Step 1.16 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-450.973999
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	956.833008
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.32728004
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.990696013
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.32728004
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.990696013
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2758
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6362
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	3
CDD_Vecu_Volt_G_f32[0]	9
CDD_Vecu_Volt_G_f32[1]	10
Rte_ModeMachine_StaMd_SystemState_Mode	3





Name	Input Value		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-458.299011		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.5351009		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	5251		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCui	3.5545001		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCui	0.828881979		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrTrq	(-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-458.299011	-458.299011	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	956.833008	956.833008	~
CDD MtrCurr1TempOffset Volt G f32[0]	3 5545001	3 5545001	_

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-458.299011	-458.299011	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	956.833008	956.833008	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.5545001	3.5545001	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.990696013	0.990696013	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.828881979	0.828881979	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.990696013	0.990696013	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5251	5251	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6362	6362	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	~
CDD_SysState_Cnt_G_Enum[1]	3	3	~
CDD_Vecu_Volt_G_f32[0]	20.5351009	20.5351009	~
CDD_Vecu_Volt_G_f32[1]	10	10	-

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.17 (Repeat Count = 1)	v v
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1204.5
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	428.871002
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.53718996
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.34743023
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.53718996
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34743023
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4613
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2400
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	3
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	29
CDD_Vecu_Volt_G_f32[1]	30
Rte_ModeMachine_StaMd_SystemState_Mode	0
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-840.299988
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	24.0664997
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	1375
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.36310005
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-840.299988	-840.299988	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	428.871002	428.871002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.36310005	4.36310005	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.34743023	4.34743023	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0	0	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34743023	4.34743023	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1375	1375	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2400	2400	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	✓

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Name	Actual Value	Expected Value	Result
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	0	0	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	24.0664997	24.0664997	~
CDD_Vecu_Volt_G_f32[1]	30	30	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.18 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-836.937988
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	73.8448029
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.20215988
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.71273005
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.20215988
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.71273005
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5986
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3273
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	0
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	15
CDD_Vecu_Volt_G_f32[1]	16
Rte_ModeMachine_StaMd_SystemState_Mode	2
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	747.35199
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	24.2945995
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commit	5300
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.73531008
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	5
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrqt	0

Rte_Task_zms_To.Rte_Rb.Rte_Sa_CDDInterface_CDDInterface_Perz.Rte_intrint	(0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	747.35199	747.35199	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	73.8448029	73.8448029	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.73531008	2.73531008	•
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.71273005	1.71273005	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	5	5	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.71273005	1.71273005	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5300	5300	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3273	3273	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	2	2	•
CDD_SysState_Cnt_G_Enum[1]	2	2	~
CDD_Vecu_Volt_G_f32[0]	24.2945995	24.2945995	•
CDD_Vecu_Volt_G_f32[1]	16	16	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.19 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-902.63501
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	863.776978
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.62017012

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Name	Input Value		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.56127977		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.62017012		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.56127977		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1702		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1231		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	1		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	10		
CDD_Vecu_Volt_G_f32[1]	24		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1243.14001		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	29.9566002		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm(1885		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurrent and the control of the control of$	0.441742003		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	3.5453999		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos$	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1243.14001	1243.14001	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	863.776978	863.776978	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.441742003	0.441742003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.56127977	4.56127977	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.5453999	3.5453999	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.56127977	4.56127977	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1885	1885	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1231	1231	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	2	2	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	29.9566002	29.9566002	~
CDD_Vecu_Volt_G_f32[1]	24	24	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

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Name	Input Value		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrqt	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-658.497986	-658.497986	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	525.135986	525.135986	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.81072998	4.81072998	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.03612006	1.03612006	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.03612006	1.03612006	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2694	2694	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6922	6922	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	1	1	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	12.4942999	12.4942999	~
CDD_Vecu_Volt_G_f32[1]	12	12	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.21 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-29.5091
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	718.439026
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.96669006
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.76337004
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.96669006
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.657562017
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4418
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3107
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	3
CDD_SysState_Cnt_G_Enum[1]	1
CDD_Vecu_Volt_G_f32[0]	25
CDD_Vecu_Volt_G_f32[1]	10.6225004
Rte_ModeMachine_StaMd_SystemState_Mode	1
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	718.439026
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	10.6225004
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	3107
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	4.76337004
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.657562017
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1

Title_Task_zitis_10.title_Nb.title_Sa_CDDfitterface_CDDfitterface_Fetz.title_Mit Title	' ·		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-29.5091	-29.5091	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	718.439026	718.439026	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.96669006	2.96669006	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.76337004	4.76337004	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.96669006	2.96669006	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.657562017	0.657562017	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4418	4418	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3107	3107	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	~
CDD_SysState_Cnt_G_Enum[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	25	25	~
CDD_Vecu_Volt_G_f32[1]	10.6225004	10.6225004	~



Τ					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.22 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-384.337006		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1203.10999		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60235		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.37077999		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.60235		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.49700999		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6038		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2623		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	3		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	31		
CDD_Vecu_Volt_G_f32[1]	20.5625		
Rte_ModeMachine_StaMd_SystemState_Mode	3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1203.10999		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.5625		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	2623		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.37077999		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrCurr	2.49700999		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1		
Namo	Actual Value	Expected Value	Result

Rte_Task_zms_10.Rte_Rb.Rte_Sa_CDDinterface_CDDinterface_Perz.Rte_Mtr Trqt	-1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-384.337006	-384.337006	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1203.10999	1203.10999	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60235	1.60235	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.37077999	3.37077999	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.60235	1.60235	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	2.49700999	2.49700999	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6038	6038	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	2623	2623	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	~
CDD_SysState_Cnt_G_Enum[1]	3	3	~
CDD_Vecu_Volt_G_f32[0]	31	31	~
CDD_Vecu_Volt_G_f32[1]	20.5625	20.5625	~

T .				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

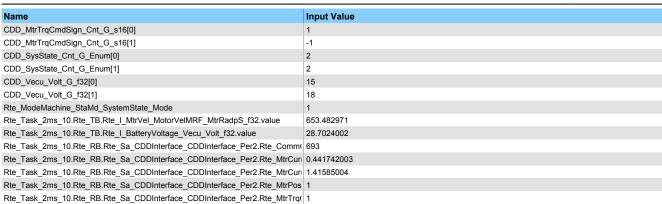
Test Step 1.23 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-750.877991
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-5.8327899
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.11789989
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.10511005
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.11789989
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.10511005
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	672
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3353
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	4

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Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	653.482971	653.482971	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	-5.8327899	-5.8327899	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.441742003	0.441742003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.10511005	1.10511005	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.41585004	1.41585004	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.10511005	1.10511005	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	693	693	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3353	3353	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	1	1	~
CDD_SysState_Cnt_G_Enum[1]	2	2	~
CDD_Vecu_Volt_G_f32[0]	28.7024002	28.7024002	~
CDD_Vecu_Volt_G_f32[1]	18	18	~

Т				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.24 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-506.942993		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	489.006012		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.23248005		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.34561014		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.23248005		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34561014		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4365		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1567		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	1		
CDD_SysState_Cnt_G_Enum[1]	1		
CDD_Vecu_Volt_G_f32[0]	18		
CDD_Vecu_Volt_G_f32[1]	8		
Rte_ModeMachine_StaMd_SystemState_Mode	3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	972.143982		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.5450993		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	418		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCui	1.16745996		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCui	2.43678999		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	(1		
Name	Actual Value	Expected Value	Resul
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	•

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	972.143982	972.143982	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	489.006012	489.006012	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.16745996	1.16745996	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.34561014	4.34561014	~

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Name	Actual Value	Expected Value	Result
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.43678999	2.43678999	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.34561014	4.34561014	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	418	418	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	1567	1567	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	•
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	•
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	~
CDD_SysState_Cnt_G_Enum[1]	1	1	~
CDD_Vecu_Volt_G_f32[0]	21.5450993	21.5450993	•
CDD_Vecu_Volt_G_f32[1]	8	8	~

T .					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.25 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-591.851013		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	80.7081985		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.479669005		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.14370012		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.479669005		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.14370012		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4264		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4415		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	2		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	7		
CDD_Vecu_Volt_G_f32[1]	21		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	461.765991		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	9.79251957		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte Comm	6482		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurrent and the control of the control of$	0.299508989		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.16656017		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq	0		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	461.765991	461.765991	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	80.7081985	80.7081985	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.299508989	0.299508989	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.14370012	4.14370012	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.16656017	4.16656017	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.14370012	4.14370012	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6482	6482	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4415	4415	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	✓
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	9.79251957	9.79251957	~
CDD_Vecu_Volt_G_f32[1]	21	21	✓

Т				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Test Step 1.26 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-844.638		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1228.16003		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.17770004		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.78542995		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.17770004		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.78542995		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5754		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	482		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	0		
CDD_SysState_Cnt_G_Enum[1]	2		
CDD_Vecu_Volt_G_f32[0]	9		
CDD_Vecu_Volt_G_f32[1]	5		
Rte_ModeMachine_StaMd_SystemState_Mode	2		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	487.270996		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	8.03582001		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Common	4301		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	1.60074997		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.2244401		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrqt	-1		
Name	Actual Value	Expected Value	Result
CDD AnnDataEwdPthAccessBfr Cnt G u16	0	0	-

Tite_Task_zins_To.txte_tas.txte_da_obblintenace_obblintenace_i ciz.txte_wii riq	· ·		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	487.270996	487.270996	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1228.16003	1228.16003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60074997	1.60074997	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.78542995	4.78542995	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.2244401	2.2244401	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.78542995	4.78542995	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4301	4301	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	482	482	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	2	2	~
CDD_SysState_Cnt_G_Enum[1]	2	2	~
CDD_Vecu_Volt_G_f32[0]	8.03582001	8.03582001	~
CDD_Vecu_Volt_G_f32[1]	5	5	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.27 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1107.12
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	32.0822983
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.96684003
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.441742003
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.96684003
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.250820011
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	2176
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	3483
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	0
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	22
CDD_Vecu_Volt_G_f32[1]	21.4122009
Rte_ModeMachine_StaMd_SystemState_Mode	0

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		(0
Name	Input Value		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	32.0822983		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.4122009		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm	3483		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.441742003		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	0.250820011		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrTrq	0		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	1	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1107.12	-1107.12	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	32.0822983	32.0822983	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.96684003	1.96684003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.441742003	0.441742003	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	1.96684003	1.96684003	~
CDD Marching Town Officet Volta C 622[4]	0.350930011	0.250920011	

1	1	~
-1107.12	-1107.12	~
32.0822983	32.0822983	~
1.96684003	1.96684003	~
0.441742003	0.441742003	~
1.96684003	1.96684003	~
0.250820011	0.250820011	~
2176	2176	~
3483	3483	~
1	1	~
1	1	~
1	1	~
0	0	~
2	2	~
0	0	~
22	22	~
21.4122009	21.4122009	~
	32.0822983 1.96684003 0.441742003 1.96684003 0.250820011 2176 3483 1 1 1 0 2 0 22	1 1 -1107.12 -1107.12 32.0822983 32.0822983 1.96684003 1.96684003 0.441742003 1.96684003 0.250820011 0.250820011 2176 2176 3483 3483 1 1 1 1 0 0 2 2 0 0 22 2

Τ				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.28 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-361.294006
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	415.226013
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.86658603
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.96747017
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.86658603
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.96747017
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5848
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6399
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	1
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	27
CDD_Vecu_Volt_G_f32[1]	29
Rte_ModeMachine_StaMd_SystemState_Mode	1
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1014.35999
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	22.7768002
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm(6119
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCuri	4.86899996
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCuri	0.279628009
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq(1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1014.35999	1014.35999	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	415.226013	415.226013	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.86899996	4.86899996	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.96747017	4.96747017	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.279628009	0.279628009	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.96747017	4.96747017	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6119	6119	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6399	6399	✓
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓

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Name	Actual Value	Expected Value	Result
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	1	1	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	22.7768002	22.7768002	~
CDD_Vecu_Volt_G_f32[1]	29	29	~

Τ			V	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.29 (Repeat Count = 1)		
Name	Input Value	
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1	
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-805.948975	
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	797.966003	
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.72437	
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.78024298	
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.72437	
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.78024298	
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3670	
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6377	
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	
CDD_SysState_Cnt_G_Enum[0]	0	
CDD_SysState_Cnt_G_Enum[1]	0	
CDD_Vecu_Volt_G_f32[0]	29	
CDD_Vecu_Volt_G_f32[1]	10	
Rte_ModeMachine_StaMd_SystemState_Mode	2	
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	120.797997	
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	18.8687992	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commit	3154	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	4.84580994	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.03030992	
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1	
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrTrgi	0	

Rte_rask_zms_fo.Rte_rb.Rte_sa_codintenace_codintenace_perz.Rte_mit frq	ι U		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	120.797997	120.797997	•
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	797.966003	797.966003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.84580994	4.84580994	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.78024298	0.78024298	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.03030992	2.03030992	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.78024298	0.78024298	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3154	3154	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6377	6377	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	2	2	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	18.8687992	18.8687992	~
CDD_Vecu_Volt_G_f32[1]	10	10	~

T			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.30 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-367.003998
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1333.90002
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.32502007

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Name	Input Value		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.79194021		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.32502007		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.79194021		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6436		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	338		
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1		
CDD_SysState_Cnt_G_Enum[0]	0		
CDD_SysState_Cnt_G_Enum[1]	0		
CDD_Vecu_Volt_G_f32[0]	30		
CDD_Vecu_Volt_G_f32[1]	10		
Rte_ModeMachine_StaMd_SystemState_Mode	3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	1074.33997		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	20.7642994		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	6156		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurrent and approximation of the control $	3.99714994		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.444205999		
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPoserserver and the property of the prop$	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	0		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	1074.33997	1074.33997	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1333.90002	1333.90002	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.99714994	3.99714994	✓
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.79194021	4.79194021	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.444205999	0.444205999	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.79194021	4.79194021	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6156	6156	✓
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	338	338	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	✓
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	0	0	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	✓
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	20.7642994	20.7642994	✓
CDD_Vecu_Volt_G_f32[1]	10	10	~

T				~
Actual Function	Count	Expected Function	Count	t Result
none	0	*** No Call Expected ***	0	-

Test Sten 4.24 (Benest Count = 4)	
Test Step 1.31 (Repeat Count = 1)	<u> </u>
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-311.792999
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	750.221985
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.56536007
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.792972028
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.56536007
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.792972028
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	1557
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4301
CDD_MtrPosPolarity_Cnt_G_s08[0]	1
CDD_MtrPosPolarity_Cnt_G_s08[1]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	2
CDD_Vecu_Volt_G_f32[0]	25
CDD_Vecu_Volt_G_f32[1]	8
Rte_ModeMachine_StaMd_SystemState_Mode	4
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-191.837006
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	24.0664997
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	6795
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrCuri	1.60108995
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	3.47305012
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1

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Name	Input Value		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	1		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-191.837006	-191.837006	✓
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	750.221985	750.221985	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	1.60108995	1.60108995	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	0.792972028	0.792972028	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.47305012	3.47305012	✓
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	0.792972028	0.792972028	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	6795	6795	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4301	4301	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	✓
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	4	4	✓
CDD_SysState_Cnt_G_Enum[1]	2	2	~
CDD_Vecu_Volt_G_f32[0]	24.0664997	24.0664997	✓
CDD_Vecu_Volt_G_f32[1]	8	8	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.32 (Repeat Count = 1)	✓
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-17.7012005
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1226.71997
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.48852992
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.57865
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	3.48852992
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.57865
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5433
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6343
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	2
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	10
CDD_Vecu_Volt_G_f32[1]	10
Rte_ModeMachine_StaMd_SystemState_Mode	3
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-731.166992
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	5
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commit	388
$Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur$	3.94904995
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.94375992
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	1

Title_Task_zins_10.1tle_tb.fitle_3a_CDDInterface_CDDInterface_Fetz.fitle_with Tight	'		
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-731.166992	-731.166992	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	1226.71997	1226.71997	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	3.94904995	3.94904995	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	1.57865	1.57865	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	2.94375992	2.94375992	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	1.57865	1.57865	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	388	388	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6343	6343	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	5	5	~
CDD_Vecu_Volt_G_f32[1]	10	10	~



T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.33 (Repeat Count = 1)			✓
Name	Input Value		
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1		
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1180.60999		
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	803.369019		
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	4.47603989		
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.9499402		
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	4.47603989		
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.9499402		
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	3719		
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6795		
CDD_MtrPosPolarity_Cnt_G_s08[0]	1		
CDD_MtrPosPolarity_Cnt_G_s08[1]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	1		
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1		
CDD_SysState_Cnt_G_Enum[0]	2		
CDD_SysState_Cnt_G_Enum[1]	3		
CDD_Vecu_Volt_G_f32[0]	23		
CDD_Vecu_Volt_G_f32[1]	8		
Rte_ModeMachine_StaMd_SystemState_Mode	3		
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-1123.93005		
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	31		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Commo	4030		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCurr	0.441742003		
Rte Task 2ms 10.Rte RB.Rte Sa CDDInterface CDDInterface Per2.Rte MtrCuri	0.250820011		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	-1		
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1		
Namo	Actual Value	Expected Value	Docult

Rte_rask_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Mtrrr		1	1
Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-1123.93005	-1123.93005	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	803.369019	803.369019	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.441742003	0.441742003	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	4.9499402	4.9499402	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	4.9499402	4.9499402	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	4030	4030	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	6795	6795	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1	-1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	1	1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	1	1	~
CDD_SysState_Cnt_G_Enum[0]	3	3	~
CDD_SysState_Cnt_G_Enum[1]	3	3	~
CDD_Vecu_Volt_G_f32[0]	31	31	~
CDD_Vecu_Volt_G_f32[1]	8	8	~

T T			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.34 (Repeat Count = 1)	
Name	Input Value
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	1
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-692.187988
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	643.562012
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	0.872577012
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.78780007
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.872577012
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	3.78780007
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5321
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4074
CDD_MtrPosPolarity_Cnt_G_s08[0]	-1
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1

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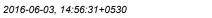


Name	Input Value
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1
CDD_SysState_Cnt_G_Enum[0]	3
CDD_SysState_Cnt_G_Enum[1]	0
CDD_Vecu_Volt_G_f32[0]	7
CDD_Vecu_Volt_G_f32[1]	15
Rte_ModeMachine_StaMd_SystemState_Mode	4
Rte_Task_2ms_10.Rte_TB.Rte_I_MtrVel_MotorVelMRF_MtrRadpS_f32.value	-159.466003
Rte_Task_2ms_10.Rte_TB.Rte_I_BatteryVoltage_Vecu_Volt_f32.value	21.5
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_Comm0	5427
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCur	2.43364
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrCuri	0.250820011
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrPos	1
Rte_Task_2ms_10.Rte_RB.Rte_Sa_CDDInterface_CDDInterface_Per2.Rte_MtrTrq0	-1

Name	Actual Value	Expected Value	Result
CDD_AppDataFwdPthAccessBfr_Cnt_G_u16	0	0	~
CDD_MRFMtrVel_MtrRadpS_G_f32[0]	-159.466003	-159.466003	~
CDD_MRFMtrVel_MtrRadpS_G_f32[1]	643.562012	643.562012	~
CDD_MtrCurr1TempOffset_Volt_G_f32[0]	2.43364	2.43364	~
CDD_MtrCurr1TempOffset_Volt_G_f32[1]	3.78780007	3.78780007	~
CDD_MtrCurr2TempOffset_Volt_G_f32[0]	0.250820011	0.250820011	~
CDD_MtrCurr2TempOffset_Volt_G_f32[1]	3.78780007	3.78780007	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[0]	5427	5427	~
CDD_MtrCurrOffComOffset_Cnt_G_u16[1]	4074	4074	~
CDD_MtrPosPolarity_Cnt_G_s08[0]	1	1	~
CDD_MtrPosPolarity_Cnt_G_s08[1]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[0]	-1	-1	~
CDD_MtrTrqCmdSign_Cnt_G_s16[1]	-1	-1	~
CDD_SysState_Cnt_G_Enum[0]	4	4	~
CDD_SysState_Cnt_G_Enum[1]	0	0	~
CDD_Vecu_Volt_G_f32[0]	21.5	21.5	~
CDD_Vecu_Volt_G_f32[1]	15	15	~

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

CDDInterface_Init2





Project CDDInterface_FIASA

Module CDDInterface **Test Object** CDDInterface_Init2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CDDInterface_FIASA
Configuration File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Sa_CDDInterface.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT) \StdDef\u00e4include -I\$(Compiler Install Path)\include

Name	Text
Module 'CDDInterface'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Sa_CDDInterface.c Code File(s) Version:4 Module Design Document:NA Module Design Document Version:NA Data Dictionary Version:1
	Data Dictionary Version: 1 Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):1714 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:6/3/2016 Comments:"Note1: Inline Function defined in GlobalMacro.h is not unit tested. Note2:""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

CDDInterface_Init2

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Workspace File

D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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CDDInterface_Init2

Test Case 1: Boundary Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS1.1 499.00 Cycles

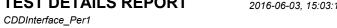
Description Vector Description:

TS1.1 Check for Stub count

Test Step 1.1 (Repeat Count = 1)

Τ				•	,
Actual Function	Count	Expected Function	Count	Resul	t
EnableMtrCtlInterrupt	1	EnableMtrCtlInterrupt	1		ē

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Project	
Module	
Test Object	

Instrumentation: Test Object Only



Statistics

Total Testcases	
Successful	✓
Failed	
Not Executed	

Module Properties

Project Root Directory			
Configuration File			
Target Environment			
Kind of Test			
Linker Options			
Source File(s)			
File			
Compiler Options			

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	000000000000000000000000000000000000000						

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0

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Attributes	
Name	Value
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CDDInterface_FIASA\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS1.1 1110.00 Cycles
TS1.2 1107.00 Cycles

Description

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
Name	input value
a *a a	
0 •0 0	
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Name		Input Value	
	• 0		
	• 0		
	• 0		

Name		Actual Value	Expected Value	Result
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				✓

Actual Function	Count	Expected Function	Count	Result
T				✓

Test Step 1.2 (Repeat Cour	nt = 1)	✓
Name		Input Value
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CDDInte	errace_Per1			MACILAG
Name		Input Value		
Name	• п	input value		
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	o • o			
	• •			
	• • •			
Name		 Actual Value	Expected Value	Result
Name		Actual value	Expected value	Result
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				✓
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				✓
-				✓
				✓
				✓
-				✓
				✓
-				✓
				✓
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T				V
Actual Function	Count	Expected Function	Count	Result
				~

CDDInterface_Per1

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Test Case 2: Boundary Test

•

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Specification

CDDInterface_Per1

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS2.1 1091.00 Cycles
TS2.2 1090.00 Cycles
TS2.2 1090.00 Cycles
TS2.3 1051.00 Cycles
TS2.3 1051.00 Cycles
TS2.4 1016.00 Cycles
TS2.5 1016.00 Cycles
TS2.6 1016.00 Cycles
TS2.7 1048.00 Cycles
TS2.8 106.00 Cycles
TS2.8 106.00 Cycles
TS2.10 1048.00 Cycles
TS2.11 1016.00 Cycles
TS2.11 1016.00 Cycles
TS2.12 1016.00 Cycles
TS2.13 1016.00 Cycles
TS2.14 1016.00 Cycles
TS2.15 1016.00 Cycles
TS2.16 1016.00 Cycles
TS2.17 1016.00 Cycles
TS2.17 1016.00 Cycles
TS2.18 1016.00 Cycles
TS2.18 1016.00 Cycles
TS2.19 1016.00 Cycles TS2.18 1016.00 Cycles
TS2.19 1016.00 Cycles
TS2.20 1016.00 Cycles
TS2.21 1038.00 Cycles
TS2.22 1016.00 Cycles
TS2.22 1016.00 Cycles
TS2.23 1016.00 Cycles
TS2.24 1016.00 Cycles
TS2.25 1016.00 Cycles
TS2.26 1016.00 Cycles
TS2.27 1016.00 Cycles
TS2.28 1016.00 Cycles
TS2.29 1016.00 Cycles
TS2.29 1016.00 Cycles
TS2.31 1016.00 Cycles
TS2.31 1016.00 Cycles
TS2.32 1016.00 Cycles
TS2.33 1016.00 Cycles TS2.34 1038.00 Cycles TS2.34 1036.00 Cycles TS2.35 1016.00 Cycles TS2.36 1016.00 Cycles TS2.37 1016.00 Cycles TS2.37 1016.00 Cycles TS2.38 1016.00 Cycles TS2.38 1016.00 Cycles TS2.40 1016.00 Cycles TS2.41 1016.00 Cycles TS2.42 1044.00 Cycles TS2.43 1016.00 Cycles TS2.44 1016.00 Cycles TS2.45 1038.00 Cycles TS2.46 1016.00 Cycles TS2.47 1016.00 Cycles TS2.48 1016.00 Cycles TS2.49 1016.00 Cycles TS2.50 1044.00 Cycles TS2.51 1016.00 Cycles TS2.51 1016.00 Cycles TS2.51 1016.00 Cycles TS2.52 1016.00 Cycles
TS2.53 1016.00 Cycles
TS2.54 1016.00 Cycles
TS2.55 1016.00 Cycles
TS2.55 1016.00 Cycles
TS2.55 1016.00 Cycles
TS2.57 1016.00 Cycles
TS2.58 1016.00 Cycles
TS2.59 1016.00 Cycles
TS2.60 1024.00 Cycles
TS2.61 1045.00 Cycles
TS2.63 1016.00 Cycles
TS2.63 1016.00 Cycles
TS2.64 1016.00 Cycles
TS2.65 1016.00 Cycles
TS2.65 1016.00 Cycles
TS2.66 1016.00 Cycles
TS2.67 1016.00 Cycles
TS2.68 1016.00 Cycles
TS2.68 1016.00 Cycles
TS2.71 1016.00 Cycles
TS2.71 1016.00 Cycles
TS2.72 1016.00 Cycles
TS2.73 1043.00 Cycles
TS2.73 1043.00 Cycles
TS2.75 1016.00 Cycles
TS2.75 1016.00 Cycles
TS2.75 1016.00 Cycles
TS2.77 1016.00 Cycles TS2.78 1016.00 Cycles TS2.79 1016.00 Cycles TS2.80 1016.00 Cycles TS2.81 1024.00 Cycles TS2.82 1016.00 Cycles TS2.83 1016.00 Cycles TS2.84 1038.00 Cycles TS2.84 1038.00 Cycles TS2.85 1016.00 Cycles TS2.86 1016.00 Cycles TS2.87 1016.00 Cycles TS2.88 1016.00 Cycles TS2.89 1022.00 Cycles TS2.90 1016.00 Cycles TS2.91 1016.00 Cycles TS2.92 1016.00 Cycles TS2.93 1016.00 Cycles TS2.94 1024.00 Cycles TS2.95 1016.00 Cycles TS2.96 1026.00 Cycles TS2.96 1026.00 Cycles TS2.97 1016.00 Cycles TS2.98 1016.00 Cycles TS2.99 1016.00 Cycles TS2.100 1016.00 Cycles TS2.101 1016.00 Cycles TS2.102 1016.00 Cycles TS2.103 1016.00 Cycles TS2.104 1016.00 Cycles TS2.105 1016.00 Cycles TS2.105 1016.00 Cycles TS2.105 1016.00 Cycles TS2.106 1016.00 Cycles TS2.108 1016.00 Cycles TS2.108 1016.00 Cycles TS2.108 1026.00 Cycles

CDDInterface_Per1

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CDDInterface_Per1

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Description

CDDInterface_Per1

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Test Step 2.1 (Repeat Count = 1)	🗸
Name	Input Value
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Name					Input Value	
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Name	Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.2 (Repeat Count	= 1)	✓
Name		Input Value
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CDDInterfac	ce_Per1				Razorcat
Name			Input Value		
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Name			Actual Value	Expected Value	Result
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Name	Ac	ctual Value	Expected Value	Result
				✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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ep 2.3 (Repeat Count = 1)	
	Input Value
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Actual Function



Count Result

Name Actual Value Expected Value Result Actual Value Actual Value Expected Value Result								I -		
Name Actual Value Expected Value Result Actual Value Expected Value Result Actual Value Actu	Name							Input Value		
Name Actual Value Expected Value Result Actual Value Expected Value Result Actual Value Actu										
Name Actual Value Expected Value Result Actual Value Expected Value Result Actual Value Actu										
Name Actual Value Expected Value Result Actual Value				•						
Name			•	•						
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	Name							Actual Value	Expected Value	
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Count Expected Function

me	2.4 (Repeat Count = 1)	Input Value	
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CDDInterface_Per1 Name Input Value

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Test Step 2.5 (Repeat Count = 1)		✓
Name	Input Value	
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Name	 Actual Value Expected Value	Result
	Expected value	~
		✓
		✓

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Name	A	ctual Value	Expected Value	Result
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				✓
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-				✓
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-				✓
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-				✓
-				✓
-				~
				✓
-				✓
				✓
				✓

T				✓
Actual Function	Count	Expected Function	Count	Result
o •		o •		~

Test Step 2.6 (Repeat Count = 1)		✓
Name	Input Value	

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Name			Innut Value		
Name			Input Value		
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	• 0				
	• 0				
Name			Actual Value	Expected Value	Result
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T					✓
Actual Function		Count	Expected Function	Count	Result

Test Step	2.7 (Repeat Count = 1)		✓
Name		Input Value	
	0 •0 0		
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	• 0		
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Name			Input Value		
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Name			Actual Value	Expected Value	Result
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Name		Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.8 (Repeat	Count = 1)	✓
Name		Input Value
	•0 0	
	•0 0	
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	• 0	
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Name				Input Value		
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Name				Actual Value	Expected Value	Result
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						✓
						✓
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Τ			✓
Actual Function	Count	Expected Function	Count Result
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Test Step 2.9 (Repeat Count = 1)		V
Name	Input Value	
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Name	Input Value		
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Name	Actual Value	Expected Value	Result
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			✓
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Name	Actual Value	Expected Value	Result
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			✓
			✓
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			✓
			✓
	 -		✓

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Name		Actual Value	Expected Value	Result
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				✓

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Actual Function	Count	Expected Function	Count Result
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T 404 040/D 40 4 4)	
Test Step 2.10 (Repeat Count = 1)	·
Name	Input Value
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Name		Input Value
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Name	Actual Value	Expected Value	Result
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-			✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.11 (Repeat	Count = 1)	✓
Name		Input Value
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	•0 0	
	• 0	
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Name		Input Value		
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Name		Actual Value	Expected Value Re	sult
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
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Name				Input Value		
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Name				Actual Value	Expected Value	Resu
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T						
Actual Function				Expected Function	Cour	

. Step	2.13 (Repe	eat Count = 1)		
е			Input Value	
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		•		

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Razorcat

CDDIIILEITA	ce_rerr					TOIL CITIES
Name				Input Value		
Ivaille				input value		
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		• 0				
		• a				
		0	 0 0	1	l=	
Name				Actual Value	Expected Value	Result
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						✓
						~
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-						✓
						✓
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	-		-			✓

		✓
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	 ·	✓

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Actual Function	Count	Expected Function	Count	Result		
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Test Step 2.14 (Repeat Count = 1)			~
Name	Input Value		
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Name	Actual Value	Expected Value	Result
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Name	Act	ual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.15 (Repeat Count = 1) Name Input Value Input Value
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CDDInterface Per1	



Name			Input Value		
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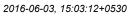
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.16 (Repeat Count = 1)				
Name			Input Value	
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Actual Function	Count	Expected Function	Count	Result
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Name	Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count Result
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Test Step	2.18 (Repe	eat Count = 1)	✓
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Name				Actual Value	Expected Value	Result
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Name	Actual Value	Expected Value	Result
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Name		Actual Value	Expected Value	Result
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T			✓
Actual Function	Count	Expected Function	Count Result
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Test Step 2.19 (Repeat Count = 1)	· ·
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.20 ((Repeat	: Count = 1)	✓
Name			Input Value
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.21 (Repeat Count = 1)	· ·
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Name					Actual Value	Expected Value		Result
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Actual Function	1			Count	Expected Function		Count	Result

ep 2.22 (Repeat Count = 1)		
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Actual Function	Count	Expected Function	Count Result
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Name	Act	ual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step	2.24 (Repeat (Count = 1)	🗸
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	Count	Count Expected Function	

Test Step 2.25 (Repeat Count = 1)					
Name			Input Value		
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Actual Function	Count	Expected Function	Count	Result
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Test Step	2.26 (Repeat Count = 1)	✓
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Name	Actual Value	Expected Value	Result

Name	Ac	ctual Value	Expected Value	Result
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Test Step 2.27 (Repeat Count = 1)		✓
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Name				Actual Value	Expected Value	Result
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Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
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Name	Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count Result
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Test Step 2.28 (Repeat Count = 1)	·
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Name	Actual Value	Expected Value	Result
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Test Step 2.29	(Repeat	Count = 1)	✓
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Actual Function	Count	Expected Function	Count	Result
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Name			Input Value		
Name			Actual Value	Expected Value	Result
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Test Step 2.32 (Repeat Count = 1)			✓
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Name	Actual Val	ue Expected Value	Result
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	Actual Function	Count	Expected Function	Count	Result
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Test Step 2.3	33 (Repea	t Count = 1)	✓
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Test Step 2.34 (Repeat Count = 1)		✓

Test Step 2	.34 (Repea	at Count = 1)	✓
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Test Step 2.35 (Repea	t Count = 1)	✓
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				*
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				~
				~
				✓
				~
				✓
				~
				~
				~

Т			✓
Actual Function	Count	Expected Function	Count Result
<u> </u>		_ •	✓

Test Step	2.36 (Repe	eat Count = 1)	√
Name			Input Value
		•0 0	
		• 0 0	
		• 0	
		• 0	
		• •	
		• •	
	-		
	•		
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	•		

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Name				Input Value		
		• 0				
		• 0				
		8				
Name				Actual Value	Expected Value	Result
						~

Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
			Y
			•
			*
			✓
	•		~
			~
	•		✓
	·		✓
			✓
	·		✓
			~
			✓
	•		✓
			✓
			✓
			✓
· · · · · · · · · · · · · · · · · · ·			✓
			✓
			✓
			✓

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Name		Actual Value	Expected Value	Result
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				~
-				~
				✓

T			✓
Actual Function	Count	Expected Function	Count Result
		<u> </u>	✓

Test Step 2.37 (Repeat Count = 1)	
Name	Innut Web.
Name	Input Value
• • •	
a •a a	
a • a	
o • o	
• 0	
• 0	
• 0	
• •	
• a	
•	
• 0	
• 0	
• •	
* <u>u</u>	
• •	
·	
• 0	
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• • •	
<u> </u>	

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Name		Input Value
	• 0	
	• 0	
•		
	О	

Name	Actual Value	Expected Value	Result
			~
			~
			~
			✓
			✓
			✓
	 ·		✓
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	 •		~
	 •		~
•	 •		~
-			✓

T				~	
Actual Function	Count	Expected Function	Count	Result	
n •		п •			

Test Step 2.38	8 (Repea	t Count = 1)	✓
Name			Input Value
		• 0 0	
		•0 0	
		• 0	
		• a	
		• 0	
		• 0	

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Name		Input Value		
	•			
	• •			
	· •			
	o • o			
	• •			
	0 0		1=	_
Name		Actual Value	Expected Value	Result
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T -			✓	
Actual Function	Count	Expected Function	Count	Result
. •				~

Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
0 •0 0	
o *o o	
· 0	
• •	
• •	
• •	
· u	
• •	
• 0	
• 0	
• 0	
• 0	
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• 0	
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a • a	
o • o	

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CDDInterface_Per1 Name Input Value . . Name Result **Actual Value Expected Value** .

T				V
Actual Function	Count	Expected Function	Count	Result
<u> </u>		a •		~

Name Input Value Input Value	Test Step 2.40 (Repea	at Count = 1)	✓
			Input Value
		• 0 0	
		• 0 0	
		• 0	
		0	
•			
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• a			
• a			
		<u> </u>	

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Name			Input Value	
	•			
	0			
	• 0			
	•			
		0 0		
	·	0 0	Actual Value Expected Value	
	·	0 0	Actual Value Expected Value	•
	·	0 0	Actual Value Expected Value	
	·	0 0	Actual Value Expected Value	
Name	•	0 0	Actual Value Expected Value	
Name			Actual Value Expected Value	
Name	•	0 0	Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name . Name			Actual Value Expected Value	
Name . Name			Actual Value Expected Value	
Name . Name			Actual Value Expected Value	
Name Name Name			Actual Value Expected Value	
Name Name Name Name Name			Actual Value Expected Value	

T			✓
Actual Function	Count	Expected Function	Count Result
· ·		· •	

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Test Step 2.41 (Repeat Count = 1)	✓
Name	Input Value
0 0	
0 •0	
a • a	
• •	
• 0	
• 0	
• a	
• 0	
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<u> </u>	
<u> </u>	
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a a	
• 0	
a • a	
a • a	
• •	
• •	
• • • • • •	
Name	Actual Value Expected Value Result
	· ·

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Name		Actual Value	Expected Value	Result
				✓
				✓
				✓
				✓
				✓
				✓
				✓
				~
				~
				~
				~
				~
				~
				~
				✓
				~
				~
				~
•				~
				~
•				✓
				~
				~
	 -			✓

T			✓
Actual Function	Count	Expected Function	Count Result
			✓

Test Step 2.42 (Repeat Count =	= 1)		V
Name		Input Value	
0 •0 0			
• 0			
· a			
• 0			
• •			
• 0			
•			
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• 0			
• 0			
• •			
•			
• 0			

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Name		Input Value	
Name		input value	
	• •		
	o • o		
	· •		
		0	
Name		Actual Value	Expected Value Result
			✓
			~
			· · · · · · · · · · · · · · · · · · ·
			✓
			✓
			✓
		·	✓
		•	✓
			·
			·
			✓
			· ·
•			· ·
		· · · · · · · · · · · · · · · · · · ·	•
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: : :			

Test Step	2.43 (Repeat Count = 1)		✓
Name		Input Value	
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	• 0		
	• 0		

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Name			Input Value		
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		• •			
		• 0			
		• 0			
		0			
Name			Actual Value	Expected Value	Result
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	-				✓
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-					*
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Name		Actual Value	Expected Value	Result
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-				~
	 •			~
				✓
	 •			~
				✓
	 •			~
-				✓
-				~
-				✓
-				~
-				✓
-				•
				✓
				~

T				✓
Actual Function	Count	Expected Function	Count	Result
		. ·		✓

Test Step 2.	44 (Repeat Count = 1)	🗸
Name	· · · (pour sount)	Input Value
	o •o o	
	0 •0 0	
	· 0	
	o • o	
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	• .	
	• 0	
	• 0	

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Name	Input Value		
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· 0			
a • a			
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• • • • • • • •			
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result
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			~
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T				V
Actual Function	Count	Expected Function	Count Res	ult
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Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
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o •o o	
• •	
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• 0	
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• 0	
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• 0	
• 0	
• •	
• 0	

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Name				Input Value		
		• 0				
		• 0				
		8				
Name				Actual Value	Expected Value	Result
						~

Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
			Y
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			*
			✓
	•		~
			~
	•		✓
	·		✓
			✓
	·		✓
			~
			✓
	•		✓
			✓
			✓
			✓
· · · · · · · · · · · · · · · · · · ·			✓
			✓
			✓
			✓

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Na	ıme	Α	Actual Value	Expected Value	Result
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					✓
					_

T				✓
Actual Function	Count	Expected Function	Count	Result
•		o •		~

Test Step 2.46 (Repe	at Count = 1)	✓
Name		Input Value
	•0 0	
	•	
	- 0	
	• 0	
	• .	
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•		
	• 0	
	• •	

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Name		Input Value
	• 8	
	• 8	
	О	

Name	Actual Value	Expected Value	Result
			~
			~
			~
			✓
			✓
			✓
	 ·		✓
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•	 •		~
-			✓

T				✓
Actual Function	Count	Expected Function	Count	Result
· ·		o •		~

Test Step 2.4	7 (Repea	t Count = 1)	✓
Name			Input Value
		• 0 0	
		• 0 0	
		• 0	
		• 0	
		• 0	
		• •	
	•		

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Name		Input Value		
	•			
	· 0			
	· •			
	· ·			
	0 0		1=	
Name		Actual Value	Expected Value Re	sult
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				✓
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T				✓
Actual Function	Count	Expected Function	Count	Result
		g •		~

	(Repeat Count = 1)	
ne		Input Value
	o •o o	
	o •o o	
	• •	
	• .	
	•	
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	* 0	
	• 0	
	•	
	o • o	
	o • o	
	o • o	

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Count Result

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Actual Function

Name			Input Value		
	 0 0				
Name			Actual Value	Expected Value	Result
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					✓
					~
					✓
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•					V
					V
•					V
•					V
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					~
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Τ					✓

Count Expected Function

Step 2.49 (Repeat Co	unt – 1)	
		Input Value
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•		
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• 0		
• 0		

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Razorcat

CDDIIILEITA	ce_rerr					TOIL CITIES
Name				Input Value		
Ivaille				input value		
		•				
		• 0				
		• a				
		0	 0 0	1	l=	
Name				Actual Value	Expected Value	Result
						~
						~
						✓
						~
-		-				✓
-						✓
						✓
						✓
						✓
						✓
						-
						•
						-
•						*
	-		-			✓

		✓
		✓
-	 •	✓
	 •	✓
	 ·	~
	 •	✓
	 ·	~
	 •	✓
	 •	~
•	 •	✓
	 ·	✓
•	 •	✓
	 ·	✓
	 •	✓
	 ·	✓
	 •	✓
	 ·	✓
		✓
	 ·	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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root otop	2.50 (Repeat Count = 1)
Name	Input Value
	a •6 a
	• 0
	•
	• 0
	· o
	••
	••
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	• •
	•
	•

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Name	Act	ual Value	Expected Value	Result
				✓
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-				✓
				✓
-				✓
				✓
-				✓
	 -			✓
				✓
	 -			✓
				✓
				~
				✓
				~
				✓
				~
-	 -			✓
				~
				✓
				~
				✓
				✓
				✓
				✓

T					
Actual Function	Count	Expected Function	Count	Result	
o •		o •		~	

Test Step:	2.51 (Repeat Count = 1)		~
Name		Input Value	
	o ••o o		
	· · ·		
	o • o		
	· 0		
	• 0		
	• 0		
	• 0		
	•		
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	•		
	• 0		
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			• • • • • • • • • • • • • • • • • • • •	
Name		Input Value		
Name		input value		
Name		Actual Value Expected Value		Result
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				✓
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				~
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				-
•	 •			~
	 •			~
				✓
				✓
				~
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	 •			
	 •			~
				~
				~
				✓
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•	 •			*
	 -			~
T				V
Actual Function	Count	Expected Function	Count	Result
		I and the second	1	

Test Step 2.52 (Repeat C	ount = 1)	✓
Name		Input Value
	• 0	
	• 0	
•		

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Name			Input Value		
	•				
	•				
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		• .			
		•			
		• •			
		• 0			
		• 0			
		0			
Name			Actual Value	Expected Value	Result
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	-				✓
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					~

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Name		Actual Value	Expected Value	Result
-				~
-				✓
				✓
-				✓
-				✓
-				✓
	 •			✓
-				✓
-				✓
-				✓
	 ÷			✓
-				✓
	 •			✓
-				✓
				✓

T				✓
Actual Function	Count	Expected Function	Count	Result
<u> </u>		. ·		~

Test Ster	o 2.53 (Repeat Count = 1)	·
Name		Input Value
	0 0	
	o • o	
	• 0	
	• 0	
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	• •	
	• 0	
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Name			Input Value		
		0 0			
		0 0			
Name			Actual Value	Expected Value	Result

Name		Actual Value	Expected Value	Result
				~
				✓
				✓
				✓
-				✓
-				✓
				~
•				✓
•	 •			~
•				✓
				~
•				~
				~
•	 •			✓
				~
•	 •			✓
	 •			~
•	 •			✓
	 •			~
•	 •			✓
				~
•	 •			✓
				✓
•				~
				~
•				~

Τ		✓
Actual Function	Count Expected Function	Count Result
		✓

Test Step 2.54 (Repeat Coun	= 1)
Name	Input Value
<u> </u>	
• 0	
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•	
•	
• 0	
•	
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Name				Input Value		
		• 0				
		• 0				
		8				
Name				Actual Value	Expected Value	Result
						~

Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
			Y
			•
			*
			✓
	•		~
			~
	•		✓
	·		✓
			✓
	·		✓
			~
			✓
			✓
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· · · · · · · · · · · · · · · · · · ·			✓
			✓
			✓
			✓

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ame		Actual Value	Expected Value	Result
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				✓
-				~

Т				
Actual Function	Count	Expected Function	Count	Result
				~

Test Step 2.55 (Repeat Count = 1)	
Name	Innuit Value
Name	Input Value
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a • a	
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Name		Input Value
	• 8	
	• 8	
	0	

Name	Actual Value	Expected Value	Result
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			✓
			✓
			✓
	 ·		✓
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	 •		~
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-			✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.56 (Repeat Count = 1)	✓
Name	Input Value
o •o o	
0 •0 0	
о • о	
· 0	
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r a	
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• 0	

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Name		Input Value		
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	· •			
	· ·			
	0 0		1=	
Name		Actual Value	Expected Value Re	sult •
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T				
Actual Function	Count	Expected Function	Count	Result
				~

Test Step 2.57 (Repeat Count = 1)	
Name	Input Value
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· 0	
a • a	
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Count Result

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Actual Function

Name			Input Value		
	 0 0				
Name			Actual Value	Expected Value	Result
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					✓
					~
					✓
					~
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					~
-					~
					~
•					V
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Τ					✓

Count Expected Function

tep 2.58 (Repeat Count = 1)	Input Value	
o •o o		
0 •0 0		
• •		
• 0		
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.59 (Repeat Count = 1)			✓
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Name	Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.60 (Repeat Count =	1)
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Name					Input Value		
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Name					Actual Value	Expected Value	Result
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Test Step	2.61 (Repea	at Count = 1)			~
Name			Input Value		
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		•0 0			
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Name		Actual Value	Expected Value	Result
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T		✓
Actual Function	Count Expected Function	Count Result
		✓

	peat Count =	Input Value	
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Name				Input Value		
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Name				Actual Value	Expected Value	Result
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Τ				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.63 (Repeat	Count = 1)	✓
Name		Input Value
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Name				Input Value		
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		• 6				
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Name				Actual Value	Expected Value	Result
						~

Name	Actual	Value Expected Value	Result
Name	Actual	value Expected value	
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Name Actual Value Expected Value

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Name						Input Value
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Name	Actual Value	Expected Value	Result
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			✓
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	 •		✓

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Actual Function	Count	Expected Function	Count	Result
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Test Step	2.65 (Repe	eat Count = 1)	✓
Name			Input Value
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Name		Input Value		
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.66 (Repeat Count = 1)	
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CDDInterface_Per1 Name Input Value . . Name Result **Actual Value Expected Value** .

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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.6	7 (Repeat	t Count = 1)	✓
Name			Input Value
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		• 0 0	
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Name			Input Value	
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	·	0 0	Actual Value Expected Value	•
	·	0 0	Actual Value Expected Value	
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Name	•	0 0	Actual Value Expected Value	
Name			Actual Value Expected Value	
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Name Name Name Name Name			Actual Value Expected Value	

T			✓
Actual Function	Count	Expected Function	Count Result
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Test Step 2.68 (Repeat Count = 1)	✓
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Name	Actual Value Expected Value Result
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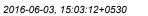
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Name		Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step	2.69 (Repeat Count = 1)		✓
Name		Input Value	
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Name					Input Value		
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Namo		О	٥		Actual Value	Expected Value	Result
Name		0			Actual Value	Expected Value	Result
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Name		a			Actual Value	Expected Value	•
Name		0			Actual Value	Expected Value	•
Name					Actual Value	Expected Value	
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2	✓	
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.71 (Repeat Count =	1)	✓
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Name		Actual Value	Expected Value	Result
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Т			✓
Actual Function	Count	Expected Function	Count Result
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Test Step 2.72 (Repeat Count	= 1)
Name	Input Value
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Name				Actual Value	Expected Value	Result
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Name	Actual Value	Expected Value	Result
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Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count Result
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Test Step 2.73 (Repeat Count = 1)	In most Walter
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Name		Input Value
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Name	Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.74	(Repeat	t Count = 1)	✓
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Actual Function	Count	Expected Function	Count	Result
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Get Stop Z/5 (kepeat Count = 1)		
	Test Step 2.75 (Repeat Count = 1)	<u> </u>
	Name	Input Value
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Name				Input Value			
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Name				Actual Value	Expected Value		Result
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Actual Function			Count	Expected Function		Count	Result

est Step 2	2.76 (Repeat Count = 1)		
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Name				Input Value		
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.77 (Repeat Count = 1)			✓
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Name	Actual Value	Expected Value	Result
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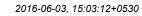
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Result	Expected Value	Actual Value		Name
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✓				-
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Name			Input Value	
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2	est Step 2.79 (Repeat Count = 1)				
Name			Input Value		
		• 0 0			
		•0 0			
		• 0			
		• 0			
		• •			
		• 0			

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Name		Input Value
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	• 0	
	• 0	
	• 0	

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Name		Actual Value	Expected Value	Result
-				✓
				✓
				✓
-				✓
				✓
				✓
				✓
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-				✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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	: Count = 1)	Input Value	
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	• 0 0		
	• 0		
	• 0		
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Name	Actual Value	Expected Value	Result
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				✓
-				✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
				~

Test Step 2.81 (Repeat Count = 1)		✓
Name	Input Value	
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Name				Input Value		
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		• 0				
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Name				Actual Value	Expected Value	Result
						~

Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
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			✓
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			✓
			✓

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Name		Actual Value	Expected Value	Result
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				✓
				~
				✓
-				~

T			✓
Actual Function	Count	Expected Function	Count Result
		<u> </u>	✓

Test Step 2.82 (Repeat Count = 1)	
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Name	Input Value
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Name			Input Value
	• 0		
	• 0		
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-	0	0 0	

Name		Actual Value	Expected Value	Result
				✓
				✓
				~
				✓
				✓
-				~
				~
•				✓
				✓
•				✓
				✓
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•				✓
	 -			✓

T .					
Actual Function	Count	Expected Function	Count	Result	
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Test Step 2.83 (F	Repeat	: Count = 1)	✓
Name			Input Value
		• 0 0	
		• 0 0	
		• 0	
		• 0	
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Name		Input Value		
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Name		Actual Value	Expected Value Re	sult
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Τ						
Actual Function	Count	Expected Function	Count	Result		
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Imple	est Step 2	2.84 (Repeat Count = 1)	
	ıme		Input Value
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Actual Function

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Count Result

Name		Input Value		
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Name		Actual Value	Expected Value	Result
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				✓
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-				✓
Т				V

Count Expected Function

st Step <u>2.</u>	85 (Repe	at Count = 1)	
e			Input Value
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Razorcat

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Name				Input Value		
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Name				Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.86 (Repeat Count = 1)			✓
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Name		Actual Value Expected Value	Result
Itulio		Expected value	Result ✓
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			✓

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Name	Act	ual Value	Expected Value	Result
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				✓
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				✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.87	' (Repeat	Count = 1)	✓
Name			Input Value
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		*0 0	
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Name			Input Value	
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Name			Actual Value Expected V	
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Test Step	2.88 (Repeat Count = 1)		✓
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Name		Input Value		
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Name		Actual Value	Expected Value	Result
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Name		Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
				-

Test Step 2.89 (Rep	peat Count = 1)	✓
Name		Input Value
	• 0 0	
	•0 0	
	• 0	
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Name					Actual Value	Expected Value	e Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.90 (Repeat Count = 1)		✓
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Name				Input Value		
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		• 0				
		8				
Name				Actual Value	Expected Value	Result
						~

Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
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Name		Actual Value	Expected Value	Result
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	 -			✓
				~
				✓

T			✓
Actual Function	Count	Expected Function	Count Result
		<u> </u>	✓

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Test Step 2.91 (Repeat Count = 1)	·
Name	Input Value
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Name		Input Value
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Name	Actual Value	Expected Value	Result
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	 •		✓

T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.92 (Repeat Count = 1)	✓
Name	Input Value
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Name		Input Value		
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	0 0		1=	
Name		Actual Value	Expected Value Re	sult
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Т				✓
Actual Function	Count	Expected Function	Count	Result
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Fast Step 2.439 (kepeat Count = 1)		
	Test Step 2.93 (Repeat Count = 1)	
	Name	Input Value
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CDDInterface_Per1 Name Input Value . . Name Result **Actual Value Expected Value** .

T			✓	
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.94 (Repeat Count = 1)	✓
Name	Input Value
0 *0 0	
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• 0	

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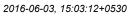
Name			Input Value	
	•			
	0			
	• 0			
	•			
		0 0		
	·	0 0	Actual Value Expected Value	
	·	0 0	Actual Value Expected Value	•
	·	0 0	Actual Value Expected Value	
	·	0 0	Actual Value Expected Value	
Name	•	0 0	Actual Value Expected Value	
Name			Actual Value Expected Value	
Name	•	0 0	Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
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Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name			Actual Value Expected Value	
Name . Name			Actual Value Expected Value	
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Name . Name			Actual Value Expected Value	
Name Name Name			Actual Value Expected Value	
Name Name Name Name Name			Actual Value Expected Value	

T			✓
Actual Function	Count	Expected Function	Count Result
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Test Step 2.95 (Repeat Count = 1)	✓
Name	Input Value
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Name	Actual Value Expected Value Result
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	<u> </u>





Name	Actual Value	Expected Value	Result
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T			✓
Actual Function	Count	Expected Function	Count Result
			✓

Test Step	2.96 (Repeat Count = 1)	🗸
Name		Input Value
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Name			Actual Value Expected Value	Result
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			Expected Function	

Test Step	2.97 (Repeat Count = 1)		√
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Name			Input Value		
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Name		Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.98 (Repeat Cou	nt = 1)	✓
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Name	Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count Res	ult
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Test Step 2.99 (Repeat Count = 1)		✓
Name	Input Value	
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Name				Input Value		
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Name				Actual Value	Expected Value	Result
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Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
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ame		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.	.100 (Repeat Count = 1)	
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Name			Input Value
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Name	Actual Value	Expected Value	Result
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T				✓
Actual Function	Count	Expected Function	Count	Result
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Test Step 2.1	Test Step 2.101 (Repeat Count = 1)					
Name			Input Value			
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Name		Actual Value	Expected Value Re	sult
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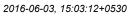


T				✓
Actual Function	Count	Expected Function	Count	Result
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Step 2.102 (Repeat Coun	
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CDDInterface_Per1

Actual Function





Count Result

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Name		Actual Value	Expected Value	Result
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Count Expected Function

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si Siep me	2.103 (Re	Jeat Count – 1)	Input Value	
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Name				Input Value		
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Name				Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.104 (Repeat Count = 1)		✓
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Name		Actual Value	Expected Value	Result
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	Actual Function	Count	Expected Function	Count	Result
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Test Step	2.105 (Rep	peat Count = 1)	🗸
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Name			Actual Value Expected Value	Result
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			Expected Function	

Test Step 2.106 (Repeat Count = 1)					✓	
Name				Input Value		
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Name			Input Value		
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Name Actual Value Expected Value R	esult

Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.108 (Repeat Cour	t = 1)
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Name				Actual Value	Expected Value	Result
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Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	
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Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count Result
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Test Step 2.109 (Repeat Count = 1)	
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Name	Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.	110 (Repe	at Count = 1)	✓
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Name		Input Value		
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Name		Actual Value	Expected Value Re	sult
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.111 (Repeat Count = 1)	· · · · · ·
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Actual Function

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Count Result

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Count Expected Function

est Step 2	2.112 (Repeat Count = 1)	
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.113 (Repeat Count = 1)			✓
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Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.	114 (Repe	at Count = 1)	✓
Name			Input Value
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Name		Input Value		
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		Expected Function	Count	

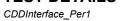
Test Step 2	est Step 2.115 (Repeat Count = 1)					
Name			Input Value			
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Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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Actual Function	Count	Expected Function	Count	Result
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Test Step 2.117 (Repeat Count = 1)	
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Name				Actual Value	Expected Value	Result
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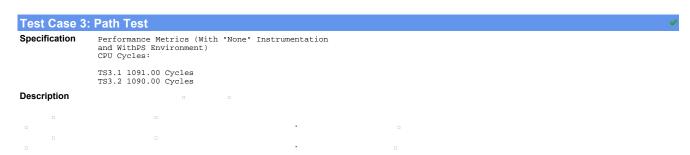
Name	Actual	Value Expected Value	Result
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Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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	р 3.1 (Кере	at Count = 1)		
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Name		Actual Value	Expected Value	Result
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Actual Function	Count	Expected Function	Count	Result
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