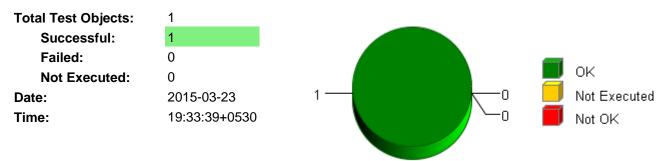


Summary

Overall Test Object Results (including Coverage)



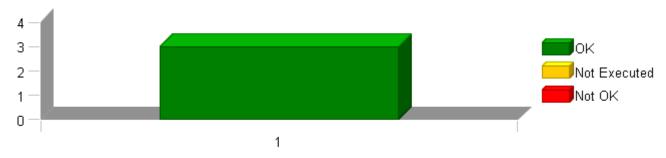
Selected Project Items

Test Object "CBD_UnitTest/ReturnFireWall/ReturnFirewall_Per1"

Used Test Environments

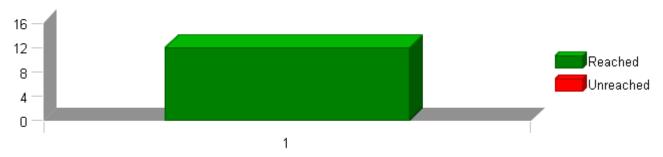
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

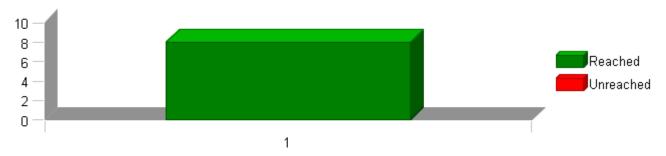
Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.



Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

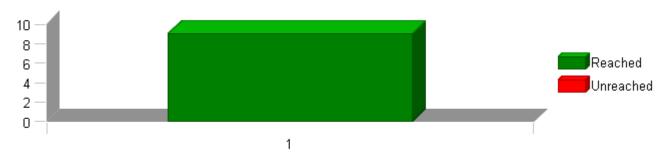
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

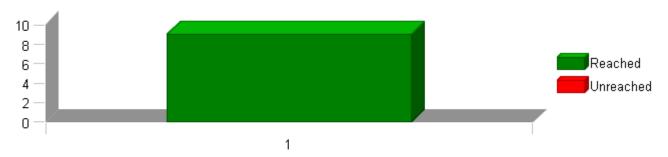


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name		C1	DC	MC/DC	МСС	Test Cases	Result
	Return	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
	ReturnFireWall	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
1	ReturnFirewall_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~

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ReturnFirewall_Per1

 Project
 Return

 Module
 ReturnFireWall

 Test Object
 ReturnFirewall_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\ReturnFirewall_CBD
Configuration File	D:\Synergy_Work_Area\ReturnFirewall_CBD\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\ReturnFirewall\src\Ap_ReturnFirewall.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -Dstatic= -I\$(PROJECTROOT)\ReturnFirewall\utp\contract -I\$(PROJECTROOT)\ReturnFirewall\utp\contract \Ap_ReturnFirewall -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -Dstatic= -I\$(PROJECTROOT)\ReturnFirewall\utp\contract -I\$(PROJECTROOT)\ReturnFirewall\utp\contract \Ap_ReturnFirewall -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification					
Name	Text				



Name of Tester: Surbhi Goel
Code File(s) Version: 10
Module Design Document: Return Firewall_MDD.docx
Module Design Document Version: 10
Data Dictionary Version: 9
Unit Test Plan Version: 10
Data Dictionary Version: 9
Unit Test Plan Version: 6
Optimization Level: Level 2
Compiler (Code Gen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes): 372
Total RAM Used (Bytes): 9
Total CALS Used (Bytes): 216
Special Test Requirements:
Test Date: 03-23-2015
Comments:
Note1: Inline Function defined in globalmacro.h is not unit tested.

Note2: In ""ReturnFirewall_Per1" function, ""Defeat_Return_Svc_Cnt_Igc" always kept FALSE
to make ""if(DefeatReturnSvc_Cnt_T_Igc I= D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) ""
condition FALSE except Boundray Test for variables used in Condition and Path coverage.

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	\$(ProgramFiles)\pls\UDE 4.0
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy Work Area\ReturnFirewall CBD\UnitTestEnv\config\UDE TMS570 DEBUG.WSP



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" Instrumentation and "WithPS" Environment)

TS1.1 794.00 Cycles Shortest Path TS1.2 1350.00 Cycles Longest Path

Description Vector Description:

TS1.1 Shortest Execution Path: ((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode))=True

TS1.2 Longest Execution Path:((DefeatReturnSvc_Cnt_T_lgc != D_FALSE_CNT_LGC) && (MECCounter_Cnt_T_enum != ProductionMode)) =

False
LimitedReturn_MtrNm_T_f32 = Limit_m(ReturnCmd_MtrNm_T_f32, LwrBound_MtrNm_T_f32, UprBound_MtrNm_T_f32)=False
(Abs_f32_m(ReturnCmd_MtrNm_T_f32 - LimitedReturn_MtrNm_T_f32) > FLT_EPSILON)= False"

Test Step 1.1 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	18022
2 RtrnFWUprBoundY MtrNm s4p11[4][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	18022
2 RtrnFWUprBoundY MtrNm s4p11[4][10]	18022

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Name -	Lancet Walnes		
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022 18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022		
t2 RtrnFWUprBoundY MtrNm s4p11[5][3]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022 18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022 18022		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	65408		
t_RtmFWVehSpd_Kph_u9p7[1] t_RtmFWVehSpd_Kph_u9p7[2]	65408 65408		
t_RtrnFWVehSpd_Kph_u9p7[3]	65408		
t RtrnFWVehSpd Kph u9p7[4]	65408		
t_RtrnFWVehSpd_Kph_u9p7[5]	65408		
t_RtrnFWVehSpd_Kph_u9p7[6]	65408		
t_RtrnFWVehSpd_Kph_u9p7[7]	65408		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe target_ReturnFirewall_Per1_			
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 LimitedReturn MtrNm f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_K	_	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	~





T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	~

Fest Step 1.2 (Repeat Count = 1)		
lame	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240 12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9] 2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336	
P_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192 	
P_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168	
P_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144	

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Name	Input Value		
t2 RtrnFWUprBoundY MtrNm s4p11[5][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][2]	-14336		
	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192		
t RtrnFWVehSpd Kph u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Return_Svc_Return_S$	l target_ReturnFirewall_Per1_Defeat_Return	rn_Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall.ReturnFirewall_ReturnFirewall_ReturnFirewall.ReturnFirewall_ReturnFirewall_ReturnFirewall.ReturnFirewall_ReturnFirewall_ReturnFirewall_ReturnFirewall.ReturnFirewall_ReturnFire$	target_ReturnFirewall_Per1_HandwheelPe	osition_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3	2 target_ReturnFirewall_Per1_LimitedReturn	n_MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum_Cnt_enu$	target_ReturnFirewall_Per1_MEC_Counter	er_Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed	_	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cfft_T_endiff) Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Farain_Cnt_1_u06) Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	

T .					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~	
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓	



Test Case 2: Boundary Test

Specification

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Performance Metrics: (With "None" Instrumentation and "WithPS" Environment)
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CPU Cycles:

TS2.1 1315.00 Cycles
TS2.2 1334.00 Cycles
TS2.3 1351.00 Cycles
TS2.3 1351.00 Cycles
TS2.4 1403.00 Cycles
TS2.5 1329.00 Cycles
TS2.6 1329.00 Cycles
TS2.6 1329.00 Cycles
TS2.7 1406.00 Cycles
TS2.9 1350.00 Cycles
TS2.9 1350.00 Cycles
TS2.10 1341.00 Cycles
TS2.11 1320.00 Cycles
TS2.11 1320.00 Cycles
TS2.12 1344.00 Cycles
TS2.13 1361.00 Cycles
TS2.14 1286.00 Cycles
TS2.15 1361.00 Cycles
TS2.15 1361.00 Cycles
TS2.16 1462.00 Cycles
TS2.17 1376.00 Cycles
TS2.18 1317.00 Cycles
TS2.19 1347.00 Cycles
TS2.19 1347.00 Cycles
TS2.20 1317.00 Cycles
TS2.21 1337.00 Cycles
TS2.22 1334.00 Cycles
TS2.23 1341.00 Cycles
TS2.24 1352.00 Cycles
TS2.25 1412.00 Cycles
TS2.26 1353.00 Cycles
TS2.27 1321.00 Cycles
TS2.28 1409.00 Cycles
TS2.29 1350.00 Cycles
TS2.29 1350.00 Cycles
TS2.23 1341.00 Cycles
TS2.23 1341.00 Cycles
TS2.23 1351.00 Cycles
TS2.23 1351.00 Cycles
TS2.23 1350.00 Cycles
TS2.23 1350.00 Cycles
TS2.31 1341.00 Cycles
TS2.33 794.00 Cycles

Description

Vector Description:

TS2.1HandwheelPosition_HwDeg_f32=Min TS2.2HandwheelPosition_HwDeg_f32=Max TS2.3HandwheelPosition_HwDeg_f32=Zero TS2.4HandwheelPosition_HwDeg_f32=Pos TS2.5HandwheelPosition_HwDeg_f32=Neg TS2.6ReturnCmd_MtrNm_f32=Min TS2.7ReturnCmd_MtrNm_f32=Max TS2.8ReturnCmd_MtrNm_f32=Zero TS2.9ReturnCmd_MtrNm_f32=Pos TS2.19ReturnCind_MtrNim_i32=Pos TS2.10ReturnCmd_MtrNm_f32=Neg TS2.11VehicleSpeed_Kph_f32=Min TS2.12VehicleSpeed_Kph_f32=Max TS2.13VehicleSpeed_Kph_f32=Pos TS2.14t_RtrnFWVehSpd_Kph_u9p7[8]=Min TS2.15t_RtrnFWVehSpd_Kph_u9p7[8]=Max TS2.15I_RtrnFWVehSpd_Kph_u9p7[8]=Max
TS2.16I_RtrnFWVehSpd_Kph_u9p7[8]=Pos
TS2.17t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Min
TS2.18t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Max
TS2.19t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Mid
TS2.20t_RtrnFWUprBoundX_HwDeg_s11p4[11]=Pos
TS2.21t_RtrnFWUprBoundY_HwDeg_s11p4[11]=Nid
TS2.22t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Min
TS2.23t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Mid
TS2.25t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Mid
TS2.25t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Mid
TS2.25t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Pos TS2.24t2_ktrnFWUprBoundYU_mtrNm_s4p11[11]=Mid
TS2.25t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Pos
TS2.26t2_RtrnFWUprBoundY0_MtrNm_s4p11[11]=Neg
TS2.27Defeat_Return_Svc_Cnt_lgc=Min
TS2.28Defeat_Return_Svc_Cnt_lgc=Max
TS2.29MEC_Counter_Cnt_enum=Min
TS2.30MEC_Counter_Cnt_enum=Max TS2.31MEC_Counter_Cnt_enum=Pos TS2.32All min

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

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TS2.33All max

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ReturnFirewall Per1 Input Value t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4] -12288 -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5] t2 RtrnFWUprBoundY_MtrNm_s4p11[1][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0] -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] -12800 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] -10240 t2 RtrnFWUprBoundY_MtrNm_s4p11[3][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9] -4096 t2 RtrnFWUprBoundY MtrNm s4p11[3][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2] 4096 t2 RtrnFWUprBoundY MtrNm s4p11[4][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7] 10240 $t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]$ 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3] -12800 $t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]$ -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] -4096

-2048

2048

3072

4096

5120

6144

7168

8192

10240

12288

14336 16384

-16384

-15360

-14336

-12800

-12288

-10240

-8192

-7168

6144

-4096

-2048

t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]

t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]

t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]

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Totalii ii ovali <u>-</u> i ori		<u> </u>	10.10
Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-1		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Retur$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_N	/trNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.39990234	-4.39990234 ± 0.00048828125	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

T4 04 0 0 (P4 04)	.a
Test Step 2.2 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408 -17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360 14236
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336 -13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtinFWOpiBoundY_MtiNin_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtmFWUprBoundY_MtrNm_s4p11[3][10]	-2048
	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	6144 7168
tz_RtrnFWUprBoundY_MtrNm_s4p11[4][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtmFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtinFWOpiBoundY_MtiNin_s4p11[5][2] t2_RtinFWUprBoundY_MtiNin_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtinFWOpiBoundY_MtiNin_s4p11[6][8]	14336
t2_RtinFWOpiBoundY_MtiNin_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
	528
	V-1
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704 880
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3] t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3] t_RtrnFWUprBoundX_HwDeg_s11p4[4] t_RtrnFWUprBoundX_HwDeg_s11p4[5]	880 1056
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3] t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880

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	I		
Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1760		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	3520		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	25472		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	32640		
t_RtrnFWVehSpd_Kph_u9p7[7]	38272		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.200000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	nt I target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc		
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HandwheelPositio$	vDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$	f32 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	ım target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	7.64990234	7.64990234 ± 0.00048828125	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2 RtrnFWUprBoundY MtrNm s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2 RtrnFWUprBoundY MtrNm s4p11[0][3]	-12800
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-1280 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240 -8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248 -1072
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072 806
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896 -720
t_RtrnFWUprBoundX_HwDeg_s11p4[7] t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-72U -544
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-344 -368
t_RtmFWUprBoundX_HwDeg_s11p4[9]	-300 -192
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	2688
	6528
t_RtrnFWVehSpd_Kph_u9p7[2]	6528 9600
t_RtrnFWVehSpd_Kph_u9p7[2] t_RtrnFWVehSpd_Kph_u9p7[3]	9600

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Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_N	/ltrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓

Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
2 RtrnFWUprBoundY MtrNm s4p11[0][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
2 RtrnFWUprBoundY MtrNm s4p11[0][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
2 RtrnFWUprBoundY MtrNm s4p11[0][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
2 RtrnFWUprBoundY MtrNm s4p11[1][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
2 RtrnFWUprBoundY MtrNm s4p11[1][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
2 RtrnFWUprBoundY MtrNm s4p11[1][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
2 RtrnFWUprBoundY MtrNm s4p11[1][9]	16384
2_RtmFWUprBoundY_MtrNm_s4p11[1][10]	17408
2_RtmFWUprBoundY_MtrNm_s4p11[1][10] 2_RtmFWUprBoundY_MtrNm_s4p11[2][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
2_RtmFWUprBoundY_MtrNm_s4p11[2][3]	-14336
2_RtmFWUprBoundY_MtrNm_s4p11[2][4]	-13312
2_KtriFWUprBoundY_MtrNm_s4p11[2][5]	-12288
2_RtmFWUprBoundY_MtrNm_s4p11[2][6]	-10240
2_RtmFWUprBoundY_MtrNm_s4p11[2][7]	-8192
z_RtmFWUprBoundY_MtrNm_s4p11[2][7] 2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6192 -6144
z_RtmFWUprBoundY_MtrNm_s4p11[2][9]	-4096
2_RtmFWUprBoundY_MtrNm_s4p11[2][10]	-2048
2_KtriFWUprBoundY_MtrNm_s4p11[2][10] 2_RtriFWUprBoundY_MtrNm_s4p11[3][0]	0
2_RtmFWUprBoundY_MtrNm_s4p11[3][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
2_RtmFWUprBoundY_MtrNm_s4p11[3][2] 2_RtmFWUprBoundY_MtrNm_s4p11[3][3]	5120
z_RtmFWUprBoundY_MtrNm_s4p11[3][4]	6144
	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7] 2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408 -16384
P_RtrnFWUprBoundY_MtrNm_s4p11[4][1] P_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696
_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520
_RtrnFWUprBoundX_HwDeg_s11p4[8] RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744 -3744
RtrnFWUprBoundX_HwDeg_s11p4[9]	-3/44
_RtrnFWVehSpd_Kph_u9p7[0]	256
_RtrnFWVehSpd_Kph_u9p7[1]	2816
RtrnFWVehSpd Kph u9p7[2]	6656
_RtrnFWVehSpd_Kph_u9p7[3]	9600
_RtrnFWVehSpd_Kph_u9p7[4]	28416
_RtrnFWVehSpd_Kph_u9p7[5]	32256
_RtrnFWVehSpd_Kph_u9p7[6]	28416
RtrnFWVehSpd_Kph_u9p7[7]	32640
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	400.5
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
arget_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.69999988
arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	45.6500015
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_	Cnt_l target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_l	HwDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
	m f32 target ReturnFirewall Per1 LimitedReturn MtrNm f32

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.44189453	3.44189453 ± 0.00048828125	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target Rte Inst Ap ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2 RtrnFWUprBoundY MtrNm s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192		
t2 RtrnFWUprBoundY MtrNm s4p11[5][7]	-7168		
t2 RtrnFWUprBoundY MtrNm s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2 RtrnFWUprBoundY MtrNm s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15384 -15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-400.25		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.10000002		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	66.4499969		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Inst_Ap_ReturnFirewall.$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosit	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_N	MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.92041016	-4.92041016 ± 0.00048828125	~





T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)		V
Name	Input Value	
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336 -13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192	
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	-6192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-0144	
t2 RtrnFWUprBoundY MtrNm s4p11[3][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-2040 0	
t2 RtrnFWUprBoundY MtrNm s4p11[4][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288	
t2 RtrnFWUprBoundY MtrNm s4p11[4][8]	14336	
t2 RtrnFWUprBoundY MtrNm s4p11[4][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312	
12_1XIIII VVODIDUUIU I _IVIIIIVIII_540 I I[3][4]	-13312	

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Input Value		
-12288		
-10240		
-8192		
-6144		
-4096		
-2048		
0		
2048		
4096		
5120		
6144		
7168		
10240		
12288		
14336		
16384		
17408		
-17408		
-16384		
-15360		
-14336		
-13312		
-12288		
-10240		
-8192		
-6144		
-4096		
-2048		
176		
352		
704		
880		
1056		
1232		
1408		
1584		
128		
2688		
6528		
9600		
14208		
19328		
27008		
32640		
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	Svc Cnt lgc	
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199		· · · · · · · · · · · · · · · · · · ·
4	4	
1	1	
=	-12288 -10240 -8192 -6144 -4096 -2048 0 2048 4096 5120 6144 7168 10240 12288 -14336 -16384 -17408 -16384 -15360 -14336 -13312 -12288 -10240 -8192 -6144 -4096 -2048 176 352 528 704 880 1056 1232 1408 1584 1616 1952 128 2688 6528 9600 14208 19328 27008 32640 0 50 2 -8.8000019 7 target_ReturnFirewall_Per1_Defeat_Return_target_ReturnFirewall_Per1_MEC_Counter_target_ReturnFirewall_Per1_ReturnCmd_Mtter_target_ReturnFirewall_Per1_ReturnCmd_Return_ReturnFirewall_Per1_ReturnFirewall_Per1_ReturnFirewall_Per1_ReturnFirewall_Per1_ReturnFirewall_Pe	-12288 -10240 -8192 -6144 -4096 -2048 -00 2048 4096 -5120 -6144 7168 -10240 -12288 -14336 -1336 -14336 -14336 -14336 -14336 -14336 -14336 -14336 -14336 -14336 -14336 -15384 -10240 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -176 -8192 -6144 -4096 -2048 -7008 -8208 -8208 -820 -8208 -820 -820 -82

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓





Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192 -7168
t2_RtinFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2 RtrnFWUprBoundY MtrNm s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtmFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2 RtrnFWUprBoundY MtrNm s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	6144 -4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	6144 -4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtmFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	16384 -16384
t2_RtmFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
tz Kulli Wopiboulidi Wullilii 54pi ijojizi	

ReturnFirewall_Per1





Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtmFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtmFWUprBoundX_HwDeg_s11p4[1]	-6400		
t RtrnFWUprBoundX HwDeg s11p4[2]	-5680		
t RtrnFWUprBoundX HwDeg s11p4[3]	-4000		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000		
t RtrnFWUprBoundX HwDeg s11p4[5]	-1584		
t RtrnFWUprBoundX HwDeg s11p4[6]	-1200		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880		
t RtrnFWUprBoundX HwDeg s11p4[9]	-800		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	7808		
t_RtrnFWVehSpd_Kph_u9p7[3]	9728		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	20608		
t_RtrnFWVehSpd_Kph_u9p7[6]	20736		
t_RtrnFWVehSpd_Kph_u9p7[7]	21248		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	54		
target_ReturnFirewall_Per1_VenicleSpeed_Rpn_isz.value target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt lac	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_intrini_isz		_	
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 ReturnCmd MtrNm f32	target_ReturnFirewall_Per1_ReturnCmd_Mt	· -	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	_	
			B "
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtmFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtmFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtmFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

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Input Value t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3] 5120 t2 RtrnFWUprBoundY_MtrNm_s4p11[7][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] 17408 t_RtrnFWUprBoundX_HwDeg_s11p4[0] -10848 t_RtrnFWUprBoundX_HwDeg_s11p4[1] -10656 t_RtrnFWUprBoundX_HwDeg_s11p4[2] -9072 t_RtrnFWUprBoundX_HwDeg_s11p4[3] -9024 -8688 t_RtrnFWUprBoundX_HwDeg_s11p4[4] t_RtrnFWUprBoundX_HwDeg_s11p4[5] -7104 t_RtrnFWUprBoundX_HwDeg_s11p4[6] -5696 t_RtrnFWUprBoundX_HwDeg_s11p4[7] -5520 t_RtrnFWUprBoundX_HwDeg_s11p4[8] -3744 t_RtrnFWUprBoundX_HwDeg_s11p4[9] -3744 t_RtrnFWUprBoundX_HwDeg_s11p4[10] -3408 t_RtrnFWVehSpd_Kph_u9p7[0] 128 t_RtrnFWVehSpd_Kph_u9p7[1] 1408 t_RtrnFWVehSpd_Kph_u9p7[2] 10368 t_RtrnFWVehSpd_Kph_u9p7[3] 9984 t_RtrnFWVehSpd_Kph_u9p7[4] 14208 t_RtrnFWVehSpd_Kph_u9p7[5] 23168 t_RtrnFWVehSpd_Kph_u9p7[6] 24448 t_RtrnFWVehSpd_Kph_u9p7[7] 25344 target ReturnFirewall Per1 Defeat Return Svc Cnt lgc.value 0 $target_Return Firewall_Per1_Handwheel Position_HwDeg_f32.value$ 100.800003 target ReturnFirewall Per1 MEC Counter Cnt enum.value target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target ReturnFirewall Per1 VehicleSpeed Kph f32.value 32.2999992 target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt | target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Rte_inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32 $target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 \\ target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 \\ tar$ target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 $target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32$ target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 **Actual Value Expected Value** Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 199 199 Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 0 0

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

0 ± 0.00048828125

0

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value





Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
2 RtrnFWUprBoundY MtrNm s4p11[2][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
:2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
2 RtrnFWUprBoundY MtrNm s4p11[5][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
2 RtrnFWUprBoundY MtrNm s4p11[5][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
2 RtrnFWUprBoundY MtrNm s4p11[5][7]	-7168
	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144
11 Opi Dodina i _ina (4) ii _o+p i ([/][o]	-4096
2 RtrnFWI InrRoundY MtrNm s4p11[7][0]	
:2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] :2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048

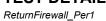
2015-03-23, 19:32:58+0530



Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	120.349998		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12.6999998		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Retur$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	2 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	Cph_f32	
Name	Actual Value	Expected Value	Result
$Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.73730469	2.73730469 ± 0.00048828125	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

T+ 0+ 0.40 (D+ 0+	
Test Step 2.10 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048





Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2 RtrnFWUprBoundY MtrNm s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2 RtrnFWUprBoundY MtrNm s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2 RtrnFWUprBoundY MtrNm s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2 RtrnFWUprBoundY MtrNm s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2 RtrnFWUprBoundY MtrNm s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360 -14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
	880
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	
t_RtrnFWUprBoundX_HwDeg_s11p4[4] t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
	1056 1232
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	





Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	145.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-2.25		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_Hand$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.52490234	5.52490234 ± 0.00048828125	~

au				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2 RtrnFWUprBoundY MtrNm s4p11[0][1]	-15360
t2 RtrnFWUprBoundY MtrNm s4p11[0][2]	-14336
t2 RtrnFWUprBoundY MtrNm s4p11[0][3]	-12800
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048

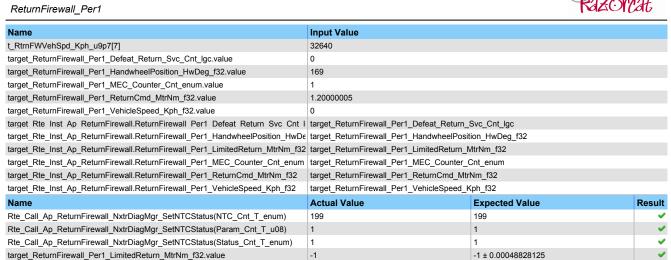




Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t RtrnFWUprBoundX HwDeg s11p4[1]	-6400
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
	-4000
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1584
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672
t_RtrnFWVehSpd_Kph_u9p7[0]	0
t_RtrnFWVehSpd_Kph_u9p7[1]	2560
t_RtrnFWVehSpd_Kph_u9p7[2]	6400
t_RtrnFWVehSpd_Kph_u9p7[3]	9600
t_RtrnFWVehSpd_Kph_u9p7[4]	12800
	19200
t_RtrnFWVehSpd_Kph_u9p7[5] t_RtrnFWVehSpd_Kph_u9p7[6]	19200 25600

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T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call ReturnFirewall Per1 CP1 ChecknointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	V

Name	Invest Value
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
2 RtrnFWUprBoundY MtrNm s4p11[1][7]	12288
t2 RtrnFWUprBoundY MtrNm s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
12 RtrnFWUprBoundY MtrNm s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
2_RtmFWUprBoundY_MtrNm_s4p11[2][2]	-15360
	-14336
12_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	14336

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408 -16384
P_RtrnFWUprBoundY_MtrNm_s4p11[4][1] P_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2 RtrnFWUprBoundY MtrNm s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848
_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656
_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072
_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024
_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688
_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104
_RtrnFWUprBoundX_HwDog_s11p4[6]	-5696 -5520
_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520 3744
_RtrnFWUprBoundX_HwDeg_s11p4[8] RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744 -3744
_ktrnFwUprBoundX_HwDeg_s11p4[9] _ktrnFWUprBoundX_HwDeg_s11p4[10]	-3/44 -3408
_RtrnFWVehSpd_Kph_u9p7[0]	128
_RtrnFWVehSpd_Kph_u9p7[1]	1408
RtrnFWVehSpd Kph u9p7[2]	6528
_RtrnFWVehSpd_Kph_u9p7[3]	9600
_RtrnFWVehSpd_Kph_u9p7[4]	14208
_RtrnFWVehSpd_Kph_u9p7[5]	19328
_RtrnFWVehSpd_Kph_u9p7[6]	14208
RtrnFWVehSpd_Kph_u9p7[7]	19840
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	193
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2
arget_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.29999995
arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_	_Cnt_I target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_	HwDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32
	m f32 target ReturnFirewall Per1 LimitedReturn MtrNm f32

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Name	Input Value		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	1.29999995	1.29999995 ± 0.00048828125	•

Τ			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14330
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192 -7400
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

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ReturnFirewall Per1 Input Value t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7] 10240 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8] t2 RtrnFWUprBoundY_MtrNm_s4p11[4][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10] 16384 -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3] -12800 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] -7168 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0] -16384 $t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]$ -15360 t2 RtrnFWUprBoundY MtrNm s4p11[7][2] -14336 $t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]$ -12800 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5] -10240 t2 RtrnFWUprBoundY MtrNm s4p11[7][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] -2048 t_RtrnFWUprBoundX_HwDeg_s11p4[0] 160 t_RtrnFWUprBoundX_HwDeg_s11p4[1] 320 t_RtrnFWUprBoundX_HwDeg_s11p4[2] 480 t_RtrnFWUprBoundX_HwDeg_s11p4[3] 640 t RtrnFWUprBoundX_HwDeg_s11p4[4] 800 t_RtrnFWUprBoundX_HwDeg_s11p4[5] 960 t_RtrnFWUprBoundX_HwDeg_s11p4[6] 1120 t_RtrnFWUprBoundX_HwDeg_s11p4[7] 1280 t_RtrnFWUprBoundX_HwDeg_s11p4[8] 1440 $t_RtrnFWUprBoundX_HwDeg_s11p4[9]$ 1600 t_RtrnFWUprBoundX_HwDeg_s11p4[10] 1760 t_RtrnFWVehSpd_Kph_u9p7[0] 128 t_RtrnFWVehSpd_Kph_u9p7[1] 2688 t RtrnFWVehSpd Kph u9p7[2] 6528 t_RtrnFWVehSpd_Kph_u9p7[3] 9600 t_RtrnFWVehSpd_Kph_u9p7[4] 14208 t_RtrnFWVehSpd_Kph_u9p7[5] 19328 t RtrnFWVehSpd Kph u9p7[6] 27008 t_RtrnFWVehSpd_Kph_u9p7[7] 32640 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 $target_Return Firewall_Per1_Handwheel Position_HwDeg_f32.value$ 505.549988 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 3.54999995 $target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value$ 55.5 $target_Rte_Inst_Ap_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I \\ target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Igc$ target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum | target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum | $target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 \\ target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 \\ target_ReturnReturnCmd_MtrNm_f32 \\ target_ReturnReturnCmd_MtrNm_f32 \\ target_ReturnR$

target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_F	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.54999995	3.54999995 ± 0.00048828125	✓



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)		✓
Name	Input Value	
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120 6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240 12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336	
t2 RtrnFWUprBoundY MtrNm s4p11[4][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312	
a_ram rropidoundi_maram_94p11[0][4]	-10012	

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2 RtrnFWUprBoundY MtrNm s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360		
t2_RtmFWOprBoundY_MtrNm_s4p11[7][2] t2_RtmFWUprBoundY_MtrNm_s4p11[7][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312 -12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	0		
t_RtrnFWVehSpd_Kph_u9p7[2]	0		
t_RtrnFWVehSpd_Kph_u9p7[3]	0		
t_RtrnFWVehSpd_Kph_u9p7[4]	0		
t_RtrnFWVehSpd_Kph_u9p7[5]	0		
t_RtrnFWVehSpd_Kph_u9p7[6]	0		
t_RtrnFWVehSpd_Kph_u9p7[7]	0		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	241.5		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	1.70000005		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	0		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt lac	
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 HandwheelPosition HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	,	_ ·-	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		_	
target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr		
		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	1	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	1.70000005	1.70000005 ± 0.00048828125	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓





Post Part	Test Step 2.15 (Repeat Count = 1)	· ·
Rig. Hard, P. Celestri-Frewall		Input Value
2. Part Purplement Mannay and 19 19 19 19 19 19 19 19		
2. Remin-Vigle-Bound, Minhm, sel-11 (1923) -12000 2. Remin-Vigle-Bound, Minhm, sel-11 (1924) -11 (1924) -11 (1924) 2. Remin-Vigle-Bound, Minhm, sel-11 (1924) -11 (1924) -11 (1924) -12 (192		
2. Ramin'spideousey Mehm sel 1003	t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
	t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
2. Perr Puri Bound's (Minhor, set 10) 4922 2. Perr Puri Bound's (Minhor, set 10) 4922 2. Perr Puri Bound's (Minhor, set 10) 4968 2. Perr Puri Bound's (Minhor, set 11) 4968 2. P		
2. Janne Wildy Beaumy Menhal 1947 10 10		
2_ Part March		
22 Pers March 1998 199		
2. RIMPHUPSONOMY_MINN_perior 1910 90 2048 2. RIMPHUPSONOMY_MINN_perior 1910 90 2048 2. RIMPHUPSONOMY_MINN_perior 1910 91 2048 2. RIMPHUPSONOMY_MINN_perior 1910 91 2048 2. RIMPHUPSONOMY_MINN_perior 1910 91 2049 2. RIMPHUPSONOMY_MIN		· · ·
2. RITHENDURGERINAN, MINNING, 1991 (19)10 2048 2. RITHENDURGERINAN, MINNING, 1991 (19)10 2072 2. RITHENDURGERINAN, MINNING, 1991 (19)11 3072 3		
2. Rinn'Ruys Bound's, Mehm as \$11 1010 3072 3		
22 PimPrVUpSoundY_Mehm_abs11[10]		2048
2. RIFFAPUR BOUNDY, MANN— 451 11[10]	t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
2. RimFWUpSourdy Mintm. sp111[19] 7168	t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
2. Parti-Vujurbaury Minits sept 1(1) 5 1 1 1 1 1 1 1 1 1	t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
Refire/Pub/Boundy Minkm sep11[19] 10240 12288 1228		
12 Partin Pulsarbourdy Minhtm. seph 11 (1) 1228 22 Partin Pulsarbourdy Minhtm. seph 11 (1) 1228 23 Partin Pulsarbourdy Minhtm. seph 11 (1) 11 (1) 1238 24 Partin Pulsarbourdy Minhtm. seph 11 (1) 11 (1) 1238 25 Partin Pulsarbourdy Minhtm. seph 11 (1) 11 (1) 1238 25 Partin Pulsarbourdy Minhtm. seph 11 (1) 11 (1) 1238 26 Partin Pulsarbourdy Minhtm. seph 11 (1) 11 (1) 1238 27 Partin Pulsarbourdy Minhtm. seph 11 (1) 11 (1) 1238 28 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 1238 29 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 1228 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 1228 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 12 (1) 12 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 20 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 21 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 22 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 23 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 24 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 25 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 26 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 27 Partin Pulsarbourdy Minhtm. seph 11 (1) 13 (1) 13 (1) 28 Partin Pulsarbo		
12. RINFPWUPBOUNTY MINNIN _94011[1]8] 14336 1433		
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12_RtmFWUprBoundY_MtrNm_s4p11[4][4] -12288 12_RtmFWUprBoundY_MtrNm_s4p11[4][5] -10240 12_RtmFWUprBoundY_MtrNm_s4p11[4][6] -8192 12_RtmFWUprBoundY_MtrNm_s4p11[4][7] -7168 12_RtmFWUprBoundY_MtrNm_s4p11[4][8] 6144 12_RtmFWUprBoundY_MtrNm_s4p11[4][9] -4096 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtmFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtmFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtmFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtmFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtmFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtmFWUprBoundY_MtrNm_s4p11[5][6] 10240 12_RtmFWUprBoundY_MtrNm_s4p11[5][6] 12288 12_RtmFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtmFWUprBoundY_MtrNm_s4p11[6][0] 16384		
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12_RtrnFWUprBoundY_MtrNm_s4p11[4][7] -7168 12_RtrnFWUprBoundY_MtrNm_s4p11[4][8] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[4][9] -4096 12_RtrnFWUprBoundY_MtrNm_s4p11[4][10] -2048 12_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtrnFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtrnFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtrnFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 1228 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 1228 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 14336 12_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384	t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
12_RtmFWUprBoundY_MtrNm_s4p11[4][8] 6144 12_RtmFWUprBoundY_MtrNm_s4p11[4][9] -4096 12_RtmFWUprBoundY_MtrNm_s4p11[4][10] -2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtmFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtmFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtmFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtmFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtmFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtmFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtmFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtmFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtmFWUprBoundY_MtrNm_s4p11[6][0] -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[4][9] -4096 12_RtrnFWUprBoundY_MtrNm_s4p11[4][10] -2048 12_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtrnFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtrnFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtrnFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
12_RtmFWUprBoundY_MtrNm_s4p11[4][10] -2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtmFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtmFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtmFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtmFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtmFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtmFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtmFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtmFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtmFWUprBoundY_MtrNm_s4p11[6][0] -16384		
12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 2048 12_RtmFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtmFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtmFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtmFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtmFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtmFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtmFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtmFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtmFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtmFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtmFWUprBoundY_MtrNm_s4p11[6][0] -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[5][1] 3072 12_RtrnFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtrnFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[5][2] 4096 12_RtrnFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 16384 12_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
12_RtrnFWUprBoundY_MtrNm_s4p11[5][3] 5120 12_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 12_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 12_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 12_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 12_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 12_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		7168
12_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 12_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 12_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 12_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384	t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384		
ız_кингwuprbounuт_мітиm_s4p1т[o][1] -15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] -14336		
t2_RtmFWUprBoundY_MtrNm_s4p11[6][3] -12800		

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtrnFWVehSpd_Kph_u9p7[0]	65408		
t_RtrnFWVehSpd_Kph_u9p7[1]	65408		
t_RtrnFWVehSpd_Kph_u9p7[2]	65408		
t_RtrnFWVehSpd_Kph_u9p7[3]	65408		
t_RtrnFWVehSpd_Kph_u9p7[4]	65408		
t_RtrnFWVehSpd_Kph_u9p7[5]	65408		
t_RtrnFWVehSpd_Kph_u9p7[6]	65408		
t_RtrnFWVehSpd_Kph_u9p7[7]	65408		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	265		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	1.85000002		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	67		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288	

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
2 RtrnFWUprBoundY MtrNm s4p11[2][4]	-13312
z_RtmFWUprBoundY_MtrNm_s4p11[2][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtmFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtmFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtmFWOpiBoundY_MtrNm_s4p11[6][10]	-2048
	0
:2_RtrnFWUprBoundY_MtrNm_s4p11[7][0] :2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

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Input Value t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3] 5120 t2 RtrnFWUprBoundY_MtrNm_s4p11[7][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] 17408 t_RtrnFWUprBoundX_HwDeg_s11p4[0] -10848 t_RtrnFWUprBoundX_HwDeg_s11p4[1] -10656 t_RtrnFWUprBoundX_HwDeg_s11p4[2] -9072 t_RtrnFWUprBoundX_HwDeg_s11p4[3] -9024 -8688 t_RtrnFWUprBoundX_HwDeg_s11p4[4] t_RtrnFWUprBoundX_HwDeg_s11p4[5] -7104 t_RtrnFWUprBoundX_HwDeg_s11p4[6] -5696 t_RtrnFWUprBoundX_HwDeg_s11p4[7] -5520 t_RtrnFWUprBoundX_HwDeg_s11p4[8] -3744 t_RtrnFWUprBoundX_HwDeg_s11p4[9] -3680 t_RtrnFWUprBoundX_HwDeg_s11p4[10] -3408 t_RtrnFWVehSpd_Kph_u9p7[0] Λ 2560 t_RtrnFWVehSpd_Kph_u9p7[1] t_RtrnFWVehSpd_Kph_u9p7[2] 6400 t_RtrnFWVehSpd_Kph_u9p7[3] 9600 t_RtrnFWVehSpd_Kph_u9p7[4] 12800 t_RtrnFWVehSpd_Kph_u9p7[5] 19200 t_RtrnFWVehSpd_Kph_u9p7[6] 25600 t_RtrnFWVehSpd_Kph_u9p7[7] 32640 target ReturnFirewall Per1 Defeat Return Svc Cnt lgc.value 0 $target_Return Firewall_Per1_Handwheel Position_HwDeg_f32.value$ 289.450012 target ReturnFirewall Per1 MEC Counter Cnt enum.value target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 2.01999998 target ReturnFirewall Per1 VehicleSpeed Kph f32.value 78.1999969 target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt | target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32 $target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 \\ target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 \\ tar$ target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 $target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32$ target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 **Actual Value Expected Value** Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 199 199 Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 0

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

2.01999998

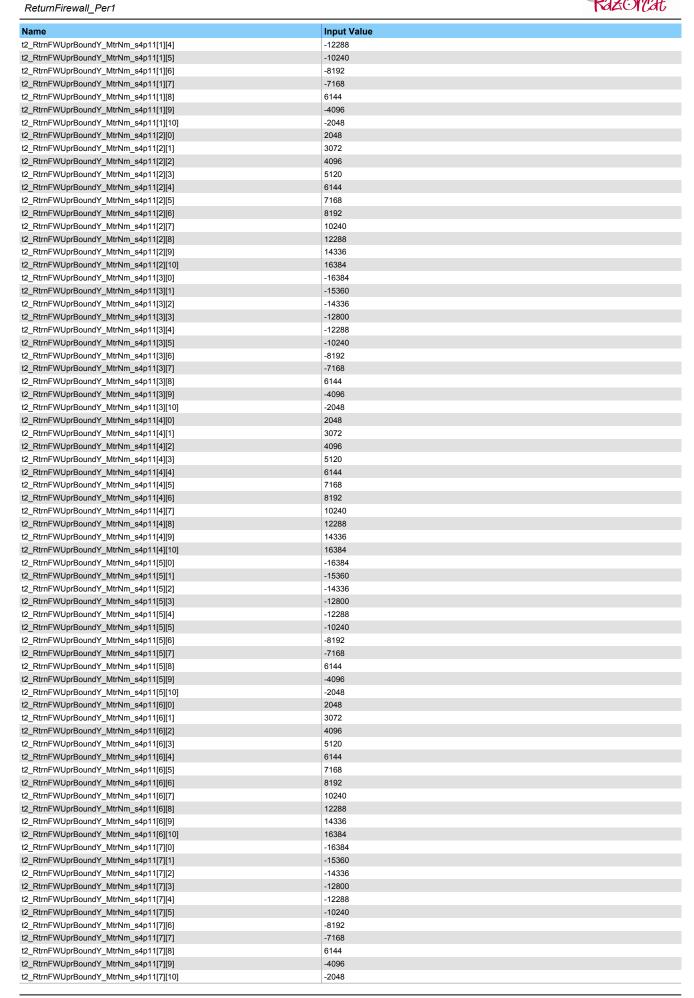
2.01999998 ± 0.00048828125

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	313		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.19000006		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	87		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt	target_ReturnFirewall_Per1_Defeat_Return	_Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwD	target_ReturnFirewall_Per1_HandwheelPos	ition_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	m target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32$	target_ReturnFirewall_Per1_ReturnCmd_Mi	trNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	Kph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target ReturnFirewall Per1 LimitedReturn MtrNm f32.value	-2	-2 ± 0.00048828125	✓

Τ			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	•

T4 04 0.40 (D4 04 - 4)	.a
Test Step 2.18 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408 -17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384 -15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtmFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtmFWUprBoundY_MtrNm_s4p11[4][0] t2_RtmFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtmFWUprBoundY_MtrNm_s4p11[4][1] t2_RtmFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtmFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtinFWOprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtmFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2 RtrnFWUprBoundY MtrNm s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2 RtrnFWUprBoundY MtrNm s4p11[5][9]	-4096
t2 RtrnFWUprBoundY MtrNm s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
	17280
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	11200
t_RtrnFWUprBoundX_HwDeg_s11p4[6] t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	17792		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	30592		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	337		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.3599999		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_Handw$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	/trNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-8.5	-8.5 ± 0.00048828125	✓

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2 RtrnFWUprBoundY MtrNm s4p11[0][1]	-15360
t2 RtrnFWUprBoundY MtrNm s4p11[0][2]	-14336
t2 RtrnFWUprBoundY MtrNm s4p11[0][3]	-12800
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048

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Input Value t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1] 3072 t2 RtrnFWUprBoundY_MtrNm_s4p11[3][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0] -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3] -12800 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] 3072 t2 RtrnFWUprBoundY_MtrNm_s4p11[5][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5] 7168 t2 RtrnFWUprBoundY MtrNm s4p11[5][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] 14336 t2 RtrnFWUprBoundY MtrNm s4p11[5][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] -16384 -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3] -12800 $t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]$ -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10] -2048 $t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]$ 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] 16384 t_RtrnFWUprBoundX_HwDeg_s11p4[0] 0 t_RtrnFWUprBoundX_HwDeg_s11p4[1] 0 $t_RtrnFWUprBoundX_HwDeg_s11p4[2]$ 0 t_RtrnFWUprBoundX_HwDeg_s11p4[3] 0 t_RtrnFWUprBoundX_HwDeg_s11p4[4] 0 0 t_RtrnFWUprBoundX_HwDeg_s11p4[5] t_RtrnFWUprBoundX_HwDeg_s11p4[6] 0 $t_RtrnFWUprBoundX_HwDeg_s11p4[7]$ 0 t_RtrnFWUprBoundX_HwDeg_s11p4[8] 0 t_RtrnFWUprBoundX_HwDeg_s11p4[9] 0 t_RtrnFWUprBoundX_HwDeg_s11p4[10] 0 t_RtrnFWVehSpd_Kph_u9p7[0] 128 t RtrnFWVehSpd Kph u9p7[1] 2688 t_RtrnFWVehSpd_Kph_u9p7[2] 6528 t_RtrnFWVehSpd_Kph_u9p7[3] 9600 t_RtrnFWVehSpd_Kph_u9p7[4] 14208 t_RtrnFWVehSpd_Kph_u9p7[5] 19328 t_RtrnFWVehSpd_Kph_u9p7[6] 27008

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Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target ReturnFirewall Per1 Defeat Return Svc Cnt Igc.value	0		
target ReturnFirewall Per1 HandwheelPosition HwDeg f32.value	361.399994		
target ReturnFirewall Per1 MEC Counter Cnt enum.value	0		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	2.52999997		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	54.75		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target ReturnFirewall Per1 Defeat Return	Svc Cnt Igc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_N	/trNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Ont_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-2	-2 ± 0.00048828125	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓

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Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2 RtrnFWUprBoundY MtrNm s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2 RtrnFWUprBoundY MtrNm s4p11[2][5]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2 RtrnFWUprBoundY MtrNm s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2 RtrnFWUprBoundY MtrNm s4p11[3][1]	2048
t2 RtrnFWUprBoundY MtrNm s4p11[3][2]	4096
t2_rxtmr woprBoundY_mitrVm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2 RtrnFWUprBoundY MtrNm s4p11[3][6]	10240
t2_RtmFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	14336
tz_ixtiii vvopibouliu1_ivitiiviii_84p11[ə][o]	14000

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408 -16384
P_RtrnFWUprBoundY_MtrNm_s4p11[4][1] P_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360 -15360
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -13312
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408
_RtrnFWUprBoundX_HwDeg_s11p4[0]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[1]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[2]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[3]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[4]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[5]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[6]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[7]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[8]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[9]	8000
_RtrnFWUprBoundX_HwDeg_s11p4[10]	8000 256
_RtrnFWVehSpd_Kph_u9p7[0] _RtrnFWVehSpd_Kph_u9p7[1]	2816
RtrnFWVehSpd Kph u9p7[2]	6656
_RtrnFWVehSpd_Kph_u9p7[3]	9600
_RtrnFWVehSpd_Kph_u9p7[4]	28416
_RtrnFWVehSpd_Kph_u9p7[5]	32000
_RtrnFWVehSpd_Kph_u9p7[6]	32256
_RtrnFWVehSpd_Kph_u9p7[7]	32640
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0
arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	385.519989
arget_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1
arget ReturnFirewall Per1 ReturnCmd MtrNm f32.value	2.72000003
arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32.8499985
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_u	
arget_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_t	

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0	0 ± 0.00048828125	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
12_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
2 RtrnFWUprBoundY MtrNm s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192

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	I		
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-8000		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	409.230011		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	2.86999989		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12.5		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Inst_Ret$			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	2.86999989	2.86999989 ± 0.00048828125	✓





Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)	Innut Value	
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096	
t2 RtrnFWUprBoundY MtrNm s4p11[2][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384	
	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]		
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312	

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408		
t2_RtmFWOprBoundY_MtrNm_s4p11[7][0]	-16384		
	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	19584		
t RtrnFWVehSpd Kph u9p7[7]	19840		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target ReturnFirewall Per1 HandwheelPosition HwDeg f32.value	433		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	3.03999996		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	12		
target_ReturnFirewall_Fer1_verificeSpeed_Rpri_is2.value target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_l		Svc Cnt lac	
target_Rte_inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Deleat_Return_Svc_Cnt_returnErrewall.ReturnFirewall Per1 HandwheelPosition HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_132 target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 MEC Counter Cnt enum		_	
	target_ReturnFirewall_Per1_MEC_Counter_		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mi	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_		
Name	Actual Value	Expected Value	Resu
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.762695313	-0.762695313 ± 0.00048828125	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	V





Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022 18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192 10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096 2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048 2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360 -14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -12800
CETATH AAONIDORHATEMINAHE 24h LI[0][2]	-1200

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Retumpliewali_Peri		12	12CILUIU
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	7808		
t_RtrnFWVehSpd_Kph_u9p7[3]	9728		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	20608		
t_RtrnFWVehSpd_Kph_u9p7[6]	20736		
t_RtrnFWVehSpd_Kph_u9p7[7]	21248		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	457		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.21000004		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	23		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3222222222222222222222222222222222222$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum_ReturnFirewall_Retu$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.21000004	3.21000004 ± 0.00048828125	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)	y
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	0

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048 4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144 7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384 17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-17406
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2 RtrnFWUprBoundY MtrNm s4p11[7][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288		
t2 RtrnFWUprBoundY MtrNm s4p11[7][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-6400		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680		
t RtrnFWUprBoundX HwDeg s11p4[3]	-4000		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-2000		
t RtrnFWUprBoundX HwDeg s11p4[5]	-1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-1200		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-960		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-800		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-672		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	10368		
t_RtrnFWVehSpd_Kph_u9p7[3]	9984		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	23168		
t_RtrnFWVehSpd_Kph_u9p7[6]	23424		
t_RtrnFWVehSpd_Kph_u9p7[7]	24064		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	481.75		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.38000011		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	44.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwD\varepsilon$	$target_ReturnFirewall_Per1_HandwheelPosi$	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	$target_ReturnFirewall_Per1_VehicleSpeed_I$	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.92919922	3.92919922 ± 0.00048828125	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	~

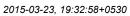
Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-12800

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Input Value t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5] -10240 t2 RtrnFWUprBoundY_MtrNm_s4p11[1][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7] 10240 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0] -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3] -12800 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] -10240 t2 RtrnFWUprBoundY_MtrNm_s4p11[3][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9] -4096 t2 RtrnFWUprBoundY MtrNm s4p11[3][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2] 4096 t2 RtrnFWUprBoundY MtrNm s4p11[4][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7] 10240 $t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]$ 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] 14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10] 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0] -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3] -12800 $t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]$ -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] -2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0] 2048 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1] 3072 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] 4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3] 5120 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5] 7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6] 8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7] 10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8] 12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9] 14336 16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10] $t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]$ -16384 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1] -15360 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2] -14336 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3] -12800 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4] -12288 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5] -10240 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6] -8192 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7] -7168 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8] 6144 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9] -4096 t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10] -2048





Name	Input Value		
t RtrnFWUprBoundX HwDeg s11p4[0]	-10848		
t RtrnFWUprBoundX HwDeg s11p4[1]	-10656		
t RtrnFWUprBoundX HwDeg s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-7104		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3680		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	505.25		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.54999995		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	55.2000008		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt	I target_ReturnFirewall_Per1_Defeat_Return	_Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Per1_HandwheelPosition_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPos	sition_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f3 and firewall_Per1_LimitedReturn_MtrNm_f3 and firewall and f$	target_ReturnFirewall_Per1_LimitedReturn_	_MtrNmf32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Counter_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Cnt_enumerical target_Rte_Inst_Ap_ReturnFirewall.Per1_MEC_Inst_Ap_ReturnFirewall.Per1_M	target_ReturnFirewall_Per1_MEC_Counter_	_Cnt_enum	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32$	target_ReturnFirewall_Per1_ReturnCmd_M	trNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32$	target_ReturnFirewall_Per1_VehicleSpeed_	_Kph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target ReturnFirewall Per1 LimitedReturn MtrNm f32.value	3.54999995	3.54999995 ± 0.00048828125	~

T ·					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~	
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~	

Test Step 2.26 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048

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Name	Input Value
2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7] 2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288 14336
2_RtmFWUprBoundY_MtrNm_s4p11[2][9]	16384
2_RtmFWUprBoundY_MtrNm_s4p11[2][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048 160
:_RtrnFWUprBoundX_HwDeg_s11p4[0]	
t_RtrnFWUprBoundX_HwDog_s11p4[1]	320
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120
:_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280 1440
_ · · · - b	

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	256		
t_RtrnFWVehSpd_Kph_u9p7[1]	2816		
t_RtrnFWVehSpd_Kph_u9p7[2]	6656		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	28416		
t_RtrnFWVehSpd_Kph_u9p7[5]	32000		
t_RtrnFWVehSpd_Kph_u9p7[6]	32256		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	529.299988		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	3.72000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	66.5		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target ReturnFirewall Per1 LimitedReturn MtrNm f32.value	2.51074219	2.51074219 ± 0.00048828125	✓

ReturnFirewall_Per1

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	2048
	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-0144 -4096
	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1952
t_RtrnFWVehSpd_Kph_u9p7[0]	128
t_RtrnFWVehSpd_Kph_u9p7[1]	1408
t_RtrnFWVehSpd_Kph_u9p7[2]	6528
	1
	9600
t_RtrnFWVehSpd_Kph_u9p7[3]	9600 14208
t_RtrnFWVehSpd_Kph_u9p7[3] t_RtrnFWVehSpd_Kph_u9p7[4]	14208
t_RtrnFWVehSpd_Kph_u9p7[3]	

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Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[7]	19840		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	50		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	7		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HandwheelPositio$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	_f32 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	5.10009766	5.10009766 ± 0.00048828125	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2 RtrnFWUprBoundY MtrNm s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	-12288
t2 RtrnFWUprBoundY MtrNm s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[0][7]	-7168
t2 RtrnFWUprBoundY MtrNm s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2 RtrnFWUprBoundY MtrNm s4p11[0][10]	-2048
t2 RtrnFWUprBoundY MtrNm s4p11[1][0]	2048
t2 RtrnFWUprBoundY MtrNm s4p11[1][1]	3072
t2 RtrnFWUprBoundY MtrNm s4p11[1][2]	4096
t2 RtrnFWUprBoundY MtrNm s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2 RtrnFWUprBoundY MtrNm s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_Ixtiii WopiBound1_MitNii_s4p11[1][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_Ixtiii WopiBoundY_MtrNm_s4p11[1][9]	14336
	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288

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Input Value
14336
16384
-16384 -15360
-14336
-12800
-12288
-10240
-8192
-7168
6144
-4096
-2048
2048
3072
4096
5120
6144
7168
8192
10240
12288
14336
16384
-16384
-15360 -14336
-12800
-12288
-10240
-8192
-7168
6144
-4096
-2048
2048
3072
4096
5120
6144
7168
8192
10240
12288
14336
16384
-8000
-6400
-5680
-4000
-2000 -1584
-1304
-960
-880
-800
-672
128
1408
7808
9728
14208
20608
20736
21248
1
70
0
8.80000019
6.60000019
54

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Name	Input Value		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	2 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0.259765625	0.259765625 ± 0.00048828125	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-16384
t2 RtrnFWUprBoundY MtrNm s4p11[2][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	0
t2_RtmFWUprBoundY_MtrNm_s4p11[3][1]	2048
	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-10240

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		(10	<i>)</i> 0
Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120 6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	12288		
t2 RtrnFWUprBoundY MtrNm s4p11[5][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-17408		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-13312		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096 -2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	0		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	17408		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-10848		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-10656		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-9072		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-9024		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-8688		
t_RtrnFWUprBoundX_HwDeg_s11p4[5] t RtrnFWUprBoundX HwDeg_s11p4[6]	-7104 -5696		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-5520		
t RtrnFWUprBoundX HwDeg s11p4[8]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-3744		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-3408		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	1408		
t_RtrnFWVehSpd_Kph_u9p7[2]	10368		
t_RtrnFWVehSpd_Kph_u9p7[3]	9984		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	23168		
t_RtrnFWVehSpd_Kph_u9p7[6]	23424		
t_RtrnFWVehSpd_Kph_u9p7[7]	24064		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	100		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I		Svc Cnt lac	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Deleat_Return_Svc_Cit_1 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_K	_	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	coul
Rte Call Ap ReturnFirewall NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	0	0 ± 0.00048828125	
			_



Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	~

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12800 -12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240 -8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2 RtrnFWUprBoundY MtrNm s4p11[2][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[3][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288

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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096		
t2 RtrnFWUprBoundY MtrNm s4p11[6][3]	5120		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144		
t2 RtrnFWUprBoundY MtrNm s4p11[6][5]	7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-14336		
t2_RtmFWUprBoundY_MtrNm_s4p11[7][2]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	120		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target ReturnFirewall Per1 ReturnCmd MtrNm f32.value	3		
target ReturnFirewall Per1 VehicleSpeed Kph f32.value	12		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I		Svc Cnt lac	
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 HandwheelPosition HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr		
		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_I	To the second se	
Name	Actual Value	Expected Value	Resul
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	3.95019531	3.95019531 ± 0.00048828125	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	✓





Test Step 2.31 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288 14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-8192 -6144
t2_RtrnFwUprBoundY_MtrNm_s4p11[1][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-6144 -4096
t2_RtmFWUprBoundY_MtrNm_s4p11[1][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336 16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-8192 -6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-0144 -4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-2048
t2 RtrnFWUprBoundY MtrNm s4p11[4][0]	0
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	14336 16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384 17408
t2_RtmFWUprBoundY_MtrNm_s4p11[5][0]	-17408
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-13312
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-6144
	4000
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-2048 0
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048

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Name	Input Value			
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	6144			
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168			
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	10240			
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	12288			
	14336			
	16384			
	17408			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-17408			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-16384			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-15360			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-14336			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-13312			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-12288			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096			
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048			
	176			
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352			
	528			
	704			
	880			
t RtrnFWUprBoundX HwDeg s11p4[5]	1056			
	1232			
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408			
	1584			
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1616			
	1952			
	256			
	2816			
	6656			
	9600			
	28416			
	32000			
t_RtrnFWVehSpd_Kph_u9p7[6]	32256			
	32640			
	0			
	145.5			
target ReturnFirewall Per1 MEC Counter Cnt enum.value	1			
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-2.25			
	32.25			
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target ReturnFirewall Per1 Defeat Return	Svc Cnt Igc		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe				
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32				
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum		
	target_ReturnFirewall_Per1_ReturnCmd_Mt	rNm_f32		
	Actual Value	Expected Value	Result	
	199	199	_	
	1. * *		1	
	1	1	✓	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	· ·	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	✓
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-18022	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-18022	

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-18022 -18022
t2_RtmFWUprBoundY_MtrNm_s4p11[0][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-18022 -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-18022 -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-18022 -18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-18022 -18022
t2_RtmFWUprBoundY_MtrNm_s4p11[5][3]	-18022
t2_RtmFWUprBoundY_MtrNm_s4p11[5][4]	-18022
t2_RtmFWUprBoundY_MtrNm_s4p11[5][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	-18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-18022

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Name				
2. RmFWUpBoundY Mirkins_stp117 5 -18022	Name	Input Value		
2, RmFWUpRoundY_MtnN_s4p117 6 18022 18	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	-18022		
2. RmFWUpRoundY_Minkm_s4p11(7) 6 -18022 -	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-18022		
2. RtmFWUprBoundY_Mirkm_s4p11[7][7] 18022 2. RtmFWUprBoundY_Mirkm_s4p11[7][9] 18022 2. RtmFWUprBoundY_Mirkm_s4p11[7][9] 18022 2. RtmFWUprBoundY_Mirkm_s4p11[7][9] 18022 2. RtmFWUprBoundY_Mirkm_s4p11[7][9] 18022 2. RtmFWUprBoundY_Hirkm_s4p11[7][9] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[0] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[3] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[3] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[6] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[6] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[8] 17280 1. RtmFWUprBoundX_HwDeg_s1tp4[9] 17280	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-18022		
Z. RtmFWUpBoundY_MtrNm_s4p11(7) 7 -18022	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-18022		
2_RtmFWUpRoundY_MthXm_s4p11[7] 8]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-18022		
Z. RtmFWUpfboundY_MtrNm_s4p11[7][9] -18022 Z. RtmFWUpfboundY_MtrNm_s4p11[7][10] -18022 Z. RtmFWUpfboundX_HWDeg_s11p4[9] -17280 Z. RtmFWUpfboundX_HWDeg_s11p4[2] -17280 Z. RtmFWUpfboundX_HWDeg_s11p4[3] -17280 Z. RtmFWUpfboundX_HWDeg_s11p4[6] -17280 Z. RtmFWUpfboundX_HWDeg_s11p4[8] -17280 Z. RtmFWUpfboundX_HWDeg_s11p4[9] -17280 Z. RtmFWUpfbo	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-18022		
2. RtmFWUpfBoundY_MtrNm_e4p11[7] 10]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-18022		
P. HrmFWUprBoundX, HwDeg_s11p4[0] 17280	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-18022		
t_RtmFWUprBoundX_HwDeg_s11p4[1]	t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-18022		
RtmFWUprBoundX_HwDeg_st1p4[2]	t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-17280		
TRINFWUpfBoundX_HwDeg_s11p4[3]	t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-17280		
RtmFWUprBoundX_HwDeg_s11p4[4]	t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-17280		
LRtnFWUprBoundX_HwDeg_s11p4[6] -17280 LRtnFWUprBoundX_HwDeg_s11p4[6] -17280 LRtnFWUprBoundX_HwDeg_s11p4[7] -17280 LRtnFWUprBoundX_HwDeg_s11p4[8] -17280 LRtnFWUprBoundX_HwDeg_s11p4[9] -17280 LRtnFWUprBoundX_HwDeg_s11p4[10] -17280 LRtnFWehSpd_Kph_u9p7[0] 0 LRtnFWehSpd_Kph_u9p7[1] 0 LRtnFWehSpd_Kph_u9p7[2] 0 LRtnFWehSpd_Kph_u9p7[3] 0 LRtnFWehSpd_Kph_u9p7[4] 0 LRtnFWehSpd_Kph_u9p7[6] 0 LRtnFWehSpd_Kph_u9p7[7] 0 LRtnFWehSpd_Kph_u9p7[7] 0 LRtnFWehSpd_Kph_u9p7[7] 0 LRtnFWehSpd_Kph_u9p7[7] 0 LRtnFWehSpd_Kph_u9p7[7] 0 LRtnFWehSpd_Kph_u9p7[8] 0 LRtnFWehSpd_Kph_u9p7[7] 0 Larget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_d32 value -900 target_ReturnFirewall_Per1_ReturnCmd_MtNm_d32 value -8.80000019 target_ReturnFirewall_Per1_ReturnGmd_MtNm_d32 value -8.80000019 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_ReturnFirewall_Per1_Hand	t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-17280		
t_RtmFWUprBoundX_HwDeg_s11p4[6] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[7] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[8] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[9] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWWehSpd_Kph_u9p7[0] 0 t_RtmFWWehSpd_Kph_u9p7[1] 0 t_RtmFWWehSpd_Kph_u9p7[3] 0 t_RtmFWWehSpd_Kph_u9p7[4] 0 t_RtmFWWehSpd_Kph_u9p7[6] 0 t_RtmFWWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[8] 0 t_RtmFWehSpd_Kph_u9p7[8]	t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-17280		
t_RtmFWUprBoundX_HwDeg_s11p4[7] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[8] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[9] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWehSpd_Kph_u9p7[0] 0 t_RtmFWehSpd_Kph_u9p7[1] 0 t_RtmFWehSpd_Kph_u9p7[2] 0 t_RtmFWehSpd_Kph_u9p7[3] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 target_ReturmFirewall_Per1_Defeat_Returm_Svc_Cnt_lgc_value 0 target_ReturmFirewall_Per1_HandwheelPosition_HwDeg_f32_value -900 target_ReturmFirewall_Per1_MEC_Counter_Cnt_enum_value 0 target_ReturmFirewall_Per1_MeticleSpeed_Kph_f32_value -8.8000019 target_ReturmFirewall_Per1_WehicleSpeed_Kph_f32_value 0 target_Ret_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_f32 target_Ret_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_f32 target_Ret_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_f32 target_ReturmFirewall_Per1_LimitedReturm_Mirm_f32 target_Ret_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_MEC_Counter_Cnt_enum target_R	t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-17280		
t_RtmFWUprBoundX_HwDeg_s11p4[8] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[9] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWUprBoundX_HwDeg_s1[1p4[10] -17280 t_RtmFWUprBogd_Kph_u9p7[1] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[2] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[3] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[3] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[5] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[6] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[6] -0 -17280 t_RtmFWUprBogd_Kph_u9p7[7] -0 -17280 target_ReturmFirewall_Per1_Defeat_Returm_Svc_Cnt_lgc.value -900 target_ReturmFirewall_Per1_Defeat_Returm_Svc_Cnt_lgc.value -900 target_ReturmFirewall_Per1_MEC_Counter_Cnt_enum.value -8.8000019 target_ReturmFirewall_Per1_VehicleSpeed_Kph_132.value -8.8000019 target_ReturmFirewall_Per1_VehicleSpeed_Kph_132.value -8.8000019 target_ReturmFirewall_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_HandwheelPosition_HwDeg_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_ReturmCmd_MtrNm_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_ReturmCmd_MtrNm_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_ReturmCmd_MtrNm_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_ReturmCmd_MtrNm_132 target_Rte_Inst_Ap_ReturmFirewall_ReturmFirewall_Per1_VehicleSpeed_Kph_132	t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-17280		
t_RtmFWUprBoundX_HwDeg_s11p4[9] -17280 t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWWehSpd_Kph_u9p7[0] 0 t_RtmFWWehSpd_Kph_u9p7[1] 0 t_RtmFWWehSpd_Kph_u9p7[3] 0 t_RtmFWWehSpd_Kph_u9p7[3] 0 t_RtmFWWehSpd_Kph_u9p7[3] 0 t_RtmFWWehSpd_Kph_u9p7[4] 0 t_RtmFWWehSpd_Kph_u9p7[5] 0 t_RtmFWWehSpd_Kph_u9p7[5] 0 t_RtmFWWehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_BeadwheelPosition_HwDeg_132 value -900 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_152 value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_152 value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_152 value 0 target_ReturnFirewall_Retur	t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-17280		
t_RtmFWUprBoundX_HwDeg_s11p4[10] -17280 t_RtmFWehSpd_Kph_u9p7[0] 0 t_RtmFWehSpd_Kph_u9p7[1] 0 t_RtmFWehSpd_Kph_u9p7[2] 0 t_RtmFWehSpd_Kph_u9p7[3] 0 t_RtmFWehSpd_Kph_u9p7[3] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 target_RetumFirewall_Per1_Defeat_Return_Svc_Cnt_lgc_value 0 target_RetumFirewall_Per1_MEC_Counter_Cnt_enum_value 0 target_RetumFirewall_Per1_MEC_Counter_Cnt_enum_value 1 target_RetumFirewall_Per1_ReturnCmd_MtrNm_f32_value 0 target_RetumFirewall_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_Defeat_RetumFirewall_Per1_LimitedRetum_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_Per1_LimitedRetum_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_RetumCmd_MtrNm_f32_target_Ret_Inst_Ap_RetumFirewall_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed_Kph_f32_target_RetumFirewall_Per1_VehicleSpeed	t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-17280		
t_RtmFWehSpd_Kph_u9p7[0] 0 t_RtmFWehSpd_Kph_u9p7[1] 0 t_RtmFWehSpd_Kph_u9p7[2] 0 t_RtmFWehSpd_Kph_u9p7[3] 0 t_RtmFWehSpd_Kph_u9p7[3] 0 t_RtmFWehSpd_Kph_u9p7[4] 0 t_RtmFWehSpd_Kph_u9p7[5] 0 t_RtmFWehSpd_Kph_u9p7[6] 0 t_RtmFWehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Leptact_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_Bet_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_Leptact_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFir	t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-17280		
t_RtmFWVehSpd_Kph_u9p7[1] 0 t_RtmFWVehSpd_Kph_u9p7[2] 0 t_RtmFWVehSpd_Kph_u9p7[3] 0 t_RtmFWVehSpd_Kph_u9p7[4] 0 t_RtmFWVehSpd_Kph_u9p7[5] 0 t_RtmFWVehSpd_Kph_u9p7[5] 0 t_RtmFWVehSpd_Kph_u9p7[6] 0 t_RtmFWVehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_igc.value 0 target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value 9000 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 1 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.val	t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-17280		
t_RtmFWVehSpd_Kph_u9p7[2] 0 t_RtmFWVehSpd_Kph_u9p7[3] 0 t_RtmFWVehSpd_Kph_u9p7[4] 0 t_RtmFWVehSpd_Kph_u9p7[5] 0 t_RtmFWVehSpd_Kph_u9p7[6] 0 t_RtmFWVehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value 0 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 1 target_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 1 target_ReturnFirewall_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 1 target_ReturnFirewall_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[3] 0 t_RtrnFWVehSpd_Kph_u9p7[4] 0 t_RtrnFWVehSpd_Kph_u9p7[5] 0 t_RtrnFWVehSpd_Kph_u9p7[6] 0 t_RtrnFWVehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value 0 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value arget_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 1 target_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc 1 target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDes_target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum_target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum_target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 1 target_Ret_Inst_Ap_ReturnFirewall_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret_Inst_Ap_Ret	t_RtrnFWVehSpd_Kph_u9p7[1]	0		
t_RtrnFWVehSpd_Kph_u9p7[4] 0 t_RtrnFWVehSpd_Kph_u9p7[5] 0 t_RtrnFWVehSpd_Kph_u9p7[6] 0 t_RtrnFWVehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value -900 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value -8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_Ret lnst_Ap_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[2]	0		
t_RtmFWVehSpd_Kph_u9p7[5] 0 t_RtmFWVehSpd_Kph_u9p7[6] 0 t_RtmFWVehSpd_Kph_u9p7[7] 0 target_ReturmFirewall_Per1_Defeat_Returm_Svc_Cnt_lgc.value 0 target_ReturmFirewall_Per1_HandwheelPosition_HwDeg_f32.value -900 target_ReturmFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturmFirewall_Per1_ReturnCmd_MtrNm_f32.value -8.80000019 target_ReturmFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_ReturmFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_ReturmFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_Ret_Inst_Ap_ReturmFirewall_Per1_Defeat_Returm_Svc_Cnt_lgc target_Rte_Inst_Ap_ReturmFirewall_Per1_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_LimitedReturm_MtrNm_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturmFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturmFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[3]	0		
t_RtmFWVehSpd_Kph_u9p7[6] 0 t_RtmFWVehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value 0 target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value -900 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value 0 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value -8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value 0 target_Ret_Inst_Ap_ReturnFirewall_Per1_Defeat_Return Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[4]	0		
t_RtmFWVehSpd_Kph_u9p7[7] 0 target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value -900 target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value -8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_Ret_Inst_Ap_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDeg target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[5]	0		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_Ret_Inst_Ap_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDeg target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[6]	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value -900 target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value -8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value -8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value -8.80000019 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	t_RtrnFWVehSpd_Kph_u9p7[7]	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_Ret_Inst_Ap_ReturnFirewall_Per1_Defeat_Return Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDec target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value -8.80000019 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_Ret_Inst_Ap_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc target_Ret_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDes_f32 target_Ret_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Ret_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value target_Rte_Inst_Ap_ReturnFirewall_Per1_Defeat_Return Svc_Cnt_Igc target_Rte_Inst_Ap_ReturnFirewall_Per1_HandwheelPosition_HwDec target_Rte_Inst_Ap_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-8.80000019		
target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDe target_Ret_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwD\varepsilon$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32 target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
	target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
Name Actual Value Expected Value Beguite	target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	Cph_f32	
Result Result	Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 199 199	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 1 1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 1 1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value 8.79980469 8.79980469 \$ 8.79980469 ± 0.00048828125	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.79980469	8.79980469 ± 0.00048828125	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	18022

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[1][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022 18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2_RtmFWUprBoundY_MtrNm_s4p11[3][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022 18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[6][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022

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ReturnFirewall_Per1

Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280		
t_RtrnFWVehSpd_Kph_u9p7[0]	65408		
t_RtrnFWVehSpd_Kph_u9p7[1]	65408		
t_RtrnFWVehSpd_Kph_u9p7[2]	65408		
t_RtrnFWVehSpd_Kph_u9p7[3]	65408		
t_RtrnFWVehSpd_Kph_u9p7[4]	65408		
t_RtrnFWVehSpd_Kph_u9p7[5]	65408		
t_RtrnFWVehSpd_Kph_u9p7[6]	65408		
t_RtrnFWVehSpd_Kph_u9p7[7]	65408		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255		
target Rte Inst Ap ReturnFirewall.ReturnFirewall Per1 Defeat Return Svc Cnt I	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Retur$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call ReturnFirewall Per1 CP1 CheckpointReached	1	Rte Call ReturnFirewall Per1 CP1 ChecknointReached	1	

Test Case 3: Path Test

Specification

Performance Metrics:
(With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS3.1 1 072 3 q ics:

ReturnFirewall_Per1

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	-16384 -15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1] t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	-14336
t2_RtinFWOpiBoundY_MtrNm_s4p11[1][2] t2_RtinFWUprBoundY_MtrNm_s4p11[1][3]	-14330
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	3072
12_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	-12288 -10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	-10240 -8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	-4096
2 RtrnFWUprBoundY MtrNm s4p11[3][10]	-2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	2048
2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	3072
2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	4096
2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	5120
2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	7168
2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	8192
2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	10240
2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	12288
2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	14336
2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	-16384
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	-15360
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	-14336
2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	-12800
2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	-12288
2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	-10240
2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	-8192
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	-7168
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	6144
2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	-4096
2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	5120 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	7168 8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	10240

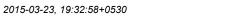
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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	14336		
t2 RtrnFWUprBoundY MtrNm s4p11[6][10]	16384		
t2 RtrnFWUprBoundY MtrNm s4p11[7][0]	-16384		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	-15360		
t2 RtrnFWUprBoundY MtrNm s4p11[7][2]	-14336		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	-12800		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	-12288		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-7168		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	160		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	320		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	480		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	640		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	800		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	960		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1120		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1280		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1440		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1600		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	1760		
t_RtrnFWVehSpd_Kph_u9p7[0]	0		
t_RtrnFWVehSpd_Kph_u9p7[1]	2560		
t_RtrnFWVehSpd_Kph_u9p7[2]	6400		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	12800		
t_RtrnFWVehSpd_Kph_u9p7[5]	19200		
t_RtrnFWVehSpd_Kph_u9p7[6]	25600		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	-900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	1		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-1		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	12		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe			
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum		_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mt	_	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_	1	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	V
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-4.39990234	-4.39990234 ± 0.00048828125	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	•

Test Step 3.2 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	0	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	2048	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	4096	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	5120	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	6144	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	7168	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	10240	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	12288	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	14336	
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	16384	





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Name	Input Value		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	-10240		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	-8192		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	-6144		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	-4096		
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	-2048		
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	176		
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	352		
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	528		
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	704		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	880		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	1056		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	1232		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	1408		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	1584		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	1760		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	3520		
t_RtrnFWVehSpd_Kph_u9p7[0]	1152		
t_RtrnFWVehSpd_Kph_u9p7[1]	3712		
t_RtrnFWVehSpd_Kph_u9p7[2]	7552		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	25472		
t_RtrnFWVehSpd_Kph_u9p7[5]	20352		
t_RtrnFWVehSpd_Kph_u9p7[6]	32640		
t_RtrnFWVehSpd_Kph_u9p7[7]	38272		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	2		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.200000003		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	32		
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_Hand$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_I	Cph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	7.64990234	7.64990234 ± 0.00048828125	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~
Pto Call PoturnEirowall Port CP1 ChecknointPeached	1	Pte Call PeturnEirowall Per1 CP1 ChecknointPeached	1	

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2 RtrnFWUprBoundY MtrNm s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2 RtrnFWUprBoundY MtrNm s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	16384
	-16384 -15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2] t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-14336 -12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-1288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2 RtrnFWUprBoundY MtrNm s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000
t_RtrnFWUprBoundX_HwDeg_s11p4[1]	-1968
t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-1600
t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-1424

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Name	Input Value		
t_RtrnFWUprBoundX_HwDeg_s11p4[4]	-1248		
t_RtrnFWUprBoundX_HwDeg_s11p4[5]	-1072		
t_RtrnFWUprBoundX_HwDeg_s11p4[6]	-896		
t_RtrnFWUprBoundX_HwDeg_s11p4[7]	-720		
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-544		
t_RtrnFWUprBoundX_HwDeg_s11p4[9]	-368		
t_RtrnFWUprBoundX_HwDeg_s11p4[10]	-192		
t_RtrnFWVehSpd_Kph_u9p7[0]	128		
t_RtrnFWVehSpd_Kph_u9p7[1]	2688		
t_RtrnFWVehSpd_Kph_u9p7[2]	6528		
t_RtrnFWVehSpd_Kph_u9p7[3]	9600		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	19328		
t_RtrnFWVehSpd_Kph_u9p7[6]	27008		
t_RtrnFWVehSpd_Kph_u9p7[7]	32640		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	0		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	0		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	-0.5		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	31.5		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HwDerturnFirewall_Pert1_HandwheelPosition_HandwheelP$	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32$	target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_k	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	-0.5	-0.5 ± 0.00048828125	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~	
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	•	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	•	

Test Step 3.4 (Repeat Count = 1)	✓ ·
Name	Input Value
Rte_Inst_Ap_ReturnFirewall	target_Rte_Inst_Ap_ReturnFirewall
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	-10240

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][3]	5120 6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4] t2_RtrnFWUprBoundY_MtrNm_s4p11[3][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	10240 12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][8] t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	-16384
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	-15360
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	-14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	-12800
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	-12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	-10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	-8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	-7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	-4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	-2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	2048
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][1]	3072
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	4096
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	5120
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	6144
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	7168
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][6]	8192
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	10240
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	12288
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	14336
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	16384 -8000
t_RtrnFWUprBoundX_HwDeg_s11p4[0]	-8000 -6400
t_RtrnFWUprBoundX_HwDeg_s11p4[1] t_RtrnFWUprBoundX_HwDeg_s11p4[2]	-5680
t_RtrnFWUprBoundX_HwDeg_s11p4[2] t_RtrnFWUprBoundX_HwDeg_s11p4[3]	-4000
t_RtmFWUprBoundX_HwDeg_s11p4[3]	-2000
t_RtmFWUprBoundX_HwDeg_s11p4[4]	-1584
t_RtmFWUprBoundX_HwDeg_s11p4[6]	-1200 -1200
t_RtmFWUprBoundX_HwDeg_s11p4[7]	-960
	-880
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	-880 -800
t_RtmFWUprBoundX_HwDeg_s11p4[8] t_RtmFWUprBoundX_HwDeg_s11p4[9]	-800
t_RtrnFWUprBoundX_HwDeg_s11p4[8]	

 $target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value$

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0.259765625 ± 0.00048828125

ReturnFirewall_Per1

Name	Input Value		
t_RtrnFWVehSpd_Kph_u9p7[2]	7808		
t_RtrnFWVehSpd_Kph_u9p7[3]	9728		
t_RtrnFWVehSpd_Kph_u9p7[4]	14208		
t_RtrnFWVehSpd_Kph_u9p7[5]	20608		
t_RtrnFWVehSpd_Kph_u9p7[6]	20736		
t_RtrnFWVehSpd_Kph_u9p7[7]	21248		
target_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value	1		
target_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	70		
target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum.value	0		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	54		
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_Inst_Ap_ReturnFirewall_Retur$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_ReturnFirewall_Per1_HandwheelPosition_HwDeltarget_ReturnFirewall_Retur$	target_ReturnFirewall_Per1_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_I	MtrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall_ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_Cnt_enum		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32		
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32	target_ReturnFirewall_Per1_VehicleSpeed_h	(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~	
BilinearXYM_s16_s16Xs16YM_Cnt	2	BilinearXYM_s16_s16Xs16YM_Cnt	2	~	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	✓	

0.259765625

Name	Input Value
Rte Inst Ap ReturnFirewall	target Rte Inst Ap ReturnFirewall
t2 RtrnFWUprBoundY MtrNm s4p11[0][0]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[0][1]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[0][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][3]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[0][4]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[0][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][6]	18022
2 RtrnFWUprBoundY MtrNm s4p11[0][7]	18022
2 RtrnFWUprBoundY MtrNm s4p11[0][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[0][10]	18022
2 RtrnFWUprBoundY MtrNm s4p11[1][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[1][2]	18022
2 RtrnFWUprBoundY MtrNm s4p11[1][3]	18022
2 RtrnFWUprBoundY MtrNm s4p11[1][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][5]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][6]	18022
2 RtrnFWUprBoundY MtrNm s4p11[1][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[1][8]	18022
2 RtrnFWUprBoundY MtrNm s4p11[1][9]	18022
2 RtrnFWUprBoundY MtrNm s4p11[1][10]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][1]	18022
2 RtrnFWUprBoundY MtrNm s4p11[2][2]	18022
2 RtrnFWUprBoundY MtrNm s4p11[2][3]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][5]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[2][10]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[3][1]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][2]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[3][3]	18022

ReturnFirewall_Per1

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Name	Input Value
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][4]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[3][5]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][6]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[3][7]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][8]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[3][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[3][10]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[4][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][1]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[4][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][4]	18022
12 RtrnFWUprBoundY MtrNm s4p11[4][5]	18022
	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][7]	
12_RtrnFWUprBoundY_MtrNm_s4p11[4][8]	18022 18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][9]	
t2_RtrnFWUprBoundY_MtrNm_s4p11[4][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][0]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][1]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][2]	18022
I2_RtrnFWUprBoundY_MtrNm_s4p11[5][3]	18022
I2_RtrnFWUprBoundY_MtrNm_s4p11[5][4]	18022
I2_RtrnFWUprBoundY_MtrNm_s4p11[5][5]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[5][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[5][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][9]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[5][10]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][0]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][2]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][4]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][5]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][6]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][7]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][8]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[6][9]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[6][10]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[7][0]	18022
t2 RtrnFWUprBoundY MtrNm s4p11[7][1]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][2]	18022
12_RtrnFWUprBoundY_MtrNm_s4p11[7][3]	18022
t2_RtrnFWUprBoundY_MtrNm_s4p11[7][4]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][5]	18022
2 RtrnFWUprBoundY MtrNm s4p11[7][6]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][7]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][8]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][9]	18022
2_RtrnFWUprBoundY_MtrNm_s4p11[7][10]	18022
_RtrnFWUprBoundX_HwDeg_s11p4[0]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[1]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[2]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[3]	17280
r_RtrnFWUprBoundX_HwDeg_s11p4[4]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[5]	17280
:_RtrnFWUprBoundX_HwDeg_s11p4[6]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[7]	17280
:_RtrnFWUprBoundX_HwDeg_s11p4[8]	17280
:_RtrnFWUprBoundX_HwDeg_s11p4[9]	17280
_RtrnFWUprBoundX_HwDeg_s11p4[10]	17280
_RtrnFWVehSpd_Kph_u9p7[0]	65408
_RtrnFWVehSpd_Kph_u9p7[1]	65408
_RtrnFWVehSpd_Kph_u9p7[2]	65408
:_RtrnFWVehSpd_Kph_u9p7[3]	65408
 _RtrnFWVehSpd_Kph_u9p7[4]	65408
E_RtrnFWVehSpd_Kph_u9p7[5]	65408
t_RtrnFWVehSpd_Kph_u9p7[6]	65408
t_RtrnFWVehSpd_Kph_u9p7[7]	65408
	1
arget ReturnFirewall Per1 Defeat Return Svc Cnt loc value	
arget_ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_lgc.value arget_ReturnFirewall_Per1_HandwheelPosition_HwDeg_f32.value	900

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Name	Input Value		
target_ReturnFirewall_Per1_ReturnCmd_MtrNm_f32.value	8.80000019		
target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32.value	255		
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_Defeat_Return_Svc_Cnt_I$	target_ReturnFirewall_Per1_Defeat_Return_	Svc_Cnt_lgc	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_HandwheelPosition_HwDe	target_ReturnFirewall_Per1_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_LimitedReturn_MtrNm_f32	target_ReturnFirewall_Per1_LimitedReturn_M	/ltrNm_f32	
$target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_MEC_Counter_Cnt_enum$	target_ReturnFirewall_Per1_MEC_Counter_0	Cnt_enum	
target_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_ReturnCmd_MtrNm_f32	target_ReturnFirewall_Per1_ReturnCmd_Mtr	Nm_f32	
et_Rte_Inst_Ap_ReturnFirewall.ReturnFirewall_Per1_VehicleSpeed_Kph_f32 target_ReturnFirewall_Per1_VehicleSpeed_Kph_f32		(ph_f32	
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	199	199	~
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_ReturnFirewall_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	8.80000019 ± 0.00048828125	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP0_CheckpointReached	1	~	
Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_ReturnFirewall_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	Rte_Call_ReturnFirewall_Per1_CP1_CheckpointReached	1	~	