Integration Manual –AssistFirewall

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# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

Note: Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be referred. Developer should track the references.

## Global Functions (Non RTE) to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Configuration Files to be provided by Integration Project

Ap\_AssistFirewall\_Cfg.h for checkpoint enable

### Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| Parameter | Notes | SWC |
| AssistFirewallGeneral/AssistFirewallCPEnable | To enable checkpoints | AssistFirewall |

### DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| ISR Name | VIM # | Priority Dependency | Notes |
| None |  |  |  |

### Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration Dataflow Requirements

## Required Global Data Inputs

BaseAssistCmd\_MtrNm\_f32

Defeat\_AsstTbl\_Service\_Cnt\_lgc

HighFreqAssist\_MtrNm\_f32

HwTorque\_HwNm\_f32

HysteresisComp\_MtrNm\_f32

MEC\_Counter\_Cnt\_enum

VehicleSpeed\_Kph\_f32

## Required Global Data Outputs

AsstFirewallActive\_Uls\_f32

CombinedAssist\_MtrNm\_f32

## Specific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| AssistFirewall\_Init |  | On Rte\_Init |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| AssistFirewall\_Per1 | triggered on TimingEvent  Disabled in WARMINIT and OFF | 2ms |

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| ASSISTFIREWALL\_START\_SEC\_VAR\_CLEARED\_32 |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| None |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note: Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note: Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 6-Nov-14 | SB |
| 2 | Updated per SF34 v009 | 15-Jan-15 | SB |