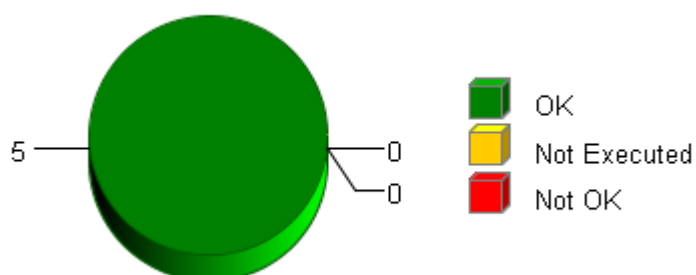


Summary

Total Test Objects: 5
Successful: 5
Failed: 0
Not Executed: 0
Date: 2015-04-21
Time: 15:38:09+0530

Overall Test Object Results (including Coverage)



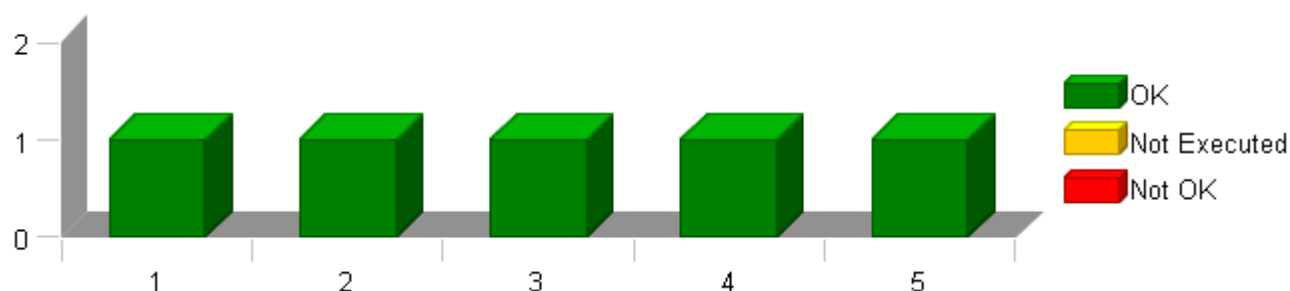
Selected Project Items

Test Collection "CBD_UnitTest"

Used Test Environments

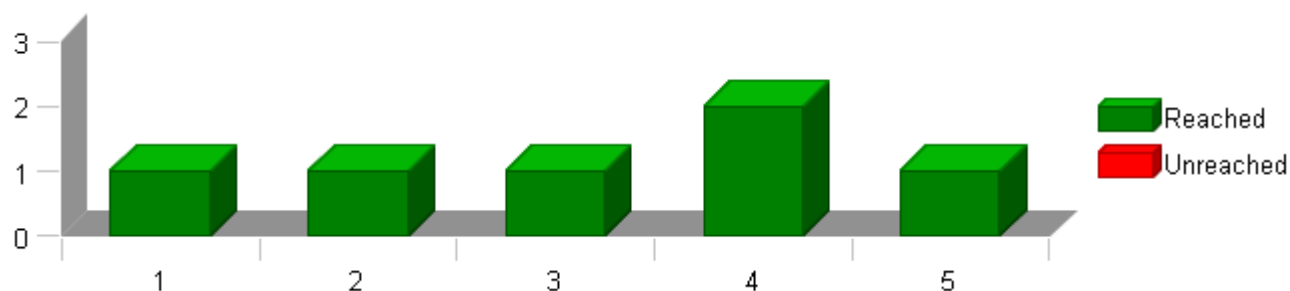
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



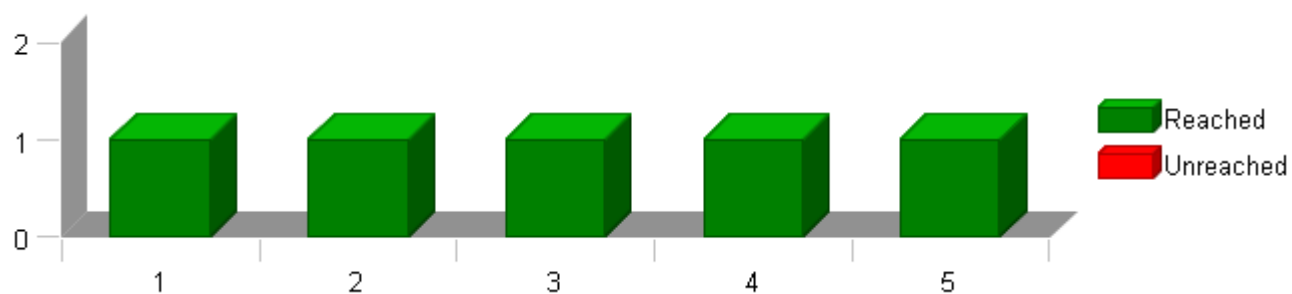
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	Test Cases	Result
	DemIf	100 %	100 %	5 of 5 passed	✓
	CBD_UnitTest	100 %	100 %	5 of 5 passed	✓
	DemIf	100 %	100 %	5 of 5 passed	✓
1	DemIf_DemShutdown	100 %	100 %	1 of 1 passed	✓
2	DemIf_DummyFunction	100 %	100 %	1 of 1 passed	✓
3	DemIf_RestartDem	100 %	100 %	1 of 1 passed	✓
4	DemIf_SetEventStatus	100 %	100 %	1 of 1 passed	✓
5	DemIf_SetOperationCycleState	100 %	100 %	1 of 1 passed	✓

TEST DETAILS REPORT

2015-04-21, 15:33:42+0530

DemIf_DemShutdown



Project	DemIf
Module	DemIf
Test Object	DemIf_DemShutdown

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DemIfsrc\Ap_DemIf.c
Compiler Options	-I\$(PROJECTROOT)\DemIfutp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4_tools\compiler\tms470\include

Comments/Description/Specification

Name	Text
Module 'DemIf'	*****Unit Test Description***** Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_DemIf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1: "CBD_Sandbox_dbg.map" Map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

2015-04-21, 15:33:42+0530

DemIf_DemShutdown



Test Case 1: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 226 Cycles

Description Vector Description:

TS1.1 Only Call trace is checked

Test Step 1.1 (Repeat Count = 1)

T				
Actual Function	Count	Expected Function	Count	Result
Dem_Shutdown	1	Dem_Shutdown	1	✓

TEST DETAILS REPORT

2015-04-21, 15:36:31+0530



DemIf_SetEventStatus

Project	DemIf
Module	DemIf
Test Object	DemIf_SetEventStatus

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DemIfsrc\Ap_DemIf.c
Compiler Options	-I\$(PROJECTROOT)\DemIfutp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification

Name	Text
Module 'DemIf'	*****Unit Test Description***** Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_DemIf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:"CBD_Sandbox_dbg.map" Map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 240 Cycles
 TS1.2 240 Cycles
 TS1.3 240 Cycles
 TS1.4 240 Cycles
 TS1.5 240 Cycles
 TS1.6 240 Cycles
 TS1.7 240 Cycles
 TS1.8 240 Cycles
 TS1.9 240 Cycles
 TS1.10 240 Cycles
 TS1.11 240 Cycles

Description Vector Description:

TS1.1 EventId min
 TS1.2 EventId max
 TS1.3 EventId pos
 TS1.4 EventStatus min
 TS1.5 EventStatus max
 TS1.6 EventStatus pos
 TS1.7 Dem_SetEventStatus=min
 TS1.8 Dem_SetEventStatus=max
 TS1.9 Dem_SetEventStatus=mid
 TS1.10 All min
 TS1.11 All max

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Dem_SetEventStatus()	0		
EventId	0		
EventStatus	45		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
Dem_SetEventStatus()	13		
EventId	255		
EventStatus	67		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	13	13	✓

T				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
Dem_SetEventStatus()	127		
EventId	90		
EventStatus	78		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	127	127	✓

T				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

TEST DETAILS REPORT

2015-04-21, 15:36:31+0530



DemIf_SetEventStatus

Test Step 1.4 (Repeat Count = 1) ✓

Name	Input Value		
Dem_SetEventStatus()	45		
EventId	34		
EventStatus	0		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	45	45	✔

T ✓				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

Test Step 1.5 (Repeat Count = 1) ✓

Name	Input Value		
Dem_SetEventStatus()	116		
EventId	56		
EventStatus	255		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	116	116	✔

T ✓				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

Test Step 1.6 (Repeat Count = 1) ✓

Name	Input Value		
Dem_SetEventStatus()	31		
EventId	67		
EventStatus	65		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	31	31	✓

T ✓				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

Test Step 1.7 (Repeat Count = 1) ✓

Name	Input Value		
Dem_SetEventStatus()	0		
EventId	12		
EventStatus	21		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	0	0	✓

T ✓				
Actual Function	Count	Expected Function	Count	Result
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓

Test Step 1.8 (Repeat Count = 1) ✓

Name	Input Value		
Dem_SetEventStatus()	255		
EventId	45		
EventStatus	31		
Name	Actual Value	Expected Value	Result
DemIf_SetEventStatus()	255	255	✓

TEST DETAILS REPORT

2015-04-21, 15:36:31+0530

DemIf_SetEventStatus



T					✓
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓	

Test Step 1.9 (Repeat Count = 1)					✓
Name		Input Value			
Dem_SetEventStatus()		113			
EventId		84			
EventStatus		56			
Name	Actual Value	Expected Value	Result		
DemIf_SetEventStatus()	113	113	✓		

T					✓
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓	

Test Step 1.10 (Repeat Count = 1)					✓
Name		Input Value			
Dem_SetEventStatus()		0			
EventId		0			
EventStatus		0			
Name	Actual Value	Expected Value	Result		
DemIf_SetEventStatus()	0	0	✓		

T					✓
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓	

Test Step 1.11 (Repeat Count = 1)					✓
Name		Input Value			
Dem_SetEventStatus()		255			
EventId		255			
EventStatus		255			
Name	Actual Value	Expected Value	Result		
DemIf_SetEventStatus()	255	255	✓		

T					✓
Actual Function	Count	Expected Function	Count	Result	
Dem_SetEventStatus	1	Dem_SetEventStatus	1	✓	

TEST DETAILS REPORT

2015-04-21, 15:37:19+0530

DemIf_SetOperationCycleState



Project	DemIf
Module	DemIf
Test Object	DemIf_SetOperationCycleState

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DemIfsrc\Ap_DemIf.c
Compiler Options	-I\$(PROJECTROOT)\DemIfutp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4_tools\compiler\tms470\include

Comments/Description/Specification

Name	Text
Module 'DemIf'	*****Unit Test Description***** Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_DemIf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1: "CBD_Sandbox_dbg.map" Map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 239 Cycles
TS1.2 239Cycles
TS1.3 239 Cycles
TS1.4 239 Cycles
TS1.5 239 Cycles
TS1.6 239 Cycles
TS1.7 239 Cycles
TS1.8 239 Cycles

Description Vector Description:

TS1.1 NxtrOperationCycleId min
TS1.2 NxtrOperationCycleId max
TS1.3 NxtrOperationCycleId pos
TS1.4 NxtrCycleState min
TS1.5 NxtrCycleState max
TS1.6 NxtrCycleState pos
TS1.7 All min
TS1.8 All max

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
NxtrCycleState	45
NxtrOperationCycleId	0

T

Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
NxtrCycleState	67
NxtrOperationCycleId	255

T

Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value
NxtrCycleState	78
NxtrOperationCycleId	90

T

Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value
NxtrCycleState	0
NxtrOperationCycleId	34

T

Actual Function	Count	Expected Function	Count	Result
Dem_SetOperationCycleState	1	Dem_SetOperationCycleState	1	✓

Test Step 1.5 (Repeat Count = 1)

Name	Input Value
NxtrCycleState	255

TEST DETAILS REPORT

2015-04-21, 15:37:19+0530



DemIf_SetOperationCycleState

Name	Input Value

TEST DETAILS REPORT

2015-04-21, 15:35:41+0530

DemIf_RestartDem



Project	DemIf
Module	DemIf
Test Object	DemIf_RestartDem

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DemIfsrc\Ap_DemIf.c
Compiler Options	-I\$(PROJECTROOT)\DemIfutp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification

Name	Text
Module 'DemIf'	*****Unit Test Description***** Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_DemIf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1:"CBD_Sandbox_dbg.map" Map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test

Specification Performance Metrics (With "None"
Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 229 Cycles

Description Vector Description:

TS1.1 Only Call trace is checked

Test Step 1.1 (Repeat Count = 1)

T				
Actual Function	Count	Expected Function	Count	Result
Dem_Init	1	Dem_Init	1	✓

TEST DETAILS REPORT

2015-04-21, 15:35:06+0530

DemIf_DummyFunction



Project	DemIf
Module	DemIf
Test Object	DemIf_DummyFunction

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_DemIf
Configuration File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\DemIfsrc\Ap_DemIf.c
Compiler Options	-I\$(PROJECTROOT)\DemIfutp\contract -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -ID:\ccsv4_tools\compiler\tms470\include

Comments/Description/Specification

Name	Text
Module 'DemIf'	*****Unit Test Description***** Name of Tester: Madhuparna Duarah Code File(s) Under Test: Ap_DemIf Code File(s) Version: 1 Module Design Document: NA Module Design Document Version: NA Data Dictionary Version: NA Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes): 24 Total RAM Used (Bytes): 0 Total CALS Used (Bytes): 0 Special Test Requirements: None Test Date: 04-21-2015 Comments: Note1: "CBD_Sandbox_dbg.map" Map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\PSA_DemIf\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test



Description Vector Description:
No input output variables present